

Control and synchronization algorithms for a grid-connected photovoltaic system under harmonic distortions, frequency variations and unbalances

N. F. Guerrero-Rodríguez^{a*}, Alexis. B. Rey-Boué^b, Luis C. Herrero-de Lucas^c, Fernando Martinez-Rodrigo^d

^{a*} Department of Electronics, Computers Technology and Projects, Universidad Politécnica de Cartagena, c/Dr. Fleming, s/n 30202, Cartagena, Murcia, Spain. Tel.: +34 968 326514; fax: +34 968 326400, E-mail address: nestor.guerrero@upct.es

^b Department of Electronics, Computers Technology and Projects, Universidad Politécnica de Cartagena, c/Dr. Fleming, s/n 30202, Cartagena, Murcia, Spain. Tel.: +34 968 325928; fax: +34 968 326400, E-mail address: alexis.rey@upct.es

^c Department of Electronics Technology, Universidad de Valladolid, c/Fco. Mendizabal 1, 47014, Valladolid, Spain. Tel.: +34 983 423521; fax: +34 983 423490, E-mail address: lcherrer@tele.uva.es

^d Department of Electronics Technology, Universidad de Valladolid, c/Fco. Mendizabal 1, 47014, Valladolid, Spain. Tel.: +34 983 423921; fax: +34 983 423490, E-mail address: fer_mart@tele.uva.es

*Corresponding autor: N. F. Guerrero-Rodríguez

ABSTRACT

In this paper, an implementation of the control and the synchronization algorithms for a Voltage Source Inverter used as the power conditioner for Photovoltaic renewable energy in a grid-connected structure is carried out. Its main purpose is to show, in a simple manner, the design and **combined operation** of the control and synchronization algorithms for attaining the proper behaviour of the Grid Inverter when the 3-phase utility grid is disturbed by voltage unbalances, frequency variations and harmonic distortions, according to **power quality standards**.

In order to obtain a high efficiency of the system during perturbations, a **Proportional Resonant** controller with a **Harmonic Compensator** structure is designed for the control algorithm, whereas a Dual Second Order Generalized Integrator Frequency-Locked Loop (**DSOGI-FLL**) is used as the synchronization algorithm.

In order to validate both the control and the synchronization algorithms, some simulations using MATLAB/SIMULINK from The MathWorks, Inc. are shown firstly, and secondly, some real-time digital simulations are carried out.

Keywords: Photovoltaic Agent, DSOGI-FLL, Real-Time Digital Simulation, Proportional Resonant, Harmonic Compensator.

55
56
57
58
59
60
61
62
63
64
65
66
67
68
69
70
71
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
91
92
93
94
95
96
97
98
99
100
101
102
103
104

1. INTRODUCTION

In the framework of a Distributed Generation (DG) system, the connection of new, clean and infinite renewable agents to the utility grid, as an alternative to traditional ones [1] for the collaborative effort towards the mitigation of the greenhouse effect [2], must be properly controlled according to the expected operating conditions of the primary energy as well as the utility grid normative. For example, different values of irradiance, temperature variation, and islanding protections are taken into account when dealing with grid-connected Photovoltaic (PV) agents, as well as the control of the power factor (PF) of the inverter-grid connection [3]. In addition, the good power quality [4] according to international standards [3,5-7] must always be observed.

Regarding the harmonic regulations, a limit of 5% for the Total Harmonic Distortion (THD_I) is established for the grid currents [7-9] and a current regulator for the 3-phase inverter currents is mandatory, meanwhile the PF control is achieved with a synchronization algorithm which detects the phase angle of the 3-phase utility grid voltages with optimal dynamic response.

Previously, grid codes have allowed PV generators disconnection in the case of grid faults, but the PV power installed in Europe has been growing continuously because the cells prices are dropping, and nowadays the PV generators can destabilize the grid if they are disconnected under grid faults. So, the European countries are changing their grid codes, Germany and Italy already did it, to increase the capability of PV generators in helping to stabilize the grid. The new codes for medium and low voltage connection of PV power include fault-ride-through capability, voltage support by means of reactive current injection and frequency dependence [10].

PV modules produce dc electric quantities and a power conditioning system is needed to convert these dc quantities into ac ones [11-13], as well as to balance the power flow between the renewable agent and the utility grid. For 3-phase systems, it is mandatory to exert a decoupled control of the instantaneous active and reactive powers of the inverter-grid connection [14]. Voltage Source Inverters (VSI) are commonly used as the power conditioner units to interface renewable resources to the utility grid in a Distributed Generation (DG) framework [11,15], and are built with semiconductor devices operating in switch-mode.

Several publications have studied and analyzed the control and synchronization algorithms separately [13,16-18], and in this paper a **combined operation** of both algorithms is studied so as to validate its performance and to ensure the higher efficiency of the PV grid-connected system when the 3-phase utility grid is affected by voltage unbalances, variations of its nominal frequency and low order harmonics. For this, the Dual Second Order Generalized Integrator Frequency Locked Loop (**DSOGI-FLL**) proposed in [13,18] is used as the synchronization algorithm together with the **Proportional Resonant (PR)** controller and a **Harmonic Compensator (HC)** structure proposed in [19].

Section 2 of this paper is focused on the study of grid-connected PV systems: the main parts of the control and power subsystems will be described with more emphasis on the DSOGI-FLL and the PR controller. **Section 3** describes the case of study of a 10kW grid-connected PV renewable agent system that will be used to validate the control and the synchronization algorithms under several disturbance conditions. In **Section 4**, the global performance of the PV renewable agent will be studied with several simulations using MATLAB/SIMULINK tool [20]. In **Section 5**, real-time digital experiments will be performed using a DS1006 DSPACE platform with several I/O, ADC and DAC blocks in order to reinforce the validity of the previous study. Finally, some conclusions are shown in **Section 6**.

Nomenclature

V_p	output voltage of the PV Generator	V_r, V_s, V_t	3-phase utility grid voltages
i_p	output current of the PV Generator	S_u, S_v, S_w	state of the power-poles
P_{PV}	power in the PV generator	\mathbf{u}	inverter voltage space vector
V_{CC}	dc bus voltage	\mathbf{i}	inverter line current space vector
V_{CC}^*	dc bus voltage reference	\mathbf{u}_{AC}	utility grid voltage space vector
i_{CC}	current that will be injected into the inverter	L	line inductance
C_{link}	dc-link capacitor	R	resistance of the line inductance
i_{clink}	current through the dc-link capacitor	V_α, V_β	$\alpha\beta$ components of space vector \mathbf{u}
θ	phase of the 3-phase utility grid voltages	i_α, i_β	$\alpha\beta$ components of space vector \mathbf{i}
ω	fundamental angular frequency	$U_{ac\alpha}, U_{ac\beta}$	$\alpha\beta$ components of space vector \mathbf{u}_{AC}
i_u, i_v, i_w	3-phase inverter line currents	p	instantaneous active power
i_r, i_s, i_t	3-phase utility grid currents	q	instantaneous reactive power
DQ	in-quadrature signals	i_d^*, i_q^*	$d-q$ reference components of vector \mathbf{i}^*
ω'	estimated angular frequency	q^*	instantaneous reactive power reference
ω	angular frequency	PI_{VCC}	voltage PI regulator
k	gain of the SOGI block	F_{sw}	switching frequency
K_I and K_P	constants of the voltage PI regulator	t_s	settling time
		τ	time constant of the first order system
		$t_{S(FLL)}$	settling time of the FLL
		Γ	gain to set $t_{S(FLL)}$

105
106
107
108
109
110
111
112
113
114
115
116
117
118
119
120

2. GRID-CONNECTED PV SYSTEM

For PV grid-connected systems, it is necessary to control the power flow between the primary renewable energy source and the utility grid [19], as well as the power factor of the inverter-grid connection with high power quality [21]. The power conditioner, working in inverter-mode (3-phase Voltage Source Inverter (VSI)), must guarantee the maximum efficiency by injecting the maximum available power at the PV module into the utility grid, as well as by controlling the power factor of the inverter-grid connection; the latter makes use of the instantaneous reactive power theory [22] for 3-phase systems which allows the control of the instantaneous active and reactive powers in decoupled axes [14]. The global 3-phase PV grid-connected system can be divided into two subsystems [21], the power and the control subsystems, whose block diagram is depicted in Fig. 1.

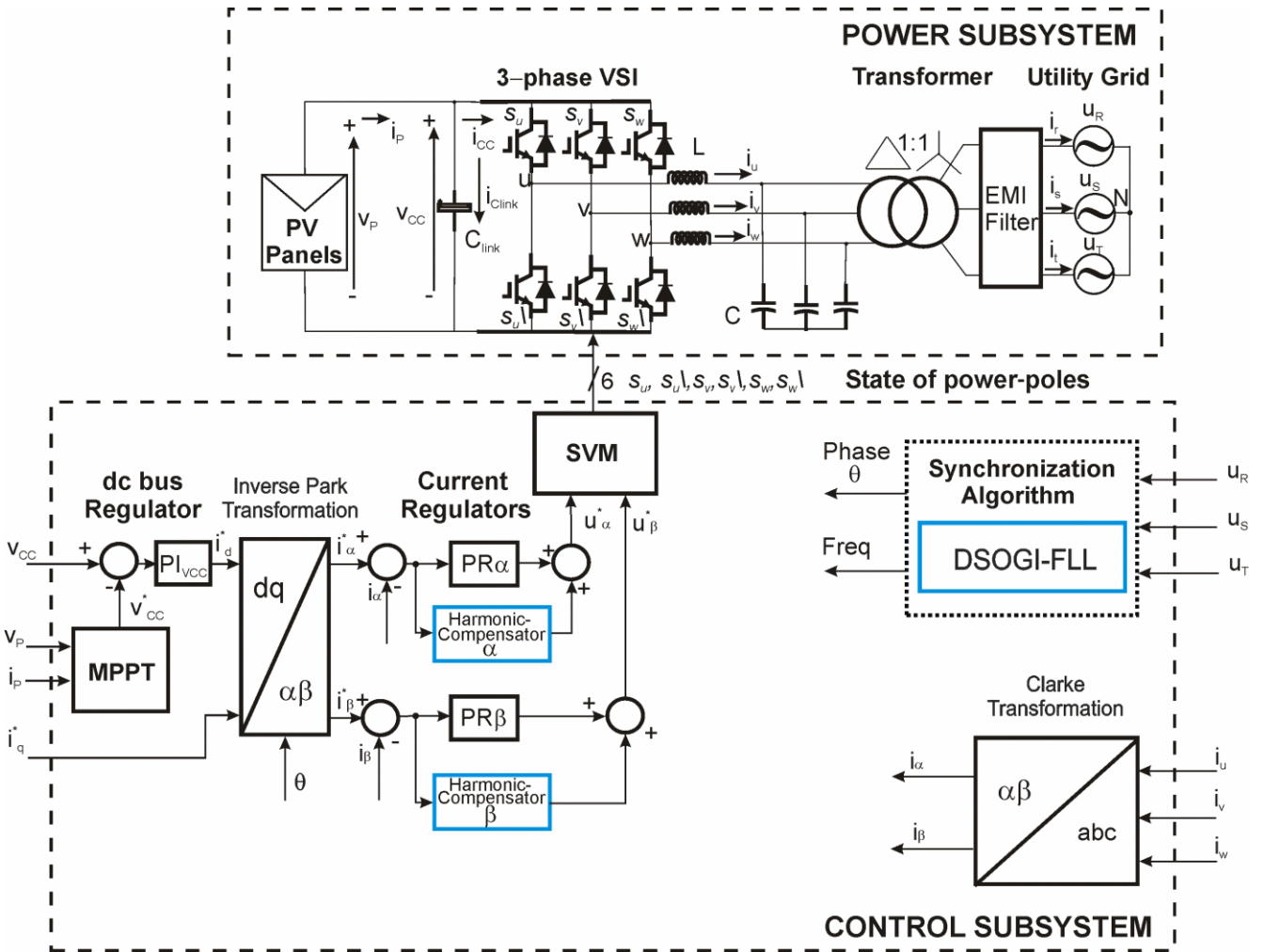


Fig. 1. Block diagram of the Power and Control Subsystems for a 3-phase grid-connected system.

121
122
123
124
125
126
127
128
129
130
131
132
133
134
135
136
137
138
139
140

2.1 Power Subsystem

The power subsystem is formed by the PV modules, the inverter, the LCL and the EMI filters. The LCL filter and the 3-phase utility grid are configured as a three wire system with two degrees of freedom.

The PV modules supply the incoming power, according the available solar irradiance [23] and the cell temperature, into the utility grid, and its size (arrange of parallel-series PV cells) will depend of the required power of the PV system [24].

The mission of the inverter, mainly built with semi-conductor electronic devices [12] (IGBTs and diodes), is to convert the generated dc voltage into suitable ac currents to be fed into the 3-phase low-voltage utility grid [11]. Pulse-width modulation (PMW) and space vector modulation (SVM) techniques are used to control the gate signals of power switches according to the averaged voltage and current references.

According to Fig. 1, the dc side of the inverter can be described as follows:

$$\begin{aligned}
i_p &= i_{clink} + i_{CC} \\
i_{CC} &= s_u \cdot i_u + s_v \cdot i_v + s_w \cdot i_w \\
i_{clink} &= C_{link} \frac{dv_{CC}}{dt} \\
P_{PV} &= i_p v_p
\end{aligned} \tag{1}$$

141

142

143 where v_p , i_p are the voltage and the output current of the PV generator, respectively, P_{PV} is the available
144 power for a specific irradiance and cell temperature, v_{CC} is the dc bus voltage, i_{clink} is the current
145 through the link capacitor C_{link} , and i_{CC} is the current delivered to the 3-phase VSI (which is a function
146 of the line currents i_u, i_v, i_w and the states of the power-poles s_u, s_v, s_w (1: 'on', 0: 'off', s_i -upper pole,
147 s_i -lower pole in the 3-phase VSI).
148

149 Neglecting the effect of the inductance of the isolation transformer and the EMI filter at the fundamental
150 and low frequencies, the voltages at the filter capacitors are approximately the same as in the three phase
151 utility grid voltages. Defining a generic 3-phase system $[x_1 \ x_2 \ x_3]$, where $x_1 \ x_2 \ x_3$ are the instantaneous 3-
152 phase variables, its corresponding generic space vector \mathbf{x} can be stated as $\mathbf{x} = [x_1 \ x_2 \ x_3]^t$, and the dynamic
153 in the ac side of the inverter can be expressed in a vector way as follows:
154

$$\mathbf{u} - \mathbf{u}_{AC} = R\mathbf{i} + L \frac{d\mathbf{i}}{dt} = \mathbf{u}_R + \mathbf{u}_L \tag{2}$$

155

156

157 where \mathbf{u} , \mathbf{i} , \mathbf{u}_{AC} are the inverter voltage, the inverter line current and the utility grid voltage space vectors
158 of its corresponding instantaneous 3-phase variables, respectively; L is the line inductance and its copper
159 resistance R .
160

161 Neglecting the losses of the distribution transformer and the EMI filter and expressing the last vector
162 equation with its $\alpha\beta$ components using the Clarke Transformation [25] (see Appendix A for the
163 transformation matrices), the power flow between the 3-phase VSI and the utility grid can be deduced
164 by using the instantaneous reactive power theory [26]. So, the instantaneous active power (p) and the
165 instantaneous reactive power (q) can be expressed as follows [22,27]:
166

$$p = v_{ac_\alpha} i_\alpha + v_{ac_\beta} i_\beta \tag{3}$$

167

$$q = v_{ac_\beta} i_\alpha - v_{ac_\alpha} i_\beta \tag{4}$$

168

169

170 where v_{ac_α} , v_{ac_β} , i_α and i_β are the $\alpha\beta$ components of 3-phase voltages and currents, respectively.
171

171

172 In addition of guaranteeing a constant power delivery to the 3-phase utility grid, PV renewable agents
173 must fulfill the power quality regulations. The maximum total harmonic distortion (THD) for the 3-
174 phase currents must be around 5% [9] according to several normative [7,8], whereas the normative for
175 the low order harmonic distortions is indeed more restrictive. A resume of different standards about
176 power quality for photovoltaic systems can be found in [28]. Ripples are created in the output currents
177 of the inverter due to the high frequency commutation of the IGBTs, meanwhile the low order harmonics
178 are produced by non-linear loads. The best solution for correcting the high frequency ripples is by using
179 an LC or an LCL filter [29] in the ac side of the inverter, meanwhile the amplitude of the low order

180 harmonic must also be attenuated to guarantee a good power quality injection to the utility grid, although
181 this a difficult task except if a harmonic-compensator (HC) algorithm is used.

182

183 In a grid-connected PV renewable agent, it is necessary to take into account the harmonic pollution due
184 to the Electromagnetic Interference (EMI). These EMIs are caused by the semiconductor electronic
185 devices (IGBTs and diodes) [30] and an EMI filter is needed to reduce it. There are too many
186 methodologies to design the appropriate EMI filter, some of them are based on trial and error [30,31],
187 and some novel methodologies are cited in several publications, including [31,32].

188

189

190 2.2 Control Subsystem

191

192 The control subsystem is formed by the Maximum Power Point Tracking (MPPT) algorithm, the
193 synchronization algorithm, and the outer voltage PI and the inner current PR regulators.

194

195 The MPPT [33] is an essential algorithm-module of a PV system for extracting the maximum available
196 power of the PV modules [34] in order to increase the efficiency of the system [14]. The study of the
197 MPPT algorithm is out of the scope of this paper, but interested readers may find some additional
198 information in the scientific literature, such as [34-39].

199

200

201 2.2.1 Synchronization algorithm

202 The synchronization algorithm for attaining a controllable power factor in the connection must detect
203 the phase of the 3-phase utility grid voltages with optimal dynamic response. However, the measured
204 signals can be contaminated with harmonics, voltage unbalances, and frequency variations [40]; also,
205 the used sensors can introduce second order harmonics due to accuracy errors.

206

207 Classical dqPLL method (in the synchronous reference frame) for synchronization is very sensible to
208 grid voltage unbalances, which also produce second order harmonics in the dc bus voltage [41]. To
209 overcome this fact, a Positive Sequence Detector (PSD) block, based on the symmetrical component
210 method or Fortescue theorem [42], is added to extract the positive sequence of the 3-phase utility grid
211 voltages, yielding the PSD+dqPLL synchronization algorithm [43]. However, due to the discrete filter
212 named S90 [43] used in the PSD block this algorithm could be sensitive to the variation of the nominal
213 frequency of the utility grid [44] and may lead to the power factor degradation of the inverter-grid
214 connection.

215

216 A solution for the frequency and phase detections when voltage unbalances and frequency variations
217 occur in the 3-phase utility grid voltages is described in [13], where a Dual Second Order Generalized
218 Integrator FLL (DSOGI-FLL) is proposed for the $\alpha\beta$ voltage components ($V_{\alpha\beta}$) in the stationary
219 reference frame. In this, the in-quadrature signals (90° shifted) for $V_{\alpha\beta}$ are computed by two Second
220 Order Generalized Integrator [45] with a Quadrature Signal Generation (SOGI-QSG) [18], one of them
221 shown in Fig. 2.

222

223 The block diagram of the SOGI, which behaves as an integrator with infinite gain, is depicted in **blue**
224 and its transfer functions is described by Eq. (5).

225

$$226 \text{SOGI}(s) = \frac{v'}{k\varepsilon_v}(s) = \frac{\omega' s}{s^2 + \omega'^2} \quad (5)$$

227

228 whereas the transfer functions of the in-quadrature signals DQ are described by Eq. (6) and (7).

229

$$230 \quad D(s) = \frac{v'}{v}(s) = \frac{k\omega' s}{s^2 + k\omega' s + \omega'^2} \quad (6)$$

231

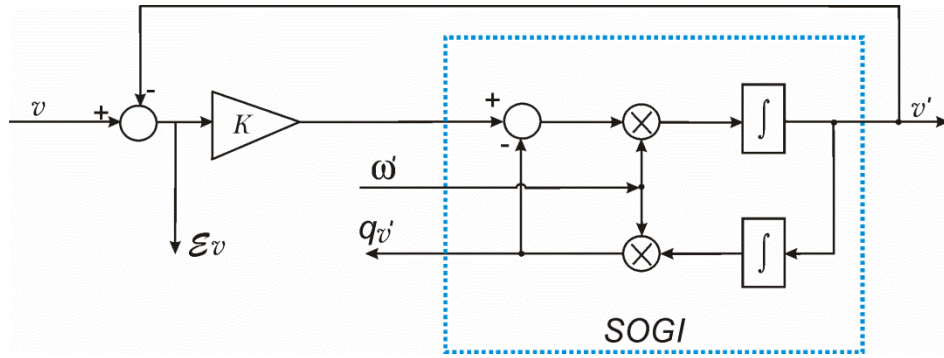
$$232 \quad Q(s) = \frac{qv'}{v}(s) = \frac{k\omega'^2}{s^2 + k\omega' s + \omega'^2} \quad (7)$$

233

234 ω' is the centre angular frequency of the adaptive filter, and k is the gain of the SOGI block. The transfer
 235 functions described by Eq. (6) and (7) suggest a band-pass and a low-pass filter behaviour, respectively,
 236 and Eq. (7) implies a constant lag of 90° between the qv' and v which will not be a function of the
 237 variation of ω' and k [18], yielding also to an insensitive system for frequency variations at the input
 238 signal v when $\omega = \omega'$ (ω is the angular frequency of v).

239

240



241

242

243

244

Fig. 2. Block diagram of a SOGI-QSG.

245 Equations (6) and (7) are second order transfer functions, and its dynamic response will depend on the
 246 localization of the poles in the complex plane. In addition, the band-pass and low-pass filter behaviour
 247 described above suggest the harmonic rejection capability of these filters. So, a trade-off between the
 248 proper bandwidth for harmonic rejection, and the proper settling time with the corresponding overshoot
 249 for the dynamic response, must be imposed for the calculation of k for a specific ω' .

250

251 Figure 3a shows the Bode plot of the transfer functions of the SOGI-QSG described by Eq. (6) and (7):
 252 the upper zone of Fig. 3a shows the magnitude of the Bode plot, meanwhile the bottom zone shows the
 253 phase angle, both for several values of k . The trace of the phase depicts a perfect 90° shifting between
 254 the in-quadrature signals for all k . In addition, Fig. 3b depicts the time evolution of the in-quadrature
 255 signals and the corresponding settling times for all k .

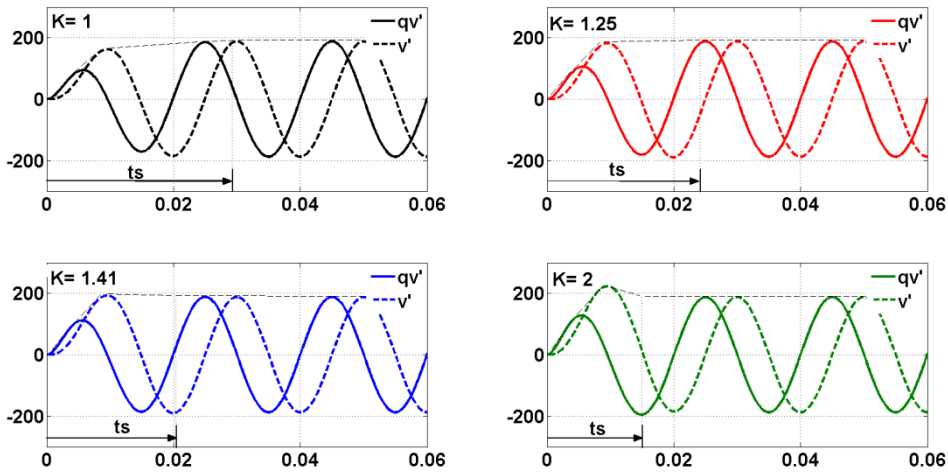
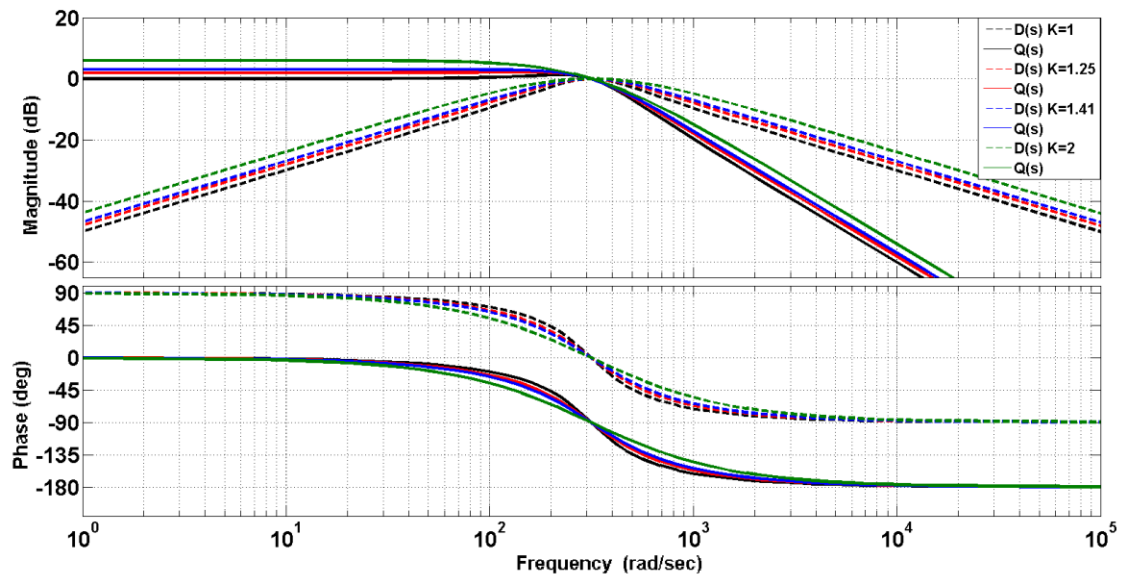


Fig. 3. a) Bode plot of a SOGI-QSG using the transfer functions of Equations (6) and (7) for several values of k .
 b) Time evolution of the in-quadature signals of the SOGI-QSG for several values of k .

Table 1 resumes the influence of gain k in the SOGI-QSG behavior according to the settling time, overshoot, damping factor, and harmonic rejection of $Q(s)$: when k increases, the settling time and the harmonic rejection decreases, but the overshoot increases instead. It must be observed that when $k = 1.414$, a good trade-off between the harmonic rejection (for the 5th and 7th harmonics) and the dynamic response is achieved, corresponding to a damping factor $\zeta_{SOGI-QSG} = 0.707$ for second order systems [46].

Table 1. SOGI-QSG behavior according gain k .

k	$\zeta_{\text{SOGI-QSG}}$	Overshoot (%)	Settling time (ms)	Harmonic Rejection of Q(s) (dB)	
				5 th	7 th
1	1	0	29.3	-27.72	-33.7
1.25	0.8	1.5	23.4	-25.89	-31.81
1.414	0.707	4.3	20.7	-24.9	-30.78
2	0.5	16.3	14.6	-22.23	-27.94

279

280

281

282

283

284

285

286

287

288

It must be pointed out that the SOGI-QSG synchronization algorithm is tuned to the centre angular frequency ω' , which is an input signal to this block (see Fig. 2), and can behave as an adaptive filter scheme if an external circuit or algorithm is able to measure or detect this frequency. The Frequency Locked Loop (FLL) structure, shown in Fig. 4 [18], can be used to measure the angular frequency ω of the input signal v (in this case, ω' is the output or estimated angular frequency of the input signal v) without using trigonometric functions [18], and making easier its implementation in conventional microcontrollers. The nominal angular frequency ω_c is feed-forward to this block in order to improve the dynamic response of the algorithm.

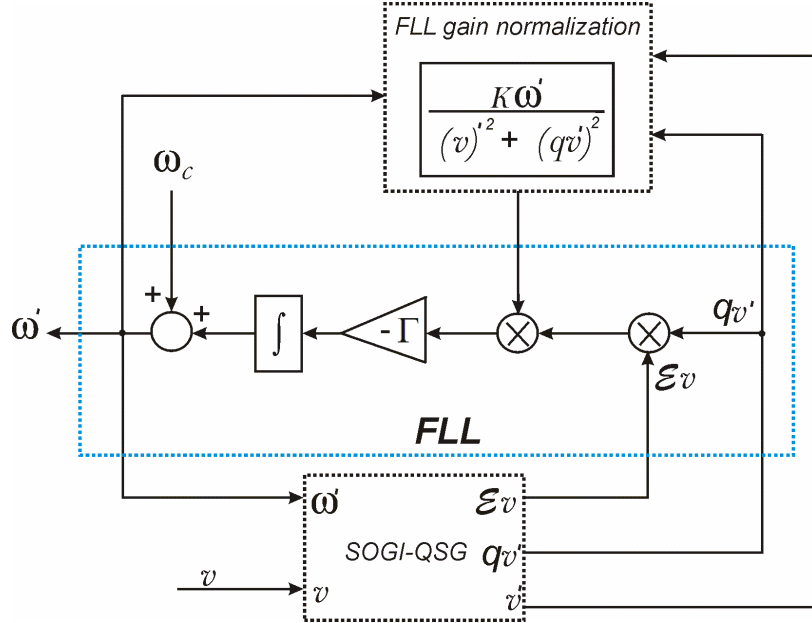


Fig. 4. Block diagram of a Frequency-Locked Loop (FLL) with gain normalization.

289

290

291

292

293

294

295

296

297

298

299

300

301

A FLL gain normalization is exerted so as to make this independent of the gain k of the SOGI-QSG block and of the amplitude of the utility grid voltage. In this case, the FLL algorithm can be approximated by a first order system with one integrator for steady state operation and linearized around the nominal output variables of the SOGI-QSG block. So, the settling time t_s can be set approximately by 5τ ($\tau = \frac{1}{\Gamma}$ is the time constant of the first order system):

$$t_{s(FLL)} \approx \frac{5}{\Gamma} \quad (8)$$

Finally, the measurement of ω' implies the feedback of the qv' signal from the SOGI-QSG block. This signal will have a small amount of harmonic contamination due to the attenuation imposed by the low-pass filter nature of the transfer function $Q(s)$ described by Eq. (7).

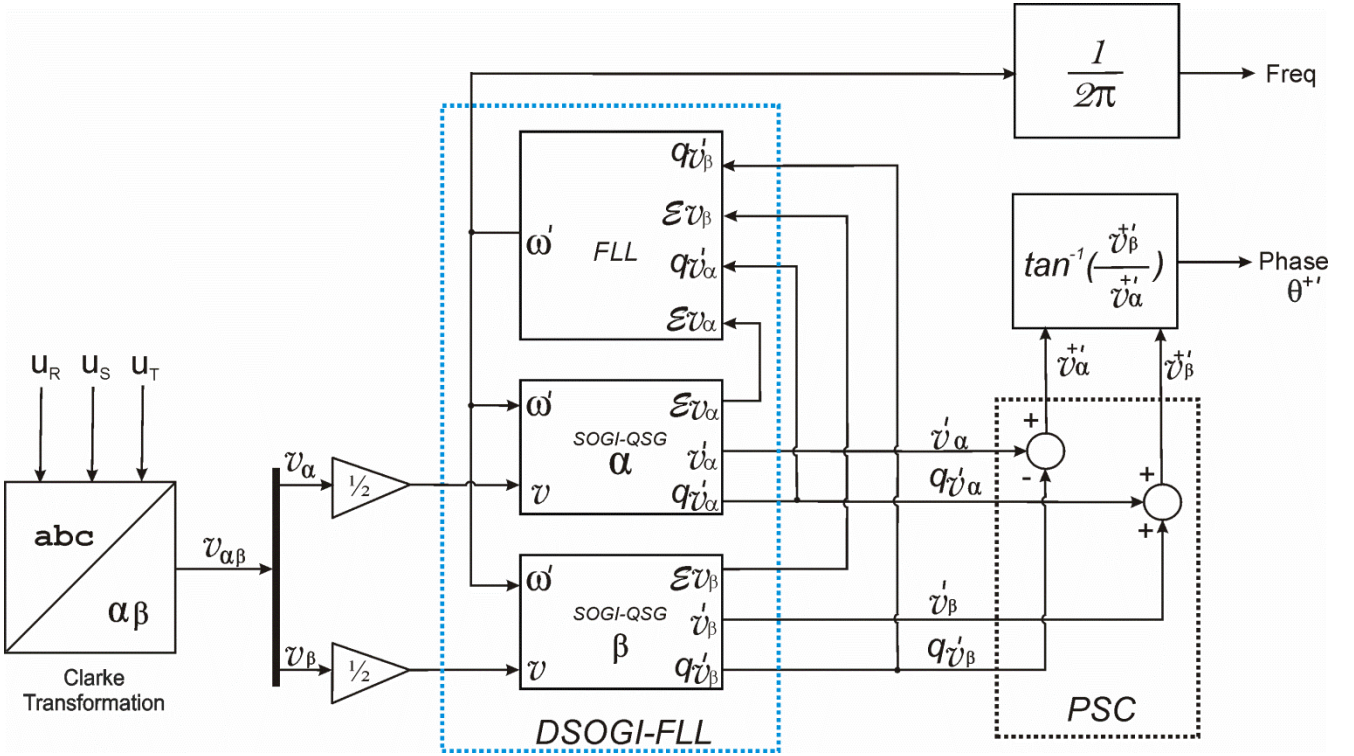
302

303 The preliminary analysis can be extended to 3-phase systems, and Fig. 5 shows the DSOGI-FLL
 304 structure proposed in [18]. Clarke transformation [25] is applied to the input 3-phase voltages in order
 305 to obtain its $\alpha\beta$ voltage components ($v_{\alpha\beta}$), and two SOGI-QSG blocks are used to obtain its in-
 306 quadrature signals. Knowing that the instantaneous positive sequence component ($v_{\alpha\beta}^+$) of a voltage
 307 vector described by $v_{\alpha\beta}$ is given by [47]:

308

$$309 \quad v_{\alpha\beta}^+ = \frac{1}{2} \begin{pmatrix} 1 & -q \\ q & 1 \end{pmatrix} v_{\alpha\beta} \quad (9)$$

310 where $q = e^{-j\frac{\pi}{2}}$ is a phase-shift operator to obtain the in-quadrature version of an original wave form, a
 311 Positive Sequence Calculator (PSC) must be designed and applied to the in-quadrature output signals so
 312 as to compute the positive sequence of the input 3-phase unbalanced voltages $v_{\alpha\beta}^{+'}$.
 313



314

315

316

317 Finally, the phase angle for the positive sequence of the 3-phase utility grid voltages can be computed
 318 as follows:

$$319 \quad \theta^{+'} = \tan^{-1} \left(\frac{v_{\beta}^{+'}}{v_{\alpha}^{+'}} \right) \quad (10)$$

320

321 In order to evaluate the performance of the DSOGI-FLL structure, some simulations depict the time
 322 evolution of the detected frequency in Fig. 6. A step of 50-60Hz is exerted in the nominal frequency of
 323 the 3-phase utility grid voltages and several gains (Γ) are imposed to study the dynamic response. Using
 324 a relatively low Γ (50 and 70) a slow settling time of the FLL is attained (trace in **red** and **blue**). On the
 325 contrary, for $\Gamma = 100$ (trace in **black**), a suitable settling time around two cycles and a half is achieved.
 326

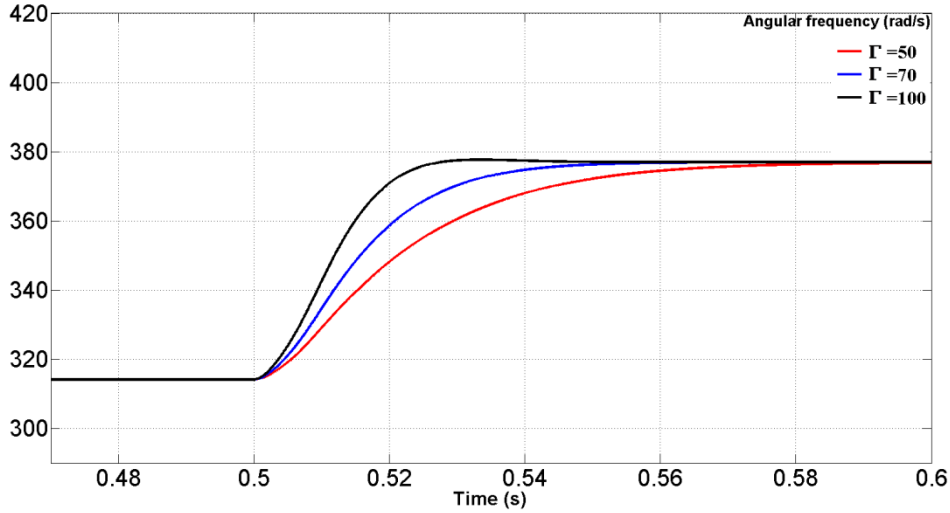


Fig. 6. Time response of the DSOGI-FLL for several Γ .

327
328
329
330
331
332
333
334

Table 2 show the relation between the gain Γ and the corresponding settling time $t_{S(FLL)}$ when a step of frequency of 50Hz to 60Hz is exerted on the utility grid frequency.

Table 2. Relation between the gain Γ and $t_{S(FLL)}$ in the FLL block.

Γ	$t_{S(FLL)}$ (ms)
50	100
70	70
100	50

335
336
337
338

2.2.2 Cascade control

339
340
341
342
343
344
345
346
347
348
349
350

The control strategy uses a cascade control: the outer voltage and the inner current regulators.

The outer loop regulator compares the dc bus voltage in the link capacitor with the reference which comes from the MPPT algorithm block, keeping a constant dc voltage and assuring the power flow balance between the PV system and the utility grid. This control loop has been performed using a PI regulator whose output is the reference d component of the inverter 3-phase currents (i_d^*) in the synchronous reference frame (dq) (see Appendix A), meanwhile the reference q component (i_q^*) is set in an open loop scheme for the power factor control of the inverter-grid connection. The PI regulator constants, K_I and K_P , are calculated under the recommendations described in [21,48] which suggests a phase margin $PM_V=63.5^\circ$ for assuring the best trade-off between the relative stability, the overshoot and the settling time of the system.

351
352
353
354
355

Classical PI regulators produce zero steady-state error for dc signals due to its integral part. In a similar way, when sinusoidal signals are to be regulated, Proportional Resonant (PR) regulators must be used instead for zero steady-state error behaviour, and the resonant part can be viewed as a generalized integrator (GI) [45]. In addition, for 3-phase unbalanced systems (three wire configuration), two PR controllers can perfectly track both the positive- and the negative- sequence of the 3-phase sinusoidal

356 references, and reject the 3-phase sinusoidal disturbances according to the internal model principle (see
 357 appendix C of [13] for a detailed explanation of this principle) because its transfer functions have a pair
 358 of conjugate poles in the complex plane at the fundamental resonance angular frequency (ω_0 and $-\omega_0$).

359
 360 Applying the inverse Park Transformation (see Appendix A) to i_d^* and i_q^* computes the sinusoidal
 361 reference currents i_α^* , i_β^* . Clarke transformation (abc- \rightarrow $\alpha\beta$) are applied to the 3-phase line currents,
 362 computing its sinusoidal $\alpha\beta$ components which are compared with i_α^* , i_β^* , and the errors are fed to the
 363 two PR controllers in the Stationary Reference Frame ($\alpha\beta$), whose outputs are the average reference
 364 sinusoidal inverter voltage ($u_{\alpha\beta}^*$). This inner current loop is closed with the SVM block which decides
 365 the state of the power-poles of the 3-phase inverter, allowing the synchronization of the inverter line
 366 currents with the utility grid voltages with zero error at steady-state, and also rejecting the disturbance
 367 produced by the 3-phase utility grid voltages which avoids the necessity of applying a feedforward open-
 368 loop control scheme.

369
 370 The ideal and the non-ideal PR controllers are described in [16], in which the former has an infinite gain
 371 within a narrow bandwidth around the fundamental resonance angular frequency ω_0 , which may lead to
 372 stability and digital implementation problems, meanwhile the latter adds the cut-off frequency ω_c , which
 373 leads to a high but finite gain, solving the stability problems and allowing an easier digital
 374 implementation.

375
 376 The non-ideal PR controller will be used in this paper, whose transfer function is described by Eq. (11).
 377 The dynamic of the system in terms of bandwidth, phase and gains margin will depend of the
 378 proportional gain K_p which is adjusted in a similar way as it is tuned in a PI controller; the steady-state
 379 error will depend of the integral gain K_i whose value will be selected comparatively high, but holding
 380 the limits for stability, and ω_c must be set empirically to 5-15 rad/s with good results [16].

381
 382

$$G_{PR}(s) = K_p + \frac{2K_i\omega_c s}{s^2 + 2\omega_c s + \omega_0^2} \quad (11)$$

383
 384 Cascading multiple generalized integrators (GI) tuned to resonate at specific low order harmonic
 385 angular frequencies $h\omega_0$ with the PR controller described above [41], enables the Harmonic Compensator
 386 (HC) structure for the rejection of the low-frequency harmonic perturbances of the 3-phase utility grid
 387 voltages according to the internal model principle mentioned above. Then, a good power quality can be
 388 delivered into the utility grid with minimal computational burden.

389
 390 The transfer function of the HC is described by Eq. (12), where h is the harmonic to be compensated,
 391 and K_{ih} , ω_{ch} can be adjusted in a similar manner as in the non-ideal PR controller.

392
 393

$$G_h(s) = \sum_{h=5,7} \frac{2K_{ih}\omega_{ch}s}{s^2 + 2\omega_{ch}s + (h\omega_0)^2} \quad (12)$$

394 Finally, the generic PR controller and the HC structure are described in Fig. 7 for the 5th and 7th
 395 harmonics, but it can be easily extended to any other low-frequency harmonic perturbation.

396
 397

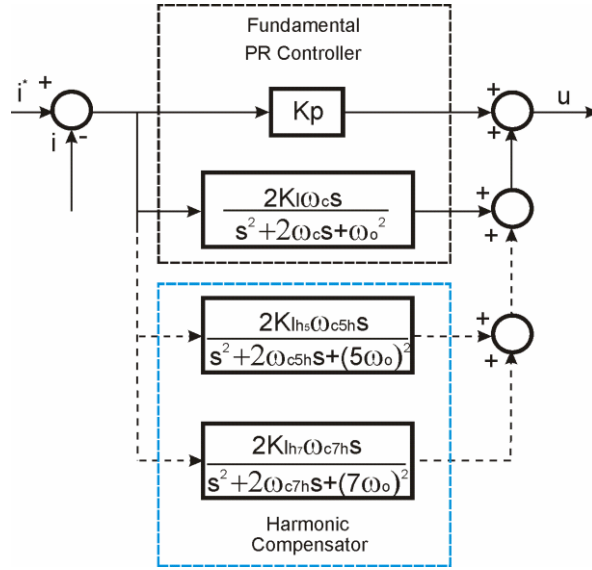


Fig. 7. PR + Harmonic Compensator structure.

3. CASE OF STUDY

A PV grid-connected system of 10kW of nominal power at standard conditions (1000 W/m² and 25 °C) [49] will be studied. The chosen parameters are based on a 3-phase inverter built with the 6-pack-integrated intelligent Power System SKIIP 513GD172-3DUL from SEMIKRON, which includes the IGBTs semiconductors for the 3-phase power-poles, and configured to work at 12.208kHz of PWM switching frequency (F_{PWM}=12.208kHz (244th harmonic)). The values of the components of the LCL filter, the distribution transformer, and the utility grid are resumed in Table 3 at left, meanwhile at right are the cutoff frequencies and damping factors of the LCL filter.

Table 3. Power Subsystem characteristics.

<ul style="list-style-type: none"> • R=0.0465Ω • L=1.1mH • C=4μF (Y-connection) • Transformer: <ul style="list-style-type: none"> • R_t= 0.247 Ω • L_t= 640 μH, neglecting the magnetizing effect • 3-phase utility grid voltage: 132.8V rms (phase-to-neutral) 	$f_c = \frac{1}{2\pi} \frac{R}{L} = 6.72Hz$	First order RL
	$f_o = \frac{1}{2\pi\sqrt{LC}} = 2400Hz$ <p>(harm = 48)</p> $\zeta = \frac{\omega_o}{2} RC = 0.0014$	Second order LC
	$f_{ot} = \frac{1}{2\pi\sqrt{L_t C}} = 3145.6Hz$ <p>(harm = 63)</p> $\zeta_t = \frac{\omega_o}{2} R_t C = 0.0098$	Second order LtC

The values of the parameters for the control subsystem and the DSOGI-FLL synchronization algorithm are resumed in Table 4:

417
418
419

Table 4. Control subsystem and the synchronization algorithm characteristics

<ul style="list-style-type: none">• $K_{INV}=400V$ ($V_{CC}=600V$)• $C_{link}=2300\mu F$ ($\Delta V_{CCmax}=60V$)• $f_{cl}=1220.8$ (open loop cross-over frequency for the inner current loop)• $PM_I=63.5^\circ$• $K_p=0.0211$• $K_I=10$• $K_{ih5}=K_{ih7}=10$• $\omega_0=314.16rad/s$• $\omega_c=10rad/s$• $\omega_{c5h}=\omega_{c7h}=10rad/s$• $f_{cV}=12.2Hz$ (open loop cross-over frequency for the outer dc bus voltage loop)• $PM_V=PM_I$ (phase margin for the outer and inner open loop transfer functions)• $\zeta_c=\frac{\sqrt{2}}{2}$ (damping factor for the closed loop outer voltage loop)• $\Delta I=16.7A$ ($S=10KVA$) (equivalent step of irradiance for the rated nominal power) <p>DSOGI-FLL parameters</p> <ul style="list-style-type: none">• $k=1.41$• $\Gamma=100$

420
421
422
423
424
425
426
427
428
429
430

4. SIMULATIONS

In order to evaluate the control and the synchronization algorithms some simulations have been performed using the MATLAB/SIMULINK [20] model of the grid-connected PV system shown in Fig. 8. The Power Subsystem is update at $T_{S_Plant}=2.56\mu s$, meanwhile the control subsystem is updated at $T_{S_Controller}=20.478\mu s$, and is modelled inside the Triggered Subsystem (named Vector Controller) which is driven by a signal emulating an Interrupt Request in the microcontroller. Analog PWM is used with a switching frequency $F_{PWM}=12.208kHz$.

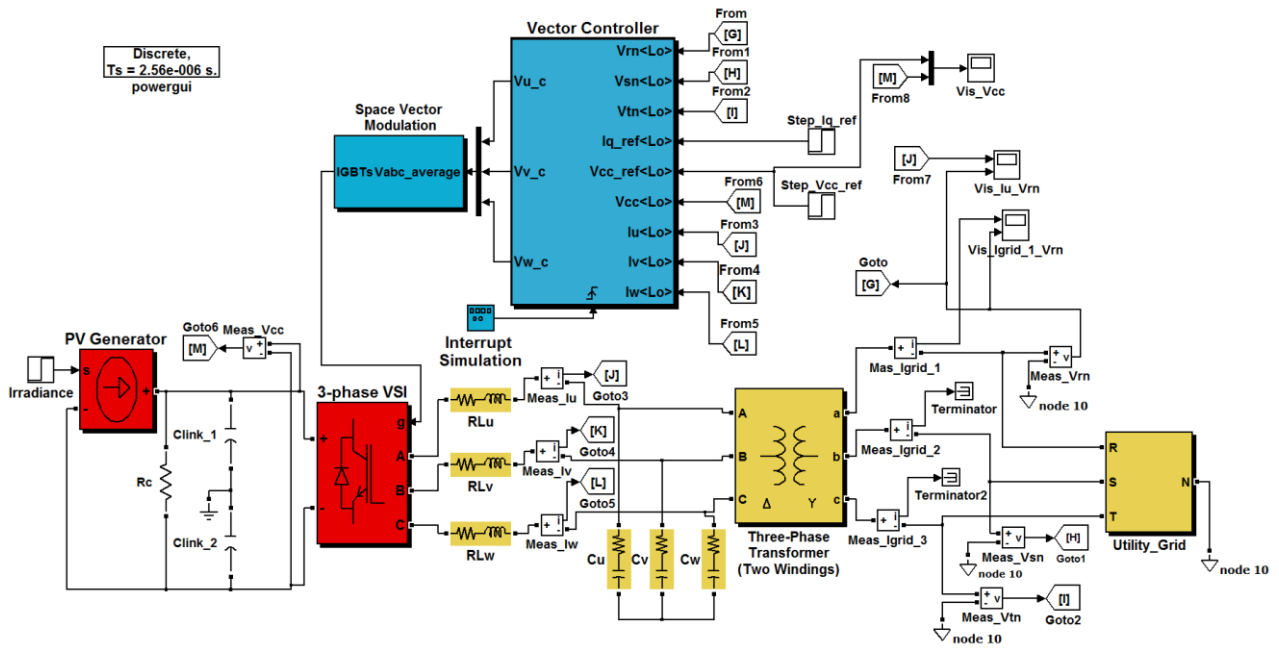


Fig. 8. SIMULINK model of the PV grid-connected system.

431
432
433
434
435
436
437
438
439
440
441
442
443
444

The SIMULINK implementation of the DSOGI-FLL synchronization algorithm together with the cascade control used in this paper are shown in Fig. 9, both included in the Vector Controller block. The cascade control is formed by the outer loop regulator that compares the dc bus voltage in the link capacitor with the reference which comes from the MPPT algorithm block¹. This control loop has been performed using a **PI regulator**. The inner control loop uses two **PR controllers** to regulate the $\alpha\beta$ components of the line currents. A **harmonic compensator (HC) structure** is used in both PR controllers.

The detailed SIMULINK implementation of the **DSOGI-FLL** synchronization algorithm and the **FLL structure** for 3-phase systems are shown in Fig. 10a and Fig. 10b, respectively. The output phase of this module is computed by an **S-Function** block as Eq. (10) suggests.

¹ The MPPT algorithm block is commonly a part of the PV Generator, but it is not modelled because it is out of the scope of this paper as said before. Instead, the reference to track the maximum power point is generated with the *Step_Vcc_ref* block so as to be able to test the proper behavior of the cascade control exerted.

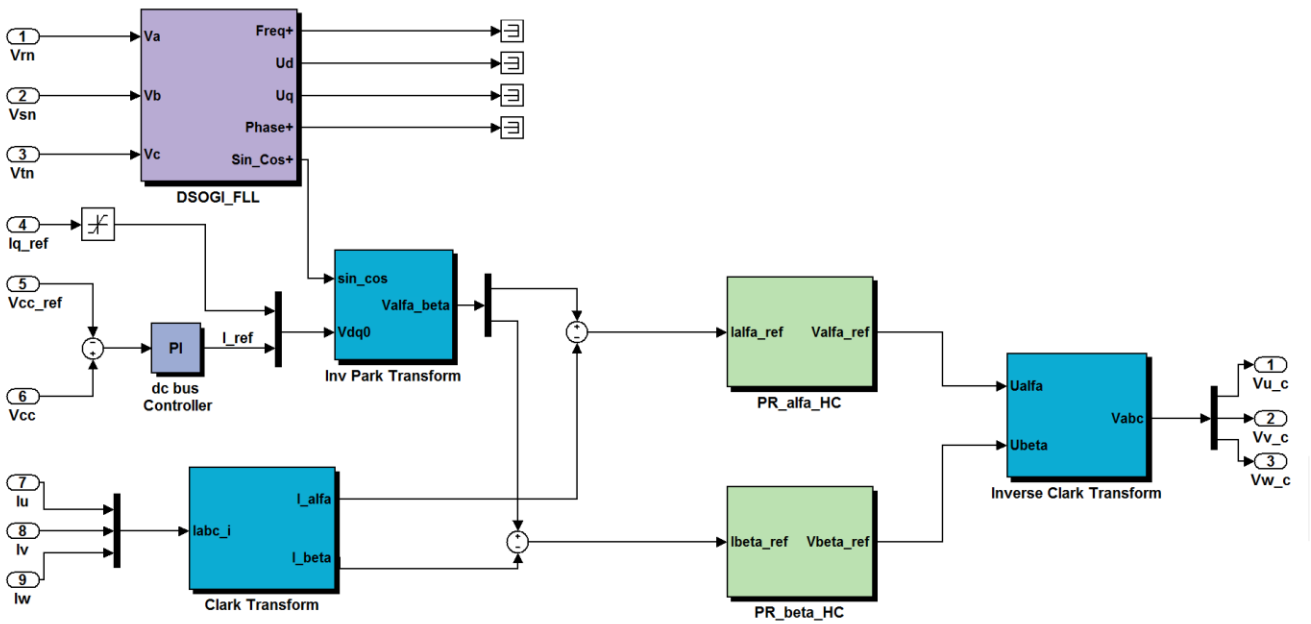
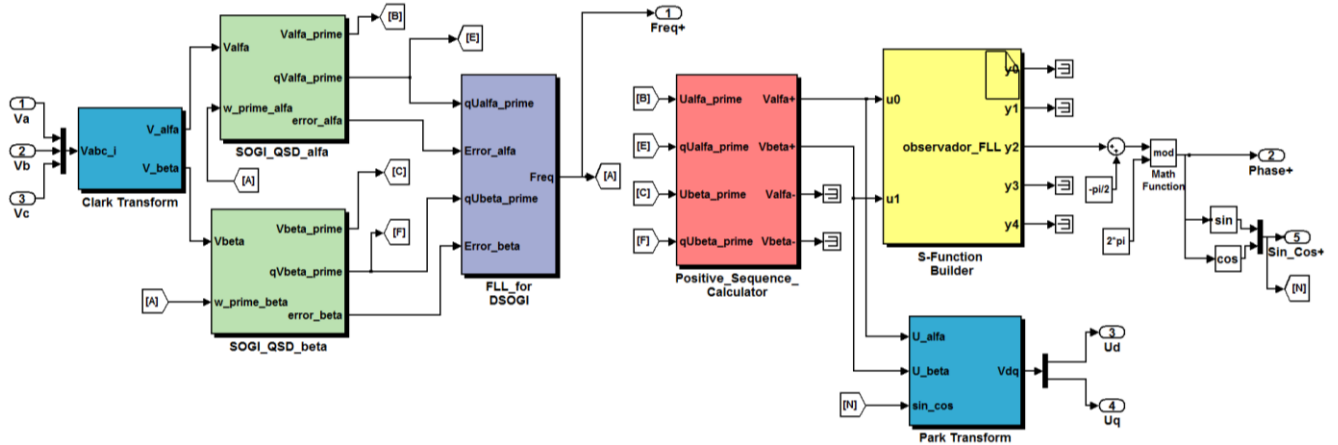


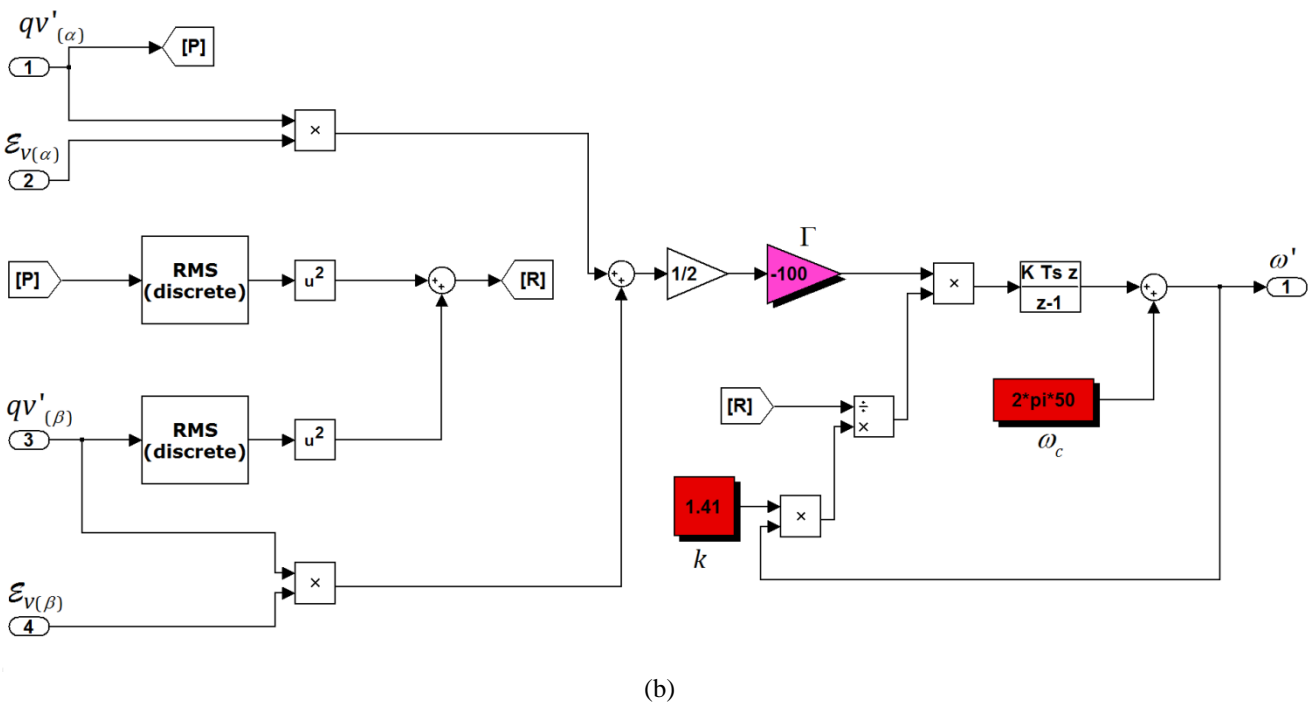
Fig. 9. SIMULINK model of the cascade control.



(a)

445
446
447
448

449
450



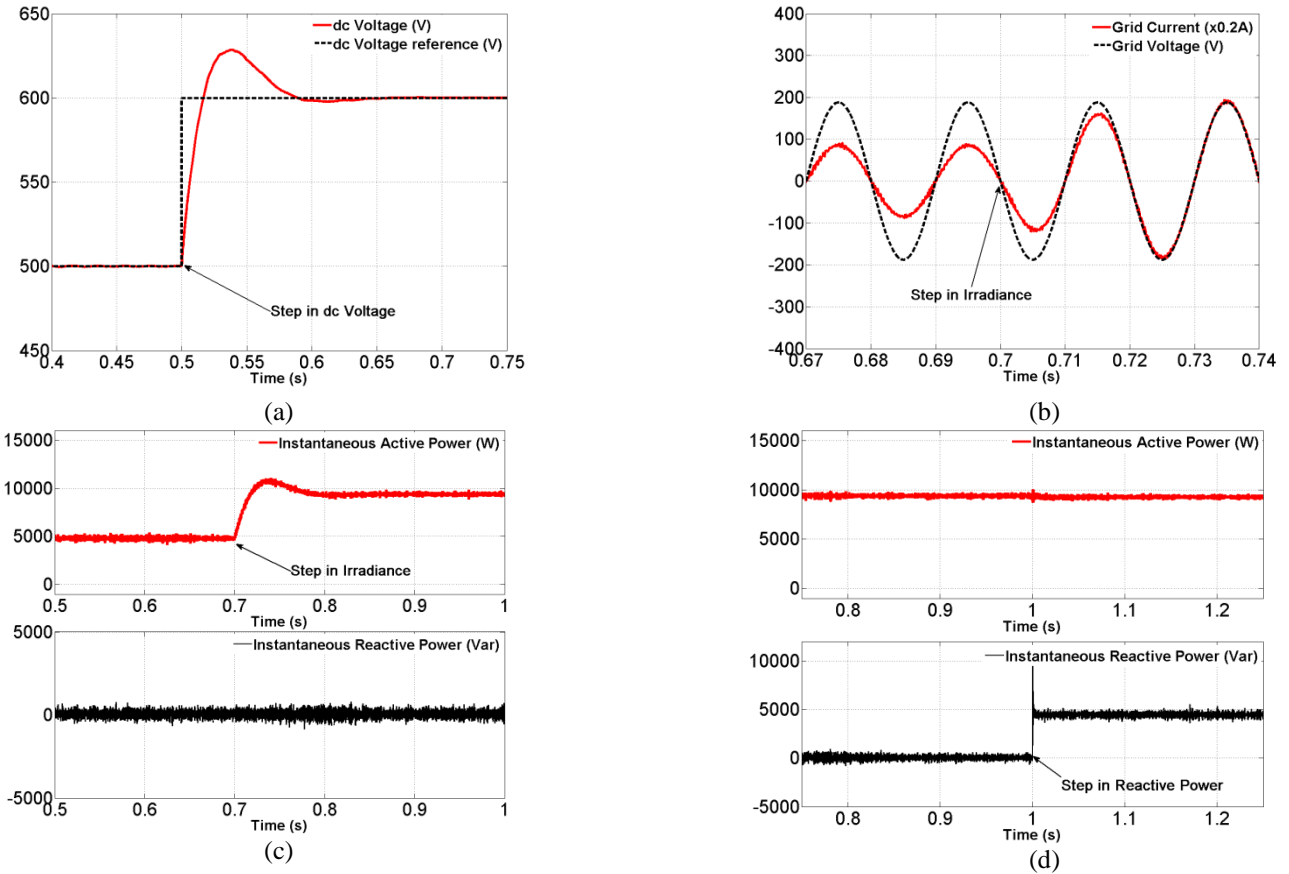
451
 452 (b)
 453 **Fig. 10.** a) SIMULINK model of the DSOGI-FLL synchronization algorithm.
 454 b) SIMULINK model of the FLL structure with gain normalization.
 455

456 In order to simulate the time response of the 3-phase grid-connected VSI under different input conditions
 457 several time simulations have been carried out.
 458

459 The time response to a change in the MPPT of the I-V curve of the PV generator is simulated with the
 460 corresponding step from 500V to nominal value (600V) in the dc bus voltage reference V^*_{CC} exerted at
 461 0.5s, and for a constant incoming output current from the PV generator for a specific irradiance; see Fig.
 462 11a: the proper time response of the dc bus voltage regulator is shown in **red**. An overshoot less than
 463 5% of the nominal dc bus voltage is attained, allowing the proper overvoltage protection of the link
 464 capacitor together with a fast settling time around 200ms.
 465

466 Figures 11b and 11c show the time response of the voltage and grid current at phase 1, and the
 467 instantaneous active and reactive powers delivered to the 3-phase utility grid, respectively, for a variation
 468 in the incoming irradiance. For this, a step in the output current of the PV generator I_g is exerted at 0.7s
 469 from a 50% up to nominal conditions with constant dc bus voltage reference, producing the increase of
 470 the instantaneous active power from 5kW to 10kW approximately (trace in **red**), meanwhile the
 471 instantaneous reactive power is almost constant (trace in **black**).
 472

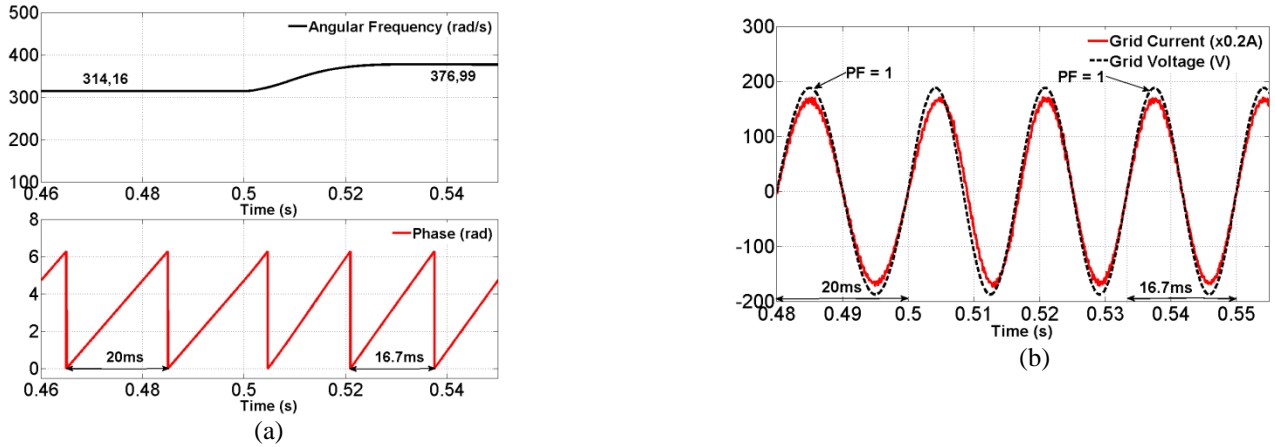
473 A step of 4.4kVar is exerted in the instantaneous reactive power reference, producing the time response
 474 shown in Fig. 11d (trace in **black**), meanwhile the instantaneous active power is almost constant at 10kW
 475 (trace in **red**) due to the decoupled control exerted.
 476



477 **Fig. 11.** a) dc bus voltage during a step in V^{*CC} (constant output current i_p in PV modules).
 478 b) Grid voltage and current at phase 1 during a step from 50% to nominal irradiance (constant V^{*CC}).
 479 c) Instantaneous active and reactive powers during a step from 50% to nominal irradiance (constant V^{*CC}).
 480 d) Instantaneous active and reactive powers during a step in reactive power (constant i_p and V^{*CC}).
 481

482 The new German grid code establishes that the generating power plants must be disconnected from the
 483 grid if the grid frequency is lower than 47.5 Hz or greater than 51.5 Hz, they must reduce the output
 484 instantaneous active power with a gradient of 40% of the rated power per hertz, if the grid frequency is
 485 greater than 50.2 Hz [50]. In Europe, the utility grid frequency is 50Hz and very seldom might have a
 486 variation of 49-50.3 [51].
 487

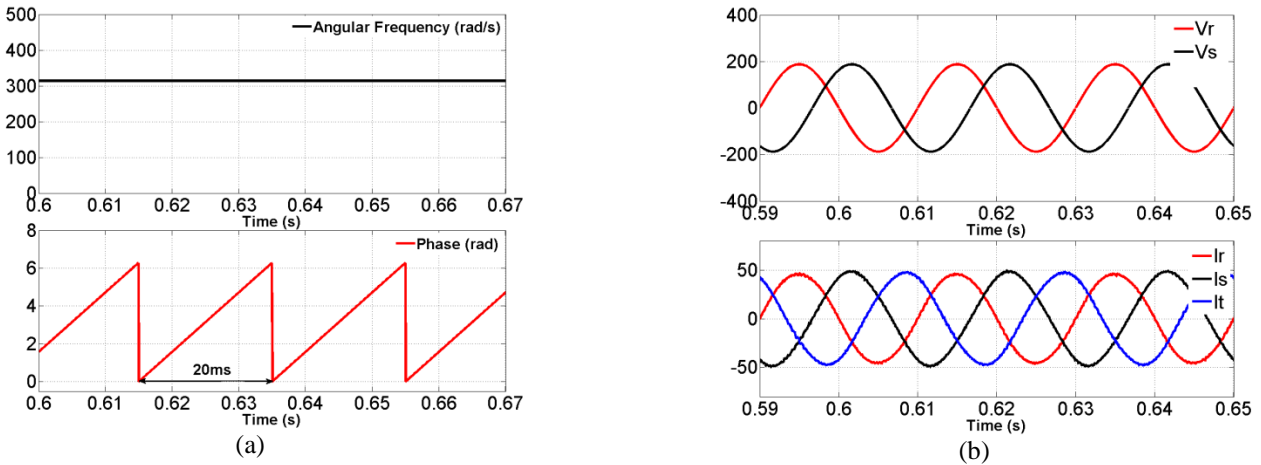
488 Transient faults can produce frequency variations and for this, some simulations during a step of
 489 frequency are performed in this section so as to test the behaviour of the DSOGI-FLL synchronization
 490 algorithm: the simulation of the time evolution of the detected frequency and phase are shown in Fig. 12
 491 in which the rms value of the 3-phase utility grid voltage is $V_{rms}=230V$ (phase-to-phase) and a step of
 492 frequency from 50Hz to 60Hz is exerted at 0.5s. A reliable frequency and phase detection is attained by
 493 the DSOGI-FLL as it can be observed in Fig. 12a, and the synchronization of the voltage and current at
 494 phase 1 is perfectly attained as it is shown in Fig. 12b, producing a unitary power factor operation before
 495 and after the step.
 496



497 **Fig. 12.** a) Time evolution of the detected frequency and phase during a step of the nominal frequency.
 498 b) Grid voltage and current at phase 1 during a step of the nominal frequency.
 499

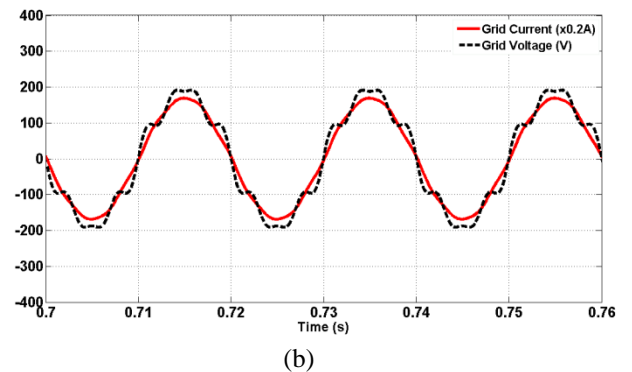
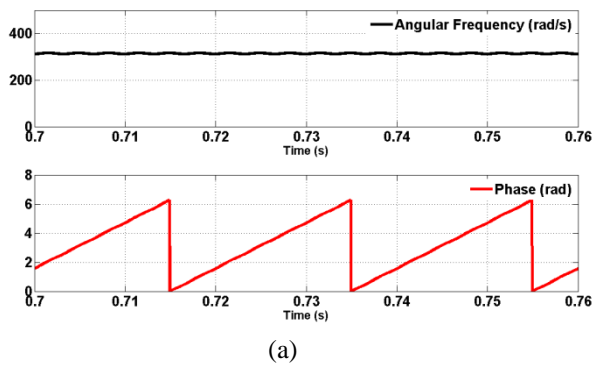
500 In order to analyze the response of the DSOGI-FLL when voltage unbalances occur in the low-voltage
 501 3-phase utility grid, Fig. 13a shows the time evolution of the detected frequency and phase by the
 502 DSOGI-FLL algorithm for a ground fault in phase 3 ($V_r=187.79V$ peak, $V_s=187.79V$ peak, $V_t=0$ (phase-
 503 to-neutral)) where an optimal frequency and phase detection is attained (2nd order harmonic free). This
 504 optimal detection is due to the use of the PSC block that was explained in Section 2.2.1 (synchronization
 505 algorithm).
 506

507 The advantages of using the PSC block in a PV grid-connected system are clearly shown in Fig. 13b:
 508 the voltage amplitudes of the 3-phase utility grid for a ground fault in phase 3 are shown in the upper
 509 zone; meanwhile the pure 3-phase sinusoidal currents are depicted in the bottom zone. This situation
 510 arises because the PSC block removes the negative sequence of the 3-phase voltages, producing a
 511 feedback phase with the equivalent positive sequence.
 512



513 **Fig. 13.** a) Time evolution of the detected frequency and phase when voltage unbalances occur (ground fault in phase 3).
 514 b) Time evolution of the grid voltages and currents when voltage unbalances occur (ground fault in phase 3).
 515

516 For the next simulations, the 5th and 7th harmonics are introduced in the 3-phase utility grid voltages with
 517 an amplitude distortion of 10% for both harmonics. The frequency and phase detection by the DSOGI-
 518 FLL is shown in Fig. 14a, and because of the effects of the high harmonic pollution in the utility grid
 519 voltages, a frequency and phase detection with harmonics can be observed. The time evolution of the
 520 grid current at phase 1 is shown in Fig.14b where an important harmonic attenuation can be observed.
 521



(a)

(b)

522 **Fig. 14.** a) Time evolution of the detected angular frequency and phase for a 5th and 7th harmonics distortion in the 3-phase
 523 utility grid voltages.
 524 b) Time evolution of the grid voltage and current at phase 1.

525
 526
 527
 528
 529
 530
 531
 532
 533
 534
 535
 536
 537
 538
 539

5. EXPERIMENTS USING REAL-TIME DIGITAL SIMULATIONS

In order to support the results obtained with simulations, a series of real-time experiments have been carried out using a DS1006 DSPACE platform with several I/O blocks. The model blocks of the control and power subsystems are built in MATLAB/SIMULINK, the C-code is generated with Real Time Workshop and downloaded into the DSPACE platform. The platform is formed by a host PC, the DS1006 DSPACE and the DS5202 Electric Motor HIL Solution boards with digital-to-analog and analog-to-digital converters interface, as well as an oscilloscope for waveforms monitoring. The configuration of the real time platform setup is shown in Fig. 15 and a photo of the experiment setup is shown in Fig.16.

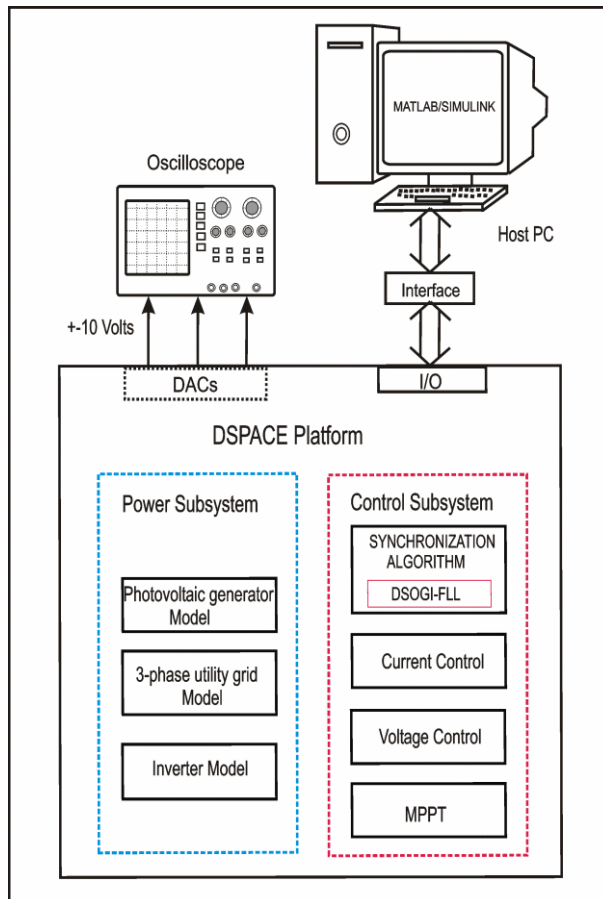


Fig. 15. Platform setup for the real-time experiments.

540
541
542

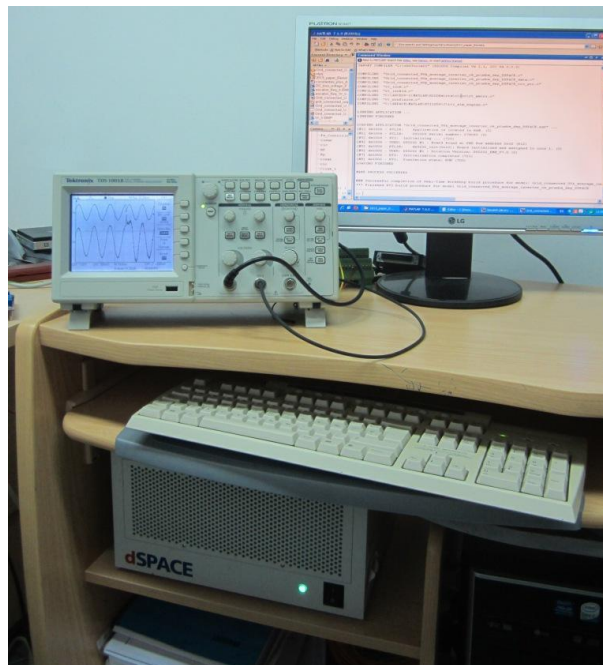


Fig. 16. Photo of the real-time digital platform setup.

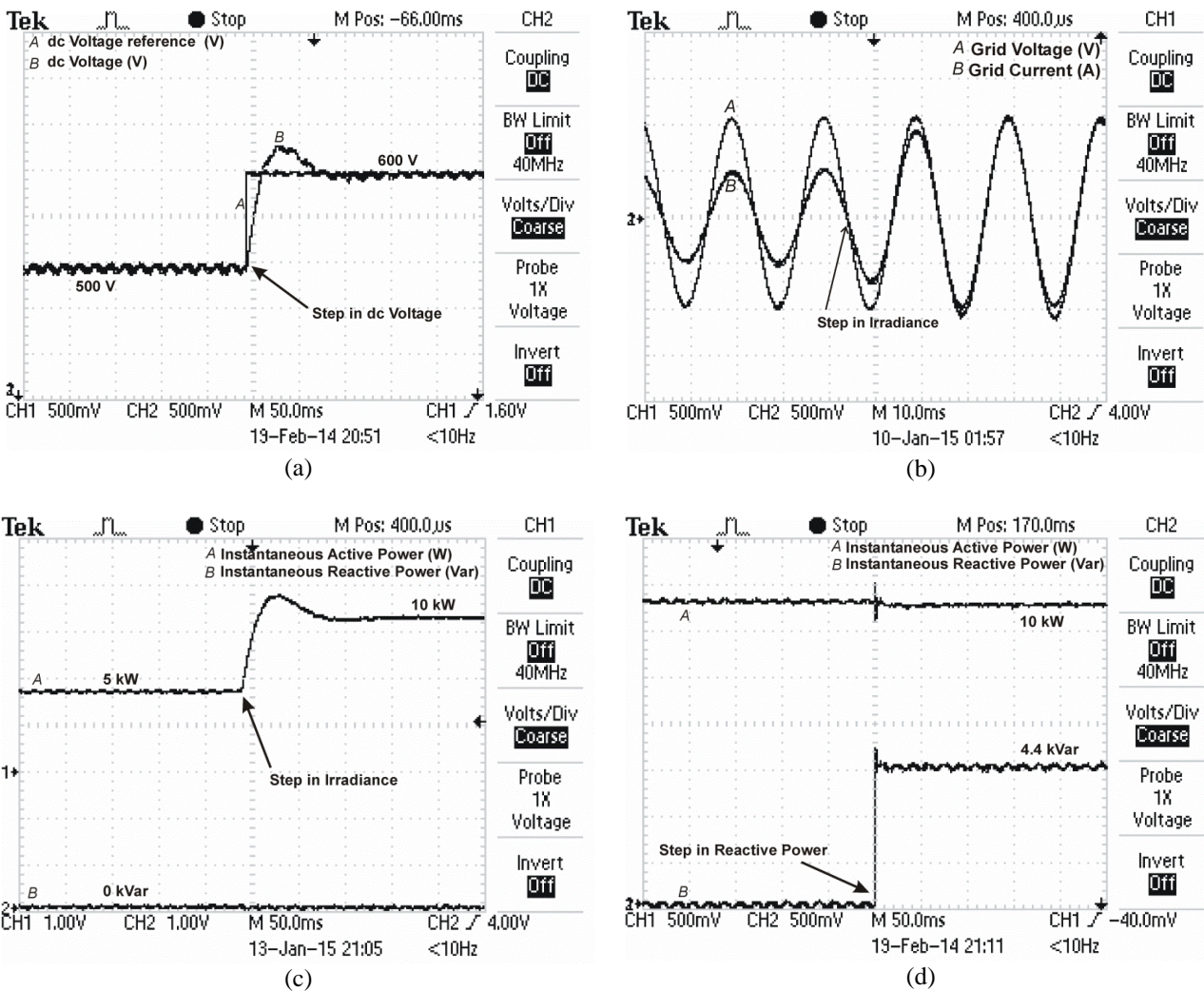
543
544
545
546
547
548
549

550
551
552
553
554
555
556
557
558
559
560
561
562
563
564
565
566
567
568
569

5.1 Testing the control algorithm

The first real-time digital test is focused in the response of the control algorithm under different perturbations coming from the PV modules. Also a step of reactive power is exerted in order to evaluate the capability of the control strategy to deal with this type of disturbances.

The real-time response of the control to a change in the MPPT of the I-V curve of the PV generator is simulated with a corresponding step from 500V to 600V (its nominal value) in the dc bus voltage reference V^*_{CC} and keeping constant the incoming output current from the PV generator for a specific irradiance, as can be seen in Fig. 17a: the proper real-time evolution of the dc bus voltage is attained. A common situation in PV systems is a variation of the solar irradiance over the PV modules and for this, a step in the output current of the PV generator I_g is exerted from a 50% up to nominal conditions with constant dc bus voltage reference: consequently, the grid current at phase 1 also increases from 50% up to its final value as can be seen in Fig. 17b, the instantaneous active power increases from 5kW to 10kW approximately as can be observed in Fig. 17c, meanwhile the instantaneous reactive power does not vary in this condition. Figure 17d shows the real-time evolution of the instantaneous active and reactive powers injected to the 3-phase utility grid when a step of 4.4kVar is exerted; in this case, the instantaneous active power is almost constant around 10kW at steady state, due to the decoupled control exerted.



570
571

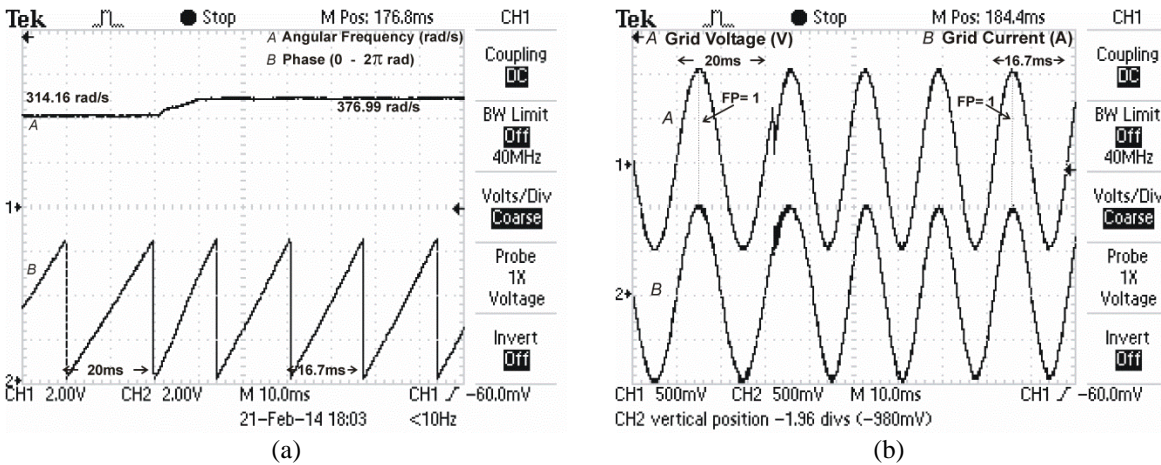
Fig. 17. a) Real-time dc bus voltage during a step in V^*_{CC} (constant output current i_p in PV modules).
b) Real-time grid voltage and current at phase 1 during a step from 50% to nominal irradiance (constant V^*_{CC}).

- 572 c) Real-time instantaneous active and reactive powers during a step from 50% to nominal irradiance (constant
 573 V^*_{CC}).
 574 d) Real-time instantaneous active and reactive powers during a step in reactive power (constant i_p and V^*_{CC}).
 575
 576

577 5.2 Testing the influence of the nominal frequency variation

578
 579 The second real-time digital test deals with the validation of the DSOGI-FLL synchronization algorithm
 580 where a step of frequency from 50Hz to 60Hz is applied. The frequency detection is shown in Fig. 18a
 581 and the time evolution of the voltage and grid current at phase 1 are depicted in Fig. 18b.
 582

583 It can be seen an optimal frequency detection by this algorithm when variations of the nominal frequency
 584 of the utility grid occur, due to no power factor degradation of the inverter-grid connection happens prior
 585 and after the step of frequency, as can be seen in Fig. 18b, attaining a unitary power factor operation.
 586 These results validate the proper behaviour of the DSOGI-FLL synchronization algorithm, as an
 587 adaptive filter for the frequency variations in the utility grid voltages.
 588

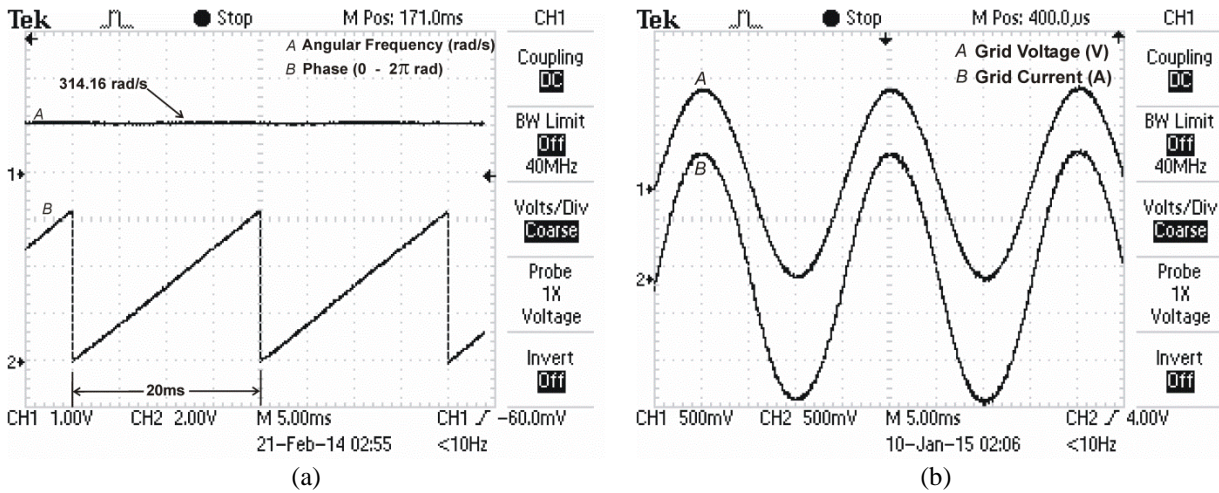


589
 590 **Fig. 18.** a) Real-time detected frequency and phase during a step of the nominal frequency.
 591 b) Real-time grid voltage and current at phase 1 during a step of the nominal frequency.
 592
 593

594 5.3 Testing the influence of the Voltage Unbalances

595
 596 The third real-time digital test has been focused in the response of the DSOGI-FLL algorithm when
 597 voltage unbalances occur in the low voltage 3-phase utility grid due to a ground fault in phase 3
 598 (modelled as $V_r=V_s=187.79V_{peak}$, $V_t=0V$ (phase-to-neutral)). Figure 19a shows the time evolution of
 599 the real-time detected frequency and phase, where a constant frequency and a pure ramp for the phase
 600 detection are attained (free of the 2nd order harmonic) as can be seen.
 601

602 Figure 19b shows the real-time evolution of the grid voltage and current at phase 1. In this, the current
 603 waveform is a pure sinusoid because no negative sequence of the 3-phase voltages is feedback into the
 604 current PR controller due to the use of the PSC block in the DSOGI-FLL synchronization algorithm.
 605
 606

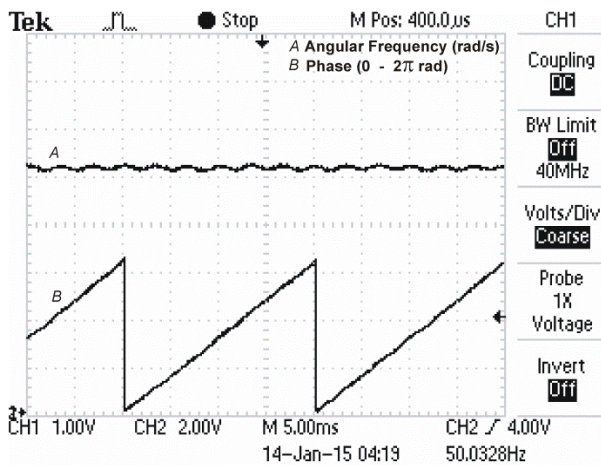


607
 608
 609
 610
 611
 612
 613
 614
 615
 616
 617
 618
 619
 620
 621
 622
 623
 624
 625
 626
 627
 628
 629
 630
 631
 632
 633

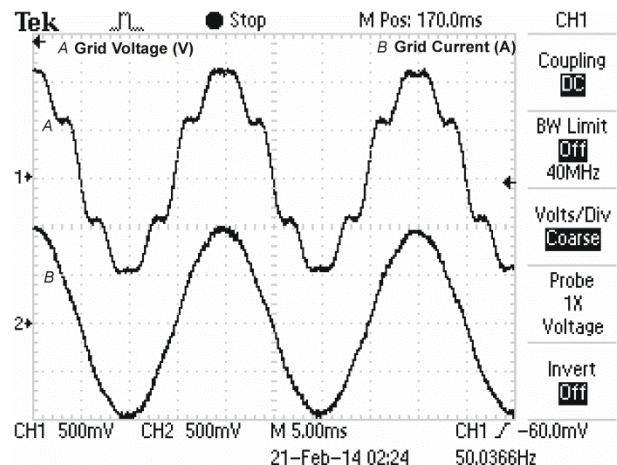
Fig. 19. a) Real- time evolution of the detected frequency and phase when voltage unbalances occur (ground fault in phase 3).
 b) Real-time evolution of the grid voltage and current at phase 1 when voltage unbalances occur (ground fault in phase 3).

5.4 Testing the influence of the Harmonic Distortion

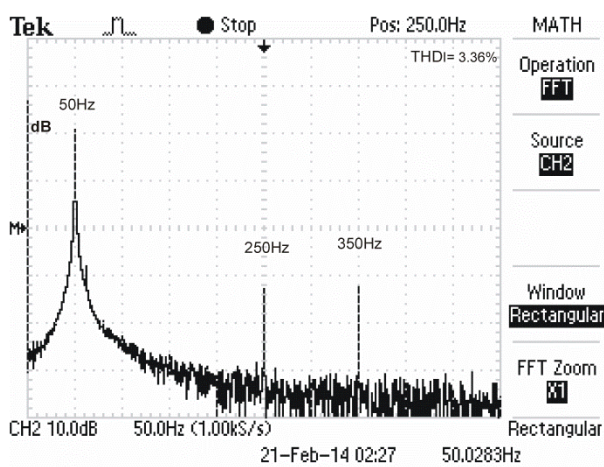
The final real-time digital test has been performed with a 10% pollution in the magnitude of the low order 5th and 7th harmonics in the 3-phase utility grid voltages and, as a consequence, the grid currents will be affected by its influence [9]. Figure 20a displays the real-time evolution of the detected angular frequency and phase, meanwhile Fig. 20b displays the utility grid voltage and current at phase 1. The frequency spectrum of the grid current at phase 1 is depicted in Fig. 20c, where an equivalent amplitude distortion of 2.24% and 2.51% for the 5th and 7th harmonics (see Fig. 20d for a zoom), respectively, and a THD_I=3.36% is observed. This THD_I indicates that the system fulfill with the harmonic standards for PV system which establishes a THD_I <5% [5,8].



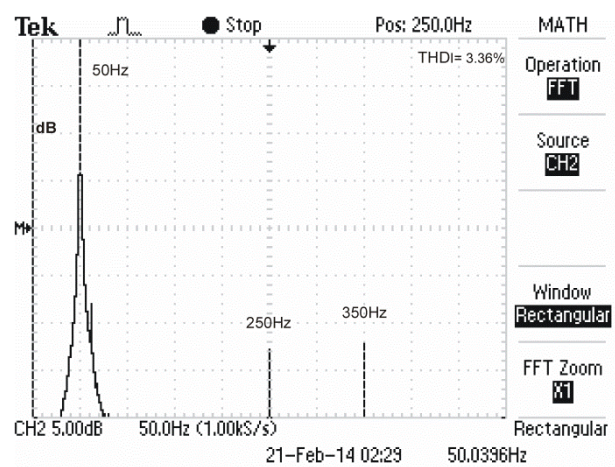
(a)



(b)



(c)



(d)

634

635

636

637

638

639

640

641

642

643

644

645

Fig. 20. a) Real-time evolution of the detected angular frequency and phase for a 5th and 7th harmonics distortion in the 3-phase utility grid voltages.

b) Real-time evolution of the grid voltage and current at phase 1.

c) Frequency spectrum of the grid current at phase 1.

d) Zoom of the frequency spectrum for the grid current at phase 1.

A resume of the harmonic distortions of the current at phase 1 and its comparison with the limits imposed by the normative [5,8] are shown in Table 5.

Table 5. Harmonic Distortions for the grid current at phase 1

Odd harmonics	Harmonic Distortion	Distortion limit
5 th	2.24%	< 4.0%
7 th	2.51%	< 4.0%
	THD _I	THD _I limit
	3.36%	< 5.0%

646

647

648

649

650

6. CONCLUSIONS

In this paper the parameters for the **combined operation** of the control and the synchronization algorithms have been chosen in order to obtain the appropriate performance of the grid-connected PV system when some perturbations as harmonic pollution, voltage unbalances and frequency variations are present. The power factor control and the power quality have been the main characteristics for the evaluation of this performance.

The DSOGI-FLL synchronization algorithm has been studied in detail, and its performance for several gains is analyzed, yielding an optimal value for guaranteeing a trade-off between the settling time, overshoot and harmonic rejection:

- 1) An unitary power factor operation is attained when frequency variations occur due to its capability of estimating the adequate phase and frequency as an adaptive filter performance.
- 2) When voltage unbalances occur, this algorithm is able to calculate the positive sequence of the utility grid voltages ensuring a distorted-free feedback phase and hence, the current controller will be able to feed the 3-phase currents into the utility grid with no second order harmonic distortion.
- 3) The adaptive filter structure is tuned to the nominal frequency of the utility grid and can produce a high rejection to the amplitude of the harmonic components in the utility grid, reducing also the resulting distortion of the feedback phase angle.

A Proportional Resonant (PR) controller has been used in the inner current loop in order to obtain zero error in steady state for sinusoidal input signals, and a Harmonic Compensator (HC) structure has been incorporated into this control strategy in order to obtain the 3-phase voltage references for the PWM block with no harmonic contamination. In this paper, the validation of this issue has been carried out by introducing an amplitude distortion in the 5th and 7th harmonics of the utility grid voltages, which finally lead to the injection of good power quality into the utility grid according to international standards.

Acknowledgements

This work has been supported by a grant from La Fundación Séneca, Agencia de Ciencia y Tecnología de la Región de Murcia, Spain, as a part of the Project Ref.11948/PI/09, entitled “Design and construction of a hardware/software prototype for the synchronization and monitoring of renewable agents in a Distributed Generation System” (Alexis. B. Rey-Boué), and a scholarship towards the PhD degree from the Universidad Politécnica de Cartagena, Spain, with the Santander Bank support (N. F. Guerrero-Rodríguez).

Appendix A

For a 3-phase system described by vector $\mathbf{x}=[x_a \ x_b \ x_c]^t$, where x_a , x_b , and x_c are instantaneous 3-phase variables and t means transpose, the Clarke and Park transformations [13] are defined in (A.1) and (A.2), respectively:

697

$$\begin{bmatrix} x_o \\ x_\alpha \\ x_\beta \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\ 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} x_a \\ x_b \\ x_c \end{bmatrix} \quad (\text{A.1})$$

698

699 where x_0 is the zero-sequence component and $x_{\alpha,\beta}$ are the $\alpha\beta$ components of vector \mathbf{x} in the stationary
700 reference frame.

701

702

$$\begin{bmatrix} x_d \\ x_q \end{bmatrix} = \begin{bmatrix} \cos\theta & \sin\theta \\ -\sin\theta & \cos\theta \end{bmatrix} \begin{bmatrix} x_\alpha \\ x_\beta \end{bmatrix} \quad (\text{A.2})$$

703

704 where $x_{d,q}$ are the dq components of vector \mathbf{x} in the synchronous reference frame, and θ is the phase
705 angle of the 3-phase utility grid voltages.

706

707 The inverse problem is also possible by computing the inverse to the above matrices, yielding the inverse
708 Clarke and Park transformations in (A.3) and (A.4), respectively.

709

710

$$\begin{bmatrix} x_a \\ x_b \\ x_c \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & 1 & 0 \\ \frac{1}{\sqrt{2}} & -\frac{1}{2} & \frac{\sqrt{3}}{2} \\ \frac{1}{\sqrt{2}} & -\frac{1}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} x_o \\ x_\alpha \\ x_\beta \end{bmatrix} \quad (\text{A.3})$$

711

712

$$\begin{bmatrix} x_\alpha \\ x_\beta \end{bmatrix} = \begin{bmatrix} \cos\theta & -\sin\theta \\ \sin\theta & \cos\theta \end{bmatrix} \begin{bmatrix} x_d \\ x_q \end{bmatrix} \quad (\text{A.4})$$

713

714

715

716 References

717

718

719 [1] S.Ruiz-Romero, A.Colmenar-Santos, R.Gild-Ortego, and A.Molina-Bonilla, Distributed
720 generation: The definitive boost for renewable energy in Spain, *Renewable Energy*, 53 (2013)
721 354-364.

722 [2] M.A.Abdullah, A.P.Agalgaonkar, and K.M.Muttaqi, Climate change mitigation with integration
723 of renewable energy resources in the electricity grid of New South Wales, Australia, *Renewable*
724 *Energy*, 66 (2014) 305-313.

- 725 [3] IEEE Recommended Practice for Utility Interface of Photovoltaic (PV) Systems. IEEE Std 929-
726 2000 . 2000.
727
- 728 [4] A.Menti, T.Zacharias, and J.Milias-Argitis, Harmonic distortion assessment for a single-phase
729 grid-connected photovoltaic system, *Renewable Energy*, 36 (2011) 360-368.
- 730 [5] IEC 61727 Ed. 2, Photovoltaic (PV) Systems-Characteristics of the Utility Interface, 2004.
731
- 732 [6] European Committee for Electrotechnical Standardization (CENELEC), EN 50160, Voltage
733 characteristics of electricity supplied by public distribution systems. 1999.
734
- 735 [7] Standard for Interconnecting Distributed Resources with Electric Power Systems, 2003.
736
- 737 [8] Recommended Practices and Requirements of Harmonic Control in Electrical Power Systems.
738 Standard ANSI/IEEE 519-1992. 1992.
739
- 740 [9] R.Ekstrom and M.Leijon, Lower order grid current harmonics for a voltage-source inverter
741 connected to a distorted grid, *Electric Power Systems Research*, 106 (2014) 226-231.
- 742 [10] Bae Youngsang, Vu Trung-Kien, and Kim Rae-Young, Implemental Control Strategy for Grid
743 Stabilization of Grid-Connected PV System Based on German Grid Code in Symmetrical Low-
744 to-Medium Voltage Network, *IEEE Transactions on Energy Conversion*, 28 (2013) 619-631.
- 745 [11] Y.Abdel-Rady, I.Mohamed, and F.El-Saadany, A Robust Natural-Frame-Based Interfacing
746 Scheme for Grid-Connected Distributed Generation Inverters, *IEEE Transactions on Energy*
747 *Conversion*, 26 (2011) 728-736.
- 748 [12] F.Blaabjerg, Z.Chen, and S.B.Kjaer, Power electronics as efficient interface in dispersed power
749 generation systems, *IEEE Transactions on Power Electronics*, 19 (2004) 1184-1194.
- 750 [13] R.Teodorescu, M.Liserre, and P.Rodriguez, *Grid Converters for Photovoltaic and Wind Power*
751 *Systems*. 2011. United Kingdom, John Wiley & Sons, Ltd.
752
- 753 [14] G.Tsengenes and G.Adamidis, Investigation of the behavior of a three phase grid-connected
754 photovoltaic system to control active and reactive power, *Electric Power Systems Research*, 81
755 (2011) 177-184.
- 756 [15] D.Boroyevich, I.Cvetkovic, R.Burgos, and D.Dong, Intergrid: A Future Electronic Energy
757 Network?, *IEEE Journal of Emerging and Selected Topics in Power Electronics*, 1 (2013).
- 758 [16] R.Teodorescu, F.Blaabjerg, M.Liserre, and PC.Loh, Proportional-resonant controllers and filters
759 for grid-connected voltage-source converters, *IEE Proceedings Electric Power Applications*, 153
760 (2006) 750-762.

- 761 [17] R.Teodorescu and F.Blaabjerg, Proportional-Resonant Controllers. A New Breed of Controllers
762 Suitable for Grid-Connected Voltage-Source Converters. International Conference on
763 Optimization of Electrical and Electronic Equipments. Optim 2004 , 9-14. 2004.
764
- 765 [18] P.Rodriguez, A.Luna, M.Ciobotaru, R.Teodorescu, and F.Blaabjerg, Advanced Grid
766 Synchronization System for Power Converters under Unbalanced and Distorted Operating
767 Conditions. IEEE Industrial Electronics, IECON. 5173-5178. 6-11-2006.
768
- 769 [19] A.V.Timbus, M.Liserre, R.Teodorescu, P.Rodriguez, and F.Blaabjerg, Evaluation of Current
770 Controllers for Distributed Power Generation Systems, IEEE Transactions on Power Electronics,
771 24 (2009) 654-664.
- 772 [20] MATLAB/SIMULINK. The MathWorks, Inc. <<http://www.mathworks.com/>>. 2008.
773
- 774 [21] A.B.Rey-Boué, R.García-Valverde, F.Ruz-Vila, and J.M.Torrelo-Ponce, An integrative
775 approach to the design methodology for 3-phase power conditioners in Photovoltaic Grid-
776 Connected systems, Energy Conversion and Management, 56 (2012) 80-95.
- 777 [22] H.Akagi, E.Hirokazu Watanabe, and M.Aredes, Instantaneous Power Theory and Applications
778 to Power Conditioning, John Wiley & Sons, 2007.
- 779 [23] V.Scarpa, S.Busso, and G.Spiazzi, Low-Complexity MPPT Technique Exploiting the PV Module
780 MPP Locus Characterization, IEEE Transactions on Industrial Electronics, 56 (2009) 1531-1538.
- 781 [24] N.Mutoh, M.Ohno, and T.Inoue, A method for MPPT control while searching for parameters
782 corresponding to weather conditions for PV generation systems. IECON 2004. 30th Annual
783 Conference of IEEE. 3, 3094-3099. 2-11-2004.
784
- 785 [25] E.Clarke, Circuit Analysis of AC Power Systems, vol. I, Wiley, 1950.
- 786 [26] H.Akagi, Y.Kanazawa, and A.Nabae, Instantaneous Reactive Power Compensators Comprising
787 Switching Devices without Energy Storage Components. IEEE Transactions on Industry
788 Applications IA-20[3], 625-630. 1984.
789
- 790 [27] MP.Kazmierkowski, R.Krishnan, and F.Blaabjerg, Control in Power Electronics, Selected
791 Problems, 2002.
- 792 [28] J.C.Hernández, J.Ortega, .De la Cruz, and D.Vera, Guidelines for the technical assessment of
793 harmonic, flicker and unbalance emission limits for PV-distributed generation, Electric Power
794 Systems Research, 81 (2011) 1247-1257.
- 795 [29] E.J.Bueno, F.Espinosa, F.J.Rodriguez, J.Urefi, and S.Cobrece, Current control of voltage source
796 converters connected to the grid through an LCL-filter, Power Electronics Specialists
797 Conference, 2004 IEEE 35th Annual, 1 (2004) 68-73.
- 798 [30] D.Hamza, Q.Mei, and P.K.Jain, Implementation of an EMI active filter in grid-tied PV micro-
799 inverter controller and stability verification. IECON. 477-482. 25-10-2012.

- 801 [31] S.Wang, C.Xu, and H.Qin, Design theory and implementation of planar EMI filter based on
802 annular integrated inductor-capacitor unit. *Electromagnetic Compatibility (APEMC)*. 129-132.
803
- 804 [32] C.Po-Shen and L.Yen-Shin, Effective EMI Filter Design Method for Three-Phase Inverter Based
805 Upon Software Noise Separation, *IEEE Transactions on Power Electronics*, 25 (2010) 2797-
806 2806.
- 807 [33] R.Kadri, J.P.Gaubert, and G.Champenois, An Improved Maximum Power Point Tracking for
808 Photovoltaic Grid-Connected Inverter Based on Voltage-Oriented Control, *IEEE Transactions*
809 *on Industrial Electronics*, 58 (2011) 66-75.
- 810 [34] N.S.D'Souza, L.A.C.Lopes, and X.Liu, Comparative study of variable size perturbation and
811 observation maximum power point trackers for PV systems, *Electric Power Systems Research*,
812 80 (2010) 296-305.
- 813 [35] K.Ishaque and Z.Salam, A review of maximum power point tracking techniques of PV system
814 for uniform insolation and partial shading condition, *Renewable and Sustainable Energy*
815 *Reviews*, 19 (2013) 475-488.
- 816 [36] C.B.Salah and M.Ouali, Comparison of fuzzy logic and neural network in maximum power point
817 tracker for PV systems, *Electric Power Systems Research*, 81 (2011) 43-50.
- 818 [37] J.Qi, Y.Zhang, and Y.Chen, Modeling and maximum power point tracking (MPPT) method for
819 PV array under partial shade conditions, *Renewable Energy*, 66 (2014) 337-345.
- 820 [38] H.Heydari-doostabad, R.Keypour, M.Reza Khalghani, and M.Hassan Khooban, A new approach
821 in MPPT for photovoltaic array based on Extremum Seeking Control under uniform and non-
822 uniform irradiances, *Renewable Energy*, 94 (2013) 28-36.
- 823 [39] A.I.Dounis, P.Kofinas, C.Alafodimos, and D.Tseles, Adaptive fuzzy gain scheduling PID
824 controller for maximum power point tracking of photovoltaic system, *Renewable Energy*, 60
825 (2013) 202-214.
- 826 [40] V.Kaura and V.Blasko, Operation of a phase locked loop system under distorted utility
827 conditions, *IEEE Transactions on Industry Applications*, 33 (1997) 58-63.
- 828 [41] F.Blaabjerg, R.Teodorescu, M.Liserre, and A.V.Timbus, Overview of Control and Grid
829 Synchronization for Distributed Power Generation Systems, *IEEE Transactions on Industrial*
830 *Electronics*, 53 (2006) 1398-1409.
- 831 [42] C.L.Fortescue, Method of Symmetrical Co-Ordinates Applied to the Solution of Polyphase
832 Networks, *American Institute of Electrical Engineers*, XXXVII (1918) 1027-1140.
- 833 [43] M.Karimi-Ghartemani and M.R.Iravani, A method for synchronization of power electronic
834 converters in polluted and variable-frequency environments, *IEEE Transactions on Power*
835 *Systems*, 19 (2004) 1263-1270.
- 836 [44] P.Rodriguez, R.Teodorescu, I.Candela, A.V.Timbus, M.Liserre, and F.Blaabjerg, New Positive-
837 sequence Voltage Detector for Grid Synchronization of Power Converters under Faulty Grid

- 838 Conditions. Power Electronics Specialists Conference, 2006. PESC '06. 37th IEEE. 1-7. 18-6-
839 2006.
840
- 841 [45] Y.Xiaoming, W.Merk, H.Stemmler, and J.Allmeling, Stationary-frame generalized integrators
842 for current control of active power filters with zero steady-state error for current harmonics of
843 concern under unbalanced and distorted operating conditions, IEEE Transactions on Industry
844 Applications, 38 (2002) 523-532.
- 845 [46] G.F.Franklin, J.D.Powell, and A.Emami-Naeini, Feedback Control of Dynamic Systems,
846 Prentice-Hall,Inc., New Jersey, 2002.
- 847 [47] W.V Lyon, Applications of the method of symmetrical components, McGraw-Hill, New York,
848 1937.
- 849 [48] S.Busso and P.Mattavelli, Digital Control in Power Electronics, Morgan & Claypool, 2006.
- 850 [49] F.Almonacid, C.Rus, L.Hontoria, and F.J.Muñoz, Characterisation of PV CIS module by
851 artificial neural networks. A comparative study with other methods, Renewable Energy, 35
852 (2010) 973-980.
- 853 [50] E.Tröster, New German Grid Codes for Connecting PV Systems to the Medium Voltage Power
854 Grid. 2nd International Workshop on Concentrating Photovoltaic Power Plants: Optical Design,
855 Production, Grid Connection, Darmstadt, Germany. 2009.
856
- 857 [51] T.Ackermann, Wind Power in Power Systems , John Wiley & Sons, Ltd., England, 2005.
858
859