

Development of the ALMA Band-3 and Band-6 Sideband-Separating SIS Mixers

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Abstract—As the Atacama Large Millimeter/submillimeter Array (ALMA) nears completion, 73 dual-polarization receivers have been delivered for each of Bands 3 (84–116 GHz) and 6 (211–275 GHz). The receivers use sideband-separating superconducting Nb/Al-AIOx/Nb tunnel-junction (SIS) mixers, developed for ALMA to suppress atmospheric noise in the image band. The mixers were designed taking into account dynamic range, input return loss, and signal-to-image conversion (which can be significant in SIS mixers). Typical SSB receiver noise temperatures in Bands 3 and 6 are 30 and 60 K, respectively, and the image rejection is typically 15 dB.

Index Terms—Atacama Large Millimeter/submillimeter Array (ALMA), heterodyne, image rejecting mixer, interferometer, millimeter-wave, radio astronomy, sideband-separating mixer, SIS junction, SIS receiver, superconductor.

I. INTRODUCTION

THE superconducting tunnel diode, specifically the superconductor–insulator–superconductor (SIS) junction, is now the preferred mixing element for low-noise heterodyne receivers at millimeter and submillimeter wavelengths. Since the introduction of the SIS mixer in 1979 [1]–[3], it has evolved from a delicate, short-lived, Pb-alloy device, difficult to fabricate reproducibly and marginally suitable for field operation, to today’s relatively robust and reproducible device, usually with Nb/Al-AIOx/Nb tunnel junctions. Early SIS mixers had SIS junctions suspended across a waveguide and one or more mechanical tuners to tune out the relatively large junction capacitance [4], a configuration with limited instantaneous bandwidth and far from suitable for operation in large numbers and at remote sites as required for the Atacama Large Millimeter/submillimeter Array (ALMA) [5]. The adaptation

of thin-film processing techniques from the semiconductor industry for use with Nb-based circuits has made possible the integration of tuning circuits with the SIS junctions to yield relatively wideband operation, typically 1.3:1 RF bandwidth and ~ 8 -GHz IF bandwidth, without mechanical tuners [6]–[8].

Before ALMA, SIS receivers operated in the simple double-sideband mode, responding to signals in both upper and lower sidebands. For spectral-line radio astronomy, which is a major focus of ALMA, atmospheric noise entering a DSB receiver in the unwanted image sideband adds substantially to the overall system noise, thereby degrading the sensitivity of the measurement. In addition, for single-dish (as opposed to interferometric) observations, spectral lines from the unwanted sideband appear at IF and can greatly complicate the identification of molecules from their emission spectra. For these reasons, the North American ALMA receivers use sideband-separating SIS mixers. Sideband separation can be achieved in several ways in heterodyne receivers. An input diplexer can be used to couple the upper and lower sidebands into separate double-sideband mixers. At millimeter and submillimeter wavelengths, mechanically tuned quasioptical diplexers such as the Mach–Zehnder and Martin–Puplett interferometers have been used [9]–[11]. However, these have insufficient bandwidth [12] to operate with ALMA’s octave or wider IF bands. For the ALMA Band-3 (84–116 GHz) and Band-6 (211–275 GHz) receivers the phasing type of sideband-separating mixer is used [13], as depicted in Fig. 1. A pair of elemental DSB mixers, driven by a common LO source, are connected by quadrature hybrids to the RF source and IF load. Because the two elemental mixers do not interact, an optimized DSB mixer is also optimal for use in the sideband-separating receiver.

An early design integrating the elements of a complete 200–300-GHz sideband-separating SIS mixer (RF quadrature hybrid, LO couplers, and two DSB SIS mixers with RF tuning circuits) on a single chip was largely successful [13], [14]. However, the large size of the chips would have required many wafers to be produced, with significantly higher cost than a waveguide-based circuit incorporating a pair of much smaller chips, each containing a single DSB SIS mixer with its RF tuning circuit and a waveguide coupling probe [15].

II. DESIGN OF THE ELEMENTAL SIS DSB MIXERS

A. SIS Junctions

The quantum theory of SIS mixers, published in 1983 and 1985 by Tucker and Feldman [16], [17], has been the basis for

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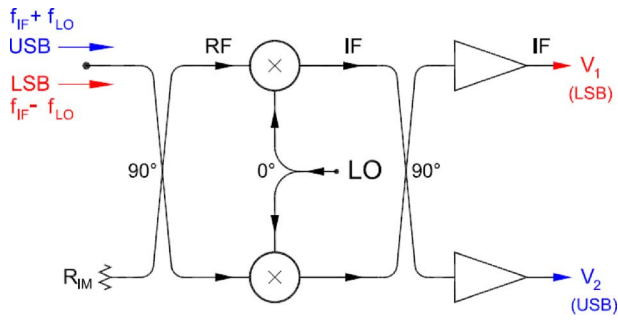


Fig. 1. Phasing type of sideband-separating mixer, as used in Band-3 and -6 ALMA SIS receivers, uses two elemental DSB mixers.

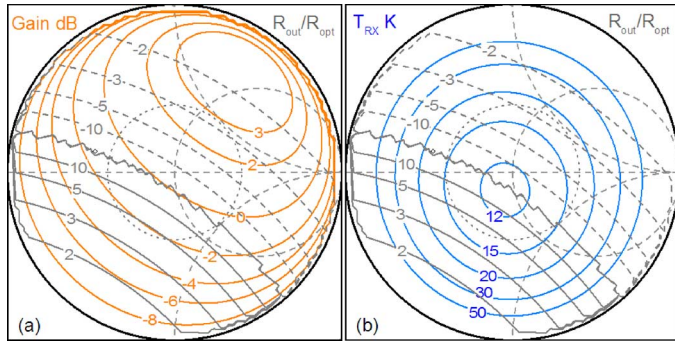


Fig. 2. For the elemental DSB Band-3 SIS mixer with $f_{LO} = 100$ GHz, contours of (a) mixer gain dB (solid orange lines) and (b) noise temperature (solid blue lines) plotted on a Smith chart of the RF source impedance normalized to the optimum source impedance R_{opt} . The gray contours are the IF output resistance of the mixer normalized to R_{opt} ; solid gray lines indicate positive output resistance and dashed gray lines indicate negative output resistance. The squiggly gray line indicates the transition from positive to negative output resistance. The dashed gray circle at $|\rho| = 0.4$ indicates the boundary of the desirable operating region [20]. The simulations used the $I(V)$ curve of Fig. 6. A 4-K IF isolator of characteristic impedance R_{opt} , and an amplifier noise temperature of 4 K were assumed.

all subsequent SIS mixer design. The noise and conversion characteristics of an SIS mixer depend on the embedding impedance seen by the junction at the sideband frequencies $n f_{LO} \pm f_{IF}$ and at the LO harmonics $n f_{LO}$. Under typical operating conditions, for an SIS junction with normal resistance R_N , an optimum RF source impedance, based on the Tucker theory, is given by the Ke and Feldman formula [18]

$$R_{opt} = 4R_N \left(2 + \frac{eV_g}{hf_{LO}} \right)^{-1}$$

where eV_g is the superconducting energy gap, h is Planck's constant, and f_{LO} is the local oscillator frequency. This formula applies to mixers with a single junction or a series array of junctions; in both cases, eV_g is the superconducting energy gap of a single junction. The simulated results in Figs. 2–5 show how the characteristics of the elemental DSB mixers for ALMA Bands 3 and 6 depend on the RF source impedance for representative LO frequencies 100 and 230 GHz, respectively. Fig. 2(a) shows contours of gain in dB (heavy orange lines) and IF output impedance normalized to R_{opt} (gray lines, dashed where the output resistance is negative) for the Band-3 mixer biased near the middle of the first photon step and with the LO

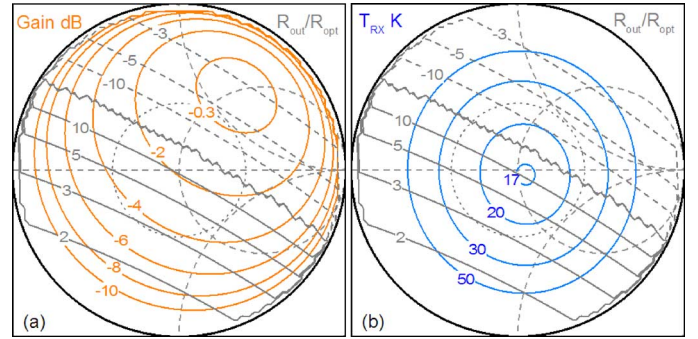


Fig. 3. For the elemental DSB Band-6 SIS mixer with $f_{LO} = 230$ GHz. The simulations used the $I(V)$ curve of Fig. 7. Details are the same as for Fig. 2.

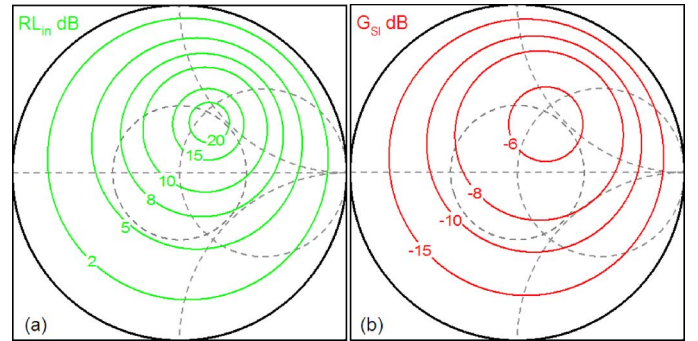


Fig. 4. For the elemental DSB Band-3 SIS mixer with $f_{LO} = 100$ GHz, contours of (a) RF input return loss in dB (solid green lines) and (b) signal-to-image conversion gain in dB (solid red lines), plotted on a Smith chart of the RF source impedance normalized to the optimum source impedance R_{opt} . The dashed gray circles at $|\rho| = 0.4$ indicate the boundary of the desirable operating region [20]. A 4-K IF isolator of characteristic impedance R_{opt} is assumed in these simulations.

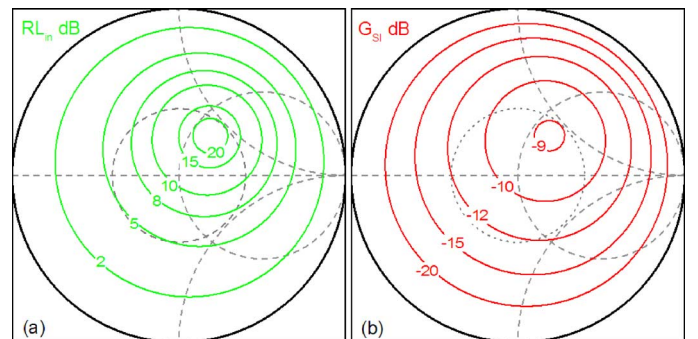


Fig. 5. For the elemental DSB Band-6 SIS mixer with $f_{LO} = 230$ GHz. Details are the same as for Fig. 4.

power adjusted for a pumping parameter $\alpha = eV_{LO}/hf = 1.2$. Fig. 2(b) shows contours of receiver noise temperature T_{RX} . Fig. 3(a) and (b) shows the same for the Band-6 mixer. Figs. 4 and 5 show the corresponding contours of RF input return loss and signal-to-image conversion gain for the elemental Band-3 and -6 DSB mixers. The contours in Figs. 2–5 are plotted on Smith charts of the RF source impedance (which includes the junction capacitance) normalized to R_{opt} . The gain is the transducer gain, with the mixer terminated in an IF isolator of characteristic impedance equal to R_{opt} for the reasons given below.

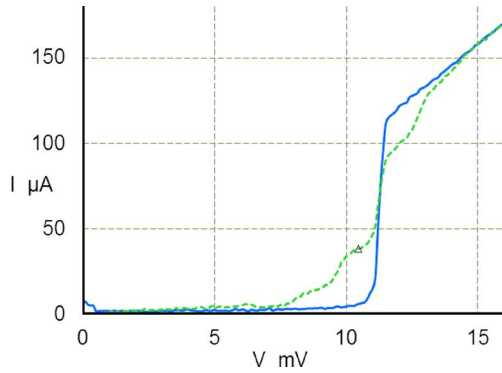


Fig. 6. Pumped and unpumped $I(V)$ curves of a typical four-junction SIS mixer for Band 3. The operating bias point is indicated by the marker. The quality factor $R(8 \text{ mV})/R_N = 31$, and the pumping parameter $\alpha \approx 1.2$.

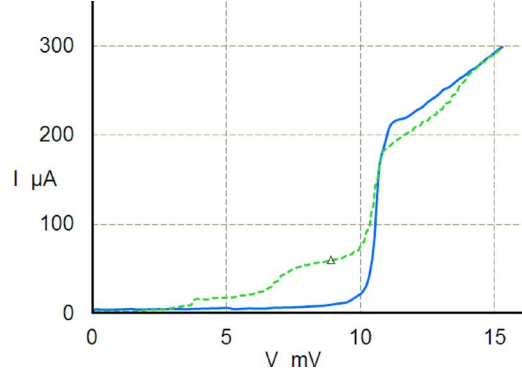


Fig. 7. Pumped and unpumped $I(V)$ curves of a typical four-junction SIS mixer for Band-6. The operating bias point is indicated by the marker. The quality factor $R(8 \text{ mV})/R_N = 21$, and the pumping parameter $\alpha \approx 1.2$.

The receiver noise temperatures are the single-sideband quantities [19] for the DSB mixer with the isolator at 4 K and an IF amplifier noise temperature of 4 K.

It is evident in Figs. 2 and 3 that the output impedance of the SIS mixers is negative when the source impedance lies in much of the upper half of the Smith chart and that the maximum conversion gain occurs in the region of negative output resistance. Negative resistance and $\text{gain} > 1$ are a result of the nonclassical nature of the SIS mixer and are not parametric effects—unlike a semiconductor diode, the capacitance of an SIS junction is not voltage-dependent. The implications of negative output resistance in designing SIS mixer circuits are discussed in [20]. To optimize the overall receiver noise, it can be appropriate to operate an SIS mixer in the region of high output resistance, positive or negative. To avoid a large RF input mismatch, and even negative RF input resistance, the IF load impedance should not be large relative to R_{opt} . We have found that an IF load equal to the optimum RF source resistance R_{opt} is a good compromise. This also results in acceptable signal-to-image conversion. The signal-to-image conversion gain G_{SI} of the elemental DSB mixers, as shown in Figs. 4(b) and 5(b), is particularly important to consider in sideband-separating receivers. Signal power converted to the image frequency can be partially reflected by any mismatched components in the input circuit, such as an imperfect OMT or feed horn, and appear at IF as a spurious image frequency signal, thereby degrading the image rejection.

The simulations used the $I(V)$ characteristics of the Band-3 and -6 production mixers, shown in Figs. 6 and 7. The LO voltage waveform is assumed to be sinusoidal; this is a good approximation [21] for SIS junctions which, by virtue of their parallel-plate construction, have a relatively large shunt capacitance. The embedding impedances seen by the SIS junctions at the higher harmonic sideband frequencies also affect mixer performance, and it is assumed here that the junctions are terminated in their own capacitance at the second-harmonic sideband frequencies $2f_{\text{LO}} \pm f_{\text{IF}}$ and short-circuited by their own capacitance at higher frequencies—this is the *quasi-five-frequency* approximation [21] which has been found to give reasonably accurate results.

The mixers for Bands 3 and 6 use series arrays of four SIS junctions with an optimum source resistance near 50Ω , which is a convenient impedance level for coupling to the waveguide probe and allows direct connection to a $50\text{-}\Omega$ IF stage. The use of a series array of N junctions in an SIS mixer has two advantages compared with a single junction. For the same overall impedance level, the individual junctions of the array are larger, which makes them easier to fabricate consistently. Also, the dynamic range (or saturation power) of an SIS mixer, which depends on $N^2 f^2$ [22], [23], is greater; this is particularly important for the ALMA receivers which are required to have less than 5% gain compression when connected to a 373-K calibration source. It was shown theoretically by Feldman and Rudner [24] that the noise of an SIS mixer with N junctions in series is the same as that of a single-junction mixer with the same impedance level as the array, i.e., for the same critical current density, the junctions in the array have N times the area of the single junction. The array requires N^2 times the LO power, but that is not a significant limitation for current single-pixel SIS receivers.

The mixing properties of an SIS junction are largely defined by its normal resistance R_N , which depends on the junction area A and critical current density J_C [25], and by the junction capacitance C_J . For the Nb/Al-AlOx/Nb junctions used in this work, $R_N A = 1.8 \times 10^{-3}/J_C$, and the specific junction capacitance C_S is given by the empirical expression [26]

$$C_S = 24.9 \log_{10} (J_C \text{ (A/cm}^2)) - 18.1 \text{ fF}/\mu\text{m}^2.$$

The choice of junction area and critical current density to give the desired R_N requires a compromise between junction quality and reproducibility on the one hand versus junction capacitance on the other. A small junction with low capacitance will have wider RF bandwidth but requires a higher value of J_C (for the desired R_N) which results in an $I(V)$ characteristic with greater leakage current and therefore higher shot noise. For the ALMA Band-3 mixers, the nominal junction diameter is $2.2 \mu\text{m}$, $J_C = 2,500 \text{ A/cm}^2$, $R_N = 76 \Omega$, and $C_S = 67 \text{ fF}/\mu\text{m}^2$, and for Band 6 the junction diameter is $1.7 \mu\text{m}$, $J_C = 5,200 \text{ A/cm}^2$, $R_N = 61 \Omega$, and $C_S = 74 \text{ fF}/\mu\text{m}^2$. (The R_N values are for the four junctions in series.)

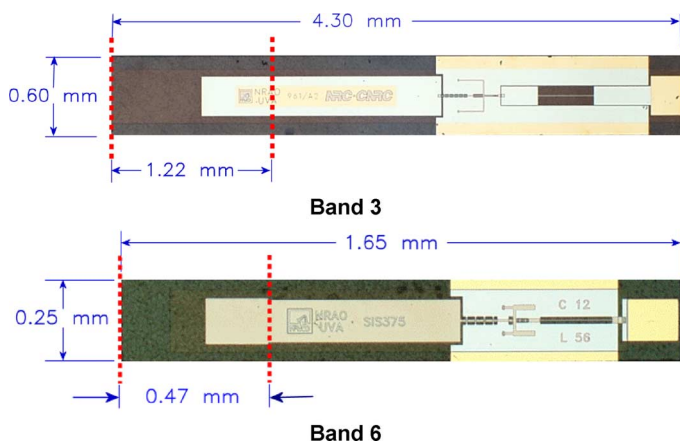


Fig. 8. Photographs of the Band-3 and -6 mixer chips (different scales). Dark areas are the background seen through the quartz substrate. The suspended stripline waveguide probe (to the left) connects through a broadband transition to a capacitively loaded CPW. The dc/IF bonding pad is at the right. The dotted red lines indicate the position of the broad-walls of the waveguide. Figs. 9 and 10 show more details.

B. Circuit Design of the Elemental DSB Mixer Chips

Substrate materials compatible with the niobium SIS mixer fabrication process are silicon ($\epsilon_r = 12$) and fused quartz ($\epsilon_r = 3.8$). A fused quartz substrate was chosen for its relatively low dielectric constant which allows a wider substrate to be used without the possibility of exciting higher modes in the substrate channel. The mixer chips were fabricated on 50-mm-diameter wafers at the University of Virginia Microfabrication Laboratory between 2003 and 2008 using their then standard Nb/Al-Ox/Nb trilayer junction process [27].

The mixer circuit is coupled to a full-height input waveguide by a rectangular suspended-stripline probe with a return loss > 21 dB over the full waveguide band [28]. Photographs of the Band-3 and -6 mixer chips are shown in Fig. 8. The suspended stripline waveguide probe (at the left) is connected to the mixer circuit through a broadband transition to capacitively loaded coplanar waveguide [13], a CPW with periodic capacitive loading by ground bridges to give a characteristic impedance of 50 ohms on the quartz substrate while suppressing slot modes in the ground plane. The Nb ground plane and wiring layer are on the same side of the quartz substrate, separated by a 285-nm SiOx dielectric layer with $\epsilon_r = 4.2$. To achieve the range of characteristic impedances required for the RF matching circuit with this thin dielectric layer without requiring impractical dimensional tolerances, CPW is used for higher impedance ($\sim 90 \Omega$) sections and microstrip is used for lower impedances ($\sim 7 \Omega$); capacitively loaded CPW is used for intermediate impedance values. A thicker 570-nm SiOx layer is used under the CPW loading capacitors.

To maximize the RF coupling bandwidth between a resistive source and a series array of capacitive devices, such as SIS junctions, the series inductance of the array must be optimized as described in [20]. If the array inductance is small it may be desirable to increase it, as in the case of the Band-3 mixer described here, but too large an array inductance can severely limit the bandwidth.

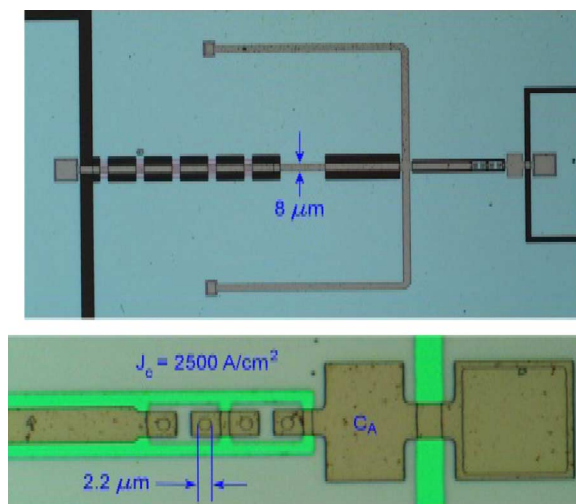


Fig. 9. RF circuit of the Band-3 SIS mixer chip [8]. Dark or green regions are the background seen through the quartz substrate and contain no metallization. The lower Nb base-electrode metallization is light gray; the darker (brown) conductors are the Nb wiring layer.

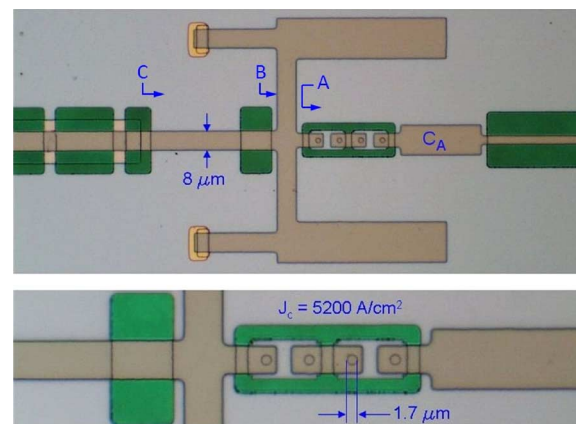


Fig. 10. RF circuit of the Band-6 SIS mixer chip [38]. Dark or green regions are the background seen through the quartz substrate and contain no metallization. The lower Nb base-electrode metallization is light gray; the darker (brown) conductors are the Nb wiring layer.

A wide IF bandwidth requires the inductance in the IF circuit of the mixer to be minimized. This is achieved using the configuration described in [7] in which the IF current bypasses the waveguide coupling circuit.

The on-chip RF circuits of the Band-3 and -6 mixers are similar, as shown in Figs. 9 and 10. At the left is the capacitively loaded CPW line from the suspended stripline waveguide probe, connected to a parallel pair of broadbanding resonators, the series array of four SIS junctions, a tuning capacitor C_A , and, at the right, the end of the RF choke. For Band 3, the two resonators are quarter-wave microstrip short-circuit stubs, and for Band 6 each resonator consists of a short inductive microstrip stub connected in parallel with a short capacitive stub, which approximates a parallel LC resonator connected at the input of the SIS array. For both mixers, the resonators are tuned near the middle of the RF band to give the desired frequency response.

For brevity, only the operation of the Band-6 circuit will be described in detail here. The equivalent circuit of the mixer chip

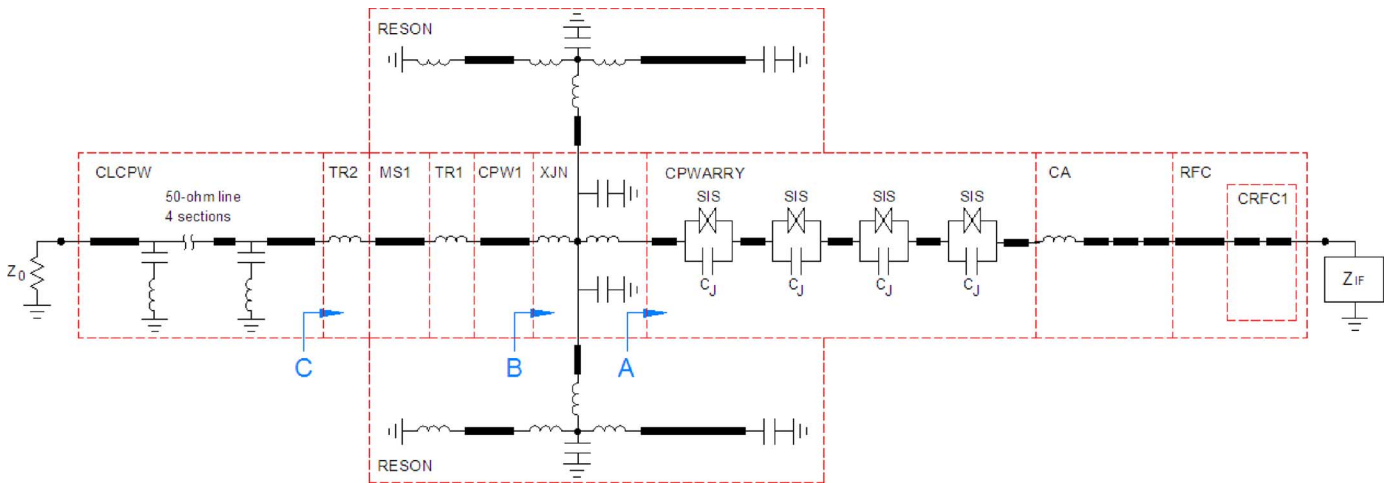


Fig. 11. Equivalent circuit of the Band-3 and -6 SIS mixers. At the left, the circuit connects to the suspended stripline waveguide probe. The two broadbanding resonators are at the top and bottom of the diagram (resonator details are for Band 6). The IF and dc connection is at the right. Locations A, B, and C correspond to those shown in Fig. 10. CLCPW is the capacitively loaded CPW input line; TR2 and TR1 represent fringing inductances at the transitions between elements; MS1 is a microstrip line; CPW1 a CPW line; XJN is the junction of CPW1, the two resonators RESON, and the series array CPWARRAY of four SIS junctions; CA is a short section of low-impedance microstrip; and RFC is the RF choke in the IF/dc circuit.

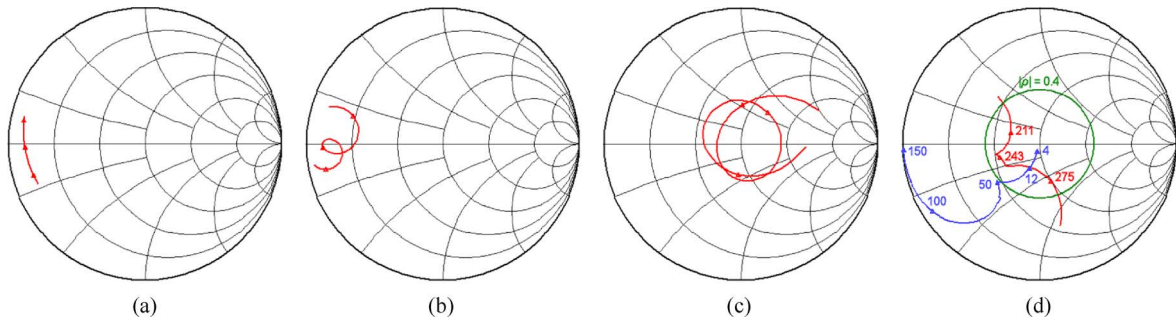


Fig. 12. Smith chart plots of the impedance at various points through the circuit of Fig. 11. (a)–(c) Reflection coefficients looking to the right at planes A, B, and C, respectively, normalized to $Z_0 = 50 \Omega$, over 200–280 GHz. (d) Reflection coefficient seen by the SIS junctions over the same RF band (red), and from 4 to 150 GHz (blue), normalized to R_{opt} . The circle $|\rho| = 0.4$ (green) is included for [20].

is shown in Fig. 11. The different sections outlined by the dashed boxes were individually modeled using Sonnet *em* [29]. Because the conductors on the chip are all superconducting Nb, they can be considered lossless, but they have a frequency-independent surface inductance due to the London penetration depth (similar to the skin depth in normal conductors) which is included explicitly in the Sonnet simulations. The surface inductance is not of consequence in the CPW sections, but it must be taken into account in the microstrip sections in which the dielectric thickness is not much greater than the penetration depth (~ 85 nm in Nb). The appropriate value of surface inductance for thick and thin superconducting transmission lines is discussed in [30]. The elements in Fig. 11 were optimized using the microwave circuit simulator MMICADv2 [31]. Fig. 12(a)–(c) shows the impedance looking into the array of SIS junctions from locations A, B, and C, as indicated in Figs. 10 and 11, when the SIS junctions have an impedance equal to their optimum source impedance. The embedding impedance Z_e seen by the four junctions is shown in Fig. 12(d). Over the RF band, Z_e lies well within the desirable region $|\rho| \leq 0.4$. (In fact, Z_e is slightly different for each of the four junctions but the differences are too small to see in this diagram.) The concept of

embedding impedance needs some clarification for the case of multiple SIS junctions. It is the impedance which would be seen by a test signal source connected in place of one of the junctions when identical sources are connected in place of the other junctions. Also shown in Fig. 12(d) is the impedance seen by the junctions over the IF band, extended to 150 GHz, with the IF port of the mixer substrate terminated in $Z_{\text{IF}} = 50 \Omega$. It is evident that the impedance is well behaved and less than 50Ω over the extended IF band. This is important for maintaining stability of the mixer as explained in [20]. (The acceptable behavior of the embedding impedance over this extended IF band, with the mixer chip terminated in 50Ω , ensures only that the chip itself is free of undesired characteristics. The structure between the chip and the IF amplifier is substantially overmoded at 150 GHz and in principle could introduce undesired resonances in the upper part of the extended IF band, although this appears not to be significant in the present design.)

C. IF Inter-Stage Network

The IF output of the Band-3 mixer chip is connected by a pair of bond wires to a 50- Ω SMA connector and then by a cable to

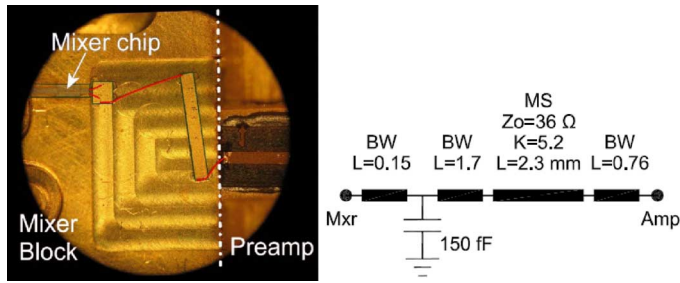


Fig. 13. Details of the IF interstage network used in the Band-6 mixer. BW = bond-wire (red), MS = microstrip. Lengths L are in mm.

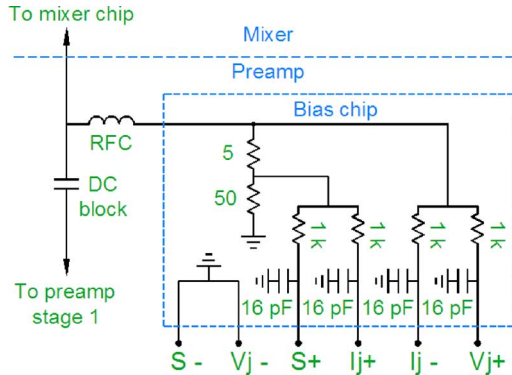


Fig. 14. Details of the SIS mixer bias circuit. The $5\text{-}\Omega$ resistor monitors the mixer current, and the 1-k resistors and 16-pF capacitors provide ESD protection.

the IF isolator and amplifier. For the Band-6 mixer, no IF isolator is used and a simple phasing network between the mixer chip and the preamplifier input allows the mixer-preamp performance to be optimized for RF input match, signal-to-image conversion, and gain flatness within the IF band. Fig. 13 shows the details of the interstage network.

D. Mixer Bias Circuit

The ALMA SIS mixers use a six-wire bias circuit based on the one described in [32], as shown in Fig. 14. The bias voltage for the mixer is developed across the $50\text{-}\Omega$ resistor by a current source connected to S^+ and S^- , and the junction current is monitored by the $5\text{-}\Omega$ resistor at $I_j^+ - I_j^-$. A servo loop controls the current source to maintain a constant bias voltage at the mixer ($V_j^+ - V_j^-$). The presence of the $50\text{-}\Omega$ resistor ensures that the bias circuit has a low impedance from dc to well beyond the bandwidth of the servo loop, which is desirable for maintaining the stability of the bias circuit.

For both Bands 3 and 6, the bias circuit is built into the IF preamplifier and connects to the mixers through the IF circuit, and in the case of Band 3, through the IF isolator whose center conductor is isolated from ground [33]. For Band 3, the bias circuit uses discrete components and for Band 6 it is on a custom silicon chip [34].

E. Magnetic Circuit

Conduction in SIS junctions is by two types of carrier, superconducting Cooper pairs (of electrons), and quasi-particles

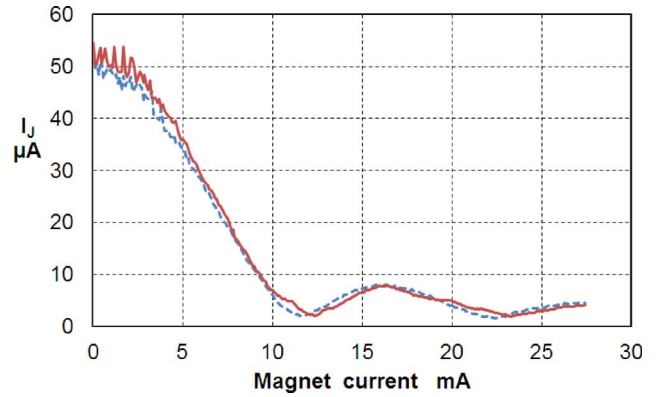


Fig. 15. Plot of critical current versus magnet current for the two mixer chips in a typical Band-6 mixer. The normal operating point is at the second minimum.

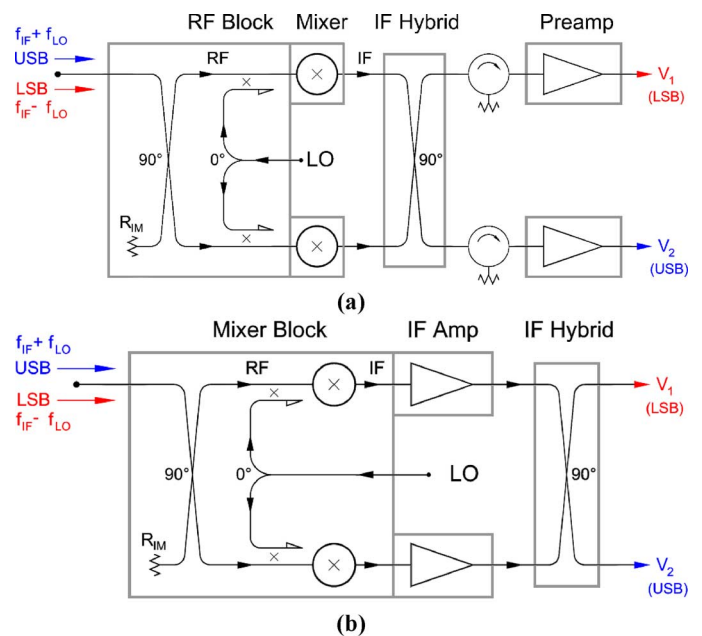


Fig. 16. Sideband-separating mixer-preamplifier configurations. (a) For Band 3, the elemental DSB mixer chips are in separate modules attached to the RF block containing the RF hybrid and LO couplers. The IF hybrid follows the mixers and the IF amplifiers are preceded by isolators. (b) For Band 6, the IF preamplifiers are attached directly to the block containing the elemental DSB mixers, RF hybrid, and LO couplers, and the IF hybrid follows the amplifiers. IF isolators are not used. For both bands, R_{IM} is the image termination on the fourth port of the RF hybrid.

(single electrons from broken Cooper pairs) [25]. It is the quasi-particle current–voltage characteristic, whose strong non-linearity is seen in Figs. 6 and 7, which is used in SIS mixers. The Cooper pair current, or Josephson current, has a complex nonlinearity which can be characterized as a voltage-controlled oscillator [35] connected in parallel with the quasi-particle circuit. The presence of the Josephson effect in an SIS mixer can seriously degrade the noise temperature [36], but fortunately the Josephson current can be suppressed by a magnetic field applied between the electrodes of an SIS junction, and many SIS mixers include a solenoid with pole pieces which concentrate a magnetic field in the vicinity of the junctions. For the Band-6 mixers, a field of ~ 180 Gauss is produced by a current of ~ 26 mA in a coil of 5500 turns of superconducting wire. The magnet

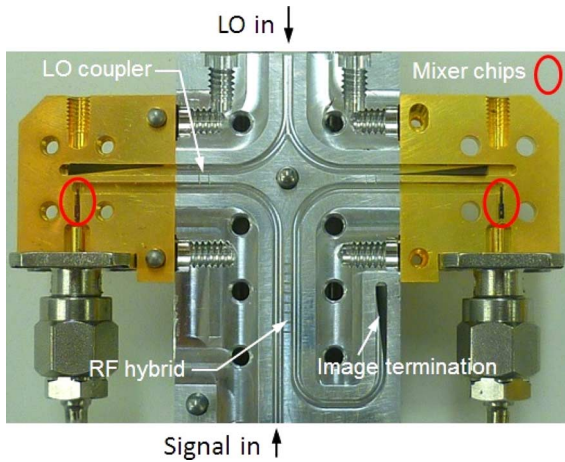


Fig. 17. One half of the Band-3 mixer assembly. The central aluminum block contains the waveguide branch-line hybrid, LO power divider, LO couplers, and the image termination. The DSB mixer chips and LO terminations are in the left and right modules. The RF signal enters through the lower waveguide and the LO power through the upper waveguide.

circuit is made of Consumet magnet iron and is similar to the one described in [37]. Fig. 15 shows the dependence of the critical current on magnet current for the two SIS chips in a typical Band-6 mixer. In Band 3, a magnetic field is not currently used. The general experience in radio astronomy is that a magnet is not required for lower frequency SIS receivers. However, it has recently been found that, on ALMA, with its very stringent stability requirements, the Band-3 receivers do need a magnetic field for the most sensitive observations; this is planned as a future upgrade.

III. SIDEBAND-SEPARATING MIXERS

For Band 6 (211–275 GHz), the complete sideband-separating mixer [38] consists of an E-plane split-block waveguide assembly containing the elemental DSB mixer chips, branch-line waveguide hybrid [15], LO couplers [39], and 4-K image termination [40], attached directly to two 4–12-GHz IF preamplifiers [41] without IF isolators. For Band 3 (84–116 GHz), the elemental DSB mixer chips [8] are mounted in separate modules which are attached to a split-block waveguide assembly containing the branch-line waveguide hybrid and LO couplers [42] and external 4–8-GHz cryogenic IF isolators are used. Fig. 16(a) and (b) shows the configuration of the Band-3 and -6 mixers. An important component is the load R_{IM} on the fourth port of the RF hybrid [40]. This is the image termination, whose thermal noise in the upper and lower sidebands is downconverted to, respectively, the lower and upper sideband IF outputs of the sideband-separating mixer. By including R_{IM} in the cryogenic mixer assembly, its contribution to the mixer noise temperature is minimized.

The amplitude and phase balance of the cryogenic IF quadrature hybrid is important in ensuring good image rejection. The initial Band-6 receivers used commercial IF hybrids which were found to be inconsistent at 4 K. Subsequently a cryogenic hybrid based on a design by Malo and Gallego [43] was implemented by MAC Technology [44] and has given excellent results.

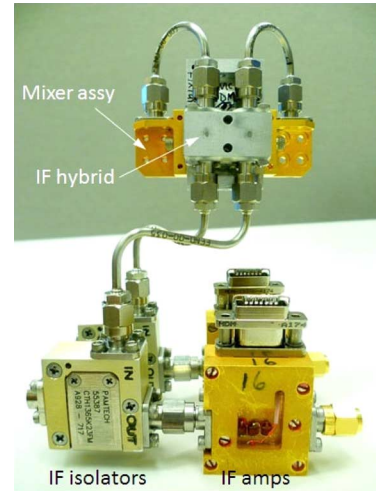


Fig. 18. Band-3 front-end. The mixer assembly is at the top, behind the IF quadrature hybrid (aluminum). Below them are the IF isolators and preamplifiers.

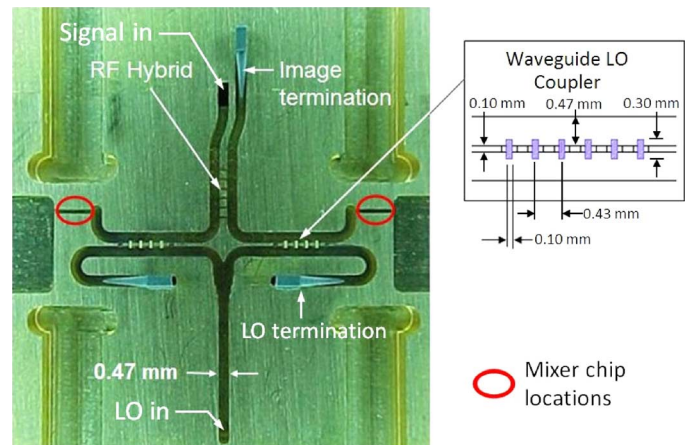


Fig. 19. One half of the Band-6 mixer, including the waveguide branch-line hybrid, LO power divider and LO couplers with their fourth-port terminations, the DSB mixer chips, and the image termination. The RF signal enters through a waveguide perpendicular to the page in the upper part of the circuit [28] and the LO enters through a similar waveguide in the lower part of the mating half of the block.

For the Band-6 mixers, the IF preamplifiers are selected to have well matched gain and phase vs frequency at 4 K; this is necessary because the preamplifiers are located in the signal path before separation of the sidebands in the IF hybrid. The amplifiers were selected in pairs with gain and phase differences which would result in no less than 17-dB image rejection (see [15, Fig. 2]). For the Band-3 configuration, gain and phase matching of the preamps is not necessary because they are after the IF hybrid, but isolators are necessary to ensure that reflection of the IF signal at the amplifier inputs does not degrade the image rejection.

Fig. 17 shows the Band-3 mixer assembly with the lids removed, and Fig. 18 shows the Band-3 mixer connected to the IF hybrid, IF isolators, and preamplifiers. Fig. 19 shows the Band-6 mixer with the lid removed. It includes the branch-line waveguide RF hybrid, LO power divider and LO couplers with their fourth-port terminations, and the image termination. Fig. 20

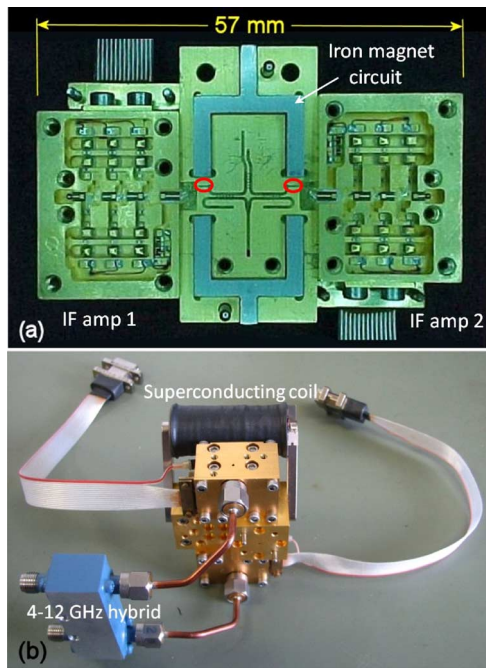


Fig. 20. Band-6 mixer assembly. (a) With preamps attached, lids removed. The red ellipses indicate the location of the mixer chips. (b) With IF hybrid and magnet circuit.

shows the Band-6 mixer-preamp assembly (a) with the mixer and amplifier lids removed and (b) with the IF hybrid and magnet circuit in place.

IV. MEASUREMENT OF THE MIXERS

The noise temperature and gain of the mixer-preamps were measured using room-temperature and liquid nitrogen loads placed in front of the vacuum window of a test Dewar. For sideband-separating receivers, it is possible to measure the image rejection accurately, without knowing the power level of the RF test signal, by using the procedure described in [45]. Knowing the image rejection, the SSB noise temperatures can be corrected to remove the contribution to the Y-factor from the image band (which acts to increase the Y-factor, thereby reducing the apparent SSB noise temperature). The mixer-preamp gain is determined from the change in IF output power of the test receiver when RF hot and cold loads with known temperatures are placed in front of the receiver, knowing the gain and noise temperature of the subsequent IF stages. For Band 3, the measured mixer-preamp SSB noise temperature, gain, and image rejection are shown in Fig. 21 for superior and typical mixer-preamps. The LO was tuned in 4-GHz increments, and at each LO frequency the mixer-preamp was measured across the IF band in 100-MHz steps. Similar results for Band 6 are shown in Fig. 22. The results in Figs. 21 and 22 include the contributions of the RF (quasi-optical) vacuum window, infrared filter, and cold optics in the Dewar and are corrected for the finite image rejection to give true SSB numbers.

V. DISCUSSION

The receiver noise temperatures shown in Figs. 21 and 22 are consistent with the analysis of the elemental DSB SIS mixers in Section II-A which is based on the measured $I(V)$ character-

TABLE I
BAND-3 NOISE ANALYSIS

BAND 3	L dB	T _{PHYS} K	T _N K	T _R K	Ref.
Window	0.02	298	1.4	27.0	[48]
I/R filter	0.01	77	0.2	25.5	
Horn + OMT	0.37	4.2	0.4	25.3	[49][50]
Waveguide	0.03	4.2	0.0	22.8	[49][50]
Image term. noise		4.2	4.6	22.6	
LO Coupled noise			3.0	18.0	
Mixer-Preamp			15.0	15.0	

TABLE II
BAND-6 NOISE ANALYSIS

BAND 6	L dB	T _{PHYS} K	T _N K	T _R K	Ref.
Window	0.04	298	2.8	44.4	[51]
I/R filter	0.02	77	0.4	41.2	
Horn + OMT	0.20	4.2	0.3	40.7	[49][50]
Waveguide	0.30	4.2	0.5	38.6	[49][50]
Image term. noise		4.2	6.6	35.6	
LO Coupled noise			3.0	29.0	
Mixer-Preamp			15.0	26.0	

istics. The approximate noise contributions of the components ahead of the Band-3 and -6 mixer-preamps are shown in Tables I and II, calculated from the loss and physical temperature of each component. In the tables, the “T_N” column gives the effective input noise temperature $T_N = T_{CW}(L - 1)$ of each component using the Callen and Welton radiation law (which is equivalent to the Planck law plus the zero-point fluctuation noise [46], [47]). The “T_R” column shows the receiver noise temperature which would be measured looking into the receiver at each component. The “image termination noise” is the contribution of the image termination resistor, R_{IM} in Figs. 1 and 16, also calculated using the Callen and Welton law (which is why the component T_N is greater than the physical temperature). The “LO coupled noise” is the sideband noise from the LO, and depends on the LO source.

During testing of some Band-6 mixer-preamps, a very weak spurious signal was occasionally detected near 230 GHz. This was eventually traced to oscillation at 230 GHz within one of the discrete InP HFET chips in the IF amplifiers. The 230-GHz signal leaked through the RF choke into the SIS mixer where it was down-converted to IF. This was cured by adjusting the drain bias or replacing the transistor.

At millimeter wavelengths, calibration of the individual sideband sensitivities of a DSB receiver is difficult and usually impractical on the telescope. The upper and lower sideband sensitivities can differ by several decibels, particularly at the extremes of the tuning range. A benefit of the sideband-separating receiver is the simplicity of calibrating the sensitivities of the upper and lower sidebands using the procedure mentioned in Section IV.

A problem well known to radio astronomers is the presence of variations, or ripples, in the baseline of a measured spectrum which make it difficult to identify weak spectral features in the source. These baseline ripples are caused by the standing wave between an imperfectly matched receiver input and the telescope optics. The standing wave pattern changes slightly during a long integration due to dimensional changes in the

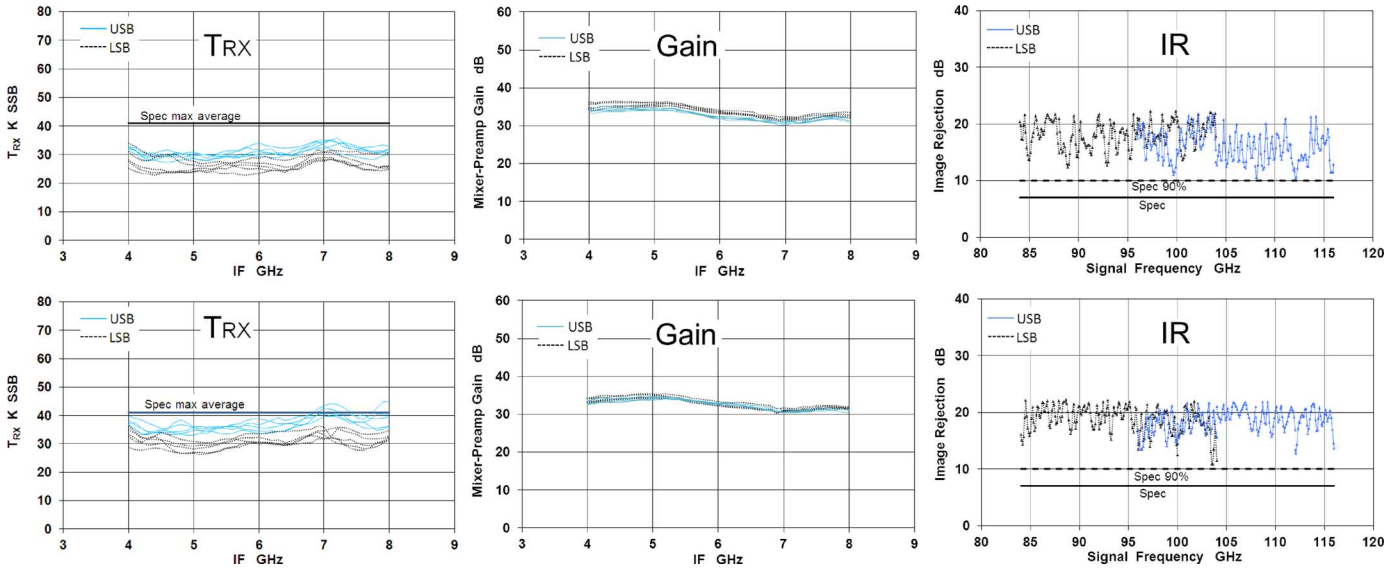


Fig. 21. Measured SSB noise temperature, gain, and image rejection, for two Band-3 mixer-preamps. The upper and lower plots are for superior and typical units, respectively. The LO was stepped from 92 to 108 GHz in 4-GHz increments at each of which the mixers were measured from 4 to 8 GHz IF in 100-MHz increments. The line labeled “Spec max average” indicates the specified maximum noise temperature measured in a 4-GHz IF bandwidth and averaged over both upper and lower sidebands in both polarization channels.

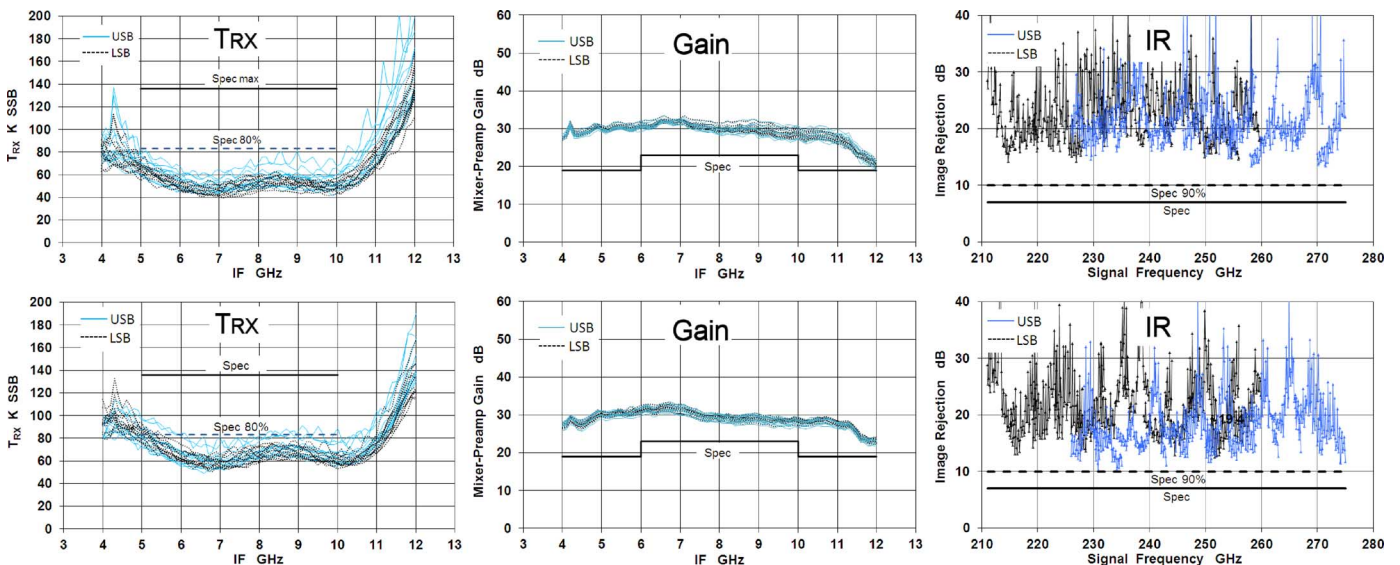


Fig. 22. Measured SSB noise temperature, gain, and image rejection, for two Band-6 mixer-preamps. The upper and lower plots are for superior and typical units, respectively. The LO was stepped from 221 to 265 GHz in 4-GHz increments at each of which the mixers were measured from 4 to 12 GHz IF in 100-MHz increments. The line labeled “Spec max” is the specified maximum noise temperature, and the line “Spec 80%” is the specified maximum noise temperature over at least 80% of the band.

telescope. This problem can be particularly severe in a receiver with a poorly matched input, which is characteristic of most DSB mixer receivers. It has been noted [52] that sideband-separating mixers described here have excellent spectral baseline stability compared with the usual DSB receivers. We believe this is a result of the RF quadrature hybrid at the input of the sideband-separating receiver. As in the case of a balanced amplifier [53], the circuit is inherently well matched, with the return loss limited only by the phase and amplitude imbalance of the hybrid and the difference between the RF input impedances of the elemental DSB mixers.

VI. CONCLUSION

A total of 146 Band-3 sideband-separating mixers have been built and tested at NRC-Canada and delivered to ALMA in 73 dual-polarized cartridges. During the 2003–2009 production period, cryogenic tests were done on 515 Band-3 DSB mixer modules and 293 sideband-separating mixer assemblies. For Band 6, a total of 158 sideband-separating mixer-preamps have been built and tested at NRAO and delivered to ALMA, 146 in 73 dual-polarized cartridges and 12 spare units. During the 2008–2012 production period, cryogenic tests were done on 780 Band-6 mixer-preamps.

The SIS mixer chips for Bands 3 and 6 were all made at the University of Virginia Microfabrication Laboratory.

As ALMA nears completion, six of the ten bands use sideband-separating SIS mixers similar in varying degrees to those described here. The Band-4, -5, -7, and -8 mixers are described in [54]–[57]. Multibeam sideband-separating SIS receivers have also been developed for use on single-dish telescopes, as described in [58] and [59].

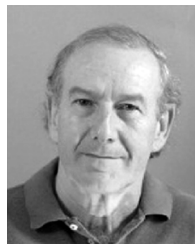
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