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The front-end electronics for the COMPASS MWPCs

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Abstract

In the COMPASS experiment 34 planes of MWPCs for about 26,000 readout channels of MWPCs are used. The very high rate of the muon and hadron beams, and the consequently high trigger rate, requires the use of a front-end electronics with new conceptual design, to have a fast DAQ with a minimum dead-time.

Its scheme will be described, together with some results of the performances achieved.

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1. Introduction

The detector of the COMPASS experiment is a multipurpose fixed target spectrometer [1] at the extracted beam of the CERN SPS accelerator complex. To provide a larger momentum and angular acceptance the whole set-up was subdivided into two spectrometer blocks. The Small Angle Spectrometer (SAS) has been designed to reconstruct particles with momentum $p > 10 \text{ GeV}/c$, while the Large Angle Spectrometer (LAS) provides acceptance for particles with momentum $p < 50 \text{ GeV}/c$. The tracking capability of the SAS is defined by the MWPCs system, described here.

2. The MWPC front-end electronics

The functional block diagram of the read-out electronics, developed in Torino, is shown in Fig. 1. It consists of a printed circuit board, called "Mother Board", fixed to the chamber frame, and the front-end board where the signals of the chamber are discriminated and digitized. The data is transferred through a fast serial link to a VME board, called "Catch" [2], developed at the university of Freiburg, which performs the task of the local event builder.

The front-end electronic is organized in triplets of cards that can read 64 channels per board. Each card houses the MAD4 preamplifier/discriminator chips [3,4], developed by the INFN section of Padua for the read out of the muon detectors of

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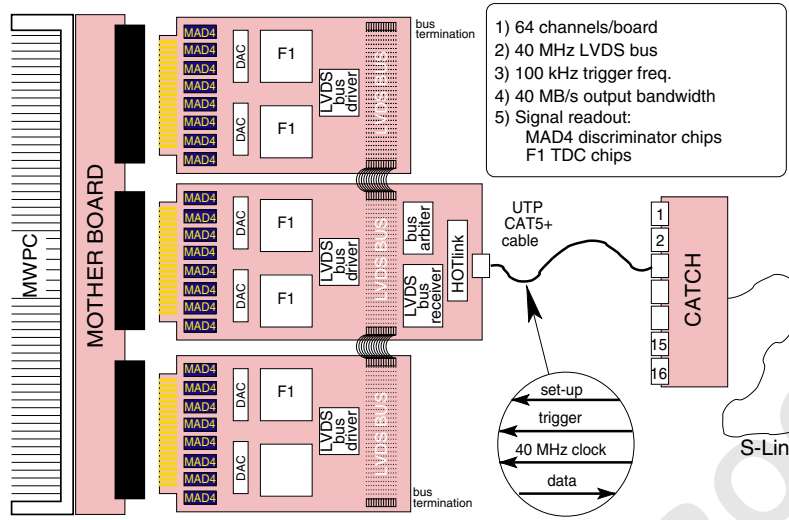
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Fig. 1. Scheme of the front-end card housing the preamplifier/discriminators, digitizing electronics, and interfaces.

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the CMS barrel, the digitalizing chips [5], and the threshold DACs. The three cards are connected together by a fast LVDS bus, which distributes the 38.8 MHz clock, trigger and synchronization signals. Production cost has been reduced by housing both analog and digital parts on the same PCB. Moreover central and side boards share a common design, except for the bus arbiter and the parallel-to-serial converter chips that allows for fast transmission of significant data to the “Catch” module, which are housed only on the central board. For noise prevention all the voltages required by the card components are regulated on-board and the ground separation technology between analog and digital grounds is used.

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A test system has also been developed, capable of injecting into each wire a charge pulse similar to the signal of a minimum ionizing particle crossing the chamber. The system is composed of a VME control board and pulser boxes fixed to the frame of each MWPC detector. It allows for tests and calibration of the front-end cards and of the read-out system, including the input connectors.

3. Front-end calibration and performances

To obtain the linearity curve of Fig. 2 the threshold of a single channel has been changed

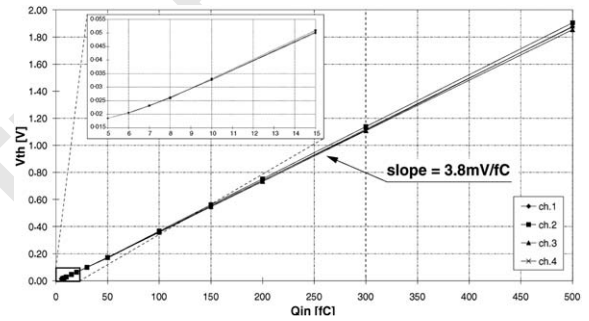
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Fig. 2. Threshold/input charge calibration curve for one MAD4 chip.

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together with the amplitude of the input signal (5–500 fC) to get a detection efficiency of the order of 50%. The results, obtained automatically for all the 26,000 channels, have been stored in a database used at the initialization time.

With the electronics connected to the MWPC, the noise has been measured, with a threshold of 4 fC, by triggering with a pulse generator at 100 Hz. The counting rate/trigger in the whole detector was found to be of the order of 2%. Negligible crosstalk has been found at 5 fC of threshold when injecting in a single wire a signal of 30 fC.

The overall efficiency of one MWPC plane, determined with a $^{90}\text{Sr}\beta^-$ source and with three

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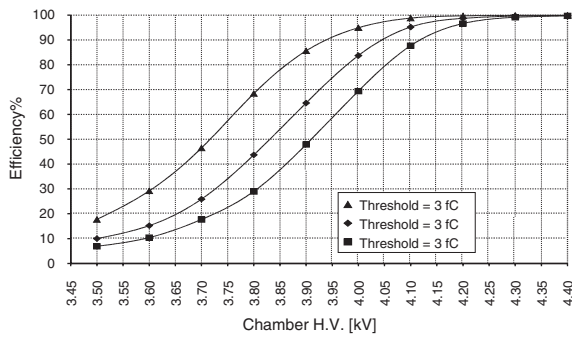


Fig. 3. Detection efficiency of one MWPC plane, measured with a $^{90}\text{Sr}\beta^-$ source for three threshold values.

different thresholds, is shown in Fig. 3. The detector is currently operated with a field voltage of 4.2 kV and a threshold of 4 fC.

4. Conclusions

The design of a conceptually new front-end electronics for the readout of the COMPASS

MWPC detectors has been described. The electronics fulfills all the demanding requirements of the COMPASS experiment in terms of low noise, low dead-time and high rate capabilities.

The installation of the COMPASS MWPC detectors has been completed in spring 2001. Two years of production running using the muon beam with intensities up to $10^8 \mu\text{m/s}$ have been completed so far. During this period the detectors have shown very good stability, providing a tracking efficiency above 96% for all planes.

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