



Optimized PID Controller of DC-DC Buck Converter based on Archimedes Optimization Algorithm

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ABSTRACT

This research assesses the suitability of the Archimedes Optimization Algorithm (AOA) as a metaheuristic technique to fine-tune a PID controller in a closed-loop DC-DC buck converter. The converter's core function is to regulate output voltage, ensuring stability despite load fluctuations and input voltage changes. The operational effectiveness of the converter hinges significantly on the gain settings of the PID controller and determining the optimal gain setting for the PID controller is a nontrivial task. For robust performance, the PID controller necessitates optimal gain settings, attainable through metaheuristic optimization. The algorithm aids in identifying ideal proportional, integral, and derivative gains based on varying load conditions. Leveraging the metaheuristic algorithm, the PID controller is optimized to minimize voltage errors, reduce overshoot, and enhance response time. The proposed PID controller, optimized using AOA, is contrasted with PID controllers tuned via alternative algorithms including the hybrid Nelder-Mead method (AEONM), artificial ecosystembased optimization (AEO), differential evolution (DE), and particle swarm optimizer (PSO). Performance evaluation involves injecting a voltage disturbance into the buck converter with load changes of up to 20%. Results demonstrate the superiority of the AOA-optimized PID controller in voltage recovery. It demonstrates a faster response time and outstanding voltage regulation performance, while also exhibiting minimal performance degradation during load changes. This study concludes that the AOA optimization algorithm surpasses other methods in tuning the PID controller for closed-loop DC-DC buck converters.

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1. Introduction

The DC-DC buck converter stands as a fundamental component in diverse electronic systems, offering voltage stabilization for equipment [1]. Its applications span across consumer electronics, telecommunications, electric vehicles, renewable energy systems, and more, where it serves as a power supply, battery charge controller, and voltage level converter [2]–[6]. This versatile role highlights its significance in powering electronics across industries.



In the realm of control strategies for buck converters, several approaches have been explored. However, due to its simplicity, adaptability, and effectiveness in handling nonlinearities, the Proportional-Integral-Derivative (PID) controller has emerged as a preferred choice [7]. While existing studies have focused on various control methodologies [8]–[18], we find that the PID controller's straightforward structure offers a compelling solution for managing the buck converter's dynamic behavior.

The previous study about the disturbance analysis on the buck converter focused on input voltage variability and the injection of disturbance voltages [1], [11], [19]–[21]. In the presence of voltage fluctuations or disturbances, the primary objective of a buck converter is to sustain a stable output voltage without being affected by the disturbance. However, in practical scenarios, buck converters are susceptible to disturbances, resulting in a temporary deviation of the output voltage from its desired level. This behavior arises due to the inherent characteristics of the feedback loop, where the voltage control is based on the output voltage error. Consequently, the disturbance-induced voltage error occurs initially, and then the controller takes corrective action to restore the output voltage to its intended level.

In order to address the effects of voltage fluctuations and disturbances on the load, it is essential to perform precise tuning of the closed-loop PID controller to achieve optimal performance. However, determining the optimal gain settings for the PID controller can be challenging due to the nonlinearity of the buck converter [22]. Various methods have been employed to identify the optimal PID gain settings, including the Ziegler-Nichols method [23]–[25], Cohen-Coon method [26]–[28], trial and error method [25], [29], and auto-tuning method [30]. While the Ziegler-Nichols and Cohen-Coon methods are more suitable for linear systems as they rely on mathematical formulas developed for such systems. The auto-tuning method offers a greater potential for achieving improved system performance in nonlinear systems. This method involves iterative testing of different PID gains until a satisfactory outcome is obtained, with the aid of an algorithm to determine the optimal PID gains.

Given the diverse range of loads that the buck converter is employed for an efficient algorithm is necessary to optimize the PID controller based on the specific load conditions. Consequently, the PID controller settings will vary depending on the load and buck converter. The PID controller consists of three gains: proportional gain, integral gain, and derivative gain, each of which can be assigned different values. Previous studies have utilized various algorithms, such as Genetic Algorithm [31], modified Lévy Flight Distribution Algorithm [32], Whale Optimization Algorithm [33], Firefly Algorithm [34], [35], Hybrid Firefly and Particle Swarm Optimization Algorithm [36], Chaotic flower pollination algorithm [37], Hybrid Whale Optimization Algorithm [38], Bat Algorithm [39], Sine Cosine Algorithm [40], Hybrid Nelder-Mead method [20], Antlion Optimization Algorithm [41], Cohort Intelligent Algorithm [42], Coevolving-AMOSA Algorithm [43] and improved Marine Predators Algorithm [44] to determine the optimal gain settings of PID controller.

Our study is motivated by the need for an effective and adaptable optimization technique for PID controllers in buck converters. AOA, known for its success in diverse optimization problems [45]–[49], holds promise in this context. By applying AOA, we seek to fine-tune the PID controller and compare its performance with existing algorithms like AEONM, AEO, PSO, and DE [20]. The benchmarks include comprehensive performance evaluations encompassing response analysis, statistical assessments, time response specifications, frequency response analysis, disturbance rejection, and parameter uncertainty analysis. The research contribution is:

- i. Evaluating the performance of a PID controller tuned by AOA in mitigating voltage disturbances in a buck converter, in comparison to other existing algorithms such as AEONM, AEO, PSO, and DE, as discussed in [20].
- ii. Examining the PID controller's robustness to a load change of 20%.
- iii. Frequency analysis on the PID controller

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2. Method

2.1. PID Controller of DC-DC Buck Converter

The DC-DC buck converter is a power electronics device that converts a higher voltage input to a lower voltage output. The operation of the converter involves switching on and off a power semiconductor device, such as a MOSFET or an IGBT, at a high frequency to control the output voltage. The converter was constructed using various components such as switch (S), diode (D), inductor (L), and capacitor (C). This study considers the resistor (R) as the load in the converter. The schematic diagram of the DC-DC buck converter utilized in the study can be seen in Fig. 1, where V_{in} is the input voltage and V_{out} is the load's voltage.

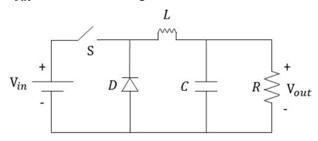


Fig. 1. DC-DC buck converter schematic diagram

In this study, each parameter employed in the converter is the same value in the paper [20]. Table 1 displays each parameter's value.

Table 1. DC-DC buck converter parameters value

Parameter	Value
V _{in}	36v
V_{out}	3v
R	6Ω
L	1mH
С	$100 \mu F$

To design a closed-loop DC-DC buck converter, it is necessary to first derive the DC-DC buck converter's transfer function. Because the converter exhibits nonlinear behavior, obtaining its transfer function is a difficult task. Hence, this study adopted the formula to calculate the gain of the DC-DC buck converter (G_{bc}) from paper [20], which is demonstrated in (1).

$$G_{bc} = V_{in} \times \frac{1/LC}{s^2 + s/RC + 1/RC}$$
(1)

By substituting the values from Table 1 into (1), the gain of the DC-DC converter can be determined and shown in (2).

$$G_{bc} = \frac{216000}{0.0006s^2 + s + 6000} \tag{2}$$

There are three control parameters in the gain equation of the PID controller, namely proportional gain (K_p) , integral gain (K_i) , and derivative gain (K_d) . The optimum value of those three parameters will be found using some different optimization algorithms to achieve the best possible response time with minimum voltage error and voltage overshoot. Equation (3) presents the transfer function of the PID controller and its corresponding K_p , K_i , and K_d gains.

$$G_{controller} = K_p + \frac{K_i}{s} + K_d s \tag{3}$$

To analyze the transient response of the closed-loop PID-controlled DC-DC buck converter, it is necessary to obtain the closed-loop system transfer function. The closed-loop block diagram of the PID controller for the DC-DC buck converter, which was taken from [20] is shown in Fig. 2.

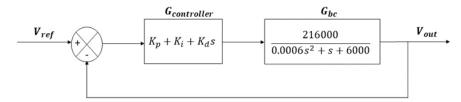


Fig. 2. Feedback loop of PID-controlled DC-DC buck converter

Since the model is in a feedback form, the closed-loop transfer function $TF_{closed-loop}$ can easily solve by using (4).

$$TF_{closed-loop} = \frac{G_{bc} \times G_{controller}}{1 + G_{bc} \times G_{controller}}$$
(4)

After obtaining the closed-loop transfer function formula, different gain values of the PID controller can be applied to the formula for step response analysis. The important parameters in disturbance injection analysis such as rise period, settling period, voltage overshoot, and steady-state error can be found through the closed-loop transfer function. This analysis is useful for evaluating the performance of the closed-loop system under different PID controller parameter values.

2.2. Self-Tuning PID Controller of DC-DC Buck Converter

In this section, the process of applying the Archimedes Optimization Algorithm (AOA) to finetune the DC-DC buck converter's PID controller will be discussed. The mathematical structure and the step-by-step guide for the implementation will be presented. Hashim et al. proposed an alternative algorithm called the Archimedes optimization algorithm (AOA) for solving the Congress on Evolutionary Computation (CEC), engineering, and standard optimization problem [50]. The AOA is a metaheuristic method based on Archimedes' principle, which explains the relationship between a fluid-immersed object and the buoyant force applied to it. This algorithm has been shown to outperform other popular metaheuristic methods such as PSO, GA, SCA, and EO in multiple benchmark tests due to its simple structure and a smaller number of coefficients. The AOA follows a step-by-step procedure, which is outlined below:

Step 1-Object initialization: All objects' positions are initialized using (5).

$$x_i = lb_i + rand \times (ub_i - lb_i) \tag{5}$$

For i = 1, 2, ..., n, where x_i is the *i*th object's position in a population with *n* number of objects. The upper and lower bounds of search space are represented by the symbols ub_i and lb_i , respectively. The *i*th objects' density (*den*), volume (*vol*), and acceleration (*acc*) are initialized using (6).

$$den_{i} = rand,$$

$$vol_{i} = rand,$$

$$acc_{i} = lb_{i} + rand \times (ub_{i} - lb_{i})$$
(6)

Where rand is a random vector of a number between [0, 1].

Step 2-*Update object's volume and density*: The updated density and volume in next iteration are given by (7).

$$den_i^{k+1} = den_i^k + rand \times (den_{best} - den_i^k)$$
$$vol_i^{k+1} = vol_i^k + rand \times (vol_{best} - vol_i^k)$$
(7)

Where k is the iteration number, den_{best} and vol_{best} are the best object's density, and volume found so far, and *rand* is another random vector of a number between [0, 1].

Step 3-Calculate transfer operator and density factor: The objects attempt to attain an equilibrium condition after initially colliding with one another. Transfer operator TF shown in (8), which changes the search from exploration to exploitation, is used in AOA to execute this.

$$TF = exp\left(\frac{k - k_{max}}{k_{max}}\right) \tag{8}$$

Where k_{max} is the maximum number of iterations. *TF* will gradually increased over iteration and until it reaches 1. Moreover, the density decreasing factor, *d*, helps AOA does for local to global search, which is expressed using (9).

$$d^{k+1} = exp\left(\frac{k_{max} - k}{k_{max}}\right) - \left(\frac{k}{k_{max}}\right)$$
(9)

Where d^{k+1} decreases with iteration that enables convergence in a previously determined promising zone. It should be noted that effective management of *TF* and *d* will provide between AOA's exploration and exploitation.

Step 4.1- Verity exploration phase: If $TF \leq 0.5$, a collision between objects occurs. Then, a random material mr is selected, and the acceleration of object is updated for iteration k + 1 using (10).

$$acc_i^{k+1} = \frac{den_{mr} + vol_{mr} \times acc_{mr}}{den_i^{k+1} \times vol_i^{k+1}}$$
(10)

Where den_{mr} , vol_{mr} , and acc_{mr} are the density, volume, and acceleration of the *i*th object.

Step 4.2-Verity exploitation phase: On the other hand, if TF > 0.5, there is no collision between objects. Then, acceleration of object is updated for iteration k + 1 using (11).

$$acc_{i}^{k+1} = \frac{den_{mr} + vol_{mr} \times acc_{best}}{den_{i}^{k+1} \times vol_{i}^{k+1}}$$
(11)

Where *acc_{best}* is the acceleration of the best object.

Step 4.3- *Calculate normalize acceleration*: The normalize acceleration is performed using (12) to calculate the percentage of change:

$$acc_{i-norm}^{k+1} = u \times \frac{acc_i^{k+1} - min(acc)}{max(acc) - min(acc)} + l$$
(12)

Where u and l are the range of normalization, which is set to 0.9 and 0.1, respectively. The acc_{i-norm}^{k+1} determines the percentage change of each object's step. The acceleration value will be low if the object is near the global optimum, suggesting the object is in the exploration phase. If not, it is still in the exploration stage. This shows how the search agent changes from exploration to exploitation phase.

Step5-Update object's position: If $TF \le 0.5$, the *i*th object's position is updated using (13).

$$x_i^{k+1} = x_i^k + C_1 \times rand \times acc_{i-norm}^{k+1} \times (x_{rand} - x_i^k) \times d$$
(13)

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Where C_1 is a constant that equals to 2, x_{rand} is a random object's position, and *rand* is a random vector of a number between [0, 1]. If TF > 0.5, the object's position is instead updated using (14).

$$x_i^{k+1} = x_{best} \times F \times C_2 \times rand \times acc_{i-norm}^{k+1} \times (T \times x_{best} - x_i^k) \times d$$
(14)

Where x_{best} is the best object's position and C_2 is a constant that equals 6. Here, the variable *T* is defined by $T = C_3 \times T$, where C_3 is a constant that equals 2. Note that *T* increases with each iteration and has a range of $[C_3 \times 0.3, 1]$. In (13), the direction of the motion is handled by the flag *F* is using (15).

$$F = \begin{cases} +1 \ if \ Pr \ \le \ 0.5 \\ -1 \ if \ Pr \ > \ 0.5 \end{cases}$$
(15)

For $Pr = 2 \times rand - C_4$, where C_4 is a constant that equals 0.5 and *rand* is a random vector of a number between [0, 1].

Step 6- *Object's position evaluation*: Finally, evaluate each object's position using objective function and saves the best solutions found so far that are corresponded to the best solution x_{best} , den_{best} , vol_{best} , and acc_{best} .

Fig. 3 displays the pseudocode of the AOA and Fig. 4 displays the flowchart of the AOA, which outlines the sequence of steps to be followed in a clear and concise manner.

2.3. The Application of AOA for PID Tuning of DC-DC Buck Converter

This section will explore the implementation of the Archimedes Optimization Algorithm (AOA) to tune the PID controller of a DC-DC buck converter using the MATLAB software. An objective function was used to evaluate the quality of a solution and guide the algorithm toward finding the best solution possible. The objective function is a key component in optimization algorithms and is typically defined by the problem being solved. The objective function of the DC-DC buck converter, *J* is based on the paper of [20] and is shown in (16).

$$J = (1 - e^{-\sigma}) \times \left(M_p + E_{ss}\right) + e^{-\sigma} \times (T_s - T_r)$$
⁽¹⁶⁾

Where M_p is percentage of voltage overshoot, E_{ss} is steady-state error, T_s is setting period and T_r is rising period. The σ is a constant, which is set as 5×10^{-5} . In this study, a lower value of the objective function indicates better performance of the DC-DC buck converter in step response analysis. The rising period, settling period, overshoot, and steady-state error are all proportional to the objective function in this study, and the converter is considered to have better performance when these parameters have lower values. Therefore, a lower value of the objective function is indicative of better system performance and MATLAB program will find the lowest values. The following is a step-bystep procedure for tuning the PID controller of the DC-DC buck converter using AOA.

Step 1: Identity the population size (n) and maximum number of iterations (k_{max}) .

Step 2: Execute the AOA method in the pseudocode of Fig. 3 and use (16) to evaluate the objective function of each object's position. Here, the PID parameters are defined as the object's position.

Step 3: When k_{max} is reached, the best object's position is the result of PID controller tuning parameters.

To be fair compared with other algorithms in paper [20], the above step was run separately 25 times, and the results were saved after each run. The optimal tuning parameters for the PID controller, as tuned by the AOA, will be based on the best result obtained from these 25 independent runs.

Procedu	re AOA (population size n , upper bound ub_i , lower bound lb_i .
	n iterations k_{max} , C_1 , C_2 , C_3 , and C_4)
	lize objects population with random positions, densities and
	using Eq. (4) and Eq. (5) respectively
	ate initial population and select the one with the best fitness
value.	
Set it	eration counter $k=1$
while	$k \leq k_{max} \operatorname{do}$
fo	r each object <i>i</i> do
	Update density and volume of each object using Eq. (6)
	Update transfer and density decreasing factors TF and d
using Eq	. (7) and Eq. (8), respectively.
	if $TF \le 0.5$ then \rightarrow Exploration phase
	Update acceleration using Eq. (9) and normalize accelera
tion usin	g Eq. (11)
	Update position using Eq. (12)
	else \rightarrow Exploitation phas
	Update acceleration using Eq. (10) and normalize acceler
ation usin	ng Eq. (11)
	Update direction flag F using Eq. (14)
	Update position using Eq. (13)
	end if
	nd for
	valuate each object and select the one with the best fitness
value.	
	et k = k + 1
	nd while
	turn object with best fitness value
end proc	edure

Fig. 3. Pseudo code of AOA

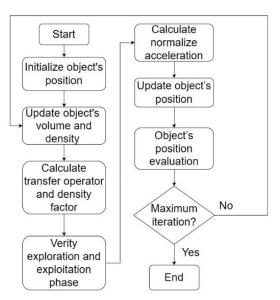


Fig. 4. Flowchart of AOA

3. Results and Discussion

Initially, the PID controller is tuned using the AOA method, as discussed in Section 2. The tuned PID controller's performance in the buck converter is then compared in terms of its robustness against input voltage disturbances and load changes. The method to evaluate the disturbance rejection capabilities of the closed-loop DC-DC buck converter is referred to paper [20], which a voltage disturbance was introduced after 5×10^{-6} seconds, resulting in a 20% voltage drop from the

reference voltage. Additionally, the buck converter's robustness was assessed by subjecting it to load changes of $\pm 20\%$ to further verify its disturbance rejection capabilities which are similar to paper [20].

3.1. Comparison Algorithms

This study specifically concentrates on the AOA for the purpose of tuning the parameters of the PID controller. Other optimization algorithms like the Hybrid Nelder-Mead Method (AEONM), Artificial Ecosystem-Based Optimization (AEO), Differential Evolution (DE), and Particle Swarm Optimizer (PSO) are compared to AOA because they all belong to candidate-based optimization methods, which rely on a set of candidates to seek the best solution. The PID controller parameter values tuning results by AEONM, AEO, DE, and PSO were directly taken from the paper [20]. Fairly speaking, AOA employed the same upper and lower PID controller parameter limits, population size, and maximum iteration as in the study done in [20]. Maximum iterations were set at 50 with a population size of 24. The PID controller parameter's upper and lower limits are displayed in Table 2.

Following 25 independent runs of the method, the optimal PID controller gain settings were determined by the lowest objective function value. Table 3 displays the lowest objective function value and PID parameters that were tuned by each algorithm in the study.

Table 2. Upper and lower limit of PID controller

Parameter	K _p	K _i	K _d
Lower bound	1	0.01	0.001
Upper bound	50	10	0.01

Table 3. Result of each algorithm

Algorithm	J	K _p	K _i	K _d
AOA	4.6845e-07	44.0089	0.01	0.01
AEONM [20]	4.8016e-07	16.8278	1.1742	0.00992
AEO [20]	4.9920e-07	33.1153	7.9506	0.00943
DE [20]	5.6191e-07	27.6235	1.3043	0.00873
PSO [20]	5.6805e-07	37.1502	3.7255	0.00821

The gain of the PID controller is obtained by substituting the PID parameters from Table 3 into (3). Then by using (4), the closed-loop transfer function of the DC-DC buck converter can then be derived and is presented in equation (17) to (21).

$$\begin{split} & TF_{AOA} \\ &= \frac{1.296s^5 + 7864s^4 + 2.247 \times 10^7 s^3 + 5.704 \times 10^{10} s^2 + 1.296 \times 10^7 s}{3.6 \times 10^{-7} s^6 + 1.297 s^5 + 7872 s^4 + 2.248 \times 10^7 s^3 + 5.707 \times 10^{10} s^2 + 1.296 \times 10^7 s} \end{split} \tag{17} \\ & TF_{AEONM} \\ &= \frac{1.286s^5 + 4324s^4 + 1.649 \times 10^7 s^3 + 2.181 \times 10^{10} s^2 + 1.522 \times 10^9 s}{3.6 \times 10^{-6} s^6 + 1.287 s^5 + 4332 s^4 + 1.650 \times 10^7 s^3 + 2.185 \times 10^{10} s^2 + 1.522 \times 10^9 s} \\ & TF_{AEO} \\ &= \frac{1.222s^5 + 6329s^4 + 1.938 \times 10^7 s^3 + 4.292 \times 10^{10} s^2 + 1.030 \times 10^{10} s}{3.6 \times 10^{-7} s^6 + 1.223 s^5 + 6337 s^4 + 1.939 \times 10^7 s^3 + 4.296 \times 10^{10} s^2 + 1.030 \times 10^{10} s} \\ & TF_{DE} \\ &= \frac{1.131s^5 + 5466s^4 + 1.728 \times 10^7 s^3 + 3.580 \times 10^{10} s^2 + 1.690 \times 10^9 s}{3.6 \times 10^{-7} s^6 + 1.133 s^5 + 5474 s^4 + 1.729 \times 10^7 s^3 + 3.584 \times 10^{10} s^2 + 1.690 \times 10^9 s} \\ & TF_{PSO} \\ & 1.064s^5 + 6588s^4 + 1.867 \times 10^7 s^3 + 4.815 \times 10^{10} s^2 + 4.828 \times 10^9 s \end{aligned} \tag{21}$$

$$=\frac{1.0648^{\circ} + 65368^{\circ} + 1.867 \times 10^{\circ} 8^{\circ} + 4.815 \times 10^{\circ} 8^{\circ} + 4.828 \times 10^{\circ} 8}{3.6 \times 10^{-7} s^{6} + 1.065 s^{5} + 6596 s^{4} + 1.868 \times 10^{7} s^{3} + 4.818 \times 10^{10} s^{2} + 4.828 \times 10^{9} s}$$

3.2. Disturbance Rejection Analysis

The disturbance rejection analysis was performed at $t = 5 \times 10^{-6}$ seconds during the system's steady-state operation to mimic real-world conditions. In this analysis, a negative disturbance of 20 percent of the voltage reference was introduced to assess the system's ability to maintain stability and accurately regulate the output under challenging conditions. By subjecting the system to this disturbance, we were able to evaluate its resilience and effectiveness in rejecting external disturbances and maintaining the desired output voltage. The results of this analysis provide valuable insights into the system's robustness and its capability to mitigate the impact of disturbances, contributing to the overall understanding of its performance characteristics. Fig. 5 illustrates the response of the system to the introduced disturbance, comparing the performance of the AOA algorithm with other compared algorithms.

The comprehensive details of the disturbance rejection analysis, including rise time, settling time, voltage overshoot, voltage peak value, and steady-state error, are presented in Table 4.

It is evident from Fig. 4 and Table 4 that the buck converter with AOA-based PID controller exhibits exceptional performance in quickly restoring the system back to the desired voltage after the disturbance occurs. It demonstrates its effectiveness in efficiently mitigating the impact of the disturbance, leading to rapid response from the desired voltage level. In comparison to the other buck converters, the buck converter with AOA-based PID controller outperforms them by achieving the fastest recovery time and demonstrating superior disturbance rejection capabilities. It exhibits a small overshoot percentage, which, while not the lowest, is still sufficiently small for practical applications. In real-world scenarios, this overshoot percentage can be considered as within the acceptable tolerance range for the desired voltage, and it does not have a significant impact on the load. This is particularly crucial in applications like computers, where voltage disturbances must be quickly rejected to minimize their impact on the computer's operation.

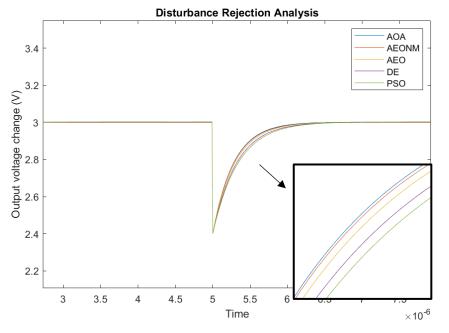


Fig. 5. Disturbance rejection demonstration of buck converter

D' /

l able 4.	Disturbance	rejection	analysis	of buck	converter	

Algorithm	Rise time (s)	Settling time (s)	Overshoot (%)	Peak value (V)	Steady-state error (V)
AOA	6.0163e-07	1.0405e-06	0.0741	3.0022	0
AEONM	6.1521e-07	1.0951e-06	0	3.0000	0
AEO	6.4060e-07	1.1165e-06	0.0527	3.0016	0
DE	6.9284e-07	1.2106e-06	0.0459	3.0014	0
PSO	7.2999e-07	1.2536e-06	0.0939	3.0028	0

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3.3. Robustness on Load Change Analysis

The robustness of the system to load changes is a crucial aspect to evaluate its performance in real-world scenarios. In this analysis, a load change of 1.2 ohms with a variation of $\pm 20\%$ was introduced to examine the system's response and stability. The system's ability to handle load changes was assessed by observing the transient response and its ability to recover to the desired operating point. The AOA algorithm, along with the compared algorithms, was subjected to this load variation, and their performances were evaluated.

The results in Table 5 indicated that the AOA algorithm exhibited remarkable robustness in dealing with the load change. It effectively adapted to the new load conditions and quickly adjusted the control parameters to maintain the desired output voltage. The system demonstrated a stable response with minimal overshoot and a fast-settling time, ensuring that the voltage remained within an acceptable range.

Rate of change (%)	Algorithm	Rise time (s)	Settling time (s)	Overshoot (%)	Peak value (V)	Steady-state error (V)
-20	AOA	6.0296e-07	1.0470e-06	0.0626	3.0019	0
-20	AEONM	6.1660e-07	1.1030e-06	0	2.9996	0
-20	AEO	6.4210e-07	1.1242e-06	0.0406	3.0012	0
-20	DE	6.9456e-07	1.2197e-06	0.0328	3.0010	0
-20	PSO	7.3190e-07	1.2629e-06	0.0800	3.0024	0
Rate of change	Algorithm	Rise time	Settling time	Overshoot	Peak value	Steady-state error
(%)	Algorithm	(s)	(s)	(%)	(V)	(V)
20	AOA	6.0076e-07	1.0362e-06	0.0817	3.0025	0
20	AEONM	6.1429e-07	1.0898e-06	0.0076	3.0002	0
20	AEO	6.3960e-07	1.1114e-06	0.0608	3.0018	0
20	DE	6.9168e-07	1.2046e-06	0.0547	3.0016	0
			1.2475e-06	0.1032	3.0031	

Table 5. Load change analysis of buck converter

During the robustness analysis with a 20% load change, the AOA-based PID controller continues to demonstrate the best performance among all controllers. It exhibits the lowest rise time and settling time while maintaining a small voltage overshoot. These findings suggest that the AOA-based PID controller adeptly manages load fluctuations, ensuring stable and precise control of the buck converter system. This makes it well-suited for deployment in renewable energy systems that often contend with challenges related to load variations.

The percentage changes of each parameter during the robustness analysis are presented in Table 6. Table 6 provides valuable insights into how the PID controller responds to the load changes and indicates the controller's ability to maintain stable performance under varying conditions.

		-20% load variation			+20% load variation		
Algorithm	Change of rise time	Change of settling time	Change of overshoot percent	Change of rise time	Change of settling time	Change of overshoot percent	
AOA	+0.2194%	+0.7385%	-0.0114%	-0.1463%	-0.4099%	0.0076%	
AEONM	+0.2400%	+0.7532%	0%	-0.1498%	-0.4797%	0.0076%	
AEO	+0.2336%	+0.6893%	-0.0122%	-0.1561%	-0.4557%	0.0081%	
DE	+0.2489%	+0.7532%	-0.0131%	-0.1664%	-0.4934%	0.0088%	
PSO	+0.2608%	+0.7385%	-0.0139%	-0.1744%	-0.4871%	0.0093%	

Table 6. Buck converter performance change analysis

These results highlight the superior robustness of the AOA-based PID controller compared to other optimization algorithms, making it a promising choice for applications where stability and accurate response during load changes are crucial. Overall, the robustness analysis demonstrated that the AOA algorithm can maintain system stability and ensure accurate control even in the presence of significant load changes. This further solidifies its effectiveness and reliability in practical applications where robust performance is paramount.

3.4. Frequency Analysis

Frequency analysis involves simulating the buck converter's performance under complex disturbance signals and high-frequency ripple voltage inputs. The phase margin in this analysis is utilized to determine the maximum stable operating frequency of the buck converter. Additionally, bandwidth evaluation is employed to assess the converter's overall performance, with a higher bandwidth indicating a wider operating range. Frequency analysis is essential in verifying the stability and efficiency of the PID controller for the buck converter. It allows evaluating how well the controller handles complex disturbances and varying voltage ripples, ensuring the converter's stable operation. The Fig. 6 and Table 7 present the outcomes of the frequency analysis for the buck converter under the influence of various PID controllers.

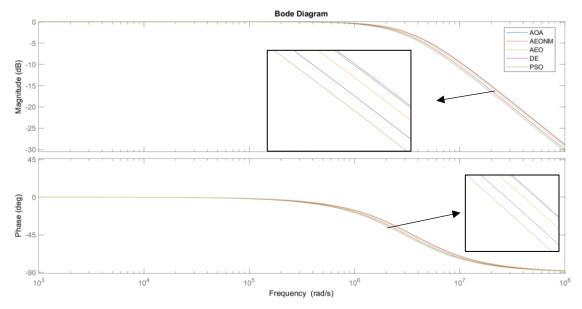


Fig. 6. Disturbance rejection demonstration of buck converter

Fig. 6 illustrates that the buck converter with the AOA-based PID controller exhibits the least magnitude dB drop and phase delay degree at the same frequency. This characteristic enhances the converter's immunity to common electrical noise and interference, making it more robust in noisy environments.

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Table 7.	Frequency	response	analysis
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Algorithm	Gain margin (dB)	Phase margin (degree)	Bandwidth (Hz)
8	8 ()		
AOA	Inf	177.7726	3.5942e+06
AEONM	Inf	-180	3.5628e+06
AEO	Inf	178.1149	3.3886e+06
DE	Inf	178.2405	3.1369e+06
PSO	Inf	177.4863	2.9515e+06

The analysis from Table 7 demonstrates that the buck converter with the AOA-based PID controller has the widest range of bandwidth, indicating its enhanced robustness and ability to handle a broader range of frequencies effectively. This wider bandwidth allows the AOA-based PID controller to respond more efficiently to various disturbances and ripple voltage inputs, ensuring stable and reliable operation of the buck converter under challenging conditions. The findings further validate the effectiveness of the AOA-based PID controller for regulating buck converters in practical scenarios, such as telecommunications, where challenges like fluctuating voltage due to high electromagnetic interference (EMI) are prevalent.

4. Conclusion

The study's conclusion provides a concise summary of the research findings. It highlights the effectiveness of the Archimedes optimization algorithm (AOA) in tuning a PID controller for a buck converter. The superiority of the AOA-based PID controller in terms of disturbance rejection and load change robustness compared to other algorithms is emphasized. The conclusion also emphasizes the stability and adaptability of the AOA-based controller, particularly in handling the buck converter's nonlinear characteristics and varying operating conditions. The study's contribution in showcasing the AOA-based PID controller's resilience to high-frequency disturbances through frequency analysis is highlighted. While the conclusion acknowledges the potential of AOA in broader applications beyond DC-DC converters, it could be strengthened by discussing potential challenges or limitations associated with AOA's application in different scenarios. Additionally, suggesting specific areas of future research beyond converters where AOA could be applied would enhance the conclusion's comprehensiveness.

In terms of future research, the AOA has potential for broader application beyond the DC-DC converter studies conducted in recent years. For instance, the AOA could be employed in analyzing other types of converters like boost converter, buck-boost converter, and flyback converter. AOA also can be used to analyze other modified DC-DC converters like Single-Inductor Multiple-Output DC-DC Converter. Additionally, the AOA's usefulness extends beyond just DC-DC converters and could be explored for addressing various real-world engineering challenges.

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