

# Towards Optimally Efficient Secret-Key Authentication from PRG

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## Abstract

We propose a new approach to the construction of secret-key authentication protocols making black-box use of any pseudorandom generator (PRG). Our authentication protocols require only two messages, have perfect completeness, and achieve concurrent man-in-the-middle security. Finally, when based on a sufficiently efficient PRG, our protocol has (amortised) complexity  $O(n)$  bit operations where  $n$  is the security parameter. To the best of our knowledge, this construction is the first with linear complexity. We achieve this at the cost of having the prover (but not the verifier) keep a small amount of state. A variant of our construction, based on a stronger security notion for the PRG, is secure even if the adversary is able to reset the prover an unbounded number of times. A practical analysis of our protocol shows our prover computation time compares favorably against a simple AES-based protocol.

## 1 Introduction

Secret-key authentication is one of most basic cryptographic tasks: a prover and a verifier share a secret key  $K$ , and the aim is to design a protocol that will allow the prover (and the prover alone) to convince the verifier that he indeed knows the key  $K$ . We call this *one-sided authentication*, to distinguish from mutual authentication where both parties must be convinced of each other's identity.

The strongest security notion in the authentication literature for one-sided authentication considers an adversary who may first interact (concurrently) as many times as he wants with the honest prover and verifier; after this, he is on his own and must attempt to falsely convince the verifier that he knows the secret key.<sup>1</sup> If no efficient adversary can win this game, we say the protocol is concurrent man-in-the-middle (MIM) secure.

Let us consider what resources we need, in terms of communication and computation, to realise (one-sided) authentication. It is easy to see that we need at least two messages, where the verifier sends the first messages. Furthermore, both messages must be long enough to not be easily guessed. Informally, if the prover's message is short, the adversary can guess what to say in the final phase; on the other hand, if the verifier's message is short, the adversary can guess what the verifier will say in the final phase and query the prover for the right answer in the first phase. Finally, we assume that both parties access  $n$  key bits, where  $n$  is the security parameter, so it follows that the computational complexity must be  $\Omega(n)$  bit-operations.

It is therefore a natural theoretical goal to build two-message authentication protocols of complexity  $O(n)$ , where  $n$  is the bit length of the shared key. Also, from a practical point of view, efficient "lightweight" authentication protocols are becoming ever more relevant as application scenarios proliferate where, for instance, the prover may be a low-cost RFID tag or smartcard.

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<sup>1</sup>Bellare and Rogaway [BR93] proposed a definition for *mutual* authentication using an attack model that is essentially the same as the concurrent man-in-the-middle attack we consider. We do not consider mutual authentication in this work, as we focus on two-message protocols, in which of course only one party can be convinced of the other's identity.

## 1.1 Background and motivation

The first solution to the authentication problem is from Goldreich, Goldwasser, and Micali [GGM86], as follows. Suppose we are given a pseudorandom function family (PRF)  $\mathcal{F}$  whose functions  $f_K$  have a key  $K$  and input/output size  $n$  bits. The crucial property of a PRF is that even an adversary who chooses the input (but does not know the key) cannot distinguish the output of  $f_K$  from random. Given a PRF, we can simply let the verifier send a random input  $x$ , and have the prover respond with  $f_K(x)$ . This simple two-round protocol already achieves concurrent MIM security<sup>2</sup>.

**Asymptotic efficiency.** From the perspective of efficiency, this simple solution is problematic in general. The standard construction of a PRF (from [GGM86]) is based on a pseudorandom generator (PRG), which is a much simpler primitive that expands a short key into a random-looking longer output. However, the GGM construction does not yield a particularly efficient PRF: even assuming a very efficient PRG that only requires a constant number of operations per output bit, the PRF will have complexity  $\Omega(n^2)$ .

There are in fact several very efficient and low-depth constructions of PRGs from specific problems, such as Learning Parity with Noise (LPN); and furthermore, even linear-time computable PRGs with linear stretch are known to follow from general and plausible assumptions<sup>3</sup>. Hence, it is compelling to try to design authentication directly based on weaker pseudorandom primitives such as PRGs, in such a way that the protocol inherits the efficiency. More specifically, we direct our attention to the open question of designing a two-round protocol to achieve (optimal) linear computational complexity.

**Practical efficiency.** In practice, a natural way to implement the simple PRF-based authentication protocol described above would be to use a block cipher such as AES in place of the PRF. While widely-used block ciphers such as AES are highly streamlined, we observe that (synchronous) *stream ciphers* can be used as PRGs and are often much faster than block ciphers. Thus, a natural question arises: can we design authentication directly based on stream ciphers, in such a way that the protocol inherits the efficiency? Efficiency gains at this scale could be particularly significant in settings where the prover is a lightweight hardware device. Indeed, building authentication based on stream ciphers has been recognised as a promising topic in the RFID authentication literature, for exactly this reason [FW08]; however, to the best of our knowledge, concrete proposals so far have required three or more rounds and have security which is (if proved at all) based upon non-standard assumptions that are stronger than assuming the stream cipher is a PRG (e.g. [LY08; BEG10]).

**Prover-stateful protocols.** In this work, we consider these questions in the *prover-stateful* setting where the prover (but not the verifier) keeps a small amount<sup>4</sup> of state. We remark that in the fully stateful model where both prover and verifier keep state and are in sync with each other, the authentication problem is straightforwardly resolvable – however, this model is much stronger than the prover-stateful model, and may require the guarantee that state is always synchronised between the prover and verifier, as well as raises questions of how to account for possible attacks where the adversary causes them to go out of sync. In contrast, the prover-stateful model requires no synchrony assumptions, and indeed amounts to the premise that the prover has a small amount of

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<sup>2</sup>Note that for authentication, it is actually sufficient to have an unpredictable function, a computationally secure “MAC”, rather than a PRF. However, we do not know of more efficient constructions of MACs either, so this does not help in terms of efficiency. In [IKOS08], Ishai et al. construct constant-overhead PRFs and MACs, but in their work the output size is much smaller than the input: in our context, this would make the prover’s answer much easier to guess than the verifier’s challenge, making the protocol insecure.

<sup>3</sup>A number of different sufficient conditions for such PRGs are known. In [IKOS08] it is observed that such PRGs follow from Alekhnovich’s variant of the Learning Parity with Noise assumption. Applebaum [App13] shows that such PRGs can be obtained from the assumption that a natural variant of Goldreich’s candidate for a one-way function in NC0 is indeed one-way. The improved HILL-style result of Vadhan and Zheng [VZ12] implies that such PRGs can be obtained from any exponentially strong one-way function that can be computed by a linear-size circuit.

<sup>4</sup>For example, the state could be a 32-bit counter that is incremented with each protocol execution.

updatable memory in addition to the space required for key storage – practically speaking, this is often true even when the prover is a small hardware device.<sup>5</sup>

The problem of optimally efficient two-round authentication has been long open in the (standard) stateless model. In this paper we ask the following question: *can we leverage a small amount of state on the prover side to build far more efficient protocols?*

## 1.2 Related work

**Authentication from weak pseudorandom primitives.** This direction has been explored in a work of Dodis et al. [DKPW12], which constructs a three-round protocol based on any weak PRF. A weak PRF is a relaxed notion of PRF in which function outputs are only required to look random for uniformly random (rather than adversarially chosen) inputs. The protocol of [DKPW12] achieves active security (a weaker notion than MIM security). Subsequently, Lyubashevsky and Masny [LM13] proposed a three-message protocol based on any weak PRF, which is secure against sequential MIM attacks<sup>6</sup>. In addition, they give a variant three-message protocol that can be built from any *randomised* weak PRF, a yet slightly weaker primitive. However, these protocols are not concurrent MIM secure (in fact, [LM13] outlines an attack), and all require three messages.

**Authentication from concrete hardness assumptions.** An obvious alternative approach to authentication is to build protocols directly from a concrete problem. Much work in this direction has been based on the LPN problem, which can be briefly stated as follows: given polynomially many samples of the form  $(\mathbf{a}, \langle \mathbf{a}, \mathbf{s} \rangle + e)$ , where  $\mathbf{a}$  is a random  $n$ -bit vector,  $\mathbf{s}$  is a secret  $n$ -bit vector fixed across all samples, and  $e$  is a bit which is 1 with probability  $\tau < 1/2$ , can we discover the secret  $\mathbf{s}$  (or at least, distinguish the samples from random)? Authentication from LPN has been studied in a series of papers, including [HB01; JW05; KSS10; Hey+12]. The latest in this line of work [Kil+11; DKPW12] have proposed two-message MIM secure protocols. Even based on a compact version of LPN (namely, the ring-LPN problem [Hey+12]), these protocols have computational complexity  $\Omega(n \log n)$ ; and moreover, they are not perfectly complete: the verifier may reject the honest prover with non-zero probability. In order to keep this error probability low, the protocol’s communication (and hence also computational) complexity must depend on the LPN noise parameter  $\tau$ , and in fact the complexity grows quite dramatically as  $\tau$  approaches  $1/2$ . Thus, these protocols incur a rather high price if we want to use a weaker variant of the LPN assumption where  $\tau$  is close to  $1/2$ .

The well-known LWE problem is syntactically similar to LPN, but is defined over a larger field, and the noise parameter  $e$  is chosen with a discrete Gaussian distribution. In [BPR12], Banerjee, Peikert and Rosen suggest a construction of a PRF directly from LWE problem. Whereas this is indeed more efficient than applying the [GGM86] construction on the PRG that follows naturally from LWE, the complexity of the resulting PRF is still  $\Omega(n \log n)$ .

**Other related work.** Recent work by Cash, Kiltz, and Tessaro [CKT16] (which is subsequent to but independent of our work) proposes MIM secure two-round authentication by a generic transformation from actively-secure protocols which satisfy certain requirements. Their construction can be instantiated from a number of existing protocols, including ones based on concrete assumptions such as LPN or the decision Diffie-Hellman assumption, and also based on weak PRFs. However, the constructions of [CKT16] only achieve sequential MIM security.

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<sup>5</sup>Interestingly, in the RFID authentication literature mentioned above, a stateful prover (i.e. RFID tag) seems to be a standard assumption: non-volatile memory is assumed to be present (after all, it is required to store the secret key information), and is assumed also to be used for storing state.

<sup>6</sup>Sequential MIM security is stronger than active security, but weaker than concurrent MIM security. In a sequential MIM attack, the adversary can interact polynomially many times with an honest prover and verifier, but the interactions cannot involve multiple concurrent sessions with the prover (or the verifier). In contrast, an active adversary can interact only with the prover.

### 1.3 Our contribution

In order to describe the security our protocol achieves, the standard security notions for authentication protocols must be extended to the prover-stateful setting. We formalise a natural extension of standard concurrent man-in-the-middle security that makes sense for the prover-stateful setting, and also define a yet stronger security notion called *multi-instance* concurrent MIM security which additionally captures the possibility of the adversary resetting the prover’s state. In the latter definition, the adversary may, in the first phase, have several concurrent sessions with many different incarnations (or “clones”) of the prover and the verifier, where each incarnation will start from a fresh initial state. Note that for a stateless protocol, the concurrent and multi-instance concurrent security notions coincide.

We then propose a new two-message authentication protocol that can be based on black-box access to any PRG. The prover (but not the verifier) keeps a state that grows logarithmically with the number of executions. The protocol has perfect completeness and is concurrent MIM secure (but not multi-instance). When based on a linear-time PRG, the amortised complexity<sup>7</sup> of the protocol is  $O(n)$ . To the best of our knowledge, our protocol is the first to have all these properties simultaneously.

A variant of the protocol is multi-instance concurrent MIM secure for any polynomial number of instances, as long as this polynomial  $\ell$  is fixed when the system is set up. If the number of instances  $\ell$  is constant, we can maintain the linear complexity of the protocol – in order to achieve this, we propose a construction of linear-time computable  $\ell$ -wise independent hash functions for any constant  $\ell$ . A recent work of Ishai et al. [IKOS08] achieved linear time but only for  $\ell = 2$  (i.e. pairwise independent hash functions), and we extend their construction to any constant  $\ell$  by invoking very efficient error-correcting codes.

Finally, we define a new, stronger variant of PRG security, under which it is possible to achieve yet better multi-instance security. Consider a PRG  $G$  as a mapping from  $n$ -bit seeds to  $n$ -bit outputs. Then clearly, the string  $G(s_1), \dots, G(s_t)$  is pseudorandom for any polynomial  $t$ , if the  $s_i$ ’s are independent and random. We say that  $G$  is  $(\mathcal{H}, \ell)$ -wise secure if this still holds, even if the  $s_i$  are not independent, but are chosen from an  $\ell$ -wise independent distribution generated by a hash function from an  $\ell$ -wise independent family  $\mathcal{H}$ .

Now, given a PRG  $G$  and an  $\ell$ -wise independent hash function family, a small modification of our authentication protocol is multi-instance concurrent MIM secure for up to  $\ell$  instances assuming only that  $G$  is a PRG (just as before); and moreover, the same protocol is *unbounded* multi-instance concurrent MIM secure if  $G$  is  $(\mathcal{H}, \ell)$ -wise secure. In other words, the protocol is secure against  $\ell$  resets of the prover if the PRG is secure; and if the adversary can do more than  $\ell$  resets, the protocol “degrades gracefully” and is still secure, but under a stronger assumption. Note that one may instantiate both  $G$  and  $\mathcal{H}$  to be linear-time.

On the practical side, we analyse the efficiency of the prover in our protocol when instantiated with the Grain stream cipher [HJM07] (from the eSTREAM portfolio), and find that the prover’s computation time compares favorably against the simple AES-based protocol. We consider our analysis to be preliminary in the sense that while AES is one natural benchmark candidate, realistically speaking, lightweight authentication protocols use a variety of techniques based on other (hardware-oriented) block ciphers or even hash functions such as SHA: thus, a more comprehensive analysis would be necessary to draw conclusions about real practicality. Nonetheless, with our preliminary analysis, we put forth the first two-round protocol with provable security based on standard security of *any* stream cipher, which is certainly efficient enough to be implemented on lightweight (RFID) hardware.

**Comparison to previous work.** Let us first consider previous authentication protocols based on *general* assumptions, where the best known results are [LM13] and [CKT16] (these works both

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<sup>7</sup>It should be noted that this refers to the time spent in normal operation between the honest prover and verifier. An adversary can force the verifier (but not the prover) to spend more time, but we do not view this as a significant problem: in practice, an adversary could always waste the verifier’s time by doing a standard denial of service attack.

base their constructions on any weak PRF). In comparison to these works, we can use any PRG (which seems like a simpler and easier to build primitive than weak PRF) and we have bounded concurrent MIM security, where [LM13] has no concurrent security. Moreover, our protocol (and [CKT16]) achieves minimal round complexity (2 rounds) whereas [LM13] requires 3 rounds. On the other hand, we need the prover to keep state and we can tolerate only an a priori bounded number of reset attacks (which is a non-issue for a stateless protocol). However, under a stronger but still general type of assumption ( $(\mathcal{H}, \ell)$ -wise secure PRG) we get unbounded instance concurrent MIM security which is a strictly stronger notion than what [LM13] and [CKT16] achieve.

As for previous work on authentication from specific assumptions such as hardness of (ring-)LWE it is less clear how to compare, because our construction is based on a general assumption that can be supported by several different specific problems. But we can at least say that all previous protocols based on specific problems such as ring-LPN or ring-LWE, have complexity  $\Omega(n \log n)$ . On the other hand, we can exhibit an assumption under which our protocol will have (amortised) complexity  $O(n)$ , while obtaining the same number of messages and same security notion as previous works. The assumption is that there exists linear time PRG  $G$ , a constant  $\ell$  and a linear time computable hash function family  $\mathcal{H}$  such that  $G$  is  $(\mathcal{H}, \ell)$ -wise secure. Concrete candidates for  $G$  are known and we propose a candidate for  $\mathcal{H}$  and any  $\ell$  in this paper.

Moreover, we note our protocol can be based on the PRG that follows naturally from LPN. Since our construction achieves perfect completeness, we avoid the bad dependence on the noise parameter  $\tau$  which was mentioned in the previous section, which has been a common property of other LPN-based authentication protocols proposed thus far.

**Ideas and techniques.** The basic idea of our protocol is that the verifier sends an  $n$ -bit random challenge  $a$  to the prover, who responds with an unconditionally secure MAC on  $a$  computed from a secret key that he shares with the verifier.

The good news is that unconditionally secure MACs (in contrast to computationally secure ones) can give us the efficiency we are after. In particular we will use a MAC of the form  $C(a) * s \oplus e$  where  $(s, e)$  is the key, and where  $C$  is a linear-time encodable linear code that is good in the sense that both its length and minimum distance are  $\Theta(n)$ . Then  $C(a)$  is the encoding of  $a$  and the product  $C(a) * s$  is the component-wise (Schur) product. This one-time MAC is linear-time computable and was proven secure in a work of Damgård and Zakarias [DZ13].

However, the bad news is that while  $s$  can be reused over several executions,  $e$  cannot: it must be a fresh random value every time, or the key will be revealed. An obvious solution is to choose  $e$  pseudorandomly using another shared key  $K$ . Computing it as  $e = f_K(a)$  where  $f$  is PRF may seem natural, but this would be pointless, because then the simpler PRF-based protocol mentioned above might as well be used, and we will again face the efficiency issues associated with using a PRF in this way. To get around this, we propose to have the prover keep a counter  $i$  that is incremented for each execution, and compute  $e$  as  $e = f_K(i)$ . The point is that now the prover does not need to compute the PRF on arbitrary unpredictable inputs, but only on values that arrive in a particular order  $i = 1, 2, \dots$ . To see why this is an advantage, note that the GGM construction forms a tree with exponentially many leaves, one for each possible input. Computing an output requires computing the path to the relevant leaf, calling the PRG once for each vertex. Now the idea is to save the last path we computed and only recompute the part that changes for the next input. If the inputs indeed arrive in order, it turns out that on average we will only need to call the PRG twice per call to the PRF.

We also propose a variant of the GGM tree where every node delivers an output value. This allows us to grow the tree as we go, so that the storage requirement only depends (logarithmically) on how many times *the protocol is actually executed* whereas using the original GGM tree would force us to decide in advance on an upper bound for the number of executions. Goldreich [Gol01] suggested a somewhat related idea where the last leaf of the tree is used as the root of a new one. In the most “extreme” variant of this technique we would get a linear list instead of a tree. This would lead to similar time complexity for the prover, but an adversary could force the verifier to spend much more time than in normal operation, potentially  $\Omega(nt)$ , where  $t$  is the number of times

the protocol is executed. The worst case for our approach is  $O(n \log t)$ .

The construction we just sketched achieves concurrent MIM security: the strongest security notion in the case where the adversary cannot clone or reset the prover. If the adversary can clone or reset, then we need multi-instance concurrent MIM security. We achieve this by modifying our protocol using an additional technique based on universal hashing. In particular, we extend a result of [IKOS08] to get a linear time computable  $\ell$ -wise independent hash function family  $\mathcal{H}$  for any constant  $\ell$  (rather than pairwise independence as in [IKOS08]). If there exists a linear time PRG  $G$  that is  $(\mathcal{H}, \ell)$ -secure, then we get a protocol that is multi-instance concurrent MIM secure and has amortised complexity  $O(n)$ .

Interestingly, the proof of security is not so straightforward as it may seem at first sight. One naturally expects a simple two-step argument: first use the security of the PRF to replace the pseudorandom  $e$  in the prover's response  $C(a) * s \oplus e$  by a really random value, and then use the unconditional security of the MAC to conclude that the protocol is secure. However, while the first step is fine, the second fails because an adversary against the protocol is more powerful than an adversary against the MAC: he can arbitrarily schedule interactions with the verifier and with the prover, so we need first to argue that access to the verifier is useless for the adversary. This requires an argument that is completely different from the standard security proof for the MAC.

## 2 Preliminaries

**Notation.** For a finite set  $B$ , we will write  $b \leftarrow B$  to denote that  $b$  is drawn uniformly randomly from  $B$ . For  $n \in \mathbb{N}$ ,  $[n]$  denotes the set  $\{1, 2, \dots, n\}$ . The relation  $\overset{s}{\approx}$  between distributions denotes statistical indistinguishability, and  $\overset{c}{\approx}$  denotes computational indistinguishability.  $\text{negl}(n)$  denotes a negligible function in parameter  $n$ , and  $\text{poly}(n)$  denotes a polynomial function. An *efficient* algorithm is one which runs in probabilistic polynomial time (PPT).

The definitions of pseudorandom generators (PRGs) and pseudorandom function families (PRFs) are given in Appendix A.

### 2.1 Authentication protocols

A *authentication protocol* is an interactive two-party protocol  $(\mathcal{P}, \mathcal{V})$  between a prover  $\mathcal{P}$  and a verifier  $\mathcal{V}$ : these may be respectively thought of as a (lightweight) *tag*, and a *reader* to which the tag is identifying itself. Both parties are PPT, and hold a shared secret  $s$  generated according to some generation algorithm  $\text{Gen}(1^\kappa)$  (where  $\kappa$  denotes the security parameter) in an initial phase. After an execution of the protocol, the verifier  $\mathcal{V}$  outputs either *accept* or *reject* – this is also called the *output* of the protocol execution.

In this work we consider *prover-stateful protocols* where the prover also maintains some (small amount of) state between protocol executions.

**Definition 2.1** (Completeness). *The completeness error of a protocol is:*

$$\Pr_{s \leftarrow \text{Gen}(1^\kappa)} [(\mathcal{P}(s), \mathcal{V}(s)) = \text{reject}].$$

*A protocol is complete if its completeness error is negligible in a security parameter. It is perfectly complete if its completeness error is zero.*

Common definitions of security for authentication protocols are given below. Note that these definitions are adapted for the prover-stateful setting; they have natural analogues in the stateless setting which is more common in the literature.

**Definition 2.2** (Active security). *An authentication protocol  $(\mathcal{P}, \mathcal{V})$  is secure against active attacks if for any secret  $s \leftarrow \text{Gen}(1^\kappa)$ , for any PPT adversary  $\mathcal{A}$  which first can interact arbitrarily polynomially many times with an honest prover  $\mathcal{P}$  (but cannot reset the prover's state), and then afterward*

(now, without access to  $\mathcal{P}$ ) interacts once with an honest verifier  $\mathcal{V}$ , it holds that

$$\Pr[(\mathcal{A}, \mathcal{V}(s)) = \text{accept}] \leq \text{negl}(\kappa).$$

**Definition 2.3** (Concurrent man-in-the-middle (MIM) security). *An authentication protocol  $(\mathcal{P}, \mathcal{V})$  is secure against concurrent man-in-the-middle attacks if for any PPT adversary  $\mathcal{A}$  which first can interact arbitrarily polynomially many times with an honest prover  $\mathcal{P}$  and/or an honest verifier  $\mathcal{V}$  (the interactions may be concurrent, but the adversary cannot reset the prover’s state), and then afterward (now, without access to  $\mathcal{P}, \mathcal{V}$ ) interacts once with an honest verifier  $\mathcal{V}'$ , it holds that*

$$\Pr[(\mathcal{A}, \mathcal{V}'(s)) = \text{accept}] \leq \text{negl}(\kappa).$$

In this setting, the adversary learns the accept/reject decisions of the verifier.

**Definition 2.4** (Multi-instance concurrent MIM security). *An authentication protocol  $(\mathcal{P}, \mathcal{V})$  is secure against multi-instance concurrent man-in-the-middle attacks if for any PPT adversary  $\mathcal{A}$  which first can interact arbitrarily polynomially many times with polynomially many honest provers  $\mathcal{P}_1, \dots, \mathcal{P}_k$  and/or honest verifiers  $\mathcal{V}_1, \dots, \mathcal{V}_k$ , and then afterward (now, without access to the  $\mathcal{P}_i, \mathcal{V}_i$ ) interacts once with an honest verifier  $\mathcal{V}'$ , it holds that*

$$\Pr[(\mathcal{A}, \mathcal{V}'(s)) = \text{accept}] \leq \text{negl}(\kappa).$$

As above, the adversary learns the accept/reject decisions of the verifier(s).

A natural relaxation of multi-instance concurrent MIM security addresses the case when the adversary has access to only a  $\ell$  provers and verifiers, where  $\ell = \text{poly}(n)$  is bounded in advance. We write  *$\ell$ -instance concurrent MIM security* to denote security against an adversary who has concurrent access to up to  $\ell$  honest provers (and verifiers), but no more. This definition allows a construction to take advantage of the fact that  $\ell$  is chosen and fixed initially. Furthermore, we define  *$\ell$ -instance active security* to be the corresponding notion for active security: that is, where the adversary has access to up to  $\ell$  honest provers.

Note that for stateful protocols, the multi-instance definition covers the case of an adversary who can reset, e.g., the prover’s state: clearly, an adversary who can do this  $\ell$  times can be emulated by an adversary having access to  $\ell$  copies of the prover.

### 3 Authentication via “one-time” MACs

In this section, our approach is to build authentication protocols from very efficient MACs. It was observed in earlier sections that known MAC constructions are not very efficient as they are based on PRFs – however, this is only true of *computationally* secure MACs. In contrast, there are *unconditionally* secure MACs that are very efficient – but these are only secure for one-time use, so have not thus far been considered suitable for authentication protocols.

**Notation.** For vectors  $v, w \in \{0, 1\}^n$ ,  $v * w$  denotes the component-wise (Schur) product, and  $v \cdot w$  is the field product (in  $\mathbb{F}_{2^n}$ ). For an error-correcting code  $C$ ,  $C.\text{Enc}$  and  $C.\text{Dec}$  denote the encoding and decoding functions, respectively.

Consider the following simple and unconditionally secure MAC: for a message  $a \in \{0, 1\}^n$ , the MAC on the message is  $a \cdot s + e$ , where  $(s, e) \in \{0, 1\}^n \times \{0, 1\}^n$  is the secret key (which is chosen uniformly at random). MACs of this form are well known, and it is also known that although a key for an unconditionally secure MAC can usually be used only once, in this case the multiplier ( $s$ ) can be reused provided that  $e$  is freshly chosen for each message (see e.g. [BDOZ11]).

In this work, we focus on a slightly different MAC, which might be considered a variant of the above. For a message  $a \in \{0, 1\}^n$ , the MAC on the message is  $C.\text{Enc}(a) * s + e \in \{0, 1\}^{cn}$

where  $C$  is an error-correcting code (with constant-fraction distance) with expansion  $c$ , and  $(s, e) \in \{0, 1\}^{cn} \times \{0, 1\}^{cn}$  is the secret key. The security of this variant MAC is shown in [DZ13].

In our protocols, we consider  $s$  to be the secret key, and generate  $e$  pseudorandomly per execution. The man-in-the-middle security of our protocol does *not* follow from MAC security, however: the standard security notion for MACs simply requires that an adversary who observes a message and a valid MAC cannot produce a different message and valid MAC. We consider a more complicated game where the adversary interacts with prover and verifier concurrently.

Finally, we remark that although our protocols are presented in terms of the variant MAC, the proofs of correctness and security all go through (with almost no changes) also when using the “ $a \cdot s + e$ ” MAC. Which version is better in a concrete application would be determined by the encoding efficiency of the error-correcting code for the relevant parameters.

### 3.1 Pseudorandom look-up function

As a building block for our authentication protocols, we construct a logarithmic-depth “look-up function” for efficient retrieval of pseudorandom values using the PRG, and show that the look-up function is a PRF. Note that this technique may be of independent interest towards generic constructions of low-depth PRFs.

**Notation.** Since a PRG  $G$  can be used to build a PRG of any stretch, we write  $G_{n \rightarrow m}$  to denote the PRG based on  $G$  which maps  $n$  bits to  $m$  bits. We write  $G_{n \rightarrow m}(r)^{[i,j]}$  to denote the substring of the PRG output  $G_{n \rightarrow m}(r) \in \{0, 1\}^m$  ranging from the  $i^{\text{th}}$  bit to the  $j^{\text{th}}$  bit, inclusive.

Given a PRG  $G$  taking an  $n$ -bit input, our goal is to generate a series of polynomially many pseudorandom values  $r_1, r_2, r_3, \dots$ , such that each  $r_i$  can be looked up in time (poly-)logarithmic in  $i$ . This is achieved using the tree structure below.

Note that while the PRF construction of [GGM86] also performs lookups in logarithmic time (and can be easily adapted to support an unlimited input domain  $\{0, 1\}^*$ , as pointed out in [Gol01]), their construction does not satisfy our required property: in their case, the lookup time for *any* output  $r_i$  is poly-logarithmic in the *total* number of queries that will ever be made to the PRF.

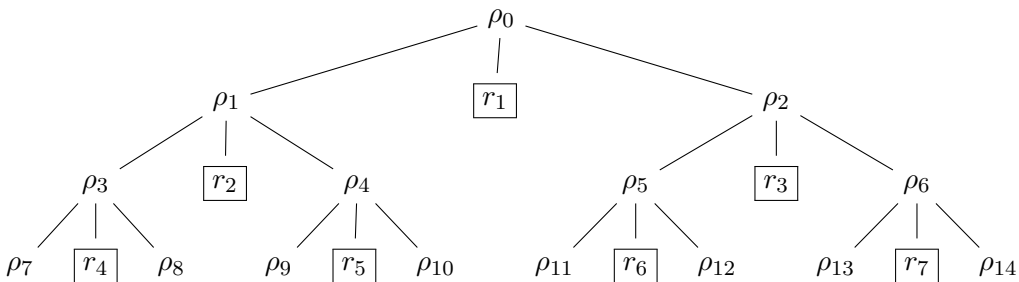


Figure 1: Tree illustrating efficient look-up of pseudorandom values (first 4 levels)

In Figure 1,  $\rho_0 \in \{0, 1\}^n$  is the original (random) input to the PRG  $G_{n \rightarrow m+2n}$ . The  $\rho_i \in \{0, 1\}^n$  are values which are subsequently pseudorandomly generated, which are used again as input to the PRG to produce more pseudorandom values: in particular, if  $\rho_i$  is a child of  $\rho_j$  in the tree, then  $\rho_i = G_{n \rightarrow m+2n}(\rho_j)^{[m+1, m+n]}$  if  $i$  is even, and  $\rho_i = G_{n \rightarrow m+2n}(\rho_j)^{[m+n+1, m+2n]}$  if  $i$  is odd. The boxed nodes  $r_i \in \{0, 1\}^m$  are leaves that represent the output pseudorandom values which we want to look up, and they are generated by  $r_i = G_n(\rho_j)^{[1, m]}$  where  $\rho_j$  is the parent node of  $r_i$ .

Let  $\text{lookup}_{n,m}^G(\rho_0, i) \in \{0, 1\}^m$  denote the  $i^{\text{th}}$  output value,  $r_i \in \{0, 1\}^m$ , obtained using the above tree method. It is clear that for any  $i$  of polynomial size, the number of PRG evaluations required to look up  $r_i$  is logarithmic. This gives rise to a PRF family with logarithmic-depth evaluations, as proven in Theorem 3.2 below. Before proving that the look-up function is a PRF, we give a simple supporting lemma.



**Lemma 3.1.** Let  $G : \{0, 1\}^n \rightarrow \{0, 1\}^m$  be a PRG. Then for any polynomial  $q = q(n)$ , it holds that there is no efficient distinguisher  $D$  for which it holds that

$$|\Pr [D((r_1, \dots, r_q)) = 1] - \Pr [D((G(s_1), \dots, G(s_q))) = 1]| \geq \text{negl}(n)$$

for all negligible functions  $\text{negl}$ , where  $r_1, \dots, r_q \leftarrow \{0, 1\}^{m(n)}$  and  $s_1, \dots, s_q \leftarrow \{0, 1\}^n$ .

*Proof.* Suppose, for contradiction, that there is a distinguisher  $\widehat{D}$  for which

$$\left| \Pr \left[ \widehat{D}((r_1, \dots, r_q)) = 1 \right] - \Pr \left[ \widehat{D}((G(s_1), \dots, G(s_q))) = 1 \right] \right| \geq 1/P(n)$$

where  $P$  is a polynomial. For  $i \in [q]$ , define  $\text{tup}_i$  to be the distribution of tuples whose first  $i$  elements are uniformly random in  $\{0, 1\}^m$  and whose remaining elements are sampled as  $G(s_{i+1}), \dots, G(s_q)$  for  $s_{i+1}, \dots, s_q \leftarrow \{0, 1\}^n$ . Let  $p_i = \Pr[\widehat{D}(\text{tup}_i) = 1]$  denote the probability that  $\widehat{D}$  outputs 1 on input from  $\text{tup}_i$ .

By our supposition, we know  $|p_0 - p_q| \geq P(n)$ . Then, since  $p_0 - p_q = \sum_{i \in [q]} (p_{i-1} - p_i)$ , there must exist  $i^* \in [q]$  such that  $|p_{i^*-1} - p_{i^*}| \geq \frac{1}{q \cdot P(n)}$ , which is non-negligible. Then there exists a distinguisher  $\widehat{D}'$  which can distinguish a *single* output of the PRG from random, as follows: on input  $r \in \{0, 1\}^m$ ,  $\widehat{D}'$  generates a tuple  $t$  whose first  $i^* - 1$  elements are random in  $\{0, 1\}^m$ , whose  $(i^*)^{\text{th}}$  element is  $r$ , and whose remaining elements are generated as  $G(s_{i^*+1}), \dots, G(s_q)$  for  $s_{i^*+1}, \dots, s_q \leftarrow \{0, 1\}^n$ . If  $r$  is truly random then  $t \leftarrow \text{tup}_{i^*}$ ; otherwise,  $t \leftarrow \text{tup}_{i^*-1}$ . Hence, running  $\widehat{D}$  on input  $t$  will distinguish with non-negligible probability between these cases. This contradicts that  $G$  is a PRG.  $\square$

**Theorem 3.2.** Let  $G$  be a PRG and  $n, m \in \mathbb{N}$  be positive integers with  $m = \text{poly}(n)$ . Then the family of functions  $\mathcal{F}^{(n,m)} \stackrel{\text{def}}{=} \{\text{lookup}_{n,m}^G(\rho, \cdot)\}_{\rho \in \{0,1\}^n}$  is a PRF with input size  $n'$  bits and output size  $n$  bits, for any  $n' = \text{poly}(n)$ .

*Proof.* The statement to prove is that for any PRG  $G$  and random  $\rho_0 \leftarrow \{0, 1\}^n$ , there is no efficient distinguisher  $D$  that satisfies

$$\left| \Pr \left[ D^{\text{lookup}_{n,m}^G(\rho_0, \cdot)}() = 1 \right] - \Pr \left[ D^{\mathcal{R}(\cdot)}() = 1 \right] \right| > \text{negl}(\kappa)$$

for all negligible functions  $\text{negl}$  and all sufficiently large values of the security parameter  $\kappa$ . In the above,  $\mathcal{R}$  is a random oracle, and  $D^{\mathcal{O}}$  may make any polynomial number of “polynomial-depth”<sup>8</sup> queries to the oracle  $\mathcal{O}$ .

Suppose, for contradiction, that there exists such a distinguisher  $\widehat{D}$ , for which the above inequality does not hold: that is, there is a polynomial  $q$  for which

$$\left| \Pr \left[ D^{\text{lookup}_{n,m}^G(\rho_0, \cdot)}() = 1 \right] - \Pr \left[ D^{\mathcal{R}(\cdot)}() = 1 \right] \right| > 1/q(\kappa).$$

Let  $T = T(\kappa)$  be the run-time of distinguisher  $\widehat{D}$ , and let  $H_j$  be a stateful algorithm which is defined as follows. (Note that when referring to tree structure, the root node is considered to be at depth 0.) On input  $i$ , the algorithm  $H_j$  does the following:

- if  $i$  has already been queried previously, look up the stored tuple  $(\text{leaf}, i, \rho_i)$ , and output  $\rho_i$ ;
- else if  $i < 2^{j+1}$  (that is, the  $i^{\text{th}}$  output node is at depth less than or equal to  $j$  in the tree), then choose some  $\rho_i \leftarrow \mathbb{Z}_n^2$  uniformly at random, store the tuple  $(\text{leaf}, i, \rho_i)$  in memory, and output  $\rho_i$ ;
- otherwise (that is, the  $i^{\text{th}}$  output node is at depth greater than  $j$  in the tree):
  - if there is no stored tuple of the form  $(\text{root}, \alpha, \rho)$ , then choose some  $\rho \leftarrow \mathbb{Z}_n^2$  uniformly at random, store the tuple  $(\text{root}, \alpha, \rho)$  in memory, and output  $\text{lookup}_{n,m}^G(\rho, \gamma)$ ;

<sup>8</sup> More precisely, the distinguisher cannot make queries that would require a super-polynomial depth look-up in the tree structure of  $\text{lookup}_{n,m}^G$ . This is because in this case,  $\text{lookup}_{n,m}^G$  would not run in polynomial time.

– otherwise, look up the stored tuple  $(\text{root}, \alpha, \rho)$  and output  $\text{lookup}_{n,m}^G(\rho, \gamma)$ ;

where  $\alpha = \alpha_{(i,j)}, \gamma = \gamma_{(i,j)}$  are defined by the following:

$$\begin{aligned}\alpha_{(i,j)} &= \lfloor (i - 2^{\lceil \log_2(i) \rceil}) / 2^j \rfloor \\ \beta_{(i,j)} &= i - 2^{\lceil \log_2(i) \rceil} \pmod{2^{\lceil \log_2(i) \rceil - j}} \\ \gamma_{(i,j)} &= 2^{(2^{\lceil \log_2(i) \rceil} - j)} + \beta_{(i,j)}.\end{aligned}$$

When considering the tree representation of  $\text{lookup}_{n,m}^G$ , the algorithms  $H_j$  can be explained in more intuitive terms as follows. For each  $j$ , the outputs of  $H_j$  behave as a random oracle up to and including depth  $j$  of the tree. Below depth  $j$ , the outputs are obtained deterministically by the  $\text{lookup}_{n,m}^G(\rho, \cdot)$  function, with the appropriate depth- $j$  value  $\rho$  (which is randomly chosen) acting as the “root node” of the subtree in which the lookup is performed.

Observe that  $H_0$  behaves exactly as  $\text{lookup}_{n,m}^G(\rho, \cdot)$  for random  $\rho \leftarrow \{0,1\}^n$ . Moreover,  $H_k$  behaves exactly like a random oracle in the distinguishing experiment, provided that all of  $\widehat{D}$ 's queries can be retrieved from depth at most  $k$ . Since the input size is  $n' = \text{poly}(n)$  bits, there exists such a maximum depth  $k$  from which queries can be retrieved, with  $k = \text{poly}(n)$ .

Let  $p_i = \Pr[\widehat{D}^{H_i}() = 1]$  denote the probability that the distinguisher outputs 1 given  $H_i$  as an oracle. By our earlier supposition,  $|p_0 - p_k| > 1/q(\kappa)$ . It follows that for some  $k^* \in [k]$ , we have  $|p_{k^*-1} - p_{k^*}| > \frac{1}{k \cdot q(\kappa)}$ . Such a  $k^*$  can be found in polynomial time with non-negligible probability<sup>9</sup>.

We now construct a new distinguisher  $\widehat{D}_{\text{PRG}}$  attacking the PRG  $G_{n \rightarrow m+2n}$ : specifically, we will show that the following expression is non-negligible:

$$\left| \Pr \left[ \widehat{D}_{\text{PRG}}((r_1, \dots, r_T)) = 1 \right] - \Pr \left[ \widehat{D}_{\text{PRG}}((G_{n \rightarrow m+2n}(s_1), \dots, G_{n \rightarrow m+2n}(s_T))) = 1 \right] \right|,$$

where  $r_1, \dots, r_T \leftarrow \{0,1\}^{m+2n}$  and  $s_1, \dots, s_T \leftarrow \{0,1\}^n$ .

$\widehat{D}_{\text{PRG}}$  operates as follows. Given input  $(\tilde{r}_1, \dots, \tilde{r}_T)$ ,  $\widehat{D}_{\text{PRG}}$  first determines a  $k^* \in [k]$  as described above, then runs  $\widehat{D}$  and responds to the oracle queries of  $\widehat{D}$  in the following way: when  $\widehat{D}$  makes query  $i$ ,  $\widehat{D}_{\text{PRG}}$  responds with  $\widetilde{H}_{k^*}(\tilde{r}_1, \dots, \tilde{r}_T)$ , where  $\widetilde{H}_{k^*}$  is a variant algorithm based on  $H_{k^*}$ .  $\widetilde{H}_{k^*}$  takes as input  $(\tilde{r}_1, \dots, \tilde{r}_T)$ , and then behaves exactly like  $H_{k^*}$ , except that the values associated with nodes at depth  $k^*$  of the tree are obtained as substrings of the input values  $\tilde{r}_1, \dots, \tilde{r}_T$ . (For a detailed formal description of  $\widetilde{H}_{k^*}$ , refer to Appendix B.)

By construction, it holds that if the inputs  $\tilde{r}_i$  are truly random, then  $\widetilde{H}_{k^*}$  and  $H_{k^*}$  behave identically; on the other hand, if the inputs  $\tilde{r}_i$  are generated by  $G_{n \rightarrow m+2n}$ , then  $\widetilde{H}_{k^*}$  and  $H_{k^*-1}$  behave identically. By the choice of  $k^*$ , we know that  $\widehat{D}$  distinguishes  $H_{k^*}$  and  $H_{k^*-1}$  with non-negligible probability. Hence,  $\widehat{D}_{\text{PRG}}$  distinguishes with (the same) non-negligible probability between the case where the  $\tilde{r}_1, \dots, \tilde{r}_T$  are random and the case where they are generated by  $G_{n \rightarrow m+2n}$ . By Lemma 3.1, this contradicts that  $G_{n \rightarrow m+2n}$  is a PRG. Therefore, our initial supposition was false: that is, there cannot exist a  $\widehat{D}$  which distinguishes between  $\text{lookup}_{n,m}^G(\rho_0, \cdot)$  and  $\mathcal{R}$  with non-negligible probability. The result follows.  $\square$

### 3.1.1 Looking up random values in order.

We would like to look up the random values  $\text{lookup}_{n,m}^G(\rho_0, \cdot)$  *in order*, that is, first  $r_1$ , then  $r_2$ , and so on. This can be done more efficiently than by traversing the tree starting at the root for each new value, essentially by storing the path to the most recently retrieved leaf, and implementing a “next leaf” function which takes the stored path as an input. Naturally, this incurs additional (logarithmic) storage cost, compared to looking up each leaf starting afresh from the root.

An algorithm to find the next leaf in a binary tree given the path to the “current” leaf is given in Algorithm 1. The description given in Algorithm 1 is recursive, for clarity of exposition. Note

<sup>9</sup> This can be done by running  $\widehat{D}$  polynomially many times on the oracles  $H_0, \dots, H_k$ , and taking the adjacent pair  $(k^* - 1, k^*)$  for which there were the most differences in output.

that in practice, there is a more efficient implementation that avoids recursion. The method returns the entire path to the next leaf, rather than just the leaf node, because the path must be passed into the next invocation of the method to obtain the following leaf.

```

1 Path pathToNextLeaf(int depth,
2                       Path currentPath, int currentLeafNum) {
3   if (depth = 1) {
4     Leaf nextLeaf = currentPath.root.rightChild();
5     return currentPath.removeEndNode().append(nextLeaf);
6   } else if (depth > 1) {
7     if (currentLeafNum is even) {
8       Leaf nextLeaf = currentPath.endNode.rightChild();
9       return currentPath.removeEndNode().append(nextLeaf);
10    } else {
11      Path pathToParent = currentPath.removeEndNode();
12      int parentLeafNum = floor(currentLeafNum/2);
13      Path pathToNextParent =
14        pathToNextLeaf(depth-1, pathToParent, parentLeafNum);
15      return pathToNextParent.append(
16        pathToNextParent.endNode.leftChild());
17    }
18  }
19 }

```

Algorithm 1: Recursive method to find next leaf from path to current leaf

**Lemma 3.3.** *For any given depth  $d$ , when Algorithm 1 is used to compute all the leaves of a complete binary tree (of depth  $d$ ) in order, the `leftChild()` and `rightChild()` methods are called exactly once for each non-leaf node in the tree. More precisely, the method employed is the following: in order to obtain the first leaf, the standard method traversing the tree downwards from the root is used; and then subsequent leaves are obtained in order by calling  $\text{pathToLeaf1} = \text{nextLeafPath}(d, \text{pathToLeaf0}, 0)$ , then  $\text{pathToLeaf2} = \text{nextLeafPath}(d, \text{pathToLeaf1}, 1)$ , etc.*

*Proof.* Given in Appendix C. □

In order to apply this method to our lookup tree, we observe that the non-leaf nodes of the lookup tree constitute a binary tree (shown in blue in Figure 2). We show that applying Algorithm 1 to this binary tree allows in-order retrieval of the first  $k$  leaf values in the look-up tree in time  $O(k)$ , for any  $k \in \mathbb{N}$ . The storage requirement is  $\log(k) \cdot \log(n)$  where  $n$  is the number of input bits to  $G$ .

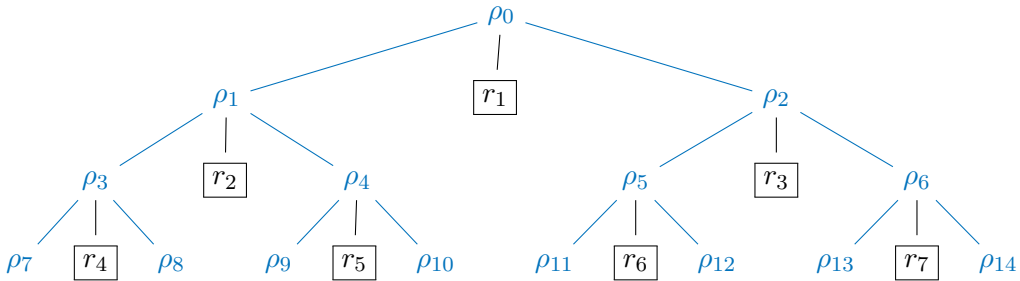


Figure 2: A binary tree within the lookup tree

**Lemma 3.4.** *For any given depth  $d \geq 1$ , when all the output values (boxed nodes) at depth  $d$  of the lookup tree are computed in order (from left to right) by:*

- first, computing the leftmost output value by traversing the tree downwards from the root,

- then, computing each subsequent output value by applying Algorithm 1 to the binary tree of non-leaf nodes up to and including depth  $d - 1$ , and calling the underlying PRG to obtain each actual (leaf) output value,

the total number  $C$  of calls to the underlying PRG  $G$  that is required to compute all the output values at depth  $d$  is exactly  $2^d + 2^{d-1} - 2$ . (Note that in order to use Algorithm 1, the path to the most recently retrieved output node must be stored at all times: this incurs logarithmic storage space.)

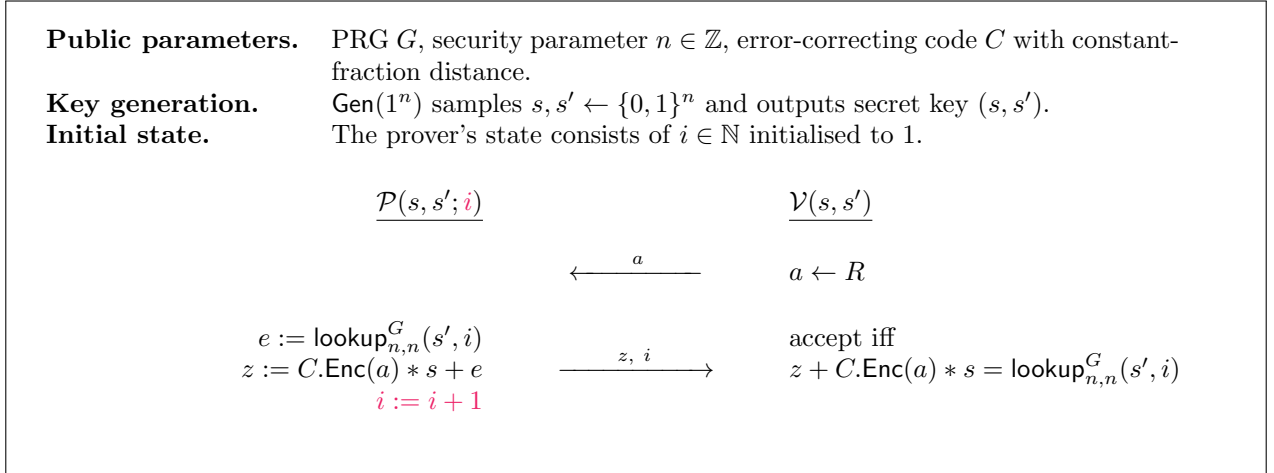
*Proof.* By the construction of the lookup tree, `leftChild()` and `rightChild()` can each be implemented by a single invocation of the PRG  $G$ . By Lemma 3.3, when Algorithm 1 is used to look up all nodes at a particular depth  $d - 1$  in a binary tree (in order), the `leftChild()` and `rightChild()` methods will each be called exactly once for every node at depth less than  $d - 1$  in the tree. Thus,  $G$  will be called twice for every node at depth less than  $d - 1$  in the binary tree. There are  $2^{d-1} - 1$  such nodes, so  $G$  will be called a total of  $2^d - 2$  times while traversing the tree. In addition, there is one invocation of  $G$  per output value, which we have not counted in the above analysis, because it is not within the *binary* tree. There are  $2^{d-1}$  output values at depth  $d$  of the lookup tree, so this adds  $2^{d-1}$  invocations to our total. Hence, the total number of calls of  $G$  required to compute all the output values at depth  $d$  (in order) is  $2^d + 2^{d-1} - 2$ .  $\square$

**Corollary 3.5.** *When computing the values  $\text{lookup}_{n,m}^G(\rho_0, i)$  for  $i = 1, 2, \dots$  (in order) by the method described in Lemma 3.4, the amortised number of calls to  $G$  per output value looked up is constant. To be precise, it is less than 1.5.*

*Proof.* For any given depth  $d > 1$ , there are  $2^{d-1}$  output values at that depth. By Lemma 3.4, all of these values can be looked up with a total of  $2^d + 2^{d-1} - 2$  calls to  $G$ . Hence, the number of calls to  $G$  per output value (at depth  $d$ ) is  $\frac{2^d + 2^{d-1} - 2}{2^d} < 1.5$ .  $\square$

### 3.2 Concurrent man-in-the-middle secure protocol

The protocol construction which follows can be realised with black-box access to any PRG, and achieves concurrent man-in-the-middle security.



Protocol 1: Concurrent MIM secure protocol

The **magenta color** in the protocol indicates (updating of) the prover's state.

**Lemma 3.6.** *Protocol 1 is perfectly complete.*

*Proof.* This is clear since  $\text{lookup}_{n,n}^G$  is deterministic.  $\square$

We first prove that Protocol 1 is actively secure, which serves as a stepping-stone to the proof of concurrent MIM security.

**Lemma 3.7.** *Protocol 1 is secure against active attacks.*

*Proof.* Let  $e_j$  denote the noise string for index  $j$ . Consider the following games:

*Game 1.*  $\mathcal{P}, \mathcal{V}$  and the adversary  $\mathcal{A}$  play the active security game.

*Game 2.*  $\mathcal{P}, \mathcal{V}$  and  $\mathcal{A}$  play the active security game as before, except that  $\mathcal{P}, \mathcal{V}$  no longer know  $s'$ , but instead have oracle access to  $\text{lookup}_{n,n}^G(s', \cdot)$ .

*Game 3.* Like Game 2, but  $\text{lookup}_{n,n}^G(s', \cdot)$  is replaced by a random oracle.

Games 1 and 2 are perfectly indistinguishable for the adversary, since the messages sent by are distributed identically in the two games. Suppose, for contradiction, that there exists an adversary  $\mathcal{A}$  which can efficiently distinguish between Games 2 and 3. Then, this adversary could be used to efficiently distinguish between (oracle access to)  $\text{lookup}_{n,n}^G(s', \cdot)$  and a random oracle – this contradicts Theorem 3.2. Therefore, Games 1, 2, and 3 are computationally indistinguishable, and so the  $e_j$  are indistinguishable from uniformly random noise.

We have established that the prover's message  $z = C.\text{Enc}(a) * s + e_j$  is indistinguishable from  $C.\text{Enc}(a) * s + r$  for random  $r$ . Hence,  $z$  is indistinguishable from random to any active adversary, regardless of the choice of  $a$ . It remains only to consider the interaction of  $\mathcal{A}$  with the honest verifier  $\mathcal{V}$ . Given a challenge  $a$  from  $\mathcal{V}$ ,  $\mathcal{A}$  can have at most negligible advantage at guessing the (unique) value of  $z$  that  $\mathcal{V}$  will accept, as shown by considering the following two cases:

1.  $\mathcal{A}$  sends an index  $i$  that was not used when talking to the honest prover. In this case, we could give the adversary the  $e$  values for this  $i$  for free (as it is independent of the what happens for the other indices). Now the adversary's task is equivalent to guessing  $C.\text{Enc}(a) * s$ , which he cannot do since he has no information about  $s$ .
2.  $\mathcal{A}$  sends an index  $i$  that was previously used in a query to the prover. Let  $z, i$  be the response (to  $a$ ) from the honest prover. Say the honest verifier sends  $a'$  and let  $z', i$  be the adversary's response. If there is a non-negligible probability that  $z'$  is accepted, then it follows that  $z - z' = (C.\text{Enc}(a) - C.\text{Enc}(a')) * s$ . This happens with negligible probability since all of  $a, a', z, z'$  were chosen independently of  $s$ .

□

**Theorem 3.8.** *Protocol 1 is secure against concurrent MIM attacks.*

*Proof.* We show that if there is an adversary  $\mathcal{A}$  which achieves a certain advantage when conducting a concurrent MIM attack, then there is another adversary  $\mathcal{A}'$  that *only talks to the prover* in Protocol 1 and achieves essentially the same advantage. First, we replace the honest verifier by a fake verifier  $\mathcal{V}'$  who has no access to  $s$  or the  $e_j$  but still gives essentially the same answers as  $\mathcal{V}$ . Then we argue that for any concurrent MIM attack, there is an equally successful *active* attack, and finally refer to Lemma 3.7 for the active security of the protocol.

$\mathcal{V}'$  works as follows: when queried by  $\mathcal{A}$ , it chooses a random challenge  $a$  (just like  $\mathcal{V}$  does). When  $\mathcal{A}$  returns an answer  $z, j$ , there are two cases to consider:

1.  $\mathcal{A}$  previously received answer  $z', j$  from  $\mathcal{P}$ , where  $z' = C.\text{Enc}(a') * s + e_j$  and  $a'$  is  $\mathcal{A}$ 's query to  $\mathcal{P}$ . Here we have two possibilities:
  - (a)  $a = a'$  (which could be the case if  $\mathcal{A}$  queried  $\mathcal{P}$  during the current protocol execution): in this case, if  $z = z'$ ,  $\mathcal{V}'$  accepts; else, it rejects.
  - (b)  $a \neq a'$ :  $\mathcal{V}'$  always rejects.
2.  $\mathcal{A}$  never previously received an answer of the form  $z', j$  from  $\mathcal{P}$ . In this case  $\mathcal{V}'$  always rejects.

Consider the first time  $\mathcal{A}$  queries the verifier.  $\mathcal{V}'$ 's challenge is distributed identically to that of  $\mathcal{V}$ . In case 1a,  $\mathcal{V}$  will accept if and only if  $z$  has the correct value  $C.\text{Enc}(a) * s + e_j$ : so  $\mathcal{V}'$  always makes the same decision as  $\mathcal{V}$ . In case 1b,  $\mathcal{V}$  only accepts if  $z = C.\text{Enc}(a) * s + e_j$ , but since

$z' = C.\text{Enc}(a') * s + e_j$  it must be that  $(z - z') = (C.\text{Enc}(a) - C.\text{Enc}(a')) * s$ . This happens with negligible probability because  $s$  is random and  $z, z', a, a'$  are all independent of  $s$ :  $\mathcal{P}$ 's responses, including  $z'$ , are independent of  $s$ ; and since this is the first query,  $\mathcal{V}$  has not seen  $s$  yet, so  $a, a'$  and  $z$  must be independent of  $s$  too. Thus,  $\mathcal{V}$  rejects with overwhelming probability, so  $\mathcal{V}'$  is statistically close to the right behavior. Finally, in case 2, no one sees  $e_j$  before  $\mathcal{A}$  produces  $z, j$ . If  $\mathcal{V}$  accepts, we have  $z = C.\text{Enc}(a) * s + e_j$ , so  $e_j = z - C.\text{Enc}(a) * s$ , which happens with negligible probability since  $z, a$  and  $s$  are independent of  $e_j$ .

Therefore, we can replace  $\mathcal{V}$  with  $\mathcal{V}'$  for the first query, and  $\mathcal{A}$ 's advantage changes at most negligibly as a result. Repeating this argument for all the queries, we reach the game where  $\mathcal{V}$  is entirely replaced by  $\mathcal{V}'$ , and  $\mathcal{A}$ 's advantage is still at most negligibly different from in the original game. Since  $\mathcal{V}'$  does not possess any secret information, an adversary can run  $\mathcal{V}'$  “in his head”. So for any adversary  $\mathcal{A}$  which has non-negligible advantage in a man-in-the-middle attack, we can construct an adversary  $\mathcal{A}'$  that emulates both  $\mathcal{A}$  and  $\mathcal{V}'$  “in his head” and achieves the same advantage, but conducting an *active* attack (since he need not interact with the real verifier  $\mathcal{V}$ ). The result then follows from Lemma 3.7.  $\square$

### 3.2.1 Linear-time implementation.

The prover in Protocol 1 can run in time  $O(n)$  and space  $O(\log(n) \cdot \log(k))$ , where  $k$  is the number of protocol executions run so far<sup>10</sup>: this is possible by using Algorithm 1 to compute  $\text{lookup}_{n,n}^G(\cdot, \cdot)$  as described in Lemma 3.4. This follows from Corollary 3.5, and the fact that there exist linear-time, linear-stretch PRGs and linear-time encodable codes with constant-factor expansion and large constant-fraction distance (such as those of Guruswami and Indyk [GI05]).

The verifier can also be implemented to run in linear time *for honest executions*, by using the same method as the prover to compute  $\text{lookup}_{n,n}^G(\cdot, \cdot)$ . Clearly, if the prover is honest, the verifier will run in linear time. If the prover cheats and breaks the sequence, then the verifier can retrieve the required  $\text{lookup}_{n,n}^G(\cdot, \cdot)$  value by the “backup method” of traversing the lookup tree downwards from the root, which takes  $O(n \cdot \log(k))$  time instead. This implementation requires  $N \cdot O(\log(n) \cdot \log(k))$  space, where  $N$  is the number of different provers with which the verifier interacts. Note that since the multiplication can be done in depth  $O(\log(n))$ , if the PRG  $G$  is of poly-logarithmic depth, then the verifier does only poly-logarithmic depth computation (even when the prover cheats).

### 3.3 $\ell$ -instance concurrent MIM secure protocol

Building upon the ideas of Protocol 1, our next protocol achieves  $\ell$ -instance concurrent MIM security for any polynomial  $\ell$ . Moreover, if  $\ell$  is constant, we can still get (amortised) linear time. The next protocol makes use of  $\ell$ -wise independent hashing, which is defined below.

**Definition 3.9** ( *$\ell$ -wise independent hash function family*). *A function family  $\mathcal{H}$  of functions that map  $n$  bits to  $m$  bits is a  $\ell$ -wise independent hash function family if for all  $y_1, \dots, y_\ell \in \{0, 1\}^m$  and for all distinct  $x_1, \dots, x_\ell \in \{0, 1\}^n$ , it holds that  $\Pr_{h \leftarrow \mathcal{H}} [h(x_1) = y_1 \wedge h(x_2) = y_2 \wedge \dots \wedge h(x_\ell) = y_\ell] = 2^{-\ell m}$ .*

**Lemma 3.10.** *Protocol 2 is perfectly complete.*

*Proof.* This is clear since  $\text{lookup}_{n,(\ell+1) \cdot n + \beta}^G$  is deterministic.  $\square$

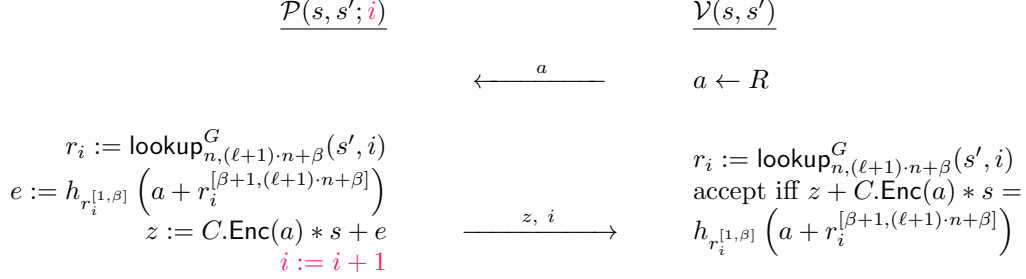
Our security proofs follow a similar structure to those of Protocol 1: we first prove  $\ell$ -instance active security, then use this to prove  $\ell$ -instance concurrent MIM security. The proofs of Lemma 3.11 and Theorem 3.12 are given in Appendix D, due to space constraints. We remark that the proof of Theorem 3.12 is very similar to that of Theorem 3.8

<sup>10</sup>In other words,  $k$  is the number of leaf values in the lookup tree that have been retrieved so far. Note that if desired, the value of  $k$  can be upper-bounded by some polynomial-size  $K$ , by “starting a new tree” after  $K$  values have been retrieved from the initial tree: the  $(K + 1)^{\text{th}}$  leaf value in the first tree serves as the root of a new tree in which subsequent lookups are done. This technique was suggested in [Gol01].

**Public parameters.**  $\ell = \text{poly}(n)$ , PRG  $G$ , security parameter  $n \in \mathbb{Z}$ , error-correcting code  $C$  with constant-fraction distance, function family  $\mathcal{H} = \{h_r\}_{r \in \{0,1\}^\beta}$  of  $2\ell$ -wise independent hash functions mapping  $(\ell + 1) \cdot n$  bits to  $n$  bits.

**Key generation.**  $\text{Gen}(1^n)$  samples  $s \leftarrow R, s' \leftarrow \{0,1\}^n$  and outputs  $(s, s')$ .

**Initial state.** The prover's state consists of  $i \in \mathbb{N}$  initialised to 1.



Protocol 2:  $\ell$ -instance concurrent MIM secure protocol

**Lemma 3.11.** *Protocol 2 is secure against  $\ell$ -instance active attacks.*

**Theorem 3.12.** *Protocol 2 is secure against  $\ell$ -instance concurrent MIM attacks.*

### 3.3.1 Linear-time implementation.

If  $\ell$  is constant, then the prover in Protocol 2 can run in (amortised) time  $O(n)$  and space  $O(\log(n) \cdot \log(k))$ , where  $k$  is the number of protocol executions run so far: as with Protocol 1, this requires the use of Algorithm 1 to compute  $\text{lookup}_{n, n}^G(\cdot, \cdot)$ , and the use of a linear-time PRG and linear-time encodable code. In addition, we require an  $\ell$ -wise independent hash function family whose functions can be sampled and computed in linear time. A construction of a hash function family satisfying these properties for constant  $\ell$  is given in Section E. As in the case of Protocol 1, the verifier in Protocol 2 can also be implemented to run in linear time when the prover is honest.

### 3.3.2 On achieving unbounded multi-instance MIM security.

Consider the PRG  $G$  as a mapping from  $n$ -bit seeds to  $n$ -bit outputs. Then clearly the string  $G(s_1), \dots, G(s_t)$  is pseudorandom for any polynomial  $t$ , if the  $s_i$ 's are independent and random. We say that  $G$  is  $(\mathcal{H}, \ell)$ -wise secure if this still holds, even if the  $s_i$  are not independent, but are chosen from an  $\ell$ -wise independent distribution generated by a hash function from  $\ell$ -wise independent family  $\mathcal{H}$  – more precisely, any subset of  $\ell$  seeds are uniformly random and independent, and  $s_i = h(x_i)$  for  $h$  chosen at random from  $\mathcal{H}$  and distinct  $x_i$ . We can now define a small change to Protocol 2: namely, in Step 2 of the Prover's computation,  $e$  is computed as

$$e := G \left( h_{r_i^{[1, \beta]}} \left( a + r_i^{[\beta+1, (\ell+1) \cdot n + \beta]} \right) \right).$$

(That is, we do the same as before, but apply  $G$  at the end.) Now, assuming  $G$  is strongly  $(\mathcal{H}, \ell)$ -wise secure, where  $\mathcal{H}$  is the hash function family used in the protocol, we will get pseudorandom output after applying  $G$ , no matter how (polynomially) many outputs we generate, As a result this variant of the protocol is Multi-instance MIM secure (with an unbounded number of instances), if  $G$  is  $(\mathcal{H}, \ell)$ -wise secure. If  $G$  has this property even for a constant  $\ell$ , the protocol can run in linear time, by using the  $\ell$ -wise independent hash functions described in Appendix E.

It is easy to see that if the adversary in fact does not invoke more than  $\ell$  instances of the prover, then this protocol is secure based *only* on the assumption that  $G$  is a secure PRG. This is simply because  $\ell$ -wise independence of  $\mathcal{H}$  implies that in such a case  $G$  is in fact invoked on independent

seeds. Therefore the protocol has “graceful degradation”: it is bounded instance MIM secure for at most  $\ell$  instances assuming only that  $G$  is a PRG, and the same protocol is also unbounded instance secure under the stronger security notion.

We emphasise that the  $(\mathcal{H}, \ell)$  security notion for a PRG is new and its plausibility depends very much on how the concrete PRG and hash function family relate to each other. It should therefore not be used without a careful analysis of the building blocks.

## 4 Comparison with AES-based protocol

Returning to a more practical perspective, we consider how our protocol would perform when instantiated with a stream cipher acting as the PRG. Since fine-tuning the efficiency of one-sided authentication is particularly relevant for lightweight hardware devices, we focus on a hardware-oriented cipher and compare performance at the relatively low 100kHz clock frequency which is common in low-end devices. By the same reasoning, we analyse the *prover’s* performance which is the bottleneck in such applications.

Our analysis considers our protocol instantiated with the Grain-128 stream cipher as the PRG. We selected Grain-128 from the eSTREAM (Profile 2)<sup>11</sup> portfolio because it is highly optimised for hardware implementation with high throughput, and it has a mode with 128 bits of security which is good for comparison to AES with 128-bit keys. Moreover, Grain has the advantage of a relatively fast initialization time compared to other Profile 2 candidates such as Trivium [FW08]. Specifically, our benchmarks are based on the performance of Grain128x32 and a comparable hardware implementation of AES (by [SMTM01]), as documented in [GB08, Table 4].

An interesting initial observation is that if one uses Grain128x32 in the GGM construction of a PRF, then the speed of a Grain-based PRF evaluation is “only” about 20 times slower than an AES evaluation. This highlights that the efficiency advantage of stream ciphers over block ciphers is a major factor that counterbalances the relative inefficiency of the GGM-style construction that is inherent in our protocols.

Indeed, as shown in Table 1, our protocol outperforms the simple AES-based protocol) *in terms of prover computation time* by a factor of three when instantiated with 2048 bits of prover state. Our figures do not account for write time to update state, in order to have a fairer comparison of the computation times, since write times vary greatly across different chips / types of memory.<sup>12</sup>

Simple PRF-based protocol		Our protocol	
AES	Grain (+GGM)	Grain (32-bit state)	Grain (2048-bit state)
0.54	10.24	3.84	0.18

Table 1: Amortised prover *computation time* (in ms) in two-round authentication

**Size of the prover’s state.** In Table 1, “32-bit state” refers to the scenario when the prover stores only a (32-bit) counter value, whereas “2048-bit state” refers to the variant when the prover stores partial paths in the modified GGM tree.

**Remarks on protocol implementation.** Note that in software, using a more standard MAC of form  $a \cdot s + e$ , where the product is in the field with  $2^n$  elements, may be an advantage (even if it is not preferable asymptotically). For instance, for  $n = 64$ , we can multiply by a fixed element  $s$  using 8 table look-ups in 16 KB of precomputed tables. However, if the prover is a small hardware device, whereas the verifier has more power, then using the MAC  $C(a) * s + e$  that we suggest may

<sup>11</sup>Profile 2 ciphers are described as “Stream ciphers for hardware applications with restricted resources such as limited storage, gate count, or power consumption.”

<sup>12</sup>To give a very rough estimate: based on the specifications of common RFID manufacturers, the write time to non-volatile memory is likely to range between less than millisecond to a few milliseconds, but varies depending on the exact hardware you want.



be even better, since we can ask the verifier to send the encoding  $C(a)$  rather than  $a$ . Our proof of security still applies if the prover checks that he receives a codeword, and this can be extremely fast in hardware if we use an LDPC (low-density parity check) code: the codeword check consists of computing parity functions of a small constant (say, 3 or 4) number of bits, and all the parity functions could be computed in parallel. This reduces the prover’s computation essentially to the cost of running the stream cipher (and we have assumed that this is the case, in computing the figures in Table 1).

**Remark on counter mode.** The use of a counter in our protocol may prompt the question: if we use a stream cipher with a nonce in counter mode, then could we run the stream cipher on consecutive counter values instead of evaluating the `lookup` function? The answer is that such a scheme would only be secure if the stream cipher were *secure against adversarially chosen nonces*. The standard assumption about stream ciphers is that nonces need not be kept secret, but are chosen honestly (rather than adversarially) by the party in possession of the key [Sch95]. The use of the `lookup` function allows us to base our protocol on *any* PRG, or *any* stream cipher.

The behaviour of stream ciphers under adversarially chosen nonces seems to be little studied/understood: for example, the eSTREAM cipher candidates do not discuss the possibility of adversarial nonces. Assuming security under adversarial nonces is equivalent to assuming *PRF security* of a stream cipher, rather than PRG security. The only instance of such an assumption being used in the literature, of which we are aware, is [BEG10] which proposes a (three-round) authentication protocol based on “PRF security of a stream cipher”, but gives no discussion of why such an assumption may be reasonable. If, somehow, one has a stream cipher satisfying this strong assumption, then our protocol would yield prover computation time of 0.12 ms with a 32-bit state. We mention this topic as an academic point, and we stress that we do not consider this to be a safe option for use with arbitrary (or perhaps any) stream ciphers.

## 5 Conclusion

We have proposed several variants of a two-round authentication protocol that can be instantiated using *any* pseudorandom generator, and have (optimal) linear time complexity with a logarithmic space requirement. We defined and proved full concurrent man-in-the-middle security in the prover-stateful model, and also showed that our variant protocols satisfy stronger *multi-instance* security. A key tool in our construction is the design of PRF-like function based on a black-box PRG, which has very efficient lookups when inputs are queried in sequential order, which may be useful in other settings. Finally, a preliminary analysis shows that the protocol is efficient enough that it is possible to implement on lightweight hardware devices.

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## A Pseudorandom primitives

We give the standard definitions of pseudorandom generators (PRGs) and pseudorandom function families (PRFs).

**Definition A.1** (Pseudorandom generator). *Let  $G : \{0, 1\}^n \rightarrow \{0, 1\}^{m(n)}$  be a deterministic polynomial-time algorithm.  $G$  is a pseudorandom generator (PRG) if  $m(n) > n$  and for any efficient distinguisher  $D$  that outputs a single bit, it holds that  $|\Pr[D(r) = 1] - \Pr[D(G(s)) = 1]| \leq \text{negl}(n)$ , where  $r \leftarrow \{0, 1\}^{m(n)}$ ,  $s \leftarrow \{0, 1\}^n$  are chosen uniformly at random, and the probabilities are taken over  $r$ ,  $s$ , and the random coins of  $D$ .*

It is well known that any pseudorandom generator implies pseudorandom generation with any polynomial expansion factor  $m(n)$ , by applying the PRG to its own output repeatedly.

**Definition A.2** (Pseudorandom function family (PRF)). *Let  $\mathcal{F} = \{F_K\}$  be family of deterministic polynomial-time keyed algorithms mapping  $n$  bits to  $m$  bits.  $\mathcal{F}$  is a pseudorandom function family (PRF), if for any efficient distinguisher  $D$  that outputs a single bit, it holds that  $|\Pr[D^{F_K} = 1] - \Pr[D^{\mathcal{R}_{n \rightarrow m}} = 1]| \leq \text{negl}(n)$ , where  $\mathcal{R}_{n \rightarrow m}$  is a random oracle mapping  $n$  bits to  $m$  bits, and the probabilities are taken over the random coins of  $D$  and the key  $K$  which is randomly chosen.*

## B Formal specification of hybrid $\tilde{H}_{k^*}$

In this section we give a formal description of the algorithm  $\tilde{H}_{k^*}$  used in the proof of Theorem 3.2.  $\tilde{H}_{k^*}$  takes as input  $(\tilde{r}_1, \dots, \tilde{r}_T)$ , and then behaves exactly like  $H_{k^*}$ , except in the following aspects:

- when  $\tilde{H}_{k^*}$  is initialised, it sets a variable  $\text{next} := 0$ ; and
- if  $\lfloor \log_2(i) \rfloor + 1 \geq k^*$  (that is, the depth of the output node for query  $i$  is at least  $k^*$ ) then  $\tilde{H}_{k^*}$  first stores the three tuples

$$\begin{aligned} & (\text{leaf}, \ell, (\tilde{r}_{\text{next}})^{[1,m]}), \\ & (\text{root}, \alpha_0, (\tilde{r}_{\text{next}})^{[m+1,m+n]}), \\ & (\text{root}, \alpha_0 + 1, (\tilde{r}_{\text{next}})^{[m+n+1,m+2n]}), \end{aligned}$$

where  $\alpha_0, \ell$  are defined by

$$\begin{aligned} \alpha_0 &= \begin{cases} 2 \cdot (i - 2^{\lfloor \log_2(i) \rfloor}) - 1 & \text{if } \lfloor \log_2(i) \rfloor + 1 = k^* \\ 2 \cdot \lfloor \alpha_{(i,k^*)} / 2 \rfloor & \text{otherwise} \end{cases}, \\ \ell &= \begin{cases} i & \text{if } \lfloor \log_2(i) \rfloor + 1 = k^* \\ 2^{(k^*-1)} + (\alpha_0 / 2) & \text{otherwise} \end{cases}; \end{aligned}$$

then  $\tilde{H}_{k^*}$  increments  $\text{next}$  by 1, and outputs  $\rho_i$ , defined by:

$$\rho_i = \begin{cases} (\tilde{r}_{\text{next}})^{[1,m]} & \text{if } \lfloor \log_2(i) \rfloor + 1 = k^* \\ \text{lookup}_{n,m}^G((\tilde{r}_{\text{next}})^{[m+1,m+n]}, \gamma_{(i,j^*)}) & \text{if } \alpha_{(i,k^*)} = \alpha_0 \\ \text{lookup}_{n,m}^G((\tilde{r}_{\text{next}})^{[m+n+1,m+2n]}, \gamma_{(i,j^*)}) & \text{if } \alpha_{(i,k^*)} = \alpha_0 + 1 \end{cases}.$$

In terms of the tree representation of the pseudorandom look-up function:  $\tilde{H}_{k^*}$  behaves exactly like  $H_{k^*}$ , except that the values associated with nodes at depth  $k^*$  are taken from the input values  $\tilde{r}_1, \dots, \tilde{r}_T$ . Note that although the number of nodes at depth  $k^*$  may be greater than  $T$ ,  $\text{next}$  can never become greater than  $T$  during an execution of  $\hat{\text{D}}_{\text{PRG}}$ , because  $\hat{\text{D}}$  cannot make more than  $T$  queries: therefore,  $\tilde{r}_{\text{next}}$  is always well-defined.

## C Ordered traversal of leaves of a binary tree

**Lemma 3.3.** *For any given depth  $d$ , when Algorithm 1 is used to compute all the leaves of a complete binary tree (of depth  $d$ ) in order, the `leftChild()` and `rightChild()` methods are called exactly once for each non-leaf node in the tree. More precisely, the method employed is the following: in order to obtain the first leaf, the standard method traversing the tree downwards from the root is used; and then subsequent leaves are obtained in order by calling `pathToLeaf1 = nextLeafPath(d, pathToLeaf0, 0)`, then `pathToLeaf2 = nextLeafPath(d, pathToLeaf1, 1)`, etc.*

*Proof.* In order to obtain the the first leaf node (and the path thereto), we need to call `leftChild()` exactly once on all nodes along that path. When  $d = 1$ , this means that `leftChild()` is called on the root node when obtaining the first leaf node. Moreover, when  $d = 1$ , it is clear (from lines 2-4) that `rightChild()` is called on the root node exactly once (when obtaining the second leaf node). Hence, the lemma holds for  $d = 1$ .

For  $d > 1$ , we argue by induction. It is sufficient to prove that for any  $d > 1$ :

1. `pathToNextLeaf` is called<sup>13</sup> exactly once for every node at depth  $d - 1$ , except the final node at depth  $d - 1$  (since for that node, there is no next leaf); and

<sup>13</sup> To be precise, when we write “`pathToNextLeaf` is called for a given node” we mean that `pathToNextLeaf(depth, path, leafNum)` is called, where `depth` is the depth of the node in the tree, `path` is the path from the root to that node, and `leafNum` is the number of the node when counting the nodes at depth `depth` from left to right.

2. `leftChild()` and `rightChild()` are called exactly once on each node at depth  $d - 1$ .

We call `nextLeafPath()` once for each `currentLeafNum`  $\in \{0, \dots, 2^{\text{depth}} - 2\}$  (from the statement of the lemma). In particular, `nextLeafPath()` is called once for the left child of each node at depth  $\text{depth} - 1$  (these nodes are exactly those for which `currentLeafNum` is even). From lines 6-8, it follows that for every node at depth  $\text{depth} - 1$ , the `rightChild()` method is called exactly once at line 7.

The recursive call to `pathToNextLeaf()` occurs on line 12, and is only executed when `currentLeafNum` is odd (by the if-clause on lines 6-14). To be precise, the odd values of `currentLeafNum` for which we run `pathToNextLeaf()` are  $\{1, 3, \dots, 2^{\text{depth}} - 3\}$ . This corresponds exactly to the set of right-children of nodes at depth  $\text{depth} - 1$ , except the last one. We see on line 12 that the path passed into the recursive call is `pathToParent`, the path to the parent of the current node. Thus, `pathToNextLeaf()` is recursively called exactly once for each node at depth  $\text{depth} - 1$ , except the last one. This satisfies condition 1.

Finally, for each path (of depth  $\text{depth} - 1$ ) which is *returned* by a recursive call to `pathToNextLeaf()`, `leftChild()` is called on the end node of the path (on line 13). Since we have already established that `pathToNextLeaf()` is recursively called exactly once for each node at depth  $\text{depth} - 1$ , except the last node, and the functionality of `pathToNextLeaf()` is to return the next node at a given depth, it follows that the paths returned by such recursive calls to `pathToNextLeaf()` lead to each node at depth  $\text{depth} - 1$ , except the *first* node. We conclude that `leftChild()` is called exactly once for each node at depth  $\text{depth} - 1$ , except the first node. Moreover, `leftChild()` is called on the first node at depth  $\text{depth} - 1$  when we initially retrieve the first leaf. So, `leftChild()` is called exactly once on each node at depth  $\text{depth} - 1$ . In conjunction with our earlier observations about calls to `rightChild()`, this means that condition 2 is satisfied. The result follows.  $\square$

## D Proof of $\ell$ -instance concurrent MIM security of Protocol 2

In this section, we show the  $\ell$ -instance concurrent MIM security of Protocol 2. We require the following technical lemma, which can be seen as a generalization of the leftover hash lemma and has a similar proof.

**Lemma D.1** ([DFMV13]). *Let  $(X_1, X_2, \dots, X_\ell) \in \mathcal{X}^\ell$  be  $\ell$  (possibly dependent) random variables such that  $H_\infty(X_i) \geq \gamma$  and  $(X_1, \dots, X_\ell)$  are pairwise different. Let  $\mathcal{H} = \{h : \mathcal{X} \rightarrow \mathcal{Y}\}$  be a family of  $2\ell$ -wise independent hash functions, with  $|\mathcal{Y}| = 2^k$ . Then for random  $h \leftarrow \mathcal{H}$  we have that the statistical distance satisfies*

$$\Delta((h, h(X_1), h(X_2), \dots, h(X_\ell)); (h, U_{\mathcal{Y}}^1, \dots, U_{\mathcal{Y}}^\ell)) \leq \frac{\ell}{2} \cdot 2^{-(\ell \cdot k - \gamma)/2},$$

where  $U_{\mathcal{Y}}^1, \dots, U_{\mathcal{Y}}^\ell$  are  $\ell$  independent and uniformly distributed variables.

We now prove two supporting lemmas, before the main theorem.

**Lemma 3.11.** *Protocol 2 is secure against  $\ell$ -instance active attacks.*

*Proof.* Recall that an  $\ell$ -instance active adversary may have concurrent access to up to  $\ell$  honest provers, but as usual, he cannot reset the provers. The updating of the prover's state in Protocol 2 ensures that for any given prover, the value  $r_i$  is freshly pseudorandomly sampled for each execution (that is, each value of the counter  $i$ ). In particular, the hash function seed  $r_i^{[1, \beta]}$  is freshly pseudorandomly sampled for each value of  $i$ , and this means that for any polynomial number of executions, with overwhelming probability, all the hash function seeds will be distinct. Therefore, an  $\ell$ -instance active adversary can obtain at most  $\ell$  outputs of the hash function  $h_s$  for any given seed  $s$ . For any  $i \in \mathbb{N}$ , let  $s \stackrel{\text{def}}{=} r_i^{[1, \beta]}$  be the corresponding seed, and let  $x \stackrel{\text{def}}{=} r_i^{[\beta+1, (\ell+1) \cdot n + \beta]}$  be the summand inside the hash function argument. Suppose that the adversary obtains samples  $h_s(a_1 + x), \dots, h_s(a_\ell + x)$  from the honest provers. If the adversary chooses some  $a_i, a_j$  to be equal,

then the samples  $h_s(a_i + x), h_s(a_j + x)$  will also be equal, so the adversary will not gain more information than if he made just one query  $a_i$ . Hence, we assume without loss of generality that the  $a_1, \dots, a_\ell$  are distinct.

Since  $G$  is a PRG, we can replace  $s$  and  $x$  with uniformly randomly chosen  $s' \leftarrow \{0, 1\}^\beta$  and  $x' \leftarrow \{0, 1\}^{(\ell+1)\cdot n}$ , and the adversary's advantage will change at most negligibly. Let  $U_{(\ell+1)\cdot n}$  be a random variable that is uniformly distributed over the set of  $((\ell+1)\cdot n)$ -bit strings. Consider the random variables  $U_{(\ell+1)\cdot n} + a_1, U_{(\ell+1)\cdot n} + a_2, \dots, U_{(\ell+1)\cdot n} + a_\ell$ . Each variable  $U_{(\ell+1)\cdot n} + a_i$  has entropy  $H(U_{(\ell+1)\cdot n} + a_i) = H_\infty(U_{(\ell+1)\cdot n} + a_i) = (\ell+1)\cdot n$ . They are not independent, but they are pairwise different because the  $a_i$ 's are distinct. Therefore, by Lemma D.1,

$$\{h_s, h_s(U_{(\ell+1)\cdot n} + a_1), \dots, h_s(U_{(\ell+1)\cdot n} + a_\ell)\} \stackrel{s}{\approx} \{h_s, U_n^1, \dots, U_n^\ell\}.$$

(In the notation of the lemma: we have  $k = n$  and  $\gamma = (\ell+1)\cdot n$ , so the distance is  $\frac{\ell}{2}2^{-n}$ , which is negligible given that  $\ell = \text{poly}(n)$ .)

Finally, since the seeds  $s_i \stackrel{\text{def}}{=} r_i^{[1,\beta]}$  are freshly pseudorandomly sampled for each value of  $i$ , the hash functions  $h_{s_1}, h_{s_2}, \dots$  used in the protocol are indistinguishable from independent random hash functions. Therefore, the output of any hash function  $h_{s_i}$  is indistinguishable from random even given the outputs of the other hash functions  $h_{s_{i'}}, h_{s_{i''}}, \dots$  from different executions of the protocol.  $\square$

**Lemma D.2.** *For any two-round authentication protocol in which the verifier sends the first message, and where the verifier's accept/reject decision is a deterministic function of the secret key, the initial message of the verifier, and the prover's response: it holds that any adversary  $\mathcal{A}$  with access to multiple honest verifiers  $\mathcal{V}_1, \dots, \mathcal{V}_\ell$  can be perfectly simulated by another adversary  $\mathcal{A}'$  with access to only one honest verifier  $\mathcal{V}$*

*Proof.* The simulation works as follows:  $\mathcal{A}'$  runs  $\mathcal{A}$ , and for every protocol session that  $\mathcal{A}$  begins with honest verifier  $\mathcal{V}_j$ ,  $\mathcal{A}'$  starts a new session with verifier  $\mathcal{V}$  and forwards the initial message  $a$  of  $\mathcal{V}$  to  $\mathcal{A}$ . Then, when  $\mathcal{A}$  returns to the open session with  $\mathcal{V}_j$  and sends a response  $b$ ,  $\mathcal{A}'$  returns to the corresponding session with  $\mathcal{V}$  and forwards  $b$  to  $\mathcal{V}$ ; and finally,  $\mathcal{A}'$  returns to  $\mathcal{A}$  the accept/reject decision of  $\mathcal{V}$ . This is a perfect simulation since for any session, the verifier's decision is a deterministic function of the secret key, the initial message  $a$  and the (adversarial) prover's response  $b$ .  $\square$

**Theorem 3.12.** *Protocol 2 is secure against  $\ell$ -instance concurrent MIM attacks.*

*Proof.* We show that given an adversary  $\mathcal{A}$  which achieves a certain advantage when conducting an  $\ell$ -instance concurrent MIM attack, it is possible to build a new adversary  $\mathcal{A}''$  that *only talks to the  $\ell$  provers* and achieves essentially the same advantage.

By Lemma D.2,  $\mathcal{A}$  can be perfectly simulated with access to just one honest verifier, so we assume henceforth that there is only one honest verifier  $\mathcal{V}$ . Next, we replace the single honest verifier  $\mathcal{V}$  by a fake verifier  $\mathcal{V}'$  who has no access to  $s$  or the  $e$  values, but still gives essentially the same answers as  $\mathcal{V}$ . Then we argue that for any  $\ell$ -instance concurrent man-in-the-middle attack, there is an equally successful  $\ell$ -instance *active* attack, and finally refer to Lemma 3.11 for the  $\ell$ -instance active security of the protocol.

$\mathcal{V}'$  works as follows: when queried by  $\mathcal{A}$ , it chooses a random challenge  $a$  (just like  $\mathcal{V}$  does). When  $\mathcal{A}$  returns an answer  $z, j$  (i.e. the second protocol message), there are two cases to consider:

1.  $\mathcal{A}$  previously received an answer  $z', j$  from  $\mathcal{P}$ , where  $z' = a' \cdot s + e_j$  and  $a'$  is  $\mathcal{A}$ 's query to  $\mathcal{P}$ . Here we have two possibilities:
  - (a)  $a = a'$  (which could be the case if  $\mathcal{A}$  queried  $\mathcal{P}$  during the current protocol execution): in this case, if  $z = z'$ ,  $\mathcal{V}'$  accepts; else, it rejects.
  - (b)  $a \neq a'$ :  $\mathcal{V}'$  always rejects.
2.  $\mathcal{A}$  never previously received an answer of the form  $z', j$  from  $\mathcal{P}$ . In this case  $\mathcal{V}'$  always rejects.

Consider the first time  $\mathcal{A}$  queries the verifier. The challenge produced by  $\mathcal{V}$  has exactly the same distribution as the one  $\mathcal{V}'$  outputs. Now, in case 1a, notice that  $\mathcal{V}$  will accept if and only if  $z$  has the correct value  $a \cdot s + e_j$ : so  $\mathcal{V}'$  always makes the same decision as  $\mathcal{V}$ . In case 1b,  $\mathcal{V}$  rejects except with negligible probability, so  $\mathcal{V}'$  is statistically close to the right behavior. This is because accepting would imply that  $z = a \cdot s + e_j$ , but we also have  $z' = a' \cdot s + e_j$  so then  $s = (z - z')(a - a')^{-1}$ . This happens with negligible probability because  $s$  is random and  $z, z', a, a'$  are all independent of  $s$ . This holds because all of  $\mathcal{P}$ 's responses (including  $z'$ ) are independent of  $s$ . Moreover, since this is the first query,  $\mathcal{V}$  has not even looked at  $s$  yet, so  $a, a'$  and  $z$  must be independent of  $s$  too. Finally, in case 2, note that no one sees  $e_j$  before the adversary produces  $z, j$ . If  $\mathcal{V}$  accepts, we have  $z = a \cdot s + e_j$ , so  $e_j = z - a \cdot s$ , which happens with negligible probability since  $z, a$  and  $s$  are independent of  $e_j$ .

Therefore, we can modify  $\mathcal{V}$  so that it behaves like  $\mathcal{V}'$  for the first query, and the adversary's advantage changes at most negligibly as a result. Repeating the same argument for all the queries, we reach the game where  $\mathcal{V}$  is entirely replaced by  $\mathcal{V}'$ , and the adversary's advantage is still at most negligibly different from in the original game.

Since  $\mathcal{V}'$  does not possess any secret information, an adversary can run  $\mathcal{V}'$  "in his head". So for any adversary  $\mathcal{A}$  which has non-negligible advantage in a  $\ell$ -instance concurrent MIM attack, we can construct an adversary  $\mathcal{A}''$  that emulates both  $\mathcal{A}$  and  $\mathcal{V}'$  "in his head" and achieves the same advantage, but conducting an  $\ell$ -instance *active* attack (since he need not interact with the real verifier  $\mathcal{V}$ ).

Finally, the security of the protocol against  $\ell$ -instance concurrent man-in-the-middle attacks follows from the security against  $\ell$ -instance concurrent active attacks, which was shown in Lemma 3.11.  $\square$

## E Linear-time $\ell$ -independent hashing

Mansour, Nisan, and Tiwari [MNT90] conjectured that pairwise-independent hash functions cannot be computed in linear time – in particular, that computing them requires  $\Omega(n \log(n))$  time. Recently, Ishai et al. [IKOS08] disproved this conjecture, and gave a construction of a linear-time computable pairwise-independent hash function. In this section, we show that the [IKOS08] construction can be extended to achieve  $\ell$ -wise independence for constant  $\ell \in \mathbb{N}$ .

**Notation.** For a vector  $v \in \{0, 1\}^n$  and a subset of indices  $S \subseteq [n]$ , we write  $v_{[S]}$  to denote the  $|S|$ -bit vector obtained by restricting  $v$  to the coordinates in  $S$ . For a tuple of indices  $t = (t_1, \dots, t_d) \in [n]^d$ , we write  $v_{[t]}$  to denote the  $d$ -bit vector  $(v_{t_1}, \dots, v_{t_d})$ . We write  $\parallel$  for vector concatenation.

Exposure resilient functions [Can+00] (also known as deterministic bit-fixing extractors [Cho+85]), are used as a building block in the construction. The definition is given below.

**Definition E.1** (Exposure resilient function). *A function  $f : \{0, 1\}^n \rightarrow \{0, 1\}^m$  is an  $\lambda$ -exposure resilient function if for any  $L \subset [n]$  of size  $|L| = n - \lambda$ , and for  $r \leftarrow \{0, 1\}^n$  and  $R \leftarrow \{0, 1\}^m$  chosen uniformly at random, the distributions  $(r_{[L]}, f(r))$  and  $(r_{[L]}, R)$  are identical.*

Our construction works as follows. For  $n \in \mathbb{N}$ , there is an  $\ell$ -wise independent hash function family  $\mathcal{H}_n^{C,G,E}$  of functions mapping  $n$  bits to  $n$  bits. Each family is parametrized by the following:

- $C : \{0, 1\}^n \rightarrow \Sigma^m$  is the encoding algorithm of an error-correcting code with a constant-size alphabet  $\Sigma$ . The code has minimum distance  $c = \Theta(n)$  and constant expansion factor (that is,  $m = \Theta(n)$ ).
- $G$  is a  $\ell$ -wise independent hash function family mapping  $\Sigma$  to  $\Sigma$ .
- $E : \Sigma^m \rightarrow \{0, 1\}^n$  is an  $\lambda$ -exposure resilient function where  $\lambda = \Theta(n)$ .



Each function in the family  $\mathcal{H}_n^{C,G,E} = \{h_g\}_{g \in G^m}$  is indexed by a vector of hash functions  $g = (g_1, \dots, g_m)$ , where each  $g_i$  is a member of the (smaller)  $\ell$ -wise independent hash function family  $G$ . To sample a hash function in  $h_g \leftarrow \mathcal{H}_n$ , simply sample  $M$  small hash functions  $g_1, \dots, g_m \leftarrow G$ .

Each hash function  $h_g$  is computed as follows.

1. Encode the input  $x \in \{0, 1\}^n$  to obtain codeword  $y = C(x) \in \Sigma^m$ .
2. For each  $i \in [m]$ , let  $z_i = g_i(y_i) \in \Sigma$ .
3. Let  $z \in \Sigma^m$  denote the concatenation of all  $z_i$ , that is,  $z = z_1 || z_2 || \dots || z_m$ . The output of the hash function is  $E(z)$ .

We remark that this procedure for computing the hash function is the same as in the [IKOS08] construction, except that in their work,  $G$  is a family of pairwise (rather than  $\ell$ -wise) independent hash functions.

**Theorem E.2** ( $\ell$ -wise independence of  $\mathcal{H}_n^{C,G,E}$ ). *Let  $\ell \geq 2$  be any constant. For any  $m = \Theta(n)$ , there exist  $\lambda, c = \Theta(n)$  such that if  $C$  is an error-correcting code with minimum distance  $c$  and codeword length  $m$ , and  $E$  is a  $\lambda$ -exposure resilient function, then  $\mathcal{H}_n^{C,G,E}$  is a family of  $\ell$ -wise independent hash functions.*

*Proof.* Let  $c = \Theta(n)$  be the following:

$$c = m - \frac{2m}{\ell(\ell - 1) + 1}. \quad (1)$$

Note that the right-hand side is  $\Theta(n)$ , because  $m = \Theta(n)$  and  $\ell = O(1)$ . Moreover, since  $\ell > 2$ , inequality 1 implies that  $0 < c < m$  as required.

Let  $x_1, \dots, x_\ell$  be any distinct vectors in  $\{0, 1\}^n$ , and let  $h_g \leftarrow \mathcal{H}_n$  be a hash function sampled from the family  $\mathcal{H}_n^{C,G,E}$ . Let the set of corresponding codewords be denoted by  $Y = \{C(x_1), \dots, C(x_\ell)\}$ .

Define the *overlap* of two codewords  $y, y' \in \Sigma^m$  as the set of positions  $k \in [m]$  for which the  $k^{\text{th}}$  elements are equal: that is, where  $y_k = y'_k$ . Formally,

$$\text{Overlap}(y, y') = \{k \in [m] : y_k = y'_k\}.$$

Building on this, we define the set  $\bar{L}$  as follows:

$$\bar{L} = [m] \setminus \bigcup_{y, y' \in Y, y \neq y'} \text{Overlap}(y, y') = \{k \in [m] : \forall y, y' \in Y, y_k \neq y'_k\}.$$

In other words,  $\bar{L}$  is the set of positions  $k$  for which the  $k^{\text{th}}$  elements of the codewords in  $Y$  are pairwise distinct.

Due to the minimum distance of the error-correcting code,

$$|\text{Overlap}(C(x), C(x'))| \leq m - c$$

for all distinct  $x, x' \in \{0, 1\}^n$ . Then, since  $|Y| = \ell$ :

$$\left| \bigcup_{y, y' \in Y, y \neq y'} \text{Overlap}(y, y') \right| \leq (m - c) \cdot \frac{\ell \cdot (\ell - 1)}{2} \quad (2)$$

$$\leq m. \quad (3)$$

Inequality 3 follows by substituting equation 1 into inequality 2. Moreover, the right-hand side of inequality 2 is clearly  $\Theta(m)$  since  $c = \Theta(m)$  and  $\ell$  is constant. From this, it follows that

$$|\bar{L}| = m - \left| \bigcup_{y, y' \in Y, y \neq y'} \text{Overlap}(y, y') \right| = \Theta(m) \quad \text{and} \quad |\bar{L}| > 0. \quad (4)$$

Recall that for each position  $k \in \bar{L}$ , it holds that the  $k^{\text{th}}$  elements of the codewords in  $Y$  are pairwise distinct. Then, since  $g_k$  is an  $\ell$ -wise independent hash function, the  $\ell$  hash function outputs  $g_k(C(x_1)), \dots, g_k(C(x_\ell))$  will be independent and uniformly distributed. Moreover, since the hash functions  $g_1, \dots, g_k$  are chosen independently, the following set consists of independent and uniformly distributed elements:

$$\{g_k(C(x_1)), \dots, g_k(C(x_\ell)) : k \in \bar{L}\}. \quad (5)$$

Recall that when computing  $h_g(x_i)$  (in step 3 of the description above), the input to  $E$  is the concatenation of the  $m$  “small hashes”,  $g_1(C(x_i)) \parallel \dots \parallel g_m(C(x_i))$ . Let  $z_i$  denote  $g_1(C(x_i)) \parallel \dots \parallel g_m(C(x_i))$ . Define  $L = [m] \setminus \bar{L}$  and  $\lambda = |L| = \Theta(m)$ . For each input  $x_i \in \{0, 1\}^n$  to the hash function  $h_g$ ,  $E$  is evaluated on an input  $z_i$  for which  $(z_i)_{\bar{L}}$  is distributed independently at random (this follows from 5). Hence, by the  $\lambda$ -exposure resilience of  $E$ , the outputs  $E(z_1), \dots, E(z_\ell)$  are distributed independently and randomly. The theorem follows.  $\square$

Finally, we show that the hash functions in  $\mathcal{H}_n^{C,T,G,E}$  can be computed and sampled in linear time.

**Theorem E.3.** *If  $C$  and  $E$  are computable in linear time, then each hash function  $h_g \in \mathcal{H}_n^{C,G,E}$  can be computed in linear time. Moreover, sampling a hash function  $h_g \leftarrow \mathcal{H}_n^{C,G,E}$  can be done in linear time.*

*Proof.* Since  $C$  is linear-time computable, step 1 is computable in linear time, and has a linear-size output  $y \in \{0, 1\}^m$ . In step 2, many small hashes of the form  $g_i(y_i)$  are computed, where  $g_i \leftarrow G$ . Since the input to the hash function  $g_i$  is of constant size, each such evaluation of  $g_i$  will take constant time. The total number of small hashes computed is  $m = \Theta(n)$  which is linear, so step 2 takes linear time. Finally, since  $E$  is computable in linear time, and is evaluated on a linear-size input  $z \in \{0, 1\}^M$ , step 3 also takes linear time.

The sampling of a hash function  $h_g \leftarrow \mathcal{H}_n^{C,G,E}$  consists of sampling  $m$  hash functions  $g_1, \dots, g_m \leftarrow G$ . Since the family  $G$  is of constant size, each  $g_i$  can be sampled in constant time, and there are linearly many of them, so the whole sampling process takes linear time.  $\square$

There are known constructions of linear-time computable functions that satisfy the properties required by  $C$  and  $E$  (for any constant  $\ell$ ):

- Guruswami and Indyk [GI05] construct error correcting codes which have linear-time encoding (and decoding) algorithms, and for any positive constant  $\varepsilon < 1$ , their construction can achieve minimum distance  $c$  such that  $c/m = \varepsilon$  with a constant-factor expansion. The encoding function of these codes would be suitable for use as  $C$ .
- Ishai et al. [IKOS08] give a construction of an infinite family of  $\lambda$ -exposure resilient functions mapping  $n$  bits to  $m$  bits, where  $\lambda = \Theta(n)$  and  $m = \Theta(n)$ .