

3-Party Secure Computation for RAMs: Optimal and Concretely Efficient

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Abstract. A distributed oblivious RAM (DORAM) is a method for accessing a secret-shared memory while hiding the accessed locations. DORAMs are the key tool for secure multiparty computation (MPC) for RAM programs that avoids expensive RAM-to-circuit transformations.

We present new and improved 3-party DORAM protocols. For a logical memory of size N and for each logical operation, our DORAM requires $O(\log N)$ local CPU computation steps. This is known to be asymptotically optimal. Our DORAM satisfies passive security in the honest majority setting. Our technique results with concretely-efficient protocols and does not use expensive cryptography (such as re-randomizable or homomorphic encryption). Specifically, our DORAM is 25X faster than the known most efficient DORAM in the same setting.

Lastly, we extend our technique to handle malicious attackers at the expense of using slightly larger blocks (i.e., $\omega(\log^2 N)$ vs. $\Omega(\log N)$). To the best of our knowledge, this is the first concretely-efficient maliciously secure DORAM.

Technically, our construction relies on a novel concretely-efficient 3-party oblivious permutation protocol. We combine it with efficient non-oblivious hashing techniques (i.e., Cuckoo hashing) to get a distributed oblivious hash table. From this, we build a full-fledged DORAM using a distributed variant of the hierarchical approach of Goldreich and Ostrovsky (J. ACM '96). These ideas, and especially the permutation protocol, are of independent interest.

1 Introduction

Secure multiparty computation (MPC) is a method that enables mutually distrustful parties to jointly compute an arbitrary function over their private inputs. Since breakthrough feasibility results in the 80s, the quest for practically efficient MPC protocols is a central research area in cryptography. Efficiency is measured in terms of local computation and/or communication, as a function of the size of the representation of the function that needs to be computed.

There are several common ways to represent computation, e.g., the circuit model or Random Access Memory (RAM) model. Any function can be computed in either of the models and a representation in one model can be translated to the other. However, such translations have a cost: a RAM program of size N can be turned into a circuit of size $O(N^3 \log N)$ [52]. Therefore, due to efficiency reasons, it would be highly desirable to be able to perform secure computation for RAM programs, *directly*.

This is challenging because MPC protocols need to guarantee, in particular, that the running time, memory accesses and communication patterns of the participants, do not depend on their private inputs. The circuit model guarantees these properties for free as circuits can be computed in a gate-by-gate fashion, independently of the inputs. In general, RAM programs do not have these features and therefore some extra work is needed.

There is a generic way to turn any RAM program into another that computes the same functionality but whose memory accesses do not reveal anything about the program’s secret inputs. This is called an *Oblivious RAM* (ORAM), originally proposed by Goldreich and Ostrovsky [26, 48, 27]. The traditional setting for ORAMs is the client-server one. That is, a large memory is stored on an untrusted server and a client can make accesses to it using a small trusted memory. Ostrovsky and Shoup [49] observed that by simulating the client of an ORAM using traditional circuit-based MPC protocol, one can generically get an MPC for RAM programs. However, designing an efficient ORAM with a client that is “compatible” with circuit-based MPC is not at all obvious and has been (so far) sub-optimal in terms of the efficiency of the resulting protocol (see, e.g., [57]).

Due to the inherent inefficiency of circuit-based MPC for certain computations, there has been significant efforts in the last decade in building efficient MPCs for RAM programs, for example [44, 21, 57, 57, 12, 8, 23]. By now, due to its relation to oblivious simulation, the common terminology for this problem is **Distributed Oblivious RAM** (DORAM)—informally, this is a protocol that allows parties to collectively maintain and perform reads/writes on a memory (a formal definition appears in Section 2.2).

The complexity measure of DORAMs of interest to us is their *computational overhead*. That is, the maximal amount of CPU instructions³ performed by each of the parties when serving a single logical request.⁴ Some prior works measure *bandwidth* overhead which accounts only for the maximal amount of bits communicated between the parties. Computational overhead is harder to optimize since an upper bound on the computational overhead implies an upper bound on the communication overhead. The other direction is not necessarily true; indeed, some prior works (e.g., [23]) optimize communication overhead at the expense of

³ We model parties as RAM machines that can perform word-level addition and standard Boolean operations at unit cost.

⁴ As commonly done in this line of works, we sometimes settle for overhead in an amortized sense, that is, we measure the average overhead over a sequence of requests. Often, such schemes can be made worst-case (“de-amortized”) using known techniques [49, 5].

increased computational overhead. Thus, we choose the more stringent measure. This is particularly important if we aim for a concretely efficient and practically useful DORAMs.

In this work, we focus on the honest majority setting and more specifically on the 3-party setting where at most one server is corrupted. We note that there are several schemes in the 2-party setting (e.g., [44, 57, 20]), but due to the nature of the dishonest majority setting, existing techniques result with asymptotically and concretely less efficient schemes than in the 3-party honest majority setting.

A variant of a scheme due to Lu and Ostrovsky [44], suggested by Faber et al. [21],⁵ gives a (3-party) DORAM with $O(\log N)$ computational overhead with block size $\Omega(\log N)$. While the asymptotical overhead of this construction is optimal, the concrete efficiency is quite poor. The reason is that their compiler requires the parties to securely and jointly compute a linear number of encryptions once in a while (which requires a circuit-based secure computation protocol of AES computation).

Later works attempt to present concretely efficient DORAMs. Wang et al. [57] and Faber et al. [21] proposed DORAMs with $O(\log N)$ computational overhead, but their block size is $\Omega(\log^2 N)$ which is less standard. Bunn et al. [8] constructed a DORAM with small concrete constants but poor asymptotic overhead ($\Omega(\sqrt{N})$). Most recently, Falk et al. [23] reduced the large constant factor of [44, 21] and achieved a scheme with $O(\log^2 N)$ computational overhead (and logarithmic communication overhead).

Moreover, all of the above scheme only guarantee security against a passive attacker, i.e., any single server cannot learn any non-trivial information about the others' inputs, as long as it follows the prescribed protocol. There are generic methods to boost security to the more standard setting of active security, where security holds even if a rouge server arbitrarily deviates from the protocol. However, these techniques do not preserve efficiency.

The current state of the art for 3-party DORAMs is summarized in Table 1. This brings us the the main problems that we consider in this work:

Is there a 3-party DORAM which is asymptotically optimal in terms of computational overhead and concretely efficient? Additionally, is there an efficient actively secure DORAM?

1.1 Our Contributions

An optimal 3-party DORAM. We present an asymptotically optimal and concretely efficient 3-party DORAM in the honest majority setting. Specifically, our DORAM has computational overhead of $O(\log N)$ and the hidden constant is rather small. The block size of our construction is $\Omega(\log N)$. Our

⁵ The protocol of Lu and Ostrovsky [44] is in the multi-party setting where there are two non-communicating servers and a single trusted lightweight client (see Section 1.3). Faber et al. [21] observed that the client in [44]'s scheme can be efficiently simulated by an MPC.

Ref.	Communication	Computation	Security	Block size	Hidden const.
[49]	$O(\log^3 N)$	$O(\log^3 N)$	Passive	$\Omega(\log N)$	Large
[44, 21]	$O(\log N)$	$O(\log N)$	Passive	$\Omega(\log N)$	Large
[57, 21]	$O(\log N)$	$O(\log N)$	Passive	$\Omega(\log^2 N)$	Large
[8]	$O(\sqrt{N})$	$O(\sqrt{N})$	Passive	$\Omega(\log N)$	Small
[23]	$O(\log N)$	$O(\log^2 N)$	Passive	$\Omega(\log N)$	Small
Our	$O(\log N)$	$O(\log N)$	Passive	$\Omega(\log N)$	Small
Our	$O(\log N)$	$O(\log N)$	Active	$\omega(\log^2 N)$	Small

Table 1. Summary of known 3-party DORAMs in the honest majority setting together with our own schemes. The first column points to the paper that obtained the DORAM. The second column states the communication overhead of the proposed construction. The third column states the computational overhead of the proposed construction. The fourth column states whether the security guarantee is for passive or active attackers. The fifth column mentions the block size used in the construction. Lastly, the sixth column states whether the hidden constants are considered large or small.

DORAM requires (amortized) only $4 \log N$ oblivious pseudorandom function (OPRF) calls per access. This is about 2 times greater than that of the DORAM of Falk et al. [23],⁶ but it is significantly more efficient than the known optimal DORAM of Lu and Ostrovsky [44, 21] that requires at least $100 \log N$ calls per access (see Appendix A for more details). This protocol is secure against passive (honest-but-curious) attackers.

A distributed oblivious permutation: Our main technical novelty is a new (concretely efficient and asymptotically optimal) 3-party oblivious permutation protocol. Our protocol can apply any permutation to the data with communication of $4nb + 2n \lceil \log n \rceil$ bits and $12nb + 2n \lceil \log n \rceil$ local CPU computation steps where n is the number of data elements to be shuffled and b is the bit-length of each data element. We also construct a procedure to invert that permutation. This procedure requires $8nb + 2n \lceil \log n \rceil$ bits of communication and $19nb + 2n \lceil \log n \rceil$ steps of local computation.

A distributed oblivious hash table: Our DORAM construction is modular and, at a high level, is reminiscent of the hierarchical ORAM technique of Goldreich and Ostrovsky [27]. Recall that [27]’s hierarchical method basically reduces the problem of maintaining a memory to the problem of building a static hash table (supporting only lookups after an initial build). To this end, we implement a concretely efficient *distributed* oblivious hashing scheme. This is the first concretely efficient and asymptotically optimal distributed oblivious hash table construction. To store n data blocks of size b bits into a distributed hash table, each party needs to perform at most $O(n \cdot (b + \lceil \log n \rceil))$ local CPU computation steps, and our lookup protocol requires $O(b) + O(\sigma \cdot b)$ local CPU computation

⁶ Here, we emphasize again that the DORAM of Falk et al. [23] requires $\Omega(\log^2 N)$ *computational* cost in addition to the communication cost. We only have $O(\log N)$ computational cost.

steps, where the first term for a lookup in a main table and the second for a linear scan of a σ -size stash. The storage size of the hash table is $O(nb)$. We obtain our distributed oblivious hash table by first randomly permuting the data to be hashed (using the above-mentioned permutation protocol) and then simply invoking an off-the-shelf (non-oblivious) distributed hashing technique.

Active security. We extend our passively secure schemes from above to be *actively* secure, without hurting efficiency, except that we rely on somewhat larger blocks. Specifically, we get a 3-party DORAM with $O(\log N)$ computational overhead and block size $\omega(\log^2 N)$. As far as we know, this is the first result achieving active security for DORAM with practical efficiency guarantees. We do not know how to achieve similar concrete efficiency guarantees with logarithmic size blocks and we leave it as an exciting open problem.

1.2 Technical Overview

Before showing the fundamental idea of our schemes, we first revisit the optimal DORAM of Lu and Ostrovsky [44, 21]. Their DORAM consists of a hierarchy of *permuted arrays*, i.e., oblivious hash tables, that are managed by multi-party protocols while hiding access patterns. The fundamental idea of their oblivious hashing (which comes from the 2-server setting [44]) is as follows. One of the two servers is *the permuter*, and set other is *the storage*. The storage sends all data that should be permuted to the permuter while *rerandomizing*, and the permuter constructs a hash table consisting of the data. The permuter sends the hash table to the storage while rerandomizing. Now, the storage can explore the table with a (randomized) query.

Though the storage can observe the access patterns on the table directly in the above scheme, the access patterns achieve one-shot obliviousness against the storage since it does not know the *permutation* for building the table. In other words, the table seems to be shuffled from the storage’s point of view, and hence the single-short exploration of elements is randomized. On the other hand, since the permuter does not observe any access on the table, it never knows the access patterns even if it knows how to construct the table.

Due to the ingenuity of the server role splitting, they achieved optimal DORAM with optimal oblivious hashing. However, as Falk et al. [23] pointed out, while this DORAM is *asymptotically* optimal it is *not practically* efficient because of the large frequency of rerandomizations required. Their rerandomization can be implemented by *oblivious pseudorandom function* (OPRF) in the context of secure multiparty computation, but as mentioned in [23], the DORAM of Lu and Ostrovsky requires at least $100 \log N$ OPRFs per access. Falk et al. improved this by a factor of 50, at the expense of increased local computation overhead— $O(\log^2 N)$ local hash function evaluations per access.

An oblivious distributed permutation protocol. Our starting point is the idea of role splitting, but with a novel modification that maintains optimality

and greatly improves practical efficiency. Our fundamental idea is as follows. Set the role of one of the *three* servers as the permuter; this server knows a permutation for hashing. The other two servers will be the storage that holds data in a secret-shared form. The servers obviously compute hash values of all secret-shared data (which can be implemented by ORPFs) and reveal them to the permuter. The permuter calculates a permutation that sorts the data to make a hash table. The servers run a role-asymmetric oblivious permutation to apply the above permutation to the data obliviously. As the output of this protocol, the two storages obtain a hash table in secret-shared form. Now, the storages can explore the table with a secret-shared query.

By the description above, only one round of OPRF evaluations is required to build a hash table. Our permutation protocol is for 3-party computation, and if one party knows a permutation, it can apply the permutation to a secret-shared array in linear time while keeping the permutation secret from the other two parties (see Section 4.1 for more details).

From an oblivious distributed permutation to a DORAM. We obtain a DORAM using only $4 \log N$ OPRF evaluations and optimal computational complexity per access. This is obtained via the following very useful observations: (1) given an oblivious permutation protocol, there is an extremely efficient way to get an oblivious distributed hash table, and (2) given the latter, we can adapt the hierarchical ORAM framework (or its optimizations) to the distributed setting. We elaborate on both bullets next. First, we observe that the shuffle-sort paradigm can be applied to hashing: if data is randomly shuffled, we can invoke an insecure oblivious hashing algorithm. This allows us to completely get rid of complicated (distributed) oblivious hashing approaches by first shuffling the input and then invoking a simple hashing procedure. Concretely, we use (plain) cuckoo hashing with a stash [36] to achieve constant lookup time (ignoring scanning a logarithmic-size stash, which we will do once per logical access).

Once we have obtained our distributed hash table, we plug it into the hierarchical ORAM setting, while extending it to the distributed setting. I.e., we implement every level in the hierarchy with an oblivious distributed hash table, as above, and where each level can hold twice more elements than its previous level. The stashes from all levels are merged into one common stash and scanning it is done once per lookup.

Maliciously secure DORAM. In our permutation protocol roles of parties are asymmetric, and in particular, the permuter has complete control over the chosen permutation. Thus, it is non-trivial to extend our ideas to the malicious setting.

To this end, we augment our permutation evaluation protocol to check that *all elements in a hash table are actually in their correct cell*. The key insight is that the correctness of a hash table can be evaluated by hash values of elements calculated by OPRF whose correctness (in turn) can be guaranteed using a known actively secure MPC framework. That is, comparing the virtual address

(obtained obliviously by OPRF) and the real address (known by all parties) of each element in a hash table, the correctness of the table can be achieved regardless of whether the permuter is the adversary or not. We observe that any attack of the permuter can be translated into some form of an *additive attack* [24, 25]. Thus, to achieve malicious security, we incorporate the permutation evaluation into an efficient evaluation process of security-with-abort MPC (e.g., [13, 32, 35]) and only need to deal with “additive attacks”. This makes our hash table validation have almost no effect on overall efficiency.

Organization

We describe our passively secure oblivious permutation and oblivious hashing in Section 4. We combine them to get our passively secure DORAM in Section 5. The actively secure extension is described in Section 6.

1.3 Related Works

Standard ORAM. The first ORAM, i.e., in the client-server model, was introduced by Goldreich and Ostrovsky [26, 48, 27]. Their best scheme had poly-logarithmic overhead in the memory size. They also proved a lower bound for a restricted class of schemes saying that logarithmic overhead is necessary [7]. Larsen and Nielsen [40] (see also [34, 37]) showed that logarithmic overhead is inherent for all schemes as long as they support “online” accesses.

There have been significant efforts to improve the overhead of ORAMs, both asymptotically and concretely [28, 55, 38, 56, 57, 11, 51, 3, 5]. A few years ago, Asharov et al. [3] (building on Patel et al. [51]) got an asymptotically optimal ORAM with logarithmic overhead. The constants underlying their scheme are enormous (due to the use of certain expander graphs; see also [19]) which makes it far from being practically useful. The best ORAM for practical purposes is still based on Path ORAM [56], a technique that suffers from \log^2 overhead in the memory size.

Multi-server ORAM. Another model that received significant attention is the *multi-server* ORAM setting. Here, there are multiple servers and a client. The client can communicate with each server but the servers cannot communicate amongst themselves. As in the client-server setting of ORAM, the client has a small trusted memory. The adversarial model is that only some fraction of the servers are corrupted, making it possible for more efficient solutions than in the standard single-server ORAM setting. This model was first introduced by Lu and Ostrovsky [44] who proposed a two-server ORAM with logarithmic communication overhead. By now, we have schemes with the same asymptotic overhead in the single-server setting [3]. Assuming larger block size, Kushilevitz and Mour [39] achieved a multi-server ORAM with sub-logarithmic communication from the client but poly-logarithmic work by the servers. Hoang et al. [30] proposed a multi-server ORAM with constant client-server communication over-

head using secret sharing and MPC techniques. Lower bounds for this setting were proven by Larsen et al. [41].

Actively secure ORAM. Since Ren et al. [53] introduced an actively secure ORAM in the standard model, various actively secure ORAMs have been proposed. Devadas et al. [18] proposed a generic construction of ORAMs that guarantees the correctness of metadata in the standard model. Also, Blass et al. [6] and Maffei et al. [45] proposed standard ORAMs (with multiple clients) that force the dishonest server or clients to behave correctly. Hoang et al. [29] construct a maliciously secure multi-server ORAM that detects servers that deviate from the protocol.

However, in the distributed model, no concrete construction for actively secure DORAM has been proposed.

2 Preliminaries

For an integer $n \in \mathbb{N}$, we denote by $[n]$ the set $\{0, \dots, n-1\}$. By \parallel we denote the operation of string concatenation.

Throughout this work, the security parameter is denoted λ , and it is given as input to algorithms in unary (i.e., as 1^λ). A function $\text{negl}: \mathbb{N} \rightarrow \mathbb{R}^+$ is *negligible* if for every constant $c > 0$ there exists an integer N_c such that $\text{negl}(\lambda) < \lambda^{-c}$ for all $\lambda > N_c$. Two sequences of random variables $X = \{X_\lambda\}_{\lambda \in \mathbb{N}}$ and $Y = \{Y_\lambda\}_{\lambda \in \mathbb{N}}$ are *computationally indistinguishable* if for any probabilistic polynomial-time algorithm \mathcal{A} , there exists a negligible function $\text{negl}(\cdot)$ such that $|\Pr[\mathcal{A}(1^\lambda, X_\lambda) = 1] - \Pr[\mathcal{A}(1^\lambda, Y_\lambda) = 1]| \leq \text{negl}(\lambda)$ for all $\lambda \in \mathbb{N}$. We say that $X \equiv Y$ for such two sequences if they define *identical* random variables for every $\lambda \in \mathbb{N}$.

Definition 2.1 (Pseudorandom functions (PRFs)). *Let F be an efficiently computable function family indexed by keys $\text{sk} \in \{0, 1\}^\lambda$, where each F_{sk} takes as input a value $x \in \{0, 1\}^{n(\lambda)}$ and outputs a value $y \in \{0, 1\}^{m(\lambda)}$. A function family F is a secure pseudorandom function (PRF) if for every (non-uniform) probabilistic polynomial-time algorithm \mathcal{A} , there is a negligible function $\text{negl}(\cdot)$ such that*

$$\left| \Pr_{\text{sk} \leftarrow \{0,1\}^\lambda} \left[\mathcal{A}^{F_{\text{sk}}(\cdot)}(1^\lambda) = 1 \right] - \Pr_{u \leftarrow U_\lambda} \left[\mathcal{A}^{u(\cdot)}(1^\lambda) = 1 \right] \right| \leq \text{negl}(\lambda),$$

for all $\lambda \in \mathbb{N}$, where U_λ is the set of all functions mapping $\{0, 1\}^{n(\lambda)}$ to $\{0, 1\}^{m(\lambda)}$.

It is known that one-way functions are existentially equivalent to PRFs for any polynomial $n(\cdot)$ and $m(\cdot)$ [31, 46]. Our construction will employ PRFs in several places and we present each part modularly with its own PRF, but note that the whole DORAM construction can be implemented with a single PRF from which we can implicitly derive all other PRFs.

2.1 Secret Sharing Schemes

A (threshold) secret sharing scheme (SSS) is a cryptographic primitive that allows to “split” a secret between a collection of parties such that a set of parties of some predefined cardinality can reconstruct the secret. A “piece” that is held by a party is called a *share*. When a secret is split into m shares and reconstructed with at least $t + 1$ shares, we refer to such a scenario as (t, m) -SSS.

To share and reconstruct a secret, we introduce three functionalities as follows. We use the notations $\llbracket \cdot \rrbracket_i$ to represent the share of party $i \in [m]$.

- $\mathcal{F}_{\text{SHARE}}$ receives a secret s and distributes the shares among the parties; share $\llbracket s \rrbracket_i$ is sent to party i .
- $\mathcal{F}_{\text{REVEAL}}$ receives shares $\llbracket s \rrbracket_i$ from at least $t + 1$ parties, recovers the secret s , and sends it to all parties.
- $\mathcal{F}_{\text{REVEAL}}^{\mathcal{P}}$ behaves the same as $\mathcal{F}_{\text{REVEAL}}$ except that it sends the recovered secret s only to parties $\in \mathcal{P}$.

Also, we use the notations $\langle \cdot \rangle_{i \in \{0,1\}}$ to represent the shares in a $(1,2)$ -additive SSS, i.e., $a = \langle a \rangle_0 + \langle a \rangle_1$ for any secret a . Under Shamir’s SSS [54] or the replicated SSS [33], any two parties $P_i, P_{i+1 \bmod 3}$ can convert their shares $\llbracket s \rrbracket_i, \llbracket s \rrbracket_{i+1 \bmod 3}$ to $\langle s \rangle_0, \langle s \rangle_1$ by performing local computation.

We extend the notation to sharing arrays. For an array \mathbf{A} of length X , we denote its x -th element by $\mathbf{A}[x]$. When secret sharing such an array, we denote its sharing by $\llbracket \mathbf{A} \rrbracket := (\llbracket a_0 \rrbracket, \dots, \llbracket a_{X-1} \rrbracket)$ and $\langle \mathbf{A} \rangle = (\langle a_0 \rangle, \dots, \langle a_{X-1} \rangle)$, where $a_x = \mathbf{A}[x]$.

For concreteness and clarity of this work, we assume that all shares $\llbracket s \rrbracket$ are of the $(1,3)$ -replicated SSS on the extension field \mathbb{Z}_{2^ℓ} as [2, 15], i.e., for any $s = \sum_{i=0}^{\ell-1} s_i 2^i$, $s_i \in \mathbb{Z}_2$, its share is of the form $\llbracket s \rrbracket = \sum_{i=0}^{\ell-1} \llbracket s_i \rrbracket 2^i$. In this setup, all shares have the following properties.

Linear homomorphism. The replicated SSS [33], by definition, support share-to-share addition by performing only local operations on each parties shares. That is, for any $a, b, c \in \mathbb{Z}_{2^\ell}$, without any interaction, the parties can compute

$$\llbracket a \rrbracket + \llbracket b \rrbracket = \llbracket a + b \rrbracket \text{ and } c \times \llbracket a \rrbracket = \llbracket ca \rrbracket.$$

We extend the above notation and operations to arrays of secrets. For any arrays $\llbracket \mathbf{A} \rrbracket = (\llbracket a_0 \rrbracket, \dots, \llbracket a_{X-1} \rrbracket)$ and $\llbracket \mathbf{B} \rrbracket = (\llbracket b_0 \rrbracket, \dots, \llbracket b_{X-1} \rrbracket)$ where $a_i, b_i \in \mathbb{Z}_{2^\ell}$, we denote entry-wise addition as $\llbracket \mathbf{A} \rrbracket + \llbracket \mathbf{B} \rrbracket := (\llbracket a_0 \rrbracket + \llbracket b_0 \rrbracket, \dots, \llbracket a_{X-1} \rrbracket + \llbracket b_{X-1} \rrbracket)$ and entry-wise multiplication by a scalar as $c \times \llbracket \mathbf{A} \rrbracket := (c \times \llbracket a_0 \rrbracket, \dots, c \times \llbracket a_{X-1} \rrbracket)$.

Bit-decomposition. Since all shares are of the form $\llbracket s \rrbracket = \sum_{i=0}^{\ell-1} \llbracket s_i \rrbracket 2^i$ where each $\llbracket s_i \rrbracket$ is a share of the replicated SSS on \mathbb{Z}_2 , parties can perform the bit-decomposition operation $\llbracket s \rrbracket \rightarrow (\llbracket s_{\ell-1} \rrbracket, \dots, \llbracket s_0 \rrbracket)$ by their local conversion.

2.2 Distributed Oblivious RAM

A RAM consists of a memory of N cells and each cell is of size w bits and it allows for “clients” to perform read and write operations of the form $(\text{op}, \text{addr}, \text{d})$,

where $\text{op} \in \{\text{read}, \text{write}\}$, $\text{addr} \in [N]$ and $\mathbf{d} \in \{0, 1\}^w \cup \{\perp\}$. If $\text{op} = \text{read}$, then $\mathbf{d} = \perp$ and the returned value is the content of the block located in logical address addr in the memory. If $\text{op} = \text{write}$, then the memory data in logical address addr is updated to \mathbf{d} . We can think of this as an ideal (reactive) functionality \mathcal{F}_{RAM} that supports the following operation:

\mathcal{F}_{RAM} :

- $v \leftarrow \text{ACCESS}(\text{op}, \text{addr}, \mathbf{d})$: The input is an operation $\text{op} \in \{\text{read}, \text{write}\}$, a key $\text{addr} \in [N]$, and a value $\mathbf{d} \in \{0, 1\}^w \cup \{\perp\}$. An internal size N array \mathbf{X} , initialized to all 0s, is maintained. The procedure does:
 1. If $\text{op} = \text{read}$, then set $\mathbf{d}^* = \mathbf{X}[\text{addr}]$.
 2. If $\text{op} = \text{write}$, then set $\mathbf{X}[\text{addr}] = \mathbf{d}$ and $\mathbf{d}^* = \mathbf{d}$.
 3. Output \mathbf{d}^* .

Distributed oblivious simulation. Our goal is to simulate a RAM correctly while guaranteeing the standard security notion of secure multi-party computation. Towards this goal, we have m servers that can communicate between themselves over a fully connected network in synchronous rounds of communication. Each server can further perform arbitrary local computation between rounds. We model each server machine as a RAM. The view of each machine includes the contents of its own memory and the contents of the incoming messages, where the latter include addresses of memory cells to access. Such a secure system is called *Distributed Oblivious RAM* (DORAM). The security guarantee stipulates that the view of a colluding subset of dishonest servers cannot learn anything about the computation being performed, except what is absolutely necessary (e.g., the length of the computation). We shall consider a passive (semi-honest) or active (malicious) adversary who controls up to $t < m$ servers.

We shall define distributed oblivious simulation with respect to an arbitrary (possibly reactive or stateful) functionality. The definition for the RAM functionality will be implied as a special case. For concreteness, it is convenient (though not necessary) to imagine that the input and RAM state are secret-shared between the servers, that is, each party holds one out of m shares of the RAM, and operations from a client are also written to each server in a secret shared fashion. We follow the real-ideal paradigm by defining two “worlds” and requiring that they are indistinguishable (following, e.g., Canetti [9]).

Definition 2.2 (View). *The view of party i consists of its auxiliary input and randomness followed by the honest input and all the messages sent and received by the party during the computation. Since we model parties as RAMs, the incoming and outgoing messages contain physical memory locations.*

In what follows, we suppress mentioning the auxiliary information to simplify notation and presentation. All of the definitions and results readily extend to the setting where auxiliary input is present.

Non-reactive functionalities. Let \mathcal{F} be a non-reactive functionality. Let Π be a distributed protocol implementing \mathcal{F} , $C \subseteq [m]$ be the set of $\leq t$ corrupted servers, and Sim be a PPT simulator. Denote $\bar{C} = [m] \setminus C$. We introduce the following experiments to define active (malicious) security and remark the necessary changes for passive (semi-honest) security.

- $\text{Real}_{\Pi, C, \mathcal{A}}^{\text{nr}}(\lambda, \{x_i\}_{i \in [m]})$: Run the protocol with security parameter λ , where honest parties (ones not in C) run the protocol Π honestly with their private input $x_i^* = x_i$, whereas corrupt parties (ones in C) get the corresponding x_i 's but can deviate from the prescribed protocol arbitrarily, according to \mathcal{A} 's strategy. Let V_i be the view of server $i \in C$ throughout the execution and let y_i be the output of some honest party $i \in \bar{C}$. Output $(\{V_i\}_{i \in C}, \{y_i\}_{i \in \bar{C}})$. In the passive (semi-honest) setting, the experiment is the same except that corrupt parties use $x_i^* = x_i$ and they follow the specification of the protocol (i.e., \mathcal{A} is passive).
- $\text{Ideal}_{\mathcal{F}, \text{Sim}, C, \mathcal{A}}^{\text{nr}}(\lambda, \{x_i\}_{i \in [m]})$: First, the adversary sees the inputs of corrupted parties $\{x_i\}_{i \in C}$ and outputs $\{x_i^*\}_{i \in C}$ that may depend on them. Denote $x_i^* = x_i$ for each $i \in \bar{C}$. Then, we invoke the functionality $y_1, \dots, y_m \leftarrow \mathcal{F}(x_1^*, \dots, x_m^*)$. Finally, the simulator is executed and the following pair is outputted $(\text{Sim}^{\mathcal{A}}(\lambda, C, \{x_i^*\}_{i \in C}), \{y_i\}_{i \in \bar{C}})$. In the passive (semi-honest) setting, the experiment is the same except that the adversary is passive and uses $x_i^* = x_i$.

A distributed protocol obviously simulates a functionality \mathcal{F} against active (resp. passive) adversaries if the corrupted servers in the real world have views that are indistinguishable from their views in the ideal world.

Definition 2.3 (Distributed oblivious simulation of non-reactive functionalities). *An m -server protocol Π (t, m) -obliviously simulates \mathcal{F} if for any attacker there exists a PPT simulator Sim such that, for every subset of t passive/active corrupt parties C , any non-uniform PPT adversary \mathcal{A} , and all inputs x_0, \dots, x_{m-1} , the distributions $\text{Real}_{\Pi, C, \mathcal{A}}^{\text{nr}}(\lambda, \{x_i\}_{i \in [m]})$ and $\text{Ideal}_{\mathcal{F}, \text{Sim}, C, \mathcal{A}}^{\text{nr}}(\lambda, \{x_i\}_{i \in [m]})$ are computationally indistinguishable.*

Reactive functionalities. A reactive functionality is one that can be repeatedly invoked and it may keep an internal secret state between invocations (a RAM is, in particular, a reactive functionality). The adversary \mathcal{A} chooses the next operation $(\text{op}, \{x_i\}_{i \in [m]})$ adaptively in each stage. In the real execution, the corrupt parties may deviate arbitrarily from the prescribed protocol and the goal is to ensure that they do not learn anything beyond what is absolutely necessary. That is, we execute the protocol in the presence of the malicious adversary. In the ideal execution, the adversary obtains inputs $\{x_i\}_{i \in C}$ and may choose new inputs $\{x_i^*\}_{i \in C}$. The new inputs are fed (together with the inputs of the honest parties) into the functionality \mathcal{F} which outputs an output $\{y_i\}_{i \in \bar{C}}$. At this point, using the output of malicious parties, a simulator must simulate the view of the malicious parties, including their internal state and the obtained messages (and

access pattern) from other servers. The adversary can then choose the next command, as well as the next input, in an adaptive manner, based on everything it has seen so far.

Definition 2.4 (Distributed oblivious simulation of a reactive functionality). We say that a stateful protocol Π is a (t, m) -distributed oblivious implementation of the reactive functionality \mathcal{F} if there exists a stateful PPT simulator Sim , such that for any non-uniform PPT (stateful) adversary \mathcal{A} , the view of the adversary \mathcal{A} in the following two experiments $\text{Real}_{\Pi, C, \mathcal{A}}(\lambda, \{x_i\}_{i \in [m]})$ and $\text{Ideal}_{\mathcal{F}, \text{Sim}, C, \mathcal{A}}(\lambda, \{x_i\}_{i \in [m]})$ is computationally indistinguishable:

$\text{Real}_{\Pi, C, \mathcal{A}}(\lambda, \{x_i\}_{i \in [m]}):$ <p>Let $(\text{op}, \{x_i\}_{i \in [m]}) \leftarrow \mathcal{A}(1^\lambda)$. Loop while $\text{op} \neq \perp$: Let $x'_i = (\text{op}, x_i)$ for each $i \in [m]$. $\{V_i\}_{i \in C}, \{y_i\}_{i \in \bar{C}}$ $\leftarrow \text{Real}_{\Pi, C, \mathcal{A}}^{\text{nr}}(\lambda, \{x'_i\}_{i \in [m]})$. $(\text{op}, \{x_i\}_{i \in [m]})$ $\leftarrow \mathcal{A}(1^\lambda, \{V_i\}_{i \in C}, \{y_i\}_{i \in \bar{C}})$.</p>	$\text{Ideal}_{\mathcal{F}, \text{Sim}, C, \mathcal{A}}(\lambda, \{x_i\}_{i \in [m]}):$ <p>Let $(\text{op}, \{x_i\}_{i \in [m]}) \leftarrow \mathcal{A}(1^\lambda)$. Loop while $\text{op} \neq \perp$: Let $x'_i = (\text{op}, x_i)$ for each $i \in [m]$. $\{V_i\}_{i \in C}, \{y_i\}_{i \in \bar{C}}$ $\leftarrow \text{Ideal}_{\mathcal{F}, \text{Sim}, C, \mathcal{A}}^{\text{nr}}(\lambda, \{x'_i\}_{i \in [m]})$. $(\text{op}, \{x_i\}_{i \in [m]})$ $\leftarrow \mathcal{A}(1^\lambda, \{V_i\}_{i \in C}, \{y_i\}_{i \in \bar{C}})$.</p>
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3 Secure Computation Building Blocks

Our schemes rely on various existing building blocks from the secure computation literature. To encapsulate these building blocks, we assume the existence of an Arithmetic Black Box (ABB) functionality, \mathcal{F}_{ABB} , which is a (reactive) multi-party functionality. \mathcal{F}_{ABB} should consist of functions MULT , RND , RESHARE , EQ , IFELSE , BITEXT , TRUNC , and PRF , each listed below.

To simplify the notation, we denote “calling a function p of \mathcal{F}_{ABB} ” as “calling \mathcal{F}_p ”, e.g., “parties call $\mathcal{F}_{\text{MULT}}$ ” represents that the parties invoke \mathcal{F}_{ABB} to call its function MULT with their inputs.

Assuming that all secrets are in \mathbb{Z}_{2^ℓ} , all implementations we introduce below consumes $O(\ell)$ -bit communication and $O(\ell)$ local CPU computation steps.

Multiplication. Let $\mathcal{F}_{\text{MULT}}$ be a secure multiplication functionality that receives $\llbracket a \rrbracket$ and $\llbracket b \rrbracket$ and returns $\llbracket ab \rrbracket$. For the $(1, 3)$ -replicated SSS on the extension field \mathbb{Z}_{2^ℓ} , we can use the implementation of Araki et al. [2] or Chida et al. [15].

For ease of notation, we occasionally denote $\mathcal{F}_{\text{MULT}}$ as $\llbracket a \rrbracket \times \llbracket b \rrbracket$.

Generating random shares. Let \mathcal{F}_{RND} be a functionality that requires no inputs but returns a share $\llbracket r \rrbracket$ of a secret random value r . In the $(1, 3)$ -replicated SSS, since the form of shares is $\llbracket a \rrbracket_i \bmod 3 = (a_i, a_{i+1})$; $a = a_0 + a_1 + a_2$, \mathcal{F}_{RND} is simply implemented in information-theoretical security as that: Each party P_i locally generate a random r_i , send it to P_{i-1} and set $\llbracket r \rrbracket_i$ as (r_i, r_{i+1}) .

It is also known that, trading off the information-theoretical security, a pseudorandom r can be shared without any communication excepting a pre-computation. This is called Pseudorandom Secret Sharing (PRSS) [16].

Resharing. Let $\mathcal{F}_{\text{RESHARE}}$ be a functionality that receives $\langle a \rangle_0$ and $\langle a \rangle_1$, and returns $\llbracket a \rrbracket$. It can be simply implemented as that the parties P_{i_0} and P_{i_1} , who have $\langle a \rangle_0$ and $\langle a \rangle_1$ respectively, secret-shares their shares as $\llbracket \langle a \rangle_i \rrbracket$ for all parties and then they observe $\llbracket a \rrbracket = \llbracket \langle a \rangle_0 + \langle a \rangle_1 \rrbracket$. Under the use of PRSS, the slightly efficient implementation is known [14].

Equality test. Let \mathcal{F}_{EQ} be a functionality that receives $\llbracket a \rrbracket$ and $\llbracket b \rrbracket$ then return $\llbracket c \rrbracket$ where $c \in \{0, 1\}$ is equal to $(a =? b)$. Though there are numerous implementations of these functionalities, for concreteness, we expect to use the one of Catrina and de Hoogh [10].

Selection. Let $\mathcal{F}_{\text{IFELSE}}$ be a functionality that receives $\llbracket c \rrbracket$, $\llbracket t \rrbracket$ and $\llbracket f \rrbracket$ such that $c \in \{0, 1\}$, and returns $\llbracket t \rrbracket$ if $c = 1$, or $\llbracket f \rrbracket$ otherwise. Observe that $\mathcal{F}_{\text{IFELSE}}$ is equal to $f + c(t - f)$.

Bit operations. Let $\mathcal{F}_{\text{BITEXT}}$, $\mathcal{F}_{\text{TRUNC}}$, and $\mathcal{F}_{\text{R-SHIFT}}$ each be a functionality that receives shares $\llbracket a \rrbracket$, whose bit-representation is $a = a_\ell \dots a_1$, and an integer $i; 1 \leq i \leq \ell$, then returns the following output:

- $\mathcal{F}_{\text{BITEXT}}(\llbracket a \rrbracket, i) \rightarrow \llbracket a_i \rrbracket$ s.t. $a_i \in \{0, 1\}$ is the i -th least significant bit of a .
- $\mathcal{F}_{\text{TRUNC}}(\llbracket a \rrbracket, i) \rightarrow \llbracket a' \rrbracket$ s.t. $a' = a_\ell \dots a_{i+1} \parallel 0^i$.
- $\mathcal{F}_{\text{R-SHIFT}}(\llbracket a \rrbracket, i) \rightarrow \llbracket a' \rrbracket$ s.t. $a' = 0^i \parallel a_\ell \dots a_{i+1}$.

Based on the local bit-decomposition described in Section 2.1, $\mathcal{F}_{\text{BITEXT}}$ can be implemented straightforwardly as that: For $\llbracket a \rrbracket = \sum_{i=0}^{\ell-1} \llbracket a_i \rrbracket 2^i$, parties extract the target bit $\llbracket a_i \rrbracket$, generate $\llbracket \vec{0} \rrbracket = \sum_{i=0}^{\ell-1} \llbracket 0 \rrbracket 2^i$, and compute $\llbracket \vec{0} \rrbracket + (\llbracket a_i \rrbracket 2^0)$. $\mathcal{F}_{\text{TRUNC}}$ and $\mathcal{F}_{\text{R-SHIFT}}$ can be realized in the similar way.

Oblivious PRF (OPRF). Let \mathcal{F}_{PRF} be a functionality that receives shares $\llbracket \text{sk} \rrbracket$ and $\llbracket x \rrbracket$ from all parties and sends them $\llbracket y \rrbracket$ where y is given by a PRF $F_{\text{sk}}(x)$. A combination of known oblivious block ciphers [1, 15, 17, 42] and $\mathcal{F}_{\text{R-SHIFT}}$ implements \mathcal{F}_{PRF} .

4 Efficient Passively Secure Distributed Oblivious Hashing

A (static) hash table is a data structure supporting three operations BUILD, LOOKUP, and EXTRACT, that realizes the following reactive functionality. The BUILD procedure creates an in-memory data structure from an input array \mathbf{I} containing real and dummy elements where each element is a (key, value) pair. Dummy elements have their key be \perp . It is assumed that all real elements in \mathbf{I} have distinct keys. The LOOKUP procedure allows a requestor to look up the value of a key. A \perp symbol is returned if the key is not found or if \perp is the requested key. We say a (key, value) pair is visited if the key was searched for and found before. Finally, EXTRACT is the destructor and it returns a list

containing unvisited elements padded with dummies to the same length as the input array \mathbf{I} .

The description of this functionality, denoted \mathcal{F}_{HT} , is described next:

$\underline{\mathcal{F}_{\text{HT}}}$:

- **BUILD(\mathbf{I})**: The input is an array $\mathbf{I} = (a_1, \dots, a_n)$ containing n elements, where each a_i is either dummy or a (key, value) pair denoted $a_i = (k_i, v_i)$. It is assumed that keys and values fit into $O(1)$ memory words and that all real keys are distinct. The procedure does:
 1. Initialize the state $\mathbf{H} = (\mathbf{I}, \mathbf{P})$ where $\mathbf{P} = \emptyset$.
- **LOOKUP(k)**: The input is a key k (that might be \perp , i.e., dummy). The procedure does:
 1. If $k \in \mathbf{P}$ (i.e., k is a recurring lookup), then halt and return \perp .
 2. If $k = \perp$ or $k \notin \mathbf{I}$, set $v^* = \perp$.
 3. Otherwise, set $v^* = v$, where v is the value corresponding to $k(\in \mathbf{I})$.
 4. Update $\mathbf{P} = \mathbf{P} \cup \{k\}$.
 5. Output v^* .
- **EXTRACT()**: The procedure does:
 1. Define $\mathbf{I}' = \{a'_1, \dots, a'_n\}$ such that: For $i \in [n]$, set $a'_i = a_i$ if $a_i = (k, v)$ and $k \notin \mathbf{P}$. Otherwise, set $a'_i = \perp$.
 2. Output \mathbf{I}' .

In this section, we propose a simple (1,3)-distributed oblivious implementation of \mathcal{F}_{HT} that is inspired by Lu et al. [44]. Since Lu’s DORAM requires too many (at least $100 \log N$) OPRF calls for distributed blocks, the practical computation cost becomes expensive even if the asymptotic overhead is down to $O(\log N)$. To reduce the practical computation complexity without increasing the asymptotic overhead, we construct a concretely efficient distributed oblivious hashing from a simple new permutation protocol for 3-party computation.

In a very high level, our distributed oblivious hashing scheme works as follows (assuming a permutation protocol):

1. Starting with (1,3)-shares of input blocks, the parties securely compute (pseudorandom) addresses for the blocks to be placed.
2. The parties divide their roles into one *permuter* and two *storages*, and then only the permuter reveals the addresses of the blocks.
3. The permuter computes a permutation for the blocks, which is a *sorting permutation* depending on the addresses.
4. The parties obliviously apply the permutation (that is secret for the storages) and the storages receive the (1,2)-shares of the permuted blocks.
5. Now, the permuted blocks can take the form of some hash table (if the permutation is valid), and only the storages can access the table.

4.1 Distributed Oblivious Permutation

As a building block for our distributed oblivious hash table, we first construct an efficient 3-party secure permutation protocol. The precise functionality that we

implement is described below. This section is devoted to the implementation of these functionalities via a 3-party protocol where at most one may be corrupted.

$\mathcal{F}_{\text{PERM}}$:

1. Receive a permutation π from the *permuter* P , and receive $(1, 3)$ -shares of an array $[\mathbf{I}]$ from all parties.
2. Obtain \mathbf{I} , compute $\pi \cdot \mathbf{I}$, and choose a random string \mathbf{R} of the same size as \mathbf{I} .
3. Send $\langle \mathbf{I}' \rangle_0 = \mathbf{R}$ to the first *storage* S_0 and $\langle \mathbf{I}' \rangle_1 = \pi \cdot \mathbf{I} - \mathbf{R}$ to the second S_1 .

$\mathcal{F}_{\text{UNPERM}}$:

1. Receive a permutation π from the *permuter* P and 2-out-of-2 shares of an array $\langle \mathbf{I} \rangle_0, \langle \mathbf{I} \rangle_1$ from the *storages* S_0, S_1 .
2. Reconstruct \mathbf{I} and compute $\pi^{-1} \cdot \mathbf{I}$.
3. Return $[\mathbf{I}'] \leftarrow \mathcal{F}_{\text{SHARE}}(\pi^{-1} \cdot \mathbf{I})$ for all parties.

Lemma 4.1 (Realization of $\mathcal{F}_{\text{Perm}}$). *There is a $(1, 3)$ -distributed oblivious implementation, described as Algorithm 1, of $\mathcal{F}_{\text{Perm}}$ in the presence of a passive adversary that controls one party. The protocol consumes $4nb + 2n\lceil \log n \rceil$ bits of communication and $12nb + 2n\lceil \log n \rceil$ local CPU computation steps where n is the number of blocks in the input array and b is the bit-length of each block. It also consumes 2 communication rounds.*

Lemma 4.2 (Realization of $\mathcal{F}_{\text{Unperm}}$). *There is a distributed 3-party protocol, described as Algorithm 2, that securely realizes $\mathcal{F}_{\text{Unperm}}$ in the presence of a passive adversary that controls one party and in the $\mathcal{F}_{\text{Reshare}}$ -hybrid model. By composition and using the implementation of $\mathcal{F}_{\text{Reshare}}$ described in Section 3, the protocol consumes $8nb + 2n\lceil \log n \rceil$ bits of communication cost and $19nb + 2n\lceil \log n \rceil$ local CPU computation steps where n is the number of blocks in the input array and b is the bit-length of each block. It also consumes 3 communication rounds.*

Algorithm 1 $(\cdot, \langle \mathbf{I}' \rangle_0, \langle \mathbf{I}' \rangle_1) \leftarrow \Pi_{\text{PERM}}((\pi, [\mathbf{I}]_0), [\mathbf{I}]_1, [\mathbf{I}]_2)$

Notation: P is the “permuter” and S_0, S_1 are two “storages.”

Require: P has a permutation π and each party has shares of an array $[\mathbf{I}]$.

Ensure: $\mathbf{I}' = \pi \cdot \mathbf{I}$.

- 1: P and S_0 convert their $(1, 3)$ -shares $[\mathbf{I}]_0, [\mathbf{I}]_1$ to $(1, 2)$ -shares $\langle \mathbf{I} \rangle_0, \langle \mathbf{I} \rangle_1$, respectively.
 - 2: P chooses random strings \mathbf{U}, \mathbf{V} of the same size as $\langle \mathbf{I} \rangle_0$, and random permutations π_0, π_1 s.t. $\pi_1 \circ \pi_0 = \pi$.
 - 3: P sends $\mathbf{U}, \mathbf{V}, \pi_0$ to S_0 and $\tilde{\mathbf{I}}_0 := \pi \cdot \langle \mathbf{I} \rangle_0 - \pi_1 \cdot \mathbf{U} - \mathbf{V}$ to S_1 .
 - 4: S_0 sends $\tilde{\mathbf{I}}_1 := \pi_0 \cdot \langle \mathbf{I} \rangle_1 + \mathbf{U}$ to S_1 .
 - 5: S_0 outputs $\langle \mathbf{I}' \rangle_0 := \mathbf{V}$, and S_1 outputs $\langle \mathbf{I}' \rangle_1 := \tilde{\mathbf{I}}_0 + \pi_1 \cdot \tilde{\mathbf{I}}_1$.
-

Algorithm 2 $[\mathbf{I}']_0, [\mathbf{I}']_1, [\mathbf{I}']_2 \leftarrow \Pi_{\text{UNPERM}}(\pi, \langle \mathbf{I} \rangle_0, \langle \mathbf{I} \rangle_1)$

Notation: P is the “permuter” and S_0, S_1 are two “storages.”

Require: P has a permutation π and S_0, S_1 have a shares $\langle \mathbf{I} \rangle_0, \langle \mathbf{I} \rangle_1$, respectively.

Ensure: $\mathbf{I}' = \pi^{-1} \cdot \mathbf{I}$.

- 1: S_1 chooses a random strings \mathbf{U}, \mathbf{V} of the same size as $\langle \mathbf{I} \rangle_0$.
 - 2: P chooses random permutations π_0, π_1 s.t. $\pi_1 \circ \pi_0 = \pi$.
 - 3: S_1 sends \mathbf{U} to P and \mathbf{V} to S_0 . P sends π_0 to S_0 and π_1 to S_1 .
 - 4: S_0 sends $\tilde{\mathbf{I}}_0 := \langle \mathbf{I} \rangle_0 + \mathbf{V}$ to P . S_1 sends $\tilde{\mathbf{I}}_1 := \pi_1^{-1} \cdot (\langle \mathbf{I} \rangle_1 - \mathbf{V}) - \mathbf{U}$ to S_0 .
 - 5: P computes $\langle \mathbf{I}' \rangle_0 := \pi^{-1} \cdot \tilde{\mathbf{I}}_0 + \pi_0^{-1} \cdot \mathbf{U}$, and S_0 computes $\langle \mathbf{I}' \rangle_1 := \pi_0^{-1} \cdot \tilde{\mathbf{I}}_1$.
 - 6: Parties call $\mathcal{F}_{\text{RESHARE}}$ to convert $\langle \mathbf{I}' \rangle_0, \langle \mathbf{I}' \rangle_1$ to $[\mathbf{I}']_0, [\mathbf{I}']_1, [\mathbf{I}']_2$.
-

Proof of Lemma 4.1. The output of S_0 is \mathbf{V} and the output of S_1 is

$$\begin{aligned} \tilde{\mathbf{I}}_0 + \pi_1 \cdot \tilde{\mathbf{I}}_1 &= \pi \cdot \langle \mathbf{I} \rangle_0 - \pi_1 \cdot \mathbf{U} - \mathbf{V} + \pi_1 \circ \pi_0 \cdot \langle \mathbf{I} \rangle_1 + \pi_1 \cdot \mathbf{U} \\ &= \pi \cdot \mathbf{I} - \mathbf{V}. \end{aligned}$$

Together, they form a $(1, 2)$ -share of $\pi \cdot \mathbf{I}$, as needed for correctness. The claimed efficiency follows by direct inspection. The strings \mathbf{U} and \mathbf{V} are each nb bits long, similarly to $\tilde{\mathbf{I}}_0$ and $\tilde{\mathbf{I}}_1$. The bit-length of π_0 and π_1 is $n \lceil \log n \rceil$, each.

For security, observe that the permuter P never receives any message and does not have any output, and therefore it is trivial to simulate its view. Similarly, the first storage server S_0 only gets one message $\mathbf{U}, \mathbf{V}, \pi_0$ (from P), all of which are uniformly random and independent of the inputs of all parties. The output of S_0 contains \mathbf{V} and so overall it is immediate to simulate its view. The only case remaining is when S_1 is corrupted. Its view in the protocol consists of $\pi_1, \tilde{\mathbf{I}}_0, \tilde{\mathbf{I}}_1$ which can be simulated by 3 uniformly random strings of appropriate length. Indeed, $\tilde{\mathbf{I}}_0$ is masked by \mathbf{V} and then $\tilde{\mathbf{I}}_1$ is masked by \mathbf{U} , all of which are not known to S_1 . \square

Proof of Lemma 4.2. Since we are in the $\mathcal{F}_{\text{RESHARE}}$ -hybrid model, for correctness we need to show that $\langle \mathbf{I}' \rangle_0 + \langle \mathbf{I}' \rangle_1 = \pi^{-1} \cdot \mathbf{I}$. Indeed,

$$\begin{aligned} \langle \mathbf{I}' \rangle_0 + \langle \mathbf{I}' \rangle_1 &= \pi^{-1} \cdot (\langle \mathbf{I} \rangle_0 + \mathbf{V}) + \pi_0^{-1} \cdot \mathbf{U} + \pi_0^{-1} \cdot (\pi_1^{-1} \cdot (\langle \mathbf{I} \rangle_1 - \mathbf{V}) - \mathbf{U}) \\ &= \pi^{-1} \cdot \mathbf{I}. \end{aligned}$$

The claimed efficiency follows by direct inspection. The strings \mathbf{U} and \mathbf{V} are each nb bits long, similarly to $\tilde{\mathbf{I}}_0$ and $\tilde{\mathbf{I}}_1$. The bit-length of π_0 and π_1 is $n \lceil \log n \rceil$, each.

For security, if S_1 is corrupted, we can easily simulate its view as it does not receive any message except π_1 which is uniformly random and independent of the other inputs. If P is corrupted, then its again immediate to simulate its view since it only receives \mathbf{U} and $\tilde{\mathbf{I}}_0 := \langle \mathbf{I} \rangle_0 + \mathbf{V}$ throughout the execution, both of which are uniformly distributed (in P 's view). Lastly, assume that S_0 is corrupted. Its view consists of \mathbf{V}, π_0 and $\tilde{\mathbf{I}}_1 := \pi_1^{-1} \cdot (\langle \mathbf{I} \rangle_1 - \mathbf{V}) - \mathbf{U}$. Since it does not know \mathbf{U} , the term $\tilde{\mathbf{I}}_1$ looks completely uniform and therefore the whole view can be simulated by 3 uniformly random strings of the appropriate length. \square

4.2 Distributed Oblivious Hashing

Equipped with the above permutation protocols, we proceed with our distributed oblivious hashing scheme. The idea is simple to describe at a high level: given a set of elements we first obliviously permute them and then we index the permuted set using a non-oblivious efficient hashing scheme. For the latter, we use *Cuckoo hashing* [50], a hashing paradigm that resolves collisions in a table by using two hash functions and two tables, cleverly assigning each element to one of the two tables, and enabling lookup using only two queries. The standard version of Cuckoo hashing suffers from inverse polynomial probability of build failure which does not suffice for our application (since we aim for negligible error). To this end, we use a variant of Cuckoo hashing where items that cannot be stored in one of the two tables are stored in a (typically small) “stash”. According to Noble [47], for any number of elements $n = \omega(\log \lambda)$, there exists Cuckoo hashing that has the table of size $\tau = (1 + \epsilon)n$ and the stash of size $\sigma = \Theta(\log \lambda)$ with negligible failure probability. Henceforth, we specify $\tau = 2n$ and $\sigma = \lceil \log \lambda \rceil$ for concrete efficiency analysis.

To ease presentation of our implementations, we use the following notations to represent shares of real/dummy blocks.

- Let $\llbracket \mathbf{d} \rrbracket = (\llbracket k \rrbracket, \llbracket v \rrbracket)$ be a share of a data block that contains shares of a key k and value v .
- Let $\llbracket \mathbf{d}^{\text{dummy}} \rrbracket = (\llbracket \perp \rrbracket, \llbracket \perp \rrbracket)$ be a share of a dummy block. We assume that \perp is a number greater than any real k .
- Let $\llbracket \mathbf{D} \rrbracket = (\llbracket \mathbf{d}_0 \rrbracket, \dots, \llbracket \mathbf{d}_{n-1} \rrbracket)$ be a share of a dataset of size n .

Algorithm 3 constructs 2-out-of-2 shares of a Cuckoo hash table, $(\langle \mathbf{T} \rangle, \langle \mathbf{S} \rangle)$, from 2-out-of-3 shares of a dataset $\llbracket \mathbf{D} \rrbracket$. In this protocol, the party assigned the role of *permuter* obtains all addresses the data should be placed and then computes a permutation π that moves the data to (one of) the corresponding addresses. Now, parties can efficiently convert \mathbf{D} into the table (\mathbf{T}, \mathbf{S}) via $\mathcal{F}_{\text{PERM}}$.

Algorithm 4 fetches a queried item from the Cuckoo hash table. The main part of this protocol is that the parties assigned the role of *storage* obtain two addresses of the queried item, access \mathbf{T} of the location indicated by them, and select the one out of the two items of \mathbf{T} . When the parties receive a dummy query, random locations of \mathbf{T} are fetched.

Algorithm 5 is a deconstruction procedure that applies the inverted permutation π^{-1} to the hash table. This π^{-1} sorts all real blocks in the hash table into the order in which they were input to Π_{BUILD} .

Lemma 4.3. *Assume that the input array consists of $n = \omega(\log \lambda)$ blocks and each block is b bits. There exists a distributed 3-party protocol, described as Algorithms 3, 4 and 5, that securely realizes \mathcal{F}_{HT} in the $(\mathcal{F}_{\text{SHARE}}, \mathcal{F}_{\text{REVEAL}}^{\mathcal{P}}, \mathcal{F}_{\text{ABB}}, \mathcal{F}_{\text{PERM}}, \mathcal{F}_{\text{UNPERM}})$ -hybrid model and in the presence of a passive adversary that controls one party.*

- The BUILD procedure consumes $O(n(b' + \lceil \log n \rceil))$ local computation steps.
- The LOOKUP procedure consumes $O(b' + b' \log \lambda)$ local computation steps.

Algorithm 3 $\pi, (\langle \mathbf{T} \rangle_i, \langle \mathbf{S} \rangle_i)_{(i \in \{0,1\})}, \llbracket s_0 \rrbracket, \llbracket s_1 \rrbracket, \mathbf{ctr}, \mathbf{P} \leftarrow \mathcal{H}_{\text{BUILD}}(\llbracket \mathbf{D} \rrbracket_0, \llbracket \mathbf{D} \rrbracket_1, \llbracket \mathbf{D} \rrbracket_2)$

Notation: Let P be the permuter and S_0, S_1 be the storages. Let $\tau := |\mathbf{T}|$ and $\sigma := |\mathbf{S}|$ be the size of expected hash table (\mathbf{T}, \mathbf{S}) storing n items, and let $n' = \tau + \sigma$.

Require: Parties have $(1, 3)$ -shares of a dataset $\llbracket \mathbf{D} \rrbracket_{i \in \{0,1,2\}} = (\llbracket d_0 \rrbracket_i, \dots, \llbracket d_{n-1} \rrbracket_i)$.

Ensure: S_0 and S_1 obtain $(1, 2)$ -shares of Cuckoo hash table, $(\langle \mathbf{T} \rangle_0, \langle \mathbf{S} \rangle_0)$ and $(\langle \mathbf{T} \rangle_1, \langle \mathbf{S} \rangle_1)$, respectively. P obtains a permutation π , and all parties hold $(1, 3)$ -shares of PRF keys, $\llbracket s_0 \rrbracket$ and $\llbracket s_1 \rrbracket$. Parties also hold a query counter \mathbf{ctr} , and S_0, S_1 hold a set \mathbf{P} , as their states.

(Computing pseudorandom addresses for the input data.)

- 1: Parties call \mathcal{F}_{RND} to generate random PRF keys $\llbracket s_0 \rrbracket$ and $\llbracket s_1 \rrbracket$.
- 2: **for all** $i \in [n]$ **do**
- 3: Parties call \mathcal{F}_{EQ} to obtain $\llbracket \text{isDummy}_i \rrbracket := \llbracket k_i =? \perp \rrbracket$.
- 4: Parties call $\mathcal{F}_{\text{IFELSE}}$ to simulate:
If $\text{isDummy}_i = 1$, then $\llbracket \tilde{k}_i \rrbracket := \llbracket \perp + i \rrbracket$; otherwise $\llbracket \tilde{k}_i \rrbracket := \llbracket k_i \rrbracket$.
- 5: Parties call \mathcal{F}_{PRF} to obtain pseudorandom addresses $\llbracket \text{addr}_{i,0} \rrbracket$ and $\llbracket \text{addr}_{i,1} \rrbracket$ from $(\llbracket s_0 \rrbracket, \llbracket \tilde{k}_i \rrbracket)$ and $(\llbracket s_1 \rrbracket, \llbracket \tilde{k}_i \rrbracket)$, respectively.

(Building a Cuckoo hash table via permutation.)

- 6: Parties call $\mathcal{F}_{\text{SHARE}}$ to generate an array $\llbracket \mathbf{E} \rrbracket$ consisting of $n' - n$ dummy blocks.
 - 7: Let $\llbracket \tilde{\mathbf{D}} \rrbracket := \llbracket \mathbf{D} \rrbracket \parallel \llbracket \mathbf{E} \rrbracket$ be a concatenated dataset.
 - 8: Parties call $\mathcal{F}_{\text{REVEAL}}^{\{P\}}$ to reveal to P all $\text{addr}_{i,0}, \text{addr}_{i,1}$ for $i \in [n]$.
 - 9: P computes a permutation $\pi: [n'] \rightarrow [n']$ that indicates the bin-placements of Cuckoo hashing. That is, π says where to place $\llbracket \tilde{\mathbf{D}} \rrbracket$'s first n elements (i.e., the elements of $\llbracket \mathbf{D} \rrbracket$), as indicated by either $\text{addr}_{i,0}$ or $\text{addr}_{i,1}$.
 - 10: Parties call $\mathcal{F}_{\text{PERM}}$ with π and $\llbracket \tilde{\mathbf{D}} \rrbracket$ to make S_0 and S_1 obtain $\langle \tilde{\mathbf{D}}' \rangle_0$ and $\langle \tilde{\mathbf{D}}' \rangle_1$ respectively, where $\tilde{\mathbf{D}}' = \pi \cdot \tilde{\mathbf{D}}$.
 - 11: Each S_i for $i \in \{0, 1\}$ organizes the array $\langle \tilde{\mathbf{D}}' \rangle_i$ into a hash table $\langle \mathbf{T} \rangle_i$ and stash $\langle \mathbf{S} \rangle_i$, by separating $\langle \tilde{\mathbf{D}}' \rangle_i$ into the first τ and the last σ elements.
 - 12: Parties set their state $\mathbf{ctr} = 0$, and S_0, S_1 allocate an empty set $\mathbf{P} = \emptyset$.
 - 13: **Return** $\pi, (\langle \mathbf{T} \rangle_0, \langle \mathbf{S} \rangle_0), (\langle \mathbf{T} \rangle_1, \langle \mathbf{S} \rangle_1), \llbracket s_0 \rrbracket, \llbracket s_1 \rrbracket, \mathbf{ctr}, \mathbf{P}$
-

– The EXTRACT procedure consumes $O(nb')$ local computation steps.

Furthermore, all those procedures consumes $O(1)$ communication rounds.

Proof. The proof of security and correctness is given in Appendix B.1. We focus on the efficiency analysis below.

Algorithm 3 consists of $O(n)$ calls of $\mathcal{F}_{\text{EQ}}, \mathcal{F}_{\text{IFELSE}}, \mathcal{F}_{\text{PRF}}, \mathcal{F}_{\text{SHARE}}$, and $\mathcal{F}_{\text{REVEAL}}$, and an invocation of $\mathcal{F}_{\text{PERM}}$. In Algorithm 4, the parties need to call $\mathcal{F}_{\text{MULT}}, \mathcal{F}_{\text{RESHARE}}, \mathcal{F}_{\text{EQ}}, \mathcal{F}_{\text{IFELSE}}, \mathcal{F}_{\text{REVEAL}}$, and \mathcal{F}_{PRF} $O(1)$ times for the table lookup, and also call $\mathcal{F}_{\text{MULT}}, \mathcal{F}_{\text{RESHARE}}, \mathcal{F}_{\text{EQ}}$, and $\mathcal{F}_{\text{IFELSE}}$ $O(\sigma)$ times for exploring the stash. Lastly, Algorithm 5 requires $\mathcal{F}_{\text{UNPERM}}$ at once. In addition, all iterative operations in Algorithm 3, 4, and 5 can be performed in parallel. \square

Algorithm 4 $[[d], [found]] \leftarrow \Pi_{\text{LOOKUP}}(\llbracket k \rrbracket, (\langle T \rangle_i, \langle S \rangle_i)_{(i \in \{0,1\})}, \llbracket s_0 \rrbracket, \llbracket s_1 \rrbracket, \text{ctr}, \mathbf{P})$

Notation: Let P be the permuter and let S_0, S_1 be the storages. Let $\sigma = |S|$.

Require: $\llbracket k \rrbracket$ is a $(1, 3)$ -share of an input key to be searched for. S_0, S_1 have the distributed hash table $(\langle T \rangle_i, \langle S \rangle_i)$ and a set \mathbf{P} . Parties have $(1, 3)$ -shares of PRF keys $\llbracket s_0 \rrbracket, \llbracket s_1 \rrbracket$ and a query counter ctr .

Ensure: $d = (k, v)$, $\text{found} = 1$ if (T, S) contains (k, v) . Otherwise, $d = (0, 0)$, $\text{found} = 0$.
(Computing pseudorandom addresses to be fetched.)

1: Parties call \mathcal{F}_{EQ} to obtain $[[\text{isDummy}]] := \llbracket k =? \perp \rrbracket$.

2: Parties call $\mathcal{F}_{\text{IFELSE}}$ to simulate:

If isDummy = 1, then set $[\tilde{k}] := \llbracket \perp + \text{ctr} \rrbracket$; otherwise $[\tilde{k}] := \llbracket k \rrbracket$.

3: Parties call \mathcal{F}_{PRF} to obtain $[\text{addr}_0], [\text{addr}_1]$ from $(\llbracket s_0 \rrbracket, [\tilde{k}]), (\llbracket s_1 \rrbracket, [\tilde{k}])$, respectively.

4: Parties call $\mathcal{F}_{\text{REVEAL}}^{\{S_0, S_1\}}$ to recover $\text{addr}_0, \text{addr}_1$ to both S_0 and S_1 . If $(\text{addr}_0, \text{addr}_1) \in \mathbf{P}$, S_0 and S_1 halt. Otherwise, they update $\mathbf{P} \leftarrow \mathbf{P} \cup \{(\text{addr}_0, \text{addr}_1)\}$.

(Searching for the table T.)

5: **for all** $i = 0, 1$ **do**

6: Parties call $\mathcal{F}_{\text{RESHARE}}$ to convert $\langle T[\text{addr}_i] \rangle = (\langle k_i \rangle, \langle v_i \rangle)$ to $(\llbracket k_i \rrbracket, \llbracket v_i \rrbracket)$.

7: Parties call \mathcal{F}_{EQ} to obtain $[[\text{isQueried}_i]] = \llbracket k_i =? k \rrbracket$.

8: Parties call $\mathcal{F}_{\text{IFELSE}}$ and $\mathcal{F}_{\text{MULT}}$ to simulate:

*If isDummy = 0 and isQueried_i = 1,
then found = 1, $[[d]] = (\llbracket k_i \rrbracket, \llbracket v_i \rrbracket)$, and $\langle T[\text{addr}_i] \rangle = \langle d^{\text{dummy}} \rangle$.*

(Searching for the stash S.)

9: **for all** $u \in [\sigma]$ **do**

10: Parties call $\mathcal{F}_{\text{RESHARE}}$ to convert $\langle S[u] \rangle = (\langle k_u \rangle, \langle v_u \rangle)$ to $(\llbracket k_u \rrbracket, \llbracket v_u \rrbracket)$.

11: Parties call \mathcal{F}_{EQ} to obtain $[[\text{isQueried}_u]] = \llbracket k_u =? k \rrbracket$.

12: Parties call $\mathcal{F}_{\text{IFELSE}}$ and $\mathcal{F}_{\text{MULT}}$ to simulate:

*If isDummy_u = 0 and isQueried_u = 1,
then found = 1, $[[d]] = (\llbracket k_u \rrbracket, \llbracket v_u \rrbracket)$, and $\langle S[u] \rangle = \langle d^{\text{dummy}} \rangle$.*

13: Parties increment ctr .

14: **Return** $[[d], [found]]$

5 Optimal DORAM Against Passive Adversary

In this section, we give our optimal 3-party DORAM that is secure against a passive adversary who colludes with one of the three servers.

Let $N \in \text{poly}(\lambda)$ be the memory size. Our DORAM is on the known hierarchical paradigm, i.e. the data structure is built via a hierarchy of $L := \lceil \log N - \log \log N \rceil$ distributed hash tables and one top-level array. All levels $i = 1, \dots, L$ in the hierarchy are implemented using our distributed oblivious hash table \mathcal{F}_{HT} from Section 4.2. The size of the stash in our distributed oblivious hash table is set to $\sigma = \lceil \log N \rceil$. The top-level array can store up to $c = 2\sigma$ data blocks. The capacity of level $i \in [L]$ is $c2^{i-1}$. Each data block may be associated with metadata that is used to keep track of the location of an element. Specifically, an ‘‘augmented data block’’ $[[d_i]] := (\llbracket d_i \rrbracket, \llbracket v_i \rrbracket)$ consists of the main

Algorithm 5 $[[D]_0, [D]_1, [D]_2 \leftarrow \Pi_{\text{EXTRACT}}(\pi, (\langle T \rangle_i, \langle S \rangle_i)_{i \in \{0,1\}})$

Notation: Let P be the permuter and let S_0, S_1 be the storages.

Require: S_0 and S_1 have the distributed hash table $(\langle T \rangle_0, \langle S \rangle_0)$ and $(\langle T \rangle_1, \langle S \rangle_1)$, respectively. P have the permutation π .

Ensure: A dataset D contains all real elements in T and S .

- 1: Each S_i for $i \in \{0, 1\}$ reorganizes $\langle T \rangle_i$ and $\langle S \rangle_i$ into an array $\langle \tilde{D} \rangle_i$ as $\tilde{D} = T \parallel S$.
 - 2: Parties call $\mathcal{F}_{\text{UNPERM}}$ with $(\pi, \langle \tilde{D} \rangle_0, \langle \tilde{D} \rangle_1)$ to obtain $[[\tilde{D}']]_{j \in \{0,1,2\}}$ where $\tilde{D}' = \pi^{-1} \cdot \tilde{D}$.
 - 3: Let $[[D]]_j$ be the first n elements of $[[\tilde{D}']]_i$.
 - 4: **Return** $[[D]]_0, [[D]]_1, [[D]]_2$
-

data block $[[d_i]] = ([[k_i]], [[v_i]])$ and additional information $[[lv_i]]$; $lv_i \in \{0, 1\}^{L-1}$ that indicates the levels to which $[[\tilde{d}_i]]$ is associated.⁷

Recall that each instance of HT_i consists of two parts: the main table and a stash. Looking forward, the stashes from all levels are combined to the top-level array, as commonly done (e.g., in [44]). Thus, when we perform LOOKUP or EXTRACT to some HT_i , we ignore stash-related operations (which will be done in a centralized fashion at the beginning of the operation).

Theorem 5.1. *There is a 3-party protocol, described as Algorithm 6 and 7, that securely realizes \mathcal{F}_{RAM} in the $(\mathcal{F}_{\text{ABB}}, \mathcal{F}_{\text{HT}})$ -hybrid model and in the presence of a passive adversary that controls one party. The construction costs $O(\log N)$ amortized computational overhead and $O(\log N)$ communication rounds with $\Omega(\log N)$ block size.*

Proof. The proof of security is given in Appendix B.2. In addition, correctness is clear from the algorithms since we are in the \mathcal{F}_{HT} -hybrid model. Indeed, a lookup is performed through the whole hierarchy and when an element is found, it is re-inserted into the hierarchy. Hence, we focus on efficiency analysis next.

Algorithm 6 requires $O(c)$ calls of $\mathcal{F}_{\text{EQ}}, \mathcal{F}_{\text{IFELSE}}$, and $\mathcal{F}_{\text{MULT}}$, $O(L)$ calls of H.LOOKUP (without stash), and an invocation of $\Pi_{\text{RESHUFFLE}}$, for $c = L = O(\log N)$. For any block size $b = \Omega(\log N)$ bits, this procedure requires $O(b \log N) + \mathcal{C}$ computational steps where \mathcal{C} is the amortized cost of $\Pi_{\text{RESHUFFLE}}$. Algorithm 7 costs $\text{H}_i.\text{EXTRACT}$ for all $i = 1, \dots, p$, and $\text{H}_p.\text{BUILD}$ at once, per $c2^{p-1}$ access. Hence, its amortized cost can be estimated as $\mathcal{C}_{\text{RESHUFFLE}} = \sum_{p=1}^L \frac{O(c2^p b)}{c2^{p-1}}$. \square

Concrete Efficiency. As we mentioned in Section 1.1, the overhead claimed above depends on a small hidden constant. Specifically, our Π_{ACCESS} consists of only $4 \log N$ (amortized) calls of \mathcal{F}_{PRF} per access, that is 25 times smaller than the known optimal DORAM [44]. For more detailed analysis, see Appendix A.

⁷ The metadata associated with each data blocks is used to avoid the stash-resampling attack of [22], same as was done in [3, 4, 22].

Algorithm 6 $\llbracket \mathbf{d} \rrbracket \leftarrow \Pi_{\text{ACCESS}}(\llbracket \mathbf{op} \rrbracket, \llbracket k \rrbracket, \llbracket v' \rrbracket)$

Require: The input contains shares of an operation $\llbracket \mathbf{op} \rrbracket$; $\mathbf{op} \in \{\text{read}, \text{write}\}$, key $\llbracket k \rrbracket$, and value $\llbracket v' \rrbracket$.

Ensure: $\mathbf{d} = (k, v)$ if the ORAM holds (k, v) , or $\mathbf{d} = (k, v')$ if $\mathbf{op} = \text{write}$. Otherwise, $\mathbf{d} = (\perp, \perp)$.

(Searching for the top-level array.)

- 1: Allocate an empty data block $\llbracket \tilde{\mathbf{d}} \rrbracket = (\llbracket \mathbf{d} \rrbracket, \llbracket \mathbf{lv} \rrbracket)$ and initialize $\llbracket \text{found} \rrbracket = \llbracket 0 \rrbracket$.
- 2: **for all** $u \in [c]$ **do**
- 3: Parties retrieve $\llbracket \tilde{\mathbf{d}}_u \rrbracket = ((\llbracket k_u \rrbracket, \llbracket v_u \rrbracket), \llbracket \mathbf{lv}_u \rrbracket)$ from the top-level array.
- 4: Parties call \mathcal{F}_{EQ} to obtain $\llbracket \text{found}_u \rrbracket = \llbracket k_u =? k \rrbracket$.
- 5: Parties call $\mathcal{F}_{\text{IFELSE}}$ and $\mathcal{F}_{\text{MULT}}$ to simulate:
If found = 0 and found_u = 1, then found = 1 and $\llbracket \tilde{\mathbf{d}} \rrbracket = \llbracket \tilde{\mathbf{d}}_u \rrbracket$.

(Searching for the cuckoo hash table in the hierarchy.)

- 6: **for** $i = 1$ to L **do**
- 7: Parties call $\mathcal{F}_{\text{BITEXT}}$ to obtain $\llbracket \mathbf{lv}_i \rrbracket$ where \mathbf{lv}_i is the i -th least significant bit of \mathbf{lv} .
- 8: Parties call $\mathcal{F}_{\text{IFELSE}}$ and $\mathcal{F}_{\text{MULT}}$ to simulate:
If found = 1 and $\mathbf{lv}_i = 0$ then $\llbracket \tilde{k} \rrbracket := \llbracket \perp \rrbracket$, otherwise $\llbracket \tilde{k} \rrbracket := \llbracket k \rrbracket$.
- 9: Parties call $\text{HT}_i.\text{LOOKUP}(\llbracket \tilde{k} \rrbracket)$ with its state $(\langle \mathbf{T}_i \rangle_0, \langle \mathbf{T}_i \rangle_1, \llbracket s_{i,0} \rrbracket, \llbracket s_{i,1} \rrbracket, \text{ctr}, \mathbf{P})$ to obtain $\llbracket \tilde{\mathbf{d}}_i \rrbracket, \llbracket \text{found}_i \rrbracket$.
- 10: Set $\llbracket \mathbf{d} \rrbracket = \llbracket \mathbf{d} \rrbracket + \llbracket \tilde{\mathbf{d}}_i \rrbracket$ and $\llbracket \text{found} \rrbracket = \llbracket \text{found} \rrbracket + \llbracket \text{found}_i \rrbracket$.

(Rewriting (if needed) and re-storing the retrieved data.)

- 11: Parties call $\mathcal{F}_{\text{IFELSE}}$ to simulate: *If found = 0 then set $\llbracket \mathbf{d} \rrbracket = \llbracket \mathbf{d}^{\text{dummy}} \rrbracket$.*
 - 12: Parties also call $\mathcal{F}_{\text{IFELSE}}$ to simulate: *If $\mathbf{op} = \text{write}$ then set $\llbracket \mathbf{d} \rrbracket = (\llbracket k \rrbracket, \llbracket v' \rrbracket)$.*
 - 13: Parties set $\llbracket \tilde{\mathbf{d}} \rrbracket = (\llbracket \mathbf{d} \rrbracket, \llbracket 0 \rrbracket)$ and concatenate it into the end of the top-level array.
 - 14: If the size of the top-level array is c , parties run $\Pi_{\text{RESHUFFLE}}()$ to refresh the hierarchy.
 - 15: **Return** $\llbracket \mathbf{d} \rrbracket$
-

6 Actively Secure Extension

We extend our oblivious hashing and DORAM to be secure against an adversary that can deviate from the prescribed protocols. Though it is *almost* feasible by a generic framework of actively secure MPC, we require a new permutation (Section 6.1) and *verifying permutations* protocol (Section 6.2) in addition.

By known frameworks of actively secure MPC with abort [13, 32, 35], we assume that our protocols are in the flow of the following three phases:

- **Randomization phase.** For any share $\llbracket a \rrbracket$, parties compute $\llbracket ra \rrbracket$ with random secret r and store $(\llbracket a \rrbracket, \llbracket ra \rrbracket)$ as the share of a with a MAC.
- **Computation phase.** Parties compute a target function F on input $(\llbracket a \rrbracket, \llbracket ra \rrbracket)$ while recording checksums. For example, let $F = f_0 \circ f_1$ and assume Π_{f_0}, Π_{f_1} that are protocols used to compute $(\llbracket f_{\{0,1\}}(a) \rrbracket, \llbracket r f_{\{0,1\}}(a) \rrbracket)$ from $(\llbracket a \rrbracket, \llbracket ra \rrbracket)$ and are further secure up to additive attacks [25, 24]. Now, the parties allocate a set of checksums $\mathcal{S} = \emptyset$ and perform Π_{f_2} and Π_{f_1} in sequence to obtain $\llbracket F(a) \rrbracket$ while storing all inputs and outputs of the protocols, i.e., $(\llbracket a \rrbracket, \llbracket ra \rrbracket)$, $(\llbracket f_2(a) \rrbracket, \llbracket r f_2(a) \rrbracket)$ and $(\llbracket F(a) \rrbracket, \llbracket r F(a) \rrbracket)$, into \mathcal{S} .

Algorithm 7 $\Pi_{\text{RESHUFFLE}}()$

Require: p is the level s.t. all $\text{HT}_{i < p}$ in the hierarchy is filled and HT_p is not.

Ensure: All $\text{HT}_{i < p}$ in the hierarchy become empty, and HT_p is filled.

(Extracting all the data that needs to be reshuffled.)

- 1: Parties retrieve all c blocks from the top-level array and let them be an array $\llbracket \mathbf{A} \rrbracket$.
- 2: For all $i = 1, \dots, p-1$, parties call $\text{HT}_i.\text{EXTRACT}()$ with its state $(\pi_i, \langle \mathbf{T}_i \rangle_0, \langle \mathbf{T}_i \rangle_1)$ to extract all real elements as $\llbracket \mathbf{D}_i \rrbracket$ and concatenate them to $\llbracket \mathbf{A} \rrbracket$ as $\llbracket \mathbf{A} \rrbracket = \llbracket \mathbf{A} \rrbracket \parallel \llbracket \mathbf{D}_i \rrbracket$.
- 3: For all elements $\llbracket \mathbf{d}_j \rrbracket = (\llbracket \mathbf{d}_j \rrbracket, \llbracket \mathbf{lv}_j \rrbracket)$ of $\llbracket \mathbf{A} \rrbracket$, parties call $\mathcal{F}_{\text{TRUNC}}$ to set the p -least significant bits of \mathbf{lv}_j to 0.

(Building a new cuckoo hash table.)

- 4: Parties call $\text{HT}_p.\text{BUILD}(\llbracket \mathbf{A} \rrbracket)$ to construct a distributed hash table $(\pi_p, (\langle \mathbf{T}_p \rangle_i, \langle \mathbf{S}_p \rangle_i)_{i \in \{0,1\}}, \llbracket s_{p,0} \rrbracket, \llbracket s_{p,1} \rrbracket, \text{ctr}, \mathbf{P})$.
- 5: Parties call $\mathcal{F}_{\text{RESHARE}}$ to convert $\langle \mathbf{S}_p \rangle_i$ to $\llbracket \mathbf{S}_p \rrbracket$ and store it to the top-level array.
- 6: For all $\llbracket \tilde{\mathbf{d}}_u \rrbracket = (\llbracket \mathbf{d}_u \rrbracket, \llbracket \mathbf{lv}_u \rrbracket)$ in the top-level array, call \mathcal{F}_{EQ} and $\mathcal{F}_{\text{IFELSE}}$ to simulate: If $k_i \neq \perp$, set $\llbracket \mathbf{lv}_u \rrbracket = \llbracket \mathbf{lv}_u \rrbracket + \llbracket 1 \rrbracket 2^{p-1}$.

- **Proof phase.** To detect cheating, parties evaluate the shares and their MACs recorded as checksums. Following the above example, the parties generate new random shares $\llbracket \rho_0 \rrbracket, \llbracket \rho_1 \rrbracket$ and $\llbracket \rho_2 \rrbracket$, and compute inner products

$$\begin{aligned} \llbracket \gamma \rrbracket &= (\llbracket \rho_0 \rrbracket \times \llbracket a \rrbracket + \llbracket \rho_1 \rrbracket \times \llbracket f_2(a) \rrbracket + \llbracket \rho_2 \rrbracket \times \llbracket F(a) \rrbracket) \text{ and} \\ \llbracket r\gamma \rrbracket &= (\llbracket \rho_0 \rrbracket \times \llbracket ra \rrbracket + \llbracket \rho_1 \rrbracket \times \llbracket rf_2(a) \rrbracket + \llbracket \rho_2 \rrbracket \times \llbracket rF(a) \rrbracket). \end{aligned}$$

The parties recover $\llbracket \eta \rrbracket = \llbracket r \rrbracket \times \llbracket \gamma \rrbracket - \llbracket r\gamma \rrbracket$, and if $\eta \neq 0$ then they abort.

For our DORAM, we should be more concerned about the form of shares than in the passive security model. Since part of building blocks in Section 3, e.g., \mathcal{F}_{PRF} , requires bit-wise operations, we should assign MACs to bits $a_{\ell-1}, \dots, a_0 \in \mathbb{Z}_2$, instead of the whole $a \in \mathbb{Z}_{2^\ell}$. According to Kikuchi et al. [35], we can provide a MAC for a bit $u \in \{0, 1\}$ as $\llbracket ru \rrbracket := (\llbracket r_{\kappa-1}u \rrbracket, \dots, \llbracket r_0u \rrbracket)$ where each r_j is in \mathbb{Z}_2 . To detect cheating with overwhelming probability $1 - \lambda^{\omega(1)}$, this κ should be $\omega(\log \lambda)$. Hence, by encoding $(\llbracket a \rrbracket, \llbracket ra \rrbracket)$ as $((\llbracket a_{\ell-1} \rrbracket, \dots, \llbracket a_0 \rrbracket), (\llbracket ra_{\ell-1} \rrbracket, \dots, \llbracket ra_{\ell-1} \rrbracket))$, the \mathcal{F}_{ABB} functionality described in Section 3 is also available in active security model. Note that the communication and computation cost of each function increases to $O(\ell')$ where $\ell' = \omega(\ell \log \lambda)$ is the bit-length of a share with MAC, and the cost of the Proof phase is at most twice that of an original function [13].

To simplify the notation, we denote $(\llbracket a \rrbracket, \llbracket ra \rrbracket)$ as $\llbracket a \rrbracket^m$ in the following.

6.1 Secure Oblivious Permutation Up To Additive Attacks

We start at constructing a secure permutation protocol up to additive attacks. In contrast to Section 4.1, we assume that a permutation π provided by one party has been already separated into π_0 and π_1 s.t. $\pi = \pi_1 \circ \pi_0$ and shared between parties. We discuss verification for the permutation π in Section 6.2, and here we focus on cheating on an array that should be permuted.

Our permutation protocol described in Algorithm 1 relies on (semi-)honest parties and is difficult to be converted to be secure up to additive attacks. In-

stead, we construct a permutation protocol using a reshare-based shuffling as in Ikarashi et al. [32]. Let $\mathcal{F}_{\text{SHUFFLE}}$ be a functionality for secure shuffling up to an additive attack s.t. it receives shares of an array $[\mathbf{I}]$ and a permutation π from honest parties and an array Δ of the same size as \mathbf{I} from an adversary, then it returns $[\pi \cdot \mathbf{I} + \Delta]$. Now, the following protocol, originally proposed by Laur et al. [43], is the implementation of $\mathcal{F}_{\text{SHUFFLE}}$ that costs n calls of $\mathcal{F}_{\text{RESHARE}}$.

$[\mathbf{I}']_0, [\mathbf{I}']_1, [\mathbf{I}']_2 \leftarrow \Pi_{\text{SHUFFLE}}((\pi, [\mathbf{I}]_0), (\pi, [\mathbf{I}]_1), [\mathbf{I}]_2) :$

1. P_0 and P_1 convert their $[\mathbf{I}]_{i \in \{0,1\}}$ to $\langle \mathbf{I} \rangle_i$ and compute $\langle \mathbf{I}' \rangle_i = \pi \cdot \langle \mathbf{I} \rangle_i$ each.
2. Parties call $\mathcal{F}_{\text{RESHARE}}$ to obtain $[\mathbf{I}']_{\{0,1,2\}}$ from $\langle \mathbf{I}' \rangle_{\{0,1\}}$.
3. Output $[\mathbf{I}']_{\{0,1,2\}}$.

Algorithm 8 $[\mathbf{I}']_0^m, [\mathbf{I}']_1^m, [\mathbf{I}']_2^m \leftarrow \Pi_{\text{PERM}}^{\text{active}}((\pi_0, \pi_1), [\mathbf{I}]_0^m, [\mathbf{I}]_1^m, [\mathbf{I}]_2^m)$

Notation: Let P be the permuter and let S_0, S_1 be the storages. Let \mathcal{S} be a set of checksums.

Require: P has both π_0, π_1 and $[\mathbf{I}]_0^m$. Each $S_{i=0,1}$ has π_i and $[\mathbf{I}]_{i+1}^m$.

Ensure: $\mathbf{I}' = \pi \cdot \mathbf{I}$ and $r\mathbf{I}' = \pi \cdot (r\mathbf{I})$ where $\pi = \pi_1 \circ \pi_0$.

- 1: Parties record their input shares into \mathcal{S} as $\mathcal{S} = \mathcal{S} \cup \{[\mathbf{I}]^m\}$, and let $[\mathbf{I}'_0]^m = [\mathbf{I}]^m$.
 - 2: **for** $i = 0, 1$ **do**
 - 3: Parties call $\mathcal{F}_{\text{SHUFFLE}}$ with inputs π_i and $[\mathbf{I}'_i]^m$ to receive $[\mathbf{I}'_{i+1}]^m = [\pi_i \cdot \mathbf{I}'_i]^m$.
 - 4: Parties record their shares into \mathcal{S} as $\mathcal{S} \leftarrow \mathcal{S} \cup \{[\mathbf{I}'_{i+1}]^m\}$.
 - 5: Parties store $[\mathbf{I}'_2]_0^m, [\mathbf{I}'_2]_1^m, [\mathbf{I}'_2]_2^m$ as $[\mathbf{I}']_0^m, [\mathbf{I}']_1^m, [\mathbf{I}']_2^m$, respectively.
 - 6: **Return** $[\mathbf{I}']_{\{0,1,2\}}^m$
-

Now, we achieve an actively secure permutation protocol for our distributed oblivious hashing in the $\mathcal{F}_{\text{SHUFFLE}}$ -hybrid model. The detailed protocol is described in Algorithm 8. We can also obtain an actively secure *unpermutation* protocol, $\Pi_{\text{UNPERM}}^{\text{active}}$, as an inverse of Algorithm 8.

6.2 Actively Secure Distributed Hashing

Even though an actively secure oblivious hashing can be obtained *almost* completely straightforwardly by replacing all functionalities in Algorithm 3, 4, and 5 by actively secure ones, it is still *not secure against a corrupted permuter* that can input an invalid permutation. We thus focus on solving this problem. The permutation π is *valid* if all keys are always found in their place, *whatever π actually is*. This means that the pseudorandom addresses can work as *witnesses* for the correctness of the hash table, i.e., we can verify π by checking whether

- (one of two pseudorandom addresses computed from a non-dummy key)
 – (the actual address where the element resides)

is equal to 0 for all real keys. This verification can be done in the BUILD procedure as follows: For all real blocks $[\mathbf{d}_i]$ located in $\mathbb{T}[\text{addr}_i^*]$ and assigned to the

pseudorandom addresses ($\llbracket \text{addr}_{i,0}^p \rrbracket, \llbracket \text{addr}_{i,1}^p \rrbracket$), parties check the following equation with uniformly random ρ_i .

$$0 =? \sum_i \llbracket \rho_i \rrbracket \times (\llbracket \text{addr}_{i,0}^p \rrbracket - \text{addr}_i^r) \times (\llbracket \text{addr}_{i,1}^p \rrbracket - \text{addr}_i^r). \quad (6.1)$$

In addition, for efficiency, we combine the above verification with the MAC verification. Remember that, in the Proof phase for MACs, fresh random shares $\llbracket \rho_i \rrbracket$ are given for each checksum ($\llbracket a \rrbracket, \llbracket ra \rrbracket$). Noticing the similarity in the use of the random ρ_i , for any r , we can transform Equation (6.1) as below:

$$\begin{aligned} [r] \times \sum_i \llbracket \rho_i \rrbracket \times (\llbracket \text{addr}_{i,0}^p \rrbracket \times \llbracket \text{addr}_{i,1}^p \rrbracket + (\text{addr}_i^r)^2) =? \\ \sum_i \llbracket \rho_i \rrbracket \times (\llbracket r \times \text{addr}_{i,0}^p \rrbracket + \llbracket r \times \text{addr}_{i,1}^p \rrbracket) \times \text{addr}_i^r. \end{aligned} \quad (6.2)$$

Now, to make parties evaluate the above equation in the Proof phase, we propose the following new protocol that checks the consistency between the virtual address and the actual address of each non-dummy block.

$\Pi_{\text{VERPERM}}(\llbracket \text{addr}_{i,0}^p \rrbracket^m, \llbracket \text{addr}_{i,1}^p \rrbracket^m, \text{addr}_i^r)$:

1. At first, parties call the actively secure $\mathcal{F}_{\text{MULT}}$ to obtain

$$\llbracket \text{addr}'_i \rrbracket^m = (\llbracket \text{addr}'_i \rrbracket, \llbracket r \times \text{addr}'_i \rrbracket) = \llbracket \text{addr}_{i,0}^p \rrbracket^m \times \llbracket \text{addr}_{i,1}^p \rrbracket^m.$$

$\llbracket \text{addr}_{i,0}^p \rrbracket^m, \llbracket \text{addr}_{i,1}^p \rrbracket^m$ and $\llbracket \text{addr}'_i \rrbracket^m$ are recorded in \mathcal{S} as checksums.

2. Parties locally compute below and record $\llbracket \text{ver}_i^{(1)} \rrbracket^m$ and $\llbracket \text{ver}_i^{(2)} \rrbracket^m$ to \mathcal{S} .

$$\llbracket \text{ver}_i^{(1)} \rrbracket^m := (\llbracket \text{addr}'_i \rrbracket + (\text{addr}_i^r)^2, (\llbracket r \times \text{addr}_{i,0}^p \rrbracket + \llbracket r \times \text{addr}_{i,1}^p \rrbracket) \times \text{addr}_i^r), \text{ and}$$

$$\llbracket \text{ver}_i^{(2)} \rrbracket^m := ((\llbracket \text{addr}_{i,0}^p \rrbracket + \llbracket \text{addr}_{i,1}^p \rrbracket) \times \text{addr}_i^r, \llbracket r \times \text{addr}'_i \rrbracket + \llbracket r \rrbracket \times (\text{addr}_i^r)^2).$$

Since whether the permuter has provided a valid permutation is equivalent to whether $\text{addr}_i^{(1)} = \text{addr}_i^{(2)}$, the verification for *checksums* $\llbracket \text{ver}_i^{(1)} \rrbracket^m$ and $\llbracket \text{ver}_i^{(2)} \rrbracket^m$ includes Equation (6.2) and its transformation. Furthermore, since the input and output of $\mathcal{F}_{\text{MULT}}$, $\llbracket \text{addr}_{i,0}^p \rrbracket^m, \llbracket \text{addr}_{i,1}^p \rrbracket^m$ and $\llbracket \text{addr}'_i \rrbracket^m$, are recorded as checksums, we can attribute the attack providing an invalid permutation to an additive attack that modifies addr_i^r to $\text{addr}_i^r + \delta$ for some δ .

Now, we describe the detailed algorithms of our actively secure hashing scheme in Algorithm 9, 10, and 11. To simplify notation, for each block $\llbracket \mathbf{d} \rrbracket = (\llbracket k \rrbracket, \llbracket v \rrbracket)$, let $\llbracket \mathbf{d} \rrbracket^m = ((\llbracket k \rrbracket, \llbracket rk \rrbracket), (\llbracket v \rrbracket, \llbracket rv \rrbracket))$. This is necessary and sufficient form of MACs for our protocols including functions applied only to k . For the original block size b , the block size of $\llbracket \mathbf{d} \rrbracket^m$ becomes $b' = \omega(b \log \lambda)$ because of MACs (see the beginning of Section 6). The parameters for the Cuckoo hash table, τ, σ , and n' , are the same as those in passive security.

Lemma 6.1. *There exists a 3-party distributed hash table, described as Algorithms 9, 10 and 11, that securely realizes \mathcal{F}_{HT} in the $(\mathcal{F}_{\text{SHARE}}, \mathcal{F}_{\text{REVEAL}}^p, \mathcal{F}_{\text{ABB}})$ -hybrid model in the presence of an active adversary that controls one party.*

Assume that the input array consists of n blocks and each block is b' bits.

- The BUILD procedure consumes $O(n(b' + \lceil \log n \rceil))$ local computation steps.
- The LOOKUP procedure consumes $O(b' + b' \log \lambda)$ local computation steps.

Algorithm 9

 $\pi_0, \pi_1, (\langle T \rangle_i^m, \langle S \rangle_i^m)_{(i \in \{0,1\})}, \llbracket s_0 \rrbracket^m, \llbracket s_1 \rrbracket^m, \text{ctr}, \mathbf{P} \leftarrow \Pi_{\text{BUILD}}^{\text{active}}(\llbracket D \rrbracket_1^m, \llbracket D \rrbracket_2^m, \llbracket D \rrbracket_3^m)$

Require: Parties have shares of a dataset $\llbracket D \rrbracket_{i \in \{0,1,2\}}^m = (\llbracket d_0 \rrbracket_i^m, \dots, \llbracket d_{n-1} \rrbracket_i^m)$.**Ensure:** Each S_i obtains a permutation π_i and a (1,2)-share of cuckoo hash table, $(\langle T \rangle_i, \langle S \rangle_i)$. P obtains permutations π_0, π_1 , and all parties hold (1,3)-shares of PRF keys, $\llbracket s_0 \rrbracket$ and $\llbracket s_1 \rrbracket$. Parties also hold a query counter ctr , and S_0, S_1 hold a set \mathbf{P} , as their states.**(Computing pseudorandom addresses and constructing a table.)**1: Parties do the same as Line 1 to 7 of Algorithm 3 with actively secure \mathcal{F}_{ABB} .Let $\llbracket \tilde{D} \rrbracket^m = \llbracket D \parallel E \rrbracket^m$ be the result of the Line 7 and $\llbracket \tilde{A} \rrbracket^m = \llbracket A \parallel E \rrbracket^m$ be the array of addresses where $\llbracket A \rrbracket^m = ((\llbracket \text{addr}_{0,0} \rrbracket^m, \llbracket \text{addr}_{0,1} \rrbracket^m), \dots, (\llbracket \text{addr}_{n-1,0} \rrbracket^m, \llbracket \text{addr}_{n-1,1} \rrbracket^m))$ is non-dummy addresses corresponding to the input $\llbracket D \rrbracket^m$.2: Before revealing A for P , parties perform the Proof phase to verify the checksums. Then, P receives all non-dummy addresses to compute a permutation π .3: P computes π_0 and π_1 s.t. $\pi_1 \circ \pi_0 = \pi$, and sends them to S_0 and S_1 respectively. If $\pi_{i \in \{0,1\}}$ is not a permutation, S_i aborts.4: Parties run $\Pi_{\text{PERM}}^{\text{active}}$ twice to obtain both $\llbracket \tilde{D}' \rrbracket^m = \llbracket \pi \cdot \tilde{D} \rrbracket^m$ and $\llbracket \tilde{A}' \rrbracket^m = \llbracket \pi \cdot \tilde{A} \rrbracket^m$.**(Verifying π .)**5: **for all** $i \in [\tau]$ **do**6: Let $\llbracket \tilde{d}'_i \rrbracket^m = (\llbracket \tilde{k}'_i \rrbracket^m, \llbracket \tilde{v}'_i \rrbracket^m)$ be the i -th element of $\llbracket \tilde{D}' \rrbracket^m$.7: Let $(\llbracket \tilde{\text{addr}}'_{i,0} \rrbracket^m, \llbracket \tilde{\text{addr}}'_{i,1} \rrbracket^m)$ be the i -th element of $\llbracket \tilde{A}' \rrbracket^m$ and let $\text{addr}'_i := i$.8: Parties call \mathcal{F}_{EQ} and $\mathcal{F}_{\text{IFELSE}}$ to simulate:If $\tilde{k}'_i = \perp$ then set $(\llbracket \text{addr}^p_{i,0} \rrbracket^m, \llbracket \text{addr}^p_{i,1} \rrbracket^m) = (\llbracket \text{addr}'_i \rrbracket^m, \llbracket \text{addr}'_i \rrbracket^m)$,otherwise set $(\llbracket \text{addr}^p_{i,0} \rrbracket^m, \llbracket \text{addr}^p_{i,1} \rrbracket^m) = (\llbracket \tilde{\text{addr}}'_{i,0} \rrbracket^m, \llbracket \tilde{\text{addr}}'_{i,1} \rrbracket^m)$.9: Parties perform $\Pi_{\text{VERPERM}}(\llbracket \text{addr}^p_{i,0} \rrbracket^m, \llbracket \text{addr}^p_{i,1} \rrbracket^m, \text{addr}'_i)$.

10: Parties perform the Proof phase to verify the checksums.

(Organizing a cuckoo hash table and stash.)11: Each $S_{i \in \{0,1\}}$ converts $\llbracket \tilde{D}' \rrbracket^m$ to $\langle \tilde{D}' \rangle_i^m$ and organize it into a hash table $\langle T \rangle_i^m$ and stash $\langle S \rangle_i^m$, by separating $\langle \tilde{D}' \rangle_i^m$ into the first τ and the last σ elements.12: Parties set their state $\text{ctr} = 0$, and S_0, S_1 allocate an empty set $\mathbf{P} = \emptyset$.13: **Return** $(\pi_0, \pi_1), (\langle T \rangle_i^m, \langle S \rangle_i^m)_{(i \in \{0,1\})}, \llbracket s_0 \rrbracket^m, \llbracket s_1 \rrbracket^m, \text{ctr}, \mathbf{P}$ – The EXTRACT procedure consumes $O(nb')$ local computation steps.Furthermore, all those procedures consumes $O(1)$ communication rounds.*Proof.* The proof of security is given in Appendix B.3. To proceed with the efficiency analysis, we start at the following claim.*Claim.* $\Pi_{\text{PERM}}^{\text{active}}$ consumes $O(nb')$ local computation steps and $O(1)$ communication rounds. In addition, Π_{VERPERM} consumes $O(b')$ local computation steps and $O(1)$ communication rounds.Each claim is clear from the algorithms of $\Pi_{\text{PERM}}^{\text{active}}$ and Π_{VERPERM} .The difference between the passively and actively secure BUILD procedure (Algorithm 3 and 9) is that the latter uses $\Pi_{\text{PERM}}^{\text{active}}$ instead of $\mathcal{F}_{\text{PERM}}$ and additionally runs $\mathcal{F}_{\text{EQ}}, \mathcal{F}_{\text{IFELSE}}, \Pi_{\text{VERPERM}}$ (each) τ times and the Proof phases twice. Since τ and n' are each $O(n)$, this difference increases the computation cost at

Algorithm 10

$$\llbracket d \rrbracket^m, \llbracket \text{found} \rrbracket^m \leftarrow \Pi_{\text{LOOKUP}}^{\text{active}}(\llbracket k \rrbracket^m, (\langle T \rangle_0^m, \langle S \rangle_0^m), (\langle T \rangle_1^m, \langle S \rangle_1^m), \llbracket s_0 \rrbracket^m, \llbracket s_1 \rrbracket^m)$$

Require: 2-out-of-3 shares of a queried key $\llbracket k \rrbracket^m$, 2-out-of-2 shares of the hash table $\langle T \rangle_i^m$ and its stash $\langle S \rangle_i^m$, and 2-out-of-3 shares of PRF keys $\llbracket s_0 \rrbracket^m, \llbracket s_1 \rrbracket^m$.

Ensure: $d = (k, v)$ and $\text{found} = 1$ if the table T contains an item (k, v) , otherwise $d = (0, 0)$ and $\text{found} = 0$.

(Computing pseudorandom addresses to be fetched.)

1: To obtain pseudorandom addresses $\llbracket \text{addr}_0 \rrbracket^m$ and $\llbracket \text{addr}_1 \rrbracket^m$, parties do the same as Line 1 to 3 of Algorithm 4 with actively secure \mathcal{F}_{ABB} .

(Searching for the cuckoo hash table and stash.)

2: Before revealing addr_0 and addr_1 to the storages, parties perform the Proof phase to verify the checksums.

3: To obtain a queried data $\llbracket d \rrbracket^m$ and flag $\llbracket \text{found} \rrbracket^m$, parties do the same as Line 5 to 13 of Algorithm 4 with actively secure \mathcal{F}_{ABB} .

4: **Return** $\llbracket d \rrbracket^m, \llbracket \text{found} \rrbracket^m$

$$\llbracket D \rrbracket_0^m, \llbracket D \rrbracket_1^m, \llbracket D \rrbracket_2^m \leftarrow \Pi_{\text{EXTRACT}}^{\text{active}}((\pi_0, \pi_1), (\langle T \rangle_0^m, \langle S \rangle_0^m), (\langle T \rangle_1^m, \langle S \rangle_1^m))$$

Require: 2-out-of-2 shares of a hash table T and stash S , and their constructing permutation (π_0, π_1) .

Ensure: A dataset D contains all real elements in T and S .

1: Each $S_{i \in \{0,1\}}$ reorganizes $\langle T \rangle_i^m$ and $\langle S \rangle_i^m$ into an array $\langle \tilde{D} \rangle_i^m$ as $\tilde{D} = T \parallel S$.

2: Parties call actively secure $\mathcal{F}_{\text{RESHARE}}$ to convert $\langle \tilde{D} \rangle_0^m, \langle \tilde{D} \rangle_1^m$ to $\llbracket \tilde{D} \rrbracket_0^m, \llbracket \tilde{D} \rrbracket_1^m, \llbracket \tilde{D} \rrbracket_2^m$.

3: Parties perform $\Pi_{\text{UNPERM}}^{\text{active}}$ with $(\pi_0^{-1}, \pi_1^{-1}, \llbracket \tilde{D} \rrbracket_0^m)$ of P , $(\pi_0^{-1}, \llbracket \tilde{D} \rrbracket_1^m)$ of S_0 and $(\pi_1^{-1}, \llbracket \tilde{D} \rrbracket_2^m)$ of S_1 , to obtain $\llbracket \pi^{-1} \cdot \tilde{D} \rrbracket_{i \in \{0,1,2\}}$.

4: Let $\llbracket D \rrbracket_i$ be the first n elements of $\llbracket \tilde{D} \rrbracket_i$.

5: **Return** $\llbracket D \rrbracket_0, \llbracket D \rrbracket_1, \llbracket D \rrbracket_2$

most $O(nb')$ from Algorithm 3. On the other hand, since Algorithm 11 requires performing $\Pi_{\text{PERM}}^{\text{active}}$ at once, it costs $O(nb')$ steps. Moreover, since the difference between the passively and actively secure LOOKUP procedure (Algorithm 4 and 10) is only that the latter additionally requires to verify $\llbracket \text{addr}_{\{0,1\}} \rrbracket^m$, the computational cost increases by at most $O(b')$ steps from Algorithm 4. \square

6.3 Actively Secure Distributed ORAM

Given our actively secure distributed oblivious hashing, we achieve an actively secure DORAM.

Theorem 6.2. *There exists a 3-party protocol that securely realizes \mathcal{F}_{RAM} in the presence of an active adversary that controls one party. In addition, assuming $N = \text{poly}(\lambda)$, this implementation consumes $O(\log N)$ amortized overhead and $O(\log N)$ communication rounds with $\omega(\log^2 N)$ block size.*

Proof. By straightforward composition, we can replace all functionalities related to \mathcal{F}_{ABB} and \mathcal{F}_{HT} in Algorithms 6 and 7 with their concrete implementations and thereby get a concrete actively secure DORAM. We can also achieve the

security proof by considering the same simulator as of Appendix B.2 except that it uses an actively secure distributed oblivious hashing simulator. Moreover, since the efficiency of each \mathcal{F}_{ABB} and \mathcal{F}_{HT} is asymptotically the same as in passive security except for the increased block size, the actively secure DORAM achieves $O(\log N)$ overhead using a slightly larger block size $b' = \omega(\log^2 N)$. \square

7 Conclusion

We proposed an optimal DORAM of $O(\log N)$ overhead with small hidden constant, that relies on only $4 \log N$ OPRF calls. The key building block is a novel 3-party permutation protocol, in which parties split their roles into one permuter that knows the whole permutation and two storages that hold a permuted table. Since these roles do not overlap, the permuter never observes access patterns to the permuted table even though it knows the structure of the table.

In addition, we extended the above (passively secure) DORAM to an actively secure one. Since our passively secure construction depends on the permutation protocol in which one party has full control of a permutation, we additionally construct a novel protocol to verify the permutation provided by the (possibly dishonest) party. Then, we achieved an actively secure DORAM of $O(\log N)$ overhead with slightly larger $\omega(\log^2 N)$ -bit block size.

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A Concrete Efficiency of Our Passively Secure DORAM and Comparison with [44] and [23]

Functionality	$\mathcal{F}_{\text{SHARE}}$	$\mathcal{F}_{\text{REVEAL}}$	$\mathcal{F}_{\text{MULT}}$	\mathcal{F}_{RND}	$\mathcal{F}_{\text{RESHARE}}$	\mathcal{F}_{EQ}	$\mathcal{F}_{\text{IFELSE}}$	$\mathcal{F}_{\text{BITEXT}}$	$\mathcal{F}_{\text{TRUNC}}$	\mathcal{F}_{PRF}
# of calls	$3L + 1$	$4L$	$3L + c$	2	$2L + 1$	$4L + c + 1$	$5L + c + 3$	L	L	$4L$

Functionality	$\mathcal{F}_{\text{PERM}}$	$\mathcal{F}_{\text{UNPERM}}$
# of calls	$p - 1$, with input of size $c2^i + \frac{c}{2}$ for each $i = 1, \dots, p - 1$ per $c2^{p-i}$ accesses.	1 , with input of size $c2^p + \frac{c}{2}$ per $c2^{p-1}$ accesses.

Table 2. The amortized number of calls of each functionality in Algorithm 6, where $L = \lceil \log N - \log \log N \rceil$ and $c = 2\lceil \log N \rceil$.

We evaluate the concrete computational complexity of our passively secure DORAM. We treat each functionality as a black box here and consider the (amortized) number of calls per access, for ease of estimation⁸. Table 2 describes the concrete number of calls of the building blocks required in Algorithm 6. Each entry of the table can be straightforwardly calculated using the facts below:

- In the main part of Algorithm 6, parties call $\mathcal{F}_{\text{MULT}}$, \mathcal{F}_{EQ} , and $\mathcal{F}_{\text{IFELSE}}$ c times (line 1 to 5), call $\mathcal{F}_{\text{MULT}}$, $\mathcal{F}_{\text{IFELSE}}$, and $\mathcal{F}_{\text{BITEXT}}$ L times (line 6 to 8), call $\mathcal{F}_{\text{IFELSE}}$ 2 times (line 11 to 12), and perform $H_i.\text{LOOKUP}$ for each $i = 1, \dots, L$.
 - Regardless of i , $H_i.\text{LOOKUP}$ described in Algorithm 4 requires 2 calls of $\mathcal{F}_{\text{REVEAL}}$, $\mathcal{F}_{\text{MULT}}$, $\mathcal{F}_{\text{RESHARE}}$, and \mathcal{F}_{PRF} , and 3 calls of \mathcal{F}_{EQ} and $\mathcal{F}_{\text{IFELSE}}$ (line 1 to 8)⁹.
- In the subroutine $\Pi_{\text{RESHUFFLE}}$ described in Algorithm 7, for each $c2^{p-1}$ accesses, parties perform $H_i.\text{EXTRACT}$ for all $i = 1, \dots, p - 1$ and $H_p.\text{BUILD}$, call $\mathcal{F}_{\text{TRUNC}}$ $c2^{p-1}$ times, and call $\mathcal{F}_{\text{RESHARE}}$, \mathcal{F}_{EQ} , and $\mathcal{F}_{\text{IFELSE}}$ $\frac{c}{2}$ times.
 - $H_i.\text{EXTRACT}$ described in Algorithm 5, where the capacity of H_i is $c2^{i-1}$, requires $\mathcal{F}_{\text{UNPERM}}$ at once with input size $|\mathbb{T} \parallel \mathbb{S}| = c2^i + \frac{c}{2}$.
 - $H_p.\text{BUILD}$ described in Algorithm 3, where the capacity of H_p is $c2^{p-1}$, requires 2 calls of \mathcal{F}_{RND} , $c2^{p-1}$ calls of \mathcal{F}_{EQ} and $\mathcal{F}_{\text{IFELSE}}$, $c2^p$ calls of $\mathcal{F}_{\text{REVEAL}}$ and \mathcal{F}_{PRF} , $c2^{p-1} + \frac{c}{2}$ calls of $\mathcal{F}_{\text{SHARE}}$, and a call of $\mathcal{F}_{\text{PERM}}$ with input size $c2^p + \frac{c}{2}$.

Comparing practical efficiency with [44] and [23]. Following a similar setting as Falk et al. [23], we can estimate the efficiency of our DORAM more concretely. From our Table 2 and Lemmas 4.1, 4.2, and Table 2 of [23], we conclude

⁸ See Table 2 in [23], for example, for more specific costs of each functionality.

⁹ Note that we can omit the stash-related operations (line 9 to 12) of $H_i.\text{LOOKUP}$ in Algorithm 6.

that Algorithm 6 requires $(164L + 9c + 40)B$ bits of communication for block size $B = \Omega(\log N)$ bits. Considering $L \sim \log N$ and $c \sim 2 \log N$, the cost of our DORAM becomes $(182 \log N + 40)B$ bits per access.

According to [23], the DORAM of Lu and Ostrovsky [44] requires at least $100 \log N$ OPRF calls and hence costs at least $2100 \log N \cdot B$ bits of communication per access¹⁰ — this is significantly higher than ours. On the other hand, the DORAM of Falk et al. [23] requires $(153.5 \log N + 6)B$ bits per access. I.e., even though the number of our calls to the most expensive building block is double, it does not affect the practical bandwidth much. Moreover, considering that the DORAM of Falk et al. requires additional computational cost for $\log^2 N$ non-oblivious hash functions per access, our optimal DORAM appears to be faster in the practical size of N .

B Security Proofs

B.1 Proof of Security and Correctness of Our Passively Secure Distributed Oblivious Hashing (Lemma 4.3)

For correctness of our scheme, by [47], we observe that the permuter P achieves a permutation π for hashing with overwhelming probability. Since we are in the $\mathcal{F}_{\text{PERM}}$ -hybrid model, all input blocks are correctly placed in their location as $\pi \cdot \mathbf{D} = \mathbf{T} \parallel \mathbf{S}$ in Algorithm 3. Similarly, since we are in the $\mathcal{F}_{\text{UNPERM}}$ -hybrid model, the blocks are correctly retrieved as $\mathbf{D} = \pi^{-1} \cdot (\mathbf{T} \parallel \mathbf{S})$ in Algorithm 5. Moreover, when the above permutation π exists, in Algorithm 4, the storages always fetch $\mathbf{T}[\text{addr}_0]$, $\mathbf{T}[\text{addr}_1]$, and \mathbf{S} , one block of which is the queried data.

For obliviousness, consider a simulator Sim that simulates the view of a passive adversary that corrupts one of three party in Π_{BUILD} , Π_{LOOKUP} and Π_{EXTRACT} .

- **Simulating Π_{BUILD} .** Receiving an instruction to simulate Π_{BUILD} with the size of input n and a collusion target $C \in \{P, S_0, S_1\}$, Sim runs the real protocol Π_{BUILD} on input n dummy blocks. If $C = P$ then Sim outputs π and all $\text{addr}_{i,j}$ provided from $\mathcal{F}_{\text{PRP}}(\llbracket s_j \rrbracket, \llbracket \perp + i \rrbracket)$ as *access patterns*. Otherwise, Sim outputs nothing. Sim holds the result of Π_{BUILD} simulation, $(\pi, (\langle \mathbf{T} \rangle_{\{0,1\}}, \langle \mathbf{S} \rangle_{\{0,1\}}), \llbracket s_{\{0,1\}} \rrbracket, \text{ctr}, \mathbf{P})$, as its state.
- **Simulating Π_{LOOKUP} .** When the adversary requests to run Π_{LOOKUP} with a key k , Sim simulates the real protocol Π_{LOOKUP} with a key \perp and its state $(\pi, (\langle \mathbf{T} \rangle_{\{0,1\}}, \langle \mathbf{S} \rangle_{\{0,1\}}), \llbracket s_{\{0,1\}} \rrbracket, \text{ctr}, \mathbf{P})$. If C is either S_0 or S_1 , Sim outputs $\text{addr}_{\{0,1\}}$ provided from $\mathcal{F}_{\text{PRP}}(\llbracket s_{\{0,1\}} \rrbracket, \llbracket \perp + \text{ctr} \rrbracket)$ as *access patterns*. Otherwise, Sim outputs nothing.
- **Simulating Π_{EXTRACT} .** When the adversary requests to run Π_{EXTRACT} , Sim halts and outputs π as an *access pattern* if $C = P$, otherwise it outputs nothing.

¹⁰ In practice, the cost of other functionalities, besides OPRF, is added to this.

Since we are in \mathcal{F}_{ABB} -hybrid model and Algorithm 3, 4 and 5 are all deterministic except for the addresses recovered to a party, we should consider only the addresses as the access patterns. Now, consider the following hybrid argument.

Hyb₀ : The real execution of Π_{BUILD} , Π_{LOOKUP} and Π_{EXTRACT} in the $(\mathcal{F}_{\text{SHARE}}, \mathcal{F}_{\text{REVEAL}}^{\mathcal{P}}, \mathcal{F}_{\text{ABB}}, \mathcal{F}_{\text{PERM}}, \mathcal{F}_{\text{UNPERM}})$ -hybrid model.

Hyb₁ : The same as Hyb₀ except that, in the execution of Π_{BUILD} (Algorithm 3), the $\mathcal{F}_{\text{IFELSE}}$ functionality of line 4 always set \tilde{k}_i to $\perp + i$.

Hyb₂ : The same as Hyb₁, but parties always search for a dummy in Π_{LOOKUP} , i.e., the $\mathcal{F}_{\text{IFELSE}}$ functionality of Algorithm 4, line 2, always set \tilde{k} to $\perp + \text{ctr}$.

Hyb₃ : The ideal simulation of Sim.

Let $\text{View}_C(\text{Hyb}_u)$ be the view of the adversary that colludes with $C \in \{P, S_0, S_1\}$ in Hyb_u. By the security of the PRF, $\text{View}_P(\text{Hyb}_0)$ and $\text{View}_P(\text{Hyb}_1)$ are computationally indistinguishable as long as $n < \text{poly}(\lambda)$. $\text{View}_{S_i}(\text{Hyb}_0)$ and $\text{View}_{S_i}(\text{Hyb}_1)$ are obviously identical for any $i \in \{0, 1\}$.

It is also obvious that $\text{View}_P(\text{Hyb}_1) \equiv \text{View}_P(\text{Hyb}_2)$. In addition, $\text{View}_{S_i}(\text{Hyb}_1)$ and $\text{View}_{S_i}(\text{Hyb}_2)$ are computationally indistinguishable for any $i \in \{0, 1\}$ by security of the PRF. Furthermore, $\text{View}_C(\text{Hyb}_3)$ is the same as $\text{View}_C(\text{Hyb}_2)$ for any $C \in \{P, S_0, S_1\}$, directly.

B.2 Proof of Security of Our Passively Secure DORAM (Theorem 5.1)

Consider the following simulator Sim that simulates the view of a passive adversary that corrupts one of three party in Π_{ACCESS} .

- Let $\text{Sim}_{\text{HT}}(n)$ be a simulator that simulates the view of a passive adversary in distributed oblivious hashing with input length n . As setup, Sim initializes $\text{Sim}_{\text{HT}}(c), \dots, \text{Sim}_{\text{HT}}(c2^{L-1})$ where $c = 2\lceil \log N \rceil$ and $L = \lceil \log N - \log \log N \rceil$. In addition, Sim allocates an empty array of size c and initialize a query counter $\text{ctr} = 0$
- Receiving a request to simulate Π_{ACCESS} , Sim simulates the ctr -th execution of Π_{ACCESS} with dummy inputs and an dummy data structure. All invocations of $\text{HT}_i.\text{LOOKUP}$ is replaced with the lookup simulation of $\text{Sim}_{\text{HT}}(c2^{i-1})$, and also all $\text{HT}_i.\text{EXTRACT}$ and $\text{HT}_i.\text{BUILD}$ in the subroutine $\Pi_{\text{RESHUFFLE}}$ are replaced with the extract and build simulation of that, respectively. Sim finally increments ctr and returns all outputs of Sim_{HT} .

Now, consider the following hybrid argument.

Hyb₀ : The real execution of Π_{ACCESS} .

Hyb₁ : The same as Hyb₀ except performing $\Pi_{\text{ACCESS}}(\llbracket \text{read} \rrbracket, \llbracket \perp \rrbracket, \llbracket \perp \rrbracket)$ for any input.

Hyb₂ : The ideal simulation of Sim.

Since we are in the $(\mathcal{F}_{\text{ABB}}, \mathcal{F}_{\text{HT}})$ -hybrid model and the parties do not reveal any secret in the algorithm Π_{ACCESS} and its subroutine $\Pi_{\text{RESHUFFLE}}$, the adversary's

view of Hyb_1 is identical to that of Hyb_0 with overwhelming probability. Furthermore, in the $(\mathcal{F}_{\text{ABB}}, \mathcal{F}_{\text{HT}})$ -hybrid model, Hyb_1 and Hyb_2 are identical. The view of the adversary in the real Π_{ACCESS} and Sim are therefore indistinguishable.

B.3 Proof of Security of Our actively Secure Distributed Oblivious Hashing (Lemma 6.1)

Security and correctness without tampering follow the proof of Lemma 4.3. Thus, it suffices here to show that an active adversary succeed in tampering with shares with negligible probability.

In the execution of $\Pi_{\text{BUILD}}^{\text{active}}$, since there are $O(n')$ checksums in \mathcal{S} including $2n$ addresses $\llbracket \text{addr}_{i,j} \rrbracket^m$, $2(n'-n)$ dummies $\llbracket \perp \rrbracket^m$, and $O(n)$ others generated during the computation of $\llbracket \text{addr}_{i,j} \rrbracket^m$, in the first Proof phase (Algorithm 9, Line 3), the adversary's success probability in the first Proof phase is proportional to $n'b\lambda^{-\omega(1)}$ where b is the original size of a block. In addition, in the second Proof phase (Line 14), there are $O(\tau)$ checksums in \mathcal{S} since there are τ calls of \mathcal{F}_{EQ} and $\mathcal{F}_{\text{IFELSE}}$, and τ executions of Π_{VERPERM} . Hence, assuming $n', b < \text{poly}(\lambda)$, the adversary's success probability is $\text{negl}(\lambda)$.

In each execution of $\Pi_{\text{LOOKUP}}^{\text{active}}$, since there are $O(1)$ checksums in \mathcal{S} , adversary's tampering is detected with overwhelming probability $1 - \lambda^{\omega(1)}$.

Though parties do not verify the checksums in $\Pi_{\text{EXTRACT}}^{\text{active}}$, all blocks in $\llbracket \tilde{\mathbf{D}} \rrbracket^m$ is stored to \mathcal{S} during the unpermutation protocol. Hence, when the extracted data is used to build a new hash table, or parties terminate the protocol (that involves the final Proof phase), all data accessed in $\Pi_{\text{EXTRACT}}^{\text{active}}$ is verified not to be tampered. Since the number of the checksums recorded in $\Pi_{\text{EXTRACT}}^{\text{active}}$ is $O(n')$, the adversary's success probability is also negligible.