

An Assessment of Predictive Current Control Applied to the Direct Matrix Converter Based on SiC-MOSFET Bidirectional Switches

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Abstract—Matrix converters are devices that allow the management of bidirectional flows of energy with reduced size, increased useful life and high efficiency in ac-ac energy conversion systems. In this paper the experimental results obtained after the application of a predictive current control technique for a SiC-MOSFET based direct matrix converter feeding an isolated load are presented. The results obtained are compared with a scheme based on IGBT technology in terms of power consumption and total harmonic distortion.

Index Terms—Matrix converter, predictive current control, SiC-MOSFET technology

I. INTRODUCTION

Nowadays, the direct matrix converter (MC) is one of the most studied devices in AC-AC power conversion systems. The topologies of the MC offer a direct acac conversion and due to the absence of dc-link capacitors they are more compact and robust. They simultaneously achieve sinusoidal input and output waveforms as well as bidirectional power flow [1], [2]. Compared to a conventional back-to-back inverter (BTB), which requires bulky storage elements, the space saved by a MC has been estimated as a factor of three. The main applications are portable generation systems, for instance in variable speed diesel generation, variable speed wind-diesel topologies, distributed generation applications, emergency vehicles, military and aerospace applications, external elevators for building construction, and skin-pass mills flow [1], [3], [4]. MC are devices constructed from bidirectional switches (Bi-Sw) that have several advantages over their (BTB) counterparty, such as smaller size, less weight and management of bidirectional flows [5]. As for the technology used in manufacturing the Bi-Sw, there are mainly two types of semiconductors, IGBTs and those based on silicon carbide technology SiC-MOSFET [6], [7]. Generally, the preferred technology has been based on IGBT and discrete diodes. However, nowadays the trend points out towards the use of SiC-MOSFETs devices, since in comparison to IGBTs, they report lower losses in switching and conduction, and can even operate at higher frequency and voltage levels [8]. In general, the integration of semiconductors

is sought in a single power module, due to the fact that through this integration it is possible to optimize spaces as well as handle higher power density. As for the operation of the MC it is preferable that the switching occurs at a high frequency in order to decrease the size of the passive components of the filters, SiC-MOSFETs are ideal for this application. In [9]–[11] a comparative analysis is made between the SiC-MOSFET and the IGBT applied in the MC, but operating in a control scheme based on modulation techniques, that do not possess the variable switching frequency characteristic of predictive control. In recent years, predictive current control applied to matrix converters has caught the attention of researchers [12], [13]. This article presents the results of the setup and experimental implementation of a predictive current control scheme for a three-phase MC based on SiC-MOSFET technology feeding an isolated load and the comparison of the perform of the system and another one based on IGBT technology. The control is implemented using the dSPACE 1103 to solve the algorithm and to determine the optimum vector to be applied at the next sampling time. While the optimal vector application and the switching strategy are carried out in a FPGA Nexys 3. The main contribution of this paper lies in the experimental comparison between IGBT technologies and SiC-MOSFET based technologies in terms of power consumption in the same conditions of input voltages and output currents under a predictive control scheme and the study of variation of the total harmonic distortion (THD) with increasing sampling frequency for both cases.

II. PROPOSED CONTROL TOPOLOGY

The proposed control scheme is presented by means of Fig. 1 which consists of a three-phase AC-AC conversion scheme using a MC in the power stage as the feeder of an isolated load. The implemented control technique is the model based predictive control (MPC) which will be described below.

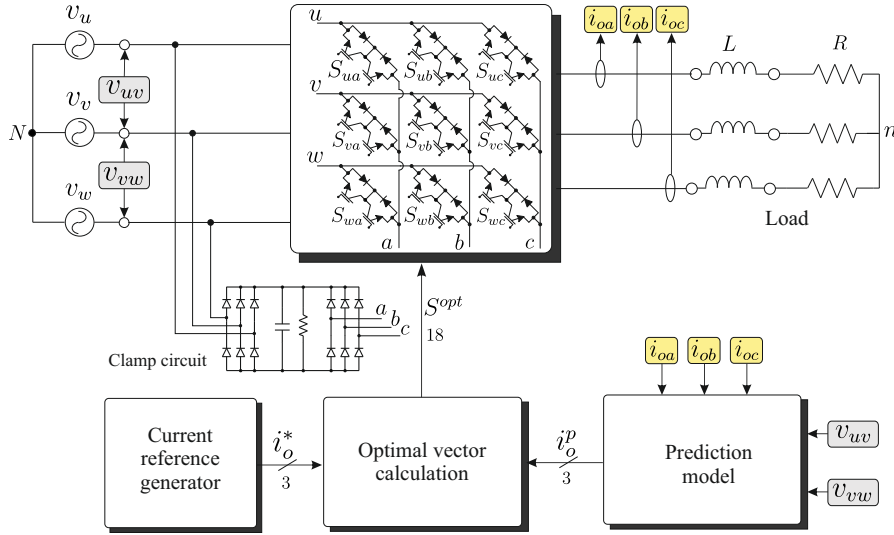


Fig. 1. Proposed experimental setup

A. Direct Matrix Converter. Basic principles

The switching function for a single switch is defined as [14]:

$$S_{ij} = \begin{cases} 0, & \text{if switch } S_{ij} \text{ is off} \\ 1, & \text{if switch } S_{ij} \text{ is on} \end{cases} \quad (1)$$

where $i \in \{u, v, w\}$ indicates the corresponding entry, $k \in \{a, b, c\}$ refers to the corresponding output. Considering that the inputs should never be short-circuited and that currents should never be interrupted abruptly, the restrictions are expressed as follows:

$$S_{ui} + S_{vi} + S_{wi} = 1, \quad \forall i \in \{a, b, c\}. \quad (2)$$

Under these constraints, the three-phased MC has 27 allowed switching states, out of the possible 512 (2^9). If the load and source are referenced with respect to the neutral point (N), then it is possible to describe the relation between the inputs and outputs of the voltage and current as follow:

$$\begin{bmatrix} v_a(t) \\ v_b(t) \\ v_c(t) \end{bmatrix} = \begin{bmatrix} S_{ua}(t) & S_{va}(t) & S_{wa}(t) \\ S_{ub}(t) & S_{vb}(t) & S_{wb}(t) \\ S_{uc}(t) & S_{vc}(t) & S_{wc}(t) \end{bmatrix} \begin{bmatrix} v_u(t) \\ v_v(t) \\ v_w(t) \end{bmatrix}, \quad (3)$$

$$\begin{bmatrix} i_u(t) \\ i_v(t) \\ i_w(t) \end{bmatrix} = \begin{bmatrix} S_{ua}(t) & S_{ub}(t) & S_{uc}(t) \\ S_{va}(t) & S_{vb}(t) & S_{vc}(t) \\ S_{wa}(t) & S_{wb}(t) & S_{wc}(t) \end{bmatrix} \begin{bmatrix} i_{oa}(t) \\ i_{ob}(t) \\ i_{oc}(t) \end{bmatrix}. \quad (4)$$

B. Predictive Control in the Direct Matrix Converter

MPC has been applied in chemical and industrial processes for several years, mainly because these are complex but highly slow-moving processes [15], [16]. Electronic power devices also have complex models but with faster dynamics. However, the capacity and speed of microcontrollers have improved greatly, making feasible the modeling of discrete systems and the processing of electrical signals. All of the above

contributes to the development of MPC control strategy in power electronics applications. On the other hand, one of the most attractive characteristics of the MPC is its intuitive and logical procedure of exposing the control problem, which makes it easy to understand and easy concept to implement.

One of the main elements to implement a MPC control strategy is a precise model of the system in order to have an adequate prediction. The model depends directly on the application of the controller [17]. In the case of power converters, these are composed of semiconductors that operate in only two states: cut and saturation. Therefore, there is always a finite number of possible combinations of the switching states in any power converter. This feature greatly simplifies the application of the MPC since because instead of having to waste time on a continuous optimization algorithm, it is possible to perform the direct evaluation of all possible switching states and select the best combination to use, according to the proposed control objectives.

The control objectives in power electronics are, generally, the tracking of currents, voltages, power, torque, flux, etc. [18]. These objectives are represented in the MPC through a cost function, which evaluates the errors between the references and the actual values of the variables of interest. Furthermore, it is possible to add some additional control objectives such as temperature control and minimization of switched and conduction losses [19], common mode voltages reduction [20], among others. Nowadays, it is possible to find in the market a vast variety of highly powerful microprocessors, which make a large number of calculations. These calculations are so fast that they can predict the behavior of variables such as real time current and voltage without a noticeable effect on the system under control, allowing the application of MPC in power electronics. The operational principle of this strategy is based on the prediction of the behavior of a system using its mathematical model and the optimization of a defined cost

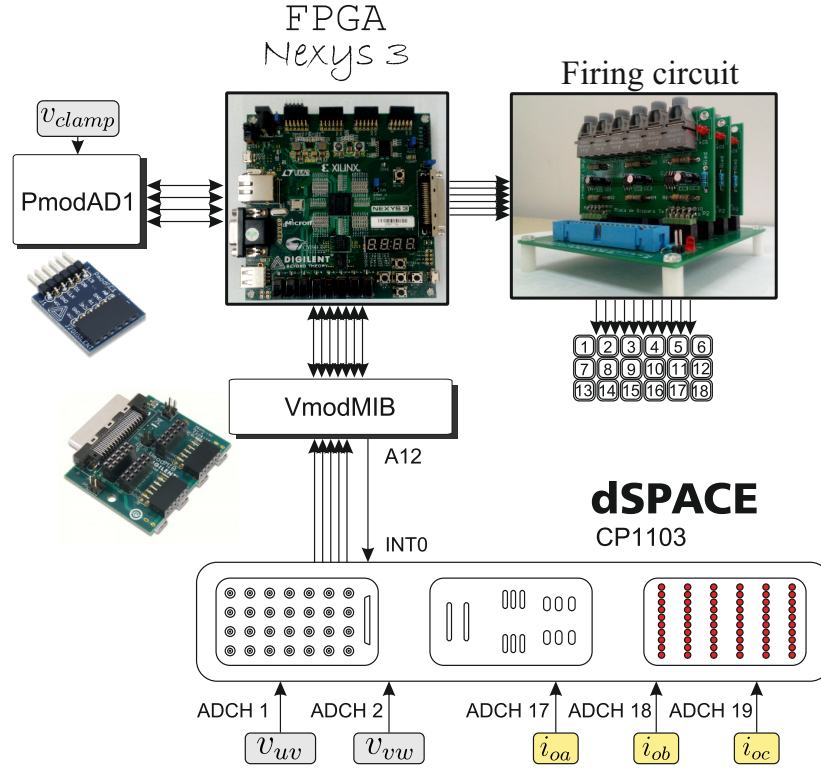


Fig. 2. Three-phase matrix converter controller setup.

function using the predicted values as a way to meet the control objectives. Applied to power converters it is possible to separate this operating principle in three steps: (i) calculation of the predicted variables using a discrete model, (ii) the evaluation of the cost function for each of the valid states of the converter, and (iii) the selection and application of the optimal switching state. These steps are present in all MPC schemes, since it is possible to modify each one independently to adjust the control scheme to any configuration.

C. Predictive Load Current Control in a Three-phase Matrix Converter

The conversion system is shown in Fig. 1. In order to control the current in the load (i_o), a mathematical model must be obtained that describes the dynamics in the load. The dynamics of the current in the load is defined by the following equation:

$$\frac{di_o(t)}{dt} = -\frac{R}{L}i_o(t) + \frac{1}{L}(v_o(t) - v_{nN}(t)), \quad (5)$$

where:

$$\mathbf{i}_o(t) = \begin{bmatrix} i_{oa}(t) \\ i_{ob}(t) \\ i_{oc}(t) \end{bmatrix}, \quad \mathbf{v}_o(t) = \begin{bmatrix} v_{aN}(t) \\ v_{bN}(t) \\ v_{cN}(t) \end{bmatrix}, \quad \mathbf{v}_{nN}(t) = \begin{bmatrix} v_{nN}(t) \\ v_{nN}(t) \\ v_{nN}(t) \end{bmatrix}, \quad (6)$$

And v_{nN} is determined based on the following equation:

$$v_{nN} = \frac{1}{3}(v_{aN} + v_{bN} + v_{cN}). \quad (7)$$

The discrete model is obtained by applying the Euler method in the equation (5), as follows:

$$\mathbf{i}_o(k+1) = \left(1 - \frac{RT_s}{L}\right)\mathbf{i}_o(k) + \frac{T_s}{L}(\mathbf{v}_o(k) - \mathbf{v}_{nN}(k)), \quad (8)$$

where T_s is the sampling time, R is the load resistance and L is the load inductance. The values of $\mathbf{v}_o(k)$ are obtained from the equation (3) for each of the 27 feasible switching combinations in order to evaluate the cost function and to select the combination that minimizes it. In general, the Clarke transform is used [21], which converts the three-phase balanced system (three components to calculate) to one simpler two-component α - β frame, through the following way:

$$\begin{aligned} x_\alpha &= \frac{2}{3}(x_a - 0.5x_b - 0.5x_c), \\ x_\beta &= \frac{2}{3}\left(\frac{\sqrt{3}}{2}x_b - \frac{\sqrt{3}}{2}x_c\right), \end{aligned} \quad (9)$$

being x_a , x_b y x_c the three-phase components and x_α and x_β the components of the new coordinates α - β . To define the cost function that achieves current tracking, the components α - β of the reference current \mathbf{i}_o^* must be determined and compared with the predicted currents \mathbf{i}_o^p .

However, due to the optimal vector calculated in the k instant is applied in $k+1$, the controller has a delay of one sampling time and it is necessary to control the behavior in $k+2$ to avoid the effect of this issue. That is, using the predictive model (8) in k with the measurements in this instant

and the MC output voltage for the optimal vector calculated in $k - 1$, then:

$$\mathbf{i}_{o\alpha\beta}^p = \left(1 - \frac{RT_s}{L}\right) \mathbf{i}_{o\alpha\beta} + \frac{T_s}{L} (\mathbf{v}_{opt\alpha\beta} - \mathbf{v}_{nN\alpha\beta}), \quad (10)$$

Then, the predicted current in $k + 2$ is calculated base on \mathbf{i}_o^p for every valid switching state vector using the follow equation:

$$\mathbf{i}_{o\alpha\beta 2}^p(j) = \left(1 - \frac{RT_s}{L}\right) \mathbf{i}_{o\alpha\beta}^p + \frac{T_s}{L} (\mathbf{v}_{o\alpha\beta}(S(j)) - \mathbf{v}_{nN\alpha\beta}), \quad (11)$$

where j represent everyone of the 27 switching combinations. Finally, the proposed cost function to achieve current control is:

$$g = (i_\alpha^* - i_{o\alpha 2}^p)^2 + (i_\beta^* - i_{o\beta 2}^p)^2, \quad (12)$$

Where i_α^* and i_β^* correspond to the components α — β of the reference current. The vector $S(j)$ which minimize g corresponds to the optimal vector that will be applied in the next sampling time.

It is convenient to mention also that, for the implementation of the MPC, the measurements of the line voltages of source v_{uv} and v_{vw} are taken, from which is possible to calculate the phase voltages v_u , v_v and v_w applying the following equations:

$$v_u = \frac{2}{3} \left(v_{uv} + \frac{1}{2} v_{vw} \right), \quad (13)$$

$$v_v = \frac{2}{3} \left(v_{vw} + \frac{1}{2} v_{wu} \right), \quad (14)$$

$$v_w = -v_u - v_v. \quad (15)$$

III. DESCRIPTION OF THE BIDIRECTIONAL SWITCH DESIGN AND EXPERIMENTAL PLATFORM

This section describes the design and implementation of the bidirectional switch used to construct the MC and presents the main experimental results obtained.

A. Bidirectional switch design

The design of the bidirectional switch has been based on the electrical diagram shown in Fig. 3. The Fig. 3(a) shows of the diagram corresponds to the bidirectional switch control circuit consisting mainly of the gate semiconductor gate controller and the optical fiber receiver modules, which receive the PWM control signal and send it to the input of the ICs gate controller, the output of the control circuit is connected to the power circuit which is observed on the Fig. 3(b) of the diagram, formed mainly by SiC-MOSFET power semiconductors, for this design, and polarization resistors. The control board is independent of the power board. The modular scheme makes the circuit to be simple to replace en case of failures.

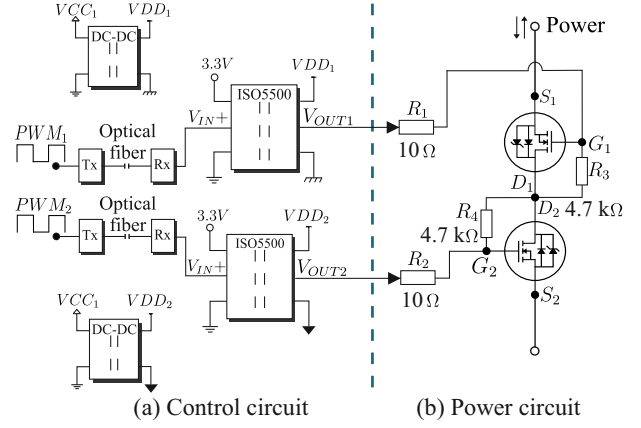


Fig. 3. Electric diagram of the bidirectional switch. (a) Control circuit and (b) Power circuit

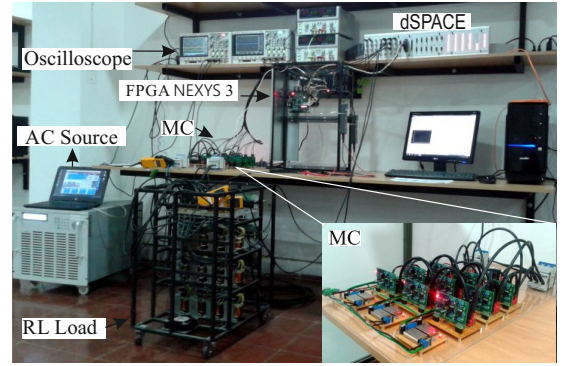


Fig. 4. Experimental setup using SiC MOSFET technology in the MC

B. Description of the experimental platform and obtained results

The experimental platform consists of a scheme that feeds a three-phase load from a source by means of an MC as shown in Fig. 2. The control scheme was implemented using the dSPACE CP1103 as the control device for optimal vector computing, communicated with a Nexys 3 FPGA in which the appropriate switching strategy is executed and the control signals are sent to the 18 switches according to the calculated vector and using optical transmission. Fig. 4 shows the experimental platform.

Posterior to assembling the two schemes with the parameters shown in Tabla I, both showed a correct operation, however the best result was obtained with the SiC-MOSFET-based scheme and is shown in Fig 5. The maximum achieved reference current was of 8 [A] peak with a maximum power around 1,2 [kW]. As for the transient response, Fig. 6 denotes the step response for the MC with IGBTs and Fig. 7 shows the response of the system to a variation in the referenced current amplitude for the MC with SiC-MOSFETs. In both cases it is possible to observe the correct tracking and satisfyingly fast response times. The input power required by each of the schemes at different sampling frequencies is shown in Fig. 8

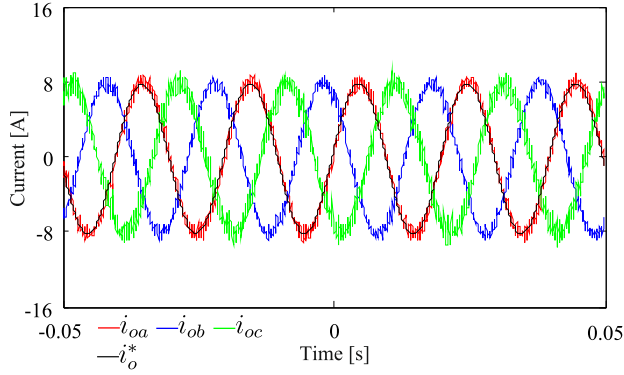


Fig. 5. Output current obtained in the SiC MOSFET MC for 1,2 kW.

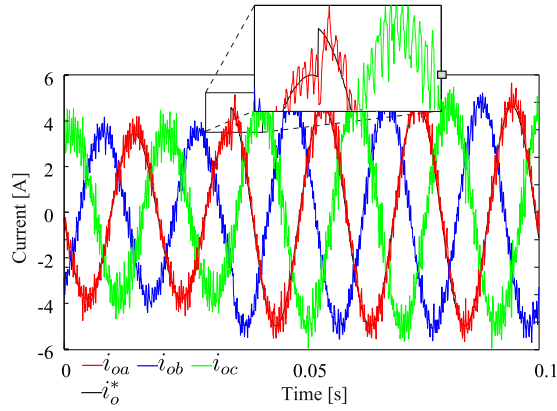


Fig. 6. Dynamic output current response of the IGBT MC for a step with $T_s = 100 \mu s$.

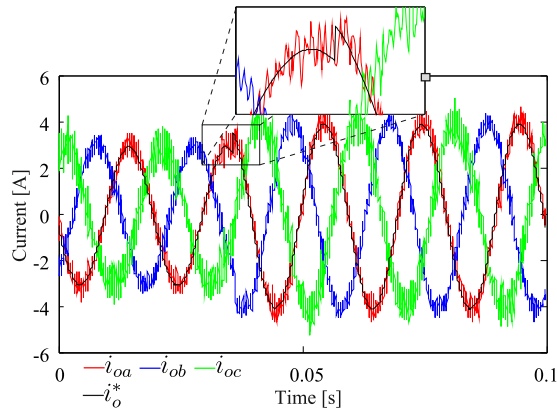


Fig. 7. Dynamic output current response of the SiC MOSFET MC for a step with $T_s = 100 \mu s$.

TABLE I
PARAMETERS OF THE EXPERIMENTAL SETUP AND THE CONTROLLER.

Description	Experimental Parameters		
	Symbol	Value	Unit
Load resistance	R	10	Ω
Load inductance	L	13	mH
Source voltage	V_s	0-125	V_{rms}
Source frequency	f_s	50	Hz

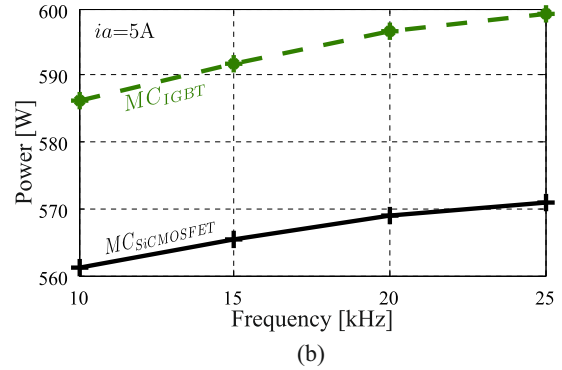
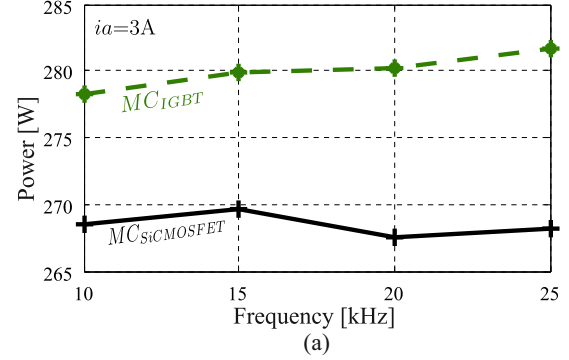


Fig. 8. Comparison between required power for distinct sampling frequencies and output currents.

when the same output current is requested. It is possible to observe that the SiC-MOSFET technology requires lower input power for the different frequencies when the same current is requested in the load.

Finally, the THD for both schemes at different sampling frequencies is shown in Fig. 9, it is possible to appreciate that there are no significant differences in this particular item.

IV. CONCLUSIONS

After completion of the work, it was possible to verify, in the first term, the correct functioning of the current predictive control scheme, both for the system with IGBT switches and for the one employing SiC-MOSFET technology. A power near 1,2 kW was achieved with currents of 1 to 8 [A] using SiC-MOSFETs, and between 1 to 5 [A] with IGBT switches. In addition, it was observed that for the same values of the required output current the SiC-MOSFETs had consumed less power, which leads to the conclusion that this proposal means a lower energy consumption at the input in order to reach the

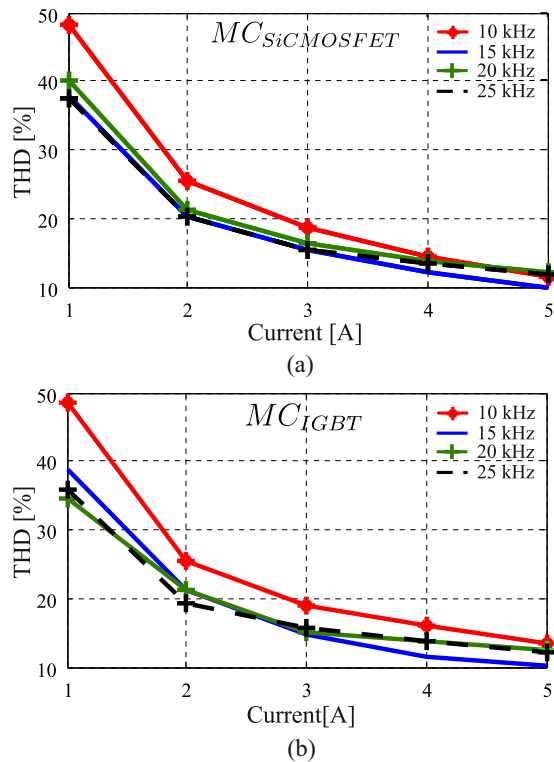


Fig. 9. THD values for different currents and sampling frequencies.

same desired values at the output. As for THD, there were no significant differences between the designs. The lack of a filter is the cause of the high THD value, which is expected to improve in a subsequent implementation. The SiC-MOSFET technology is most efficient in terms of power consumption in this experiment and it was possible to reach a current of 8 [A], which is highly superior than that achieved with the IGBT scheme.

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