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Lexical Analysis Using Table Look-Up

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Lexical analysis [Aho 77] is concerned with scanning the input for significant clusters of characters called tokens. The input stream of characters is analyzed by using delimiters and a careful description of the way various types of tokens may be constructed. There are circumstances in which this approach is not sufficiently flexible. It may not be convenient to define the nature of tokens by giving a set of rules of construction.

Such a circumstance arises during the process of building a functional model of a microprocessor system [Marsh 85]. The first step in this process is the lexical analysis of the assembler output of the microcode input of the microprocessor system. In this case the nature of the tokens to be isolated depend on the microprocessor being used and the assembler used to code the system description.

The need to develop a lexical analyzer which allows each of these two components to vary with minimal reprogramming has instigated the development of a lexical analyzer which exploits table lookup to provide the necessary flexibility.

The idea is that the information on the microprocessor and assembler is stored in a set of tables; the opcode table, the blockmarker table, the directive table, etc.. As the lexical scan proceeds these tables provide the intelligence to carry the scan forward or to require the scanner to backtrack. Each of these tables is implemented using dynamic storage allocation so that they may be modified at run time.

The number and nature of the tables needed is microprocessor/assembler dependent. At present a set of tables rich enough to handle several microprocessors is being used. Whether this information together with a microprocessor-dependent table precedence scheme can be provided as input to the scanner is currently being examined [Soda 86].

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References

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- "Lexical Analysis Using Generalized Table Look-up", D. Soda and G. Zobrist (in preparation), 1986.

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