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Excitation Allocation for Generic Identifiability of Linear Dynamic Networks With Fixed Modules

H. J. Dreef, Shengling Shi[®], *Member, IEEE*, Xiaodong Cheng[®], *Member, IEEE*, M. C. F. Donkers[®], *Senior Member, IEEE*, and Paul M. J. Van den Hof[®], *Fellow, IEEE*

Abstract—Identifiability of linear dynamic networks requires the presence of a sufficient number of external excitation signals. The problem of allocating a minimal number of external signals for guaranteeing generic network identifiability in the full measurement case has been recently addressed in the literature. Here we will extend that work by explicitly incorporating the situation that some network modules are known, and thus are fixed in the parametrized model set. The graphical approach introduced earlier is extended to this situation, showing that the presence of fixed modules reduces the required number of external signals. An algorithm is presented that allocates the external signals in a systematic fashion.

Index Terms—Network analysis and control, identification, linear systems.

I. INTRODUCTION

THE RECENT attention for dynamical systems in the format of structured interconnections of individual dynamical subsystems, has generated many challenging research questions. In the area of data-driven modeling, attention has been given to methods for modeling either a complete network or a particular subsystem on the basis of (a selection of) measured network signals. In these problems the topology of the network, i.e., the interconnection structure, can either be known or also be subject of identification [1]–[6].

For the problem of identifying a full linear dynamic network, in the situation that the topology is known, one of the key questions is whether there is enough excitation present in the data for uniquely recovering the dynamics of the network. This question is phrased through the concept of network identifiability [7]–[10], being essentially dependent on the external

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Shengling Shi is with the Delft Center for Systems and Control, Delft University of Technology, 2628 CD Delft, The Netherlands.

Xiaodong Cheng is with the Department of Engineering, University of Cambridge, Cambridge CB2 1PZ, U.K.

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signals present in the network, involving both user-chosen excitation signals and unmeasured disturbance signals, as well as on the structure of the model set reflecting possible prior knowledge on the network dynamics.

Recently, an algorithm has been presented in [11] that aims to allocate a minimum number of external excitation signals in the network, so as to guarantee that a network model set becomes generically identifiable, for the situation that all nodes in the network are measured (full measurement). By focusing on *generic* identifiability, i.e., the property holding for *almost all* models in the considered model set, the conditions for identifiability can be formulated in terms of the graph of the underlying model set, and are thus becoming easily applicable.

The graphical algorithm presented in [11] decomposes the network graph into a set of distinct pseudotrees, on the basis of which allocation of external excitation signals can simply be executed. In many network systems, the dynamics of some particular modules may be known and fixed, as e.g., userdesigned controllers. The algorithm in [11] can accommodate this to some extent by requiring that fixed modules do not need to be covered by the pseudotrees, possibly leading to a smaller number of required excitation signals. However it appears that this approach can be conservative, in the sense that it does not fully exploit the benefit of having some modules fixed, and therefore may yield more excitation signals than necessary. Therefore the key research question in this letter is: can we take advantage of the prior knowledge of these fixed modules to allocate excitation signals in a more efficient way?

In this letter, we extend the framework and algorithm in [11] to explicitly incorporate fixed modules, by relaxing and generalizing the graphical concept of pseudotree to a new concept (single-source identifiable multi-rooted graph (SIMUG)) that will be used to cover the graph of the network model set, and will be shown to provide the means for allocating external excitation signals more effectively.

After defining the appropriate network concepts in Section II, a motivating example and the resulting problem statement will be presented in Section III. This leads to a new result for identifiability and a related allocation algorithm presented in Sections IV–VI. Finally a brief example is provided.

Nomenclature: Denote \mathbb{N} and \mathbb{R} as the sets of natural and real numbers; $\mathbb{R}(z)$ is the rational function field over \mathbb{R} with variable *z*. v_i denotes the *i*-th element of a vector *v*, and A_{ij} denotes the (i, j)-th entry of a matrix *A*, and $A_{\star i}$ ($A_{i\star}$) its *i*-th

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column (row). The cardinality of a set \mathcal{V} is given by $|\mathcal{V}|$. The edges of directed graph \mathcal{G} are denoted by $E(\mathcal{G})$ and its vertices by $V(\mathcal{G})$.

II. PRELIMINARIES

A. Dynamic Network Setup

We consider a dynamic network following the setup in [4], which describes the dynamics and interconnection between a set of measured internal nodes $\{w_1, \ldots, w_L\}$. The set of measured external excitation signals $\{r_1, \ldots, r_K\}$ can be manipulated by the user and the set of unmeasured disturbance signals is given by $\{v_1, \ldots, v_L\}$. The expression for each node is given by

$$w_j(t) = \sum_{i=1}^{L} G_{ji}(q) w_i(t) + \sum_{k=1}^{K} R_{jk}(q) r_k(t) + v_j(t)$$
(1)

where $G_{ji}(q)$, $R_{jk}(q) \in \mathbb{R}(q)$ are rational transfer functions that connect the nodes and excitation signals, with time-shift operator q such that $q^{-1}w_j(t) = w_j(t-1)$. The elements $G_{ji}(q)$ are called modules, where $G_{jj}(q) = 0$, to exclude self-loops. The unmeasured process noise variables v_j are collected in the vector process $v = [v_1 \dots v_L]^{\top}$, which is modeled as a stationary stochastic process with rational spectral density $\Phi_v(\omega)$, such that there exists a p-dimensional (zero-mean) white noise process $e := [e_1 \dots e_p]^{\top}$, with $p \le L$ and covariance matrix $\Lambda > 0$ such that v(t) = H(q)e(t). The combination of all the L nodes can be written in terms of the full network expression

$$\begin{bmatrix} w_1 \\ w_2 \\ \vdots \\ w_L \end{bmatrix} = \begin{bmatrix} 0 & G_{12} & \cdots & G_{1L} \\ G_{21} & 0 & \ddots & \vdots \\ \vdots & \ddots & \ddots & G_{L-1L} \\ G_{L1} & \cdots & G_{LL-1} & 0 \end{bmatrix} \begin{bmatrix} w_1 \\ w_2 \\ \vdots \\ w_L \end{bmatrix} + R \begin{bmatrix} r_1 \\ r_2 \\ \vdots \\ r_K \end{bmatrix} + H \begin{bmatrix} e_1 \\ e_2 \\ \vdots \\ e_p \end{bmatrix},$$

where dependence on q is omitted for compactness of notation. The compact form of this equation is given by

$$w = Gw + Rr + He. \tag{2}$$

This leads to the following definition of a network model [7].

Definition 1 (Network Model): A network model of L nodes, and K external excitation signals, with a noise process of rank $p \le L$ is defined by the quadruple $M = (G, R, H, \Lambda)$, with

- $G \in \mathbb{R}^{L \times L}(q)$, diagonal entries 0, all modules strictly proper¹ and stable;
- $R \in \mathbb{R}^{L \times K}(q)$, proper, with in each row a single nonzero entry;
- *H* ∈ ℝ^{L×p}(q), proper and stable, with a left stable inverse, and a p × p submatrix being monic;
- $\Lambda \in \mathbb{R}^{p \times p}, \Lambda > 0;$
- $(I G)^{-1}$ is proper and stable (well-posedness);

For the purpose of studying network identifiability we need the following definition of a network model set.

Definition 2 (Network Model Set): A network model set \mathcal{M} is defined as $\mathcal{M} = \{M(\theta) = (G(\theta), R, H(\theta), \Lambda(\theta)); \theta \in \Theta\},\$ with $\Theta \subset \mathbb{R}^{n_{\theta}}$ a finite-dimensional parameter space, where each model satisfies the conditions of Definition 1.

In this model set *R* is considered to be known and therefore fixed. The elements in *G*, *H* and Λ can be parametrized, but some of these elements also can be known and thus fixed.

For the concept of network identifiability we follow the concept introduced in [7] and an extension towards genericity in [8], that in the current setting revolves around the transfer function $T(q, \theta) = (I - G(q, \theta))^{-1} [H(q, \theta) \quad R(q)]$ as follows.

Definition 3 (Network Identifiability): For a network model set \mathcal{M} , and a model $M(q, \theta_0) \in \mathcal{M}$ we consider the implication

$$T(q, \theta_0) = T(q, \theta_1) \implies M(\theta_0) = M(\theta_1)$$
 (3)

for all $\theta_1 \in \Theta$. Then \mathcal{M} is globally (generically) identifiable from (r, w) if implication (3) holds for all (almost all) $\theta_0 \in \Theta$.

The notion of generic identifiability is particularly attractive as it allows to be tested on the basis of graph-based tests, see e.g., [8], [10], [11]. This will be summarized in the next Subsection.

B. Graph Representation

The dynamic network interconnection structure can be represented by a directed graph \breve{G} that consists of the finite set of vertices $\breve{\mathcal{V}} := \{1, 2, ..., L\}$ and the edge set $\breve{\mathcal{E}} := \{(i, j) \in \breve{\mathcal{V}} \times \breve{\mathcal{V}} \mid G_{ji} \neq 0\}$. See for details of graph theory, e.g., [12]. The graph is simple, since no self-loops are present in the network model. The correlation structure of the noise signals ν will also be included in this graph representation by defining an extended graph as follows.

Definition 4 (Extended Graph) [11]: Consider a directed dynamic network (2). Let $\check{\mathcal{G}} = (\check{\mathcal{V}}, \check{\mathcal{E}})$ be its underlying graph. The extended graph $\mathcal{G} = (\mathcal{V}, \mathcal{E})$ is defined as

$$\mathcal{V} := \mathcal{V} \cup \{L+1, L+2, \dots, L+p\}$$

$$\tag{4}$$

$$\mathcal{E} := \check{\mathcal{E}} \cup \{(i,j) \in \mathcal{V} \times \check{\mathcal{V}} \mid H_{j,i-L}(q) \neq 0, i > L\}, \quad (5)$$

with $L = |\tilde{\mathcal{V}}|$ the number of nodes and p the number of noise signals e(t).

Actually the white noise sources *e* have been added as nodes in the graph, and edges can appear from *e*-nodes to *w*-nodes, but not reversed.

We will refer to the sets $\mathcal{N}_j^- := \{i \in \mathcal{V} | (i, j) \in \mathcal{E}\}$ and $\mathcal{N}_j^+ := \{i \in \mathcal{V} | (j, i) \in \mathcal{E}\}$ as the set of in- and out-neighbors of node *j*, respectively. A path that connects the vertices i_0 to i_n is a sequence of edges of the form $(i_{k-1}, i_k), k = 1, \ldots, n$, where every vertex occurs at most once on the path. A single vertex is also considered to have a path to itself. Two paths are vertex-disjoint if they do not share any nodes, including starting and ending nodes. The maximum number of vertex-disjoint paths from a set $\mathcal{A} \subseteq \mathcal{V}$ to a set $\mathcal{B} \subseteq \mathcal{V}$ is denoted by the operator $b_{\mathcal{A} \to \mathcal{B}}$.

Based on this network model set we define a graph representation of the model set, where both the parametrized and fixed modules and elements in the *H* matrix are explicitly represented by a set of parametrized \mathcal{E}_p and fixed edges \mathcal{E}_f .

Definition 5 (Graph Representation of Network Model Set): A network model set \mathcal{M} has a graph representation through

¹The condition of strictly proper modules can be relaxed in relation with a possible diagonal structure of Λ and the presence/absence of algebraic loops in the network, see [7].

the extended graph $\mathcal{G} = (\mathcal{V}, \mathcal{E}_p \cup \mathcal{E}_f)$, where $\mathcal{E}_p \cup \mathcal{E}_f = \mathcal{E}$ and $\mathcal{E}_p \cap \mathcal{E}_f = \emptyset$ determined by

 $\mathcal{E}_p := \{(i, j) \in \mathcal{E} \mid \text{edge } (i, j) \text{ is parametrized}\}, \text{ and}$ $\mathcal{E}_f := \{(i, j) \in \mathcal{E} \mid \text{edge } (i, j) \text{ is fixed}\}.$

This graph representation can be used to establish conditions for generic network identifiability of the network model set \mathcal{M} , given some assumptions regarding the parametrization. The following assumptions are used throughout this letter.

- Assumption 1 [10]:
- 1) All the parametrized entries in $M(\theta)$ are parametrized independently.
- 2) In model set \mathcal{M} , the rank of any submatrix of $\begin{bmatrix} G(q, \theta) I & H(q, \theta) & R(q) \end{bmatrix}$ that does not depend on θ , is equal to its structural rank.²

The second assumption here ensures that the numerical values of the fixed entries in the model set do not induce any singularity in the considered matrix. This assumption allows to formulate conditions for identifiability on the basis of the graph representation of the model set.

Lemma 1 (Generic Network Identifiability): Consider a network model set \mathcal{M} with graph representation \mathcal{G} that satisfies Assumption 1, and let $\mathcal{U} := \mathcal{R} \cup \{L + 1, \dots, L + p\}$ with $\mathcal{R} \subset \check{\mathcal{V}}$ the set of *w*-vertices that are directly excited by *r* signals. Then, the network model set \mathcal{M} is generically identifiable from (w, r) if in \mathcal{G} it holds that $b_{\mathcal{U} \to \mathcal{P}_j} = |\mathcal{P}_j|$ for all nodes $j \in \check{\mathcal{V}}$, where

$$\mathcal{P}_j \coloneqq \{i \in \mathcal{N}_i^- \mid (i,j) \in \mathcal{E}_p\}.$$
 (6)

Proof: This Lemma is formulated and proven as [11, Lemma 2] but for the set \mathcal{P}_j replaced by the set of all in-neighbours of w_j in the extended graph. With the subsystems generic identifiability result from [10, Th. 3] it can simply be shown that the original Lemma extends to the situation of considering only the subset \mathcal{P}_j of parametrized modules, rather than the full set of in-neighbours of node w_j .

Whereas Lemma 1 provides the condition for *verifying* identifiability node-wise, the target is to devise a method to allocate excitation signals that guarantees that the conditions of Lemma 1 are satisfied for all nodes simultaneously.

III. MOTIVATING EXAMPLE & PROBLEM STATEMENT

The original formulation of [11, Lemma 1] has led to an algorithm for the allocation of external excitation signals based on a covering of the extended graph of the model set with disjoint pseudotrees. A pseudotree is a subgraph in which the maximum indegree of each vertex is 1. This implies that a pseudotree can be built up of a cycle with outgoing trees starting from the vertices in the cycle. Pseudotrees are disjoint if they do not share any edges, and all outgoing edges of each vertex belong to the same pseudotree [11].

An illustrative example of a pseudotree covering is provided in Figure 1(a) where all modules are parametrized, and the graph is covered by two disjoint pseudotrees indicated in red and blue. Generic network identifiability is guaranteed if one of the roots in each pseudotree is excited, where in the case

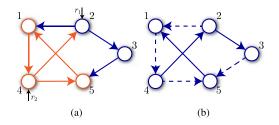


Fig. 1. An example of a network model set with: (a) only parametrized modules (solid) and (b) including fixed modules (dashed).

of a pseudotree with a cycle each vertex in the cycle acts as a root. In the example in Figure 1(a) this implies that excitation at node w_2 together with one of the nodes (w_1, w_4, w_5) is sufficient for guaranteeing generic identifiability. The result in [11] extends to the situation where some modules are fixed, by requiring that only the subgraph composed of parametrized modules should be covered in the pseudotree covering. An example of this is provided in Figure 1(b), where the fixed modules are represented by dashed lines. If we remove the fixed edges and cover the remaining graph with pseudotrees, excitation signals will be allocated at w_4 and w_5 , showing that the number of excitations increases when some modules are fixed, being a result of conservatism in the algorithm. At the same time it can be observed that the single pseudotree $w_5 - w_1 - w_4 - w_2 - w_3 - w_5$, covers all parametrized edges, and so would lead to allocation of one external excitation at any of the nodes in this cycle. However this solution, with a reduced number of excitations, will typically not be found by the pseudotree-covering algorithm. In this letter we will extend the allocation algorithm of [11] to effectively deal with the fixed modules in a structural way.

IV. GRAPH COVERING

The graphical approach to allocate excitation signals comes down to decomposing the network in sub-graphs with the property that in each sub-graph all of its modules are identifiable by applying a single excitation source. Given this decomposition, it is shown that applying excitation signals to a specific set of nodes associated with these sub-graphs leads to an identifiable network model set.

The sub-graphs that are used belong to a family of graphs, called the multi-rooted graphs, defined as follows.

Definition 6 (Multi-Rooted Graph): A connected directed graph \mathcal{T} , with $|V(\mathcal{T})| \ge 2$, is called a (directed) **multi-rooted graph** if there exists a path from each node in a nonempty set of **roots** $\Upsilon(\mathcal{T})$ to every node $i \in V(\mathcal{T})$.

For our identifiability study we will restrict attention to a particular class of multi-rooted graphs, for which each node only has maximally one incoming parametrized edge.

Definition 7 (Single-Source Identifiable Multi-Rooted Graph): A multi-rooted graph \mathcal{T} is called a single-source identifiable multi-rooted graph (SIMUG) if all nodes $j \in V(\mathcal{T})$ satisfy $|\mathcal{P}_j| \leq 1$, with \mathcal{P}_j defined in (6).

Definition 8 (Edge-Disjoint SIMUGs) [11]: Consider two multi-rooted graphs \mathcal{T}_1 and \mathcal{T}_2 as subgraphs of a directed graph \mathcal{G} . \mathcal{T}_1 and \mathcal{T}_2 are called **edge-disjoint** in \mathcal{G} if the following two conditions hold.

²The structural rank of a matrix is the maximum rank of all matrices with the same nonzero pattern [13].

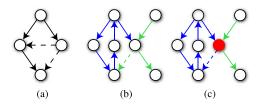


Fig. 2. (a) Example of a SIMUG, the maximum number of parametrized incoming edges of a node does not exceed 1; (b) a covering with two edge-disjoint SIMUGs; (c) The two SIMUGs are not edge-disjoint since one (red-colored) node has two outgoing edges from different SIMUGs. Fixed edges are dashed arrows.

- 1) \mathcal{T}_1 and \mathcal{T}_2 have no common edges;
- For each node *j* in *T*₁ ∪ *T*₂, all outgoing edges in *T*₁ ∪ *T*₂ belong to either *T*₁ or to *T*₂.

The concept of edge-disjoint SIMUGs is illustrated in Figure 2. In line with the disjoint edge-covering used in [11] we can now define.

Definition 9 (Edge-Disjoint SIMUG Covering): Consider a directed graph \mathcal{G} , and let $\Pi := \{\mathcal{T}_1, \mathcal{T}_2, \dots, \mathcal{T}_n\}$ be a collection of edge-disjoint SIMUGs of \mathcal{G} . The edges in a set $\mathcal{E} \subseteq E(\mathcal{G})$ are covered by Π , if $E(\mathcal{T}_1) \cup E(\mathcal{T}_2) \cup \dots \cup E(\mathcal{T}_n) = \mathcal{E}$, and Π is called an edge-disjoint SIMUG covering of \mathcal{E} .

It follows directly from the existence results of pseudotree coverings in [11] that every graph can be covered by edgedisjoint SIMUGs. The concepts that are defined above provide the means for specifying the conditions for allocating external excitaton signals so as to guarantee network identifiability.

Theorem 1: Consider a network model set \mathcal{M} that satisfies Assumption 1 and let $\mathcal{G} = (\mathcal{V}, \mathcal{E})$ be its extended graph. Then \mathcal{M} is generically identifiable from (r, w) if there exists an edge-disjoint SIMUG covering of \mathcal{E} , denoted by $\Pi = \{\mathcal{T}_1, \mathcal{T}_2, \ldots, \mathcal{T}_n\}$, such that $\forall k \in \{1, 2, \ldots, n\}$ there exists a vertex $\tau_k \in \Upsilon(\mathcal{T}_k)$ that is externally excited by an independent *r* or *e*-signal.

Proof: The proof is analogous to [11, Th. 1], with the difference that the pseudotrees that were used in [11] have to be replaced by the SIMUGs introduced here. The following reasoning from [11, Th. 1], modified to the setting in this letter, still applies: "The disjointness of the SIMUGs in Definition 8 implies that the paths in different disjoint SIMUGs are vertex-disjoint, if they have no common starting or ending nodes, and, for any vertex $j \in V(\mathcal{G})$, all the edges incident from the vertices in \mathcal{P}_j to j should belong to distinct SIMUGs. Furthermore, any two disjoint SIMUGs cannot share common root nodes, and thus $\tau_i \neq \tau_j$, for all $i \neq j$. Consequently, the above properties of disjoint SIMUGs yield that there exist $|\mathcal{P}_j|$ vertex-disjoint paths from $\{\tau_1, \tau_2, \ldots, \tau_n\}$ to \mathcal{P}_j ." Then, application of Lemma 1 shows the result.

With the result of Theorem 1, we have reduced the allocation problem to the problem of finding a minimal number of SIMUGs that cover the extended graph.

V. ALLOCATION ALGORITHM

A. Introduction

For formulating an algorithm that generates a SIMUG covering of the network, we follow an approach which is very closely related to the algorithm in [11], however adapted to the situation of having SIMUGs with possibly fixed modules. As a strategy we are going to start with an initial SIMUG covering of the network, and then we are going to merge SIMUGs so as to arrive at a smaller number of SIMUGs that cover the network. In this setting we have to formulate appropriate conditions for merging two SIMUGs, and we need new algebra for formulating the merging algorithm.

As an initial covering we start with the SIMUG covering $\Pi_0 = \{\mathcal{T}_1^{(0)}, \mathcal{T}_2^{(0)}, \dots, \mathcal{T}_{|\Pi_0|}^{(0)}\}$ where $|\Pi_0| = |\mathcal{V}| - |\mathcal{S}_{in}|$ with \mathcal{S}_{in} the set of sinks in the graph, where for each node $k \in \mathcal{V}$ that is not a sink, $\mathcal{T}_k^{(0)}$ is composed of node k and all of its outgoing edges in \mathcal{E} .

Definition 10 (Mergeability) [11]: Consider two disjoint SIMUGs \mathcal{T}_1 and \mathcal{T}_2 and $V(\mathcal{T}_1) \cap V(\mathcal{T}_2) = \emptyset$. We say that \mathcal{T}_1 is mergeable to \mathcal{T}_2 , if

- 1) the union of \mathcal{T}_1 and \mathcal{T}_2 , i.e., $(V(\mathcal{T}_1) \cup V(\mathcal{T}_2), E(\mathcal{T}_1) \cup E(\mathcal{T}_2))$ is also a SIMUG;
- and there is a directed path from every vertex *i* ∈ Υ(*T*₂) to every vertex *j* ∈ *V*(*T*₁).

To every SIMUG covering of the network a characteristic matrix is connected, that will be used for steering the merging operations of the SIMUGs. It is defined as follows.

Definition 11: Denote a set $\mathbb{S} = \{1, 0, \emptyset\}$ and let $\Pi = \{\mathcal{T}_1, \mathcal{T}_2, \dots, \mathcal{T}_n\}$ be a disjoint SIMUG covering of a directed graph. The characteristic matrix of Π is denoted by $\mathfrak{M} \in \mathbb{S}^{n \times n}$, whose (i, j)-th entry is defined as

$$\mathfrak{M}_{ij} = \begin{cases} 1 & \text{if } \mathcal{T}_i \text{ is mergeable to } \mathcal{T}_j \\ \varnothing & \text{if } \mathcal{Z}_{ij} = \emptyset, \\ 0 & \text{otherwise} \end{cases}$$
(7)

where $Z_{ij} := \{x \in V(\mathcal{T}_j) \cup V(\mathcal{T}_i) \mid |\mathcal{P}_x \cap (V(\mathcal{T}_j) \cup V(\mathcal{T}_i))| > 1\}$ is the set of nodes in \mathcal{T}_i and \mathcal{T}_j that have multiple parametrized in-neighbors in the two SIMUGs.

The characteristic matrix in (7) will serve as an algebraic means for merging SIMUGs in an algorithm that provides a covering of the network with a reduced number of SIMUGs.

B. Initial/Characteristic Matrix Algorithm

For specification of the characteristic matrix of the initial covering we follow a reasoning similar to the result of [11, Lemma 5]. However, due to the decomposition of \mathcal{G} in \mathcal{G}_f and \mathcal{G}_p , this reasoning leads to new technical expressions that require an independent proof.

Lemma 2: Given the extended graph representation $\mathcal{G} = (\mathcal{V}, \mathcal{E}_p \cup \mathcal{E}_f)$ of the network model set \mathcal{M} , for which the edges are separated in the parametrized $\mathcal{G}_p = (\mathcal{V}, \mathcal{E}_p)$ and fixed graph $\mathcal{G}_f = (\mathcal{V}, \mathcal{E}_f)$ with $n = |\mathcal{V}|$, the corresponding adjacency matrices are given by $A(\mathcal{G}_p)$ and $A(\mathcal{G}_f)$, respectively. Denote

$$a_{ij} = \left([nA(\mathcal{G}_p) - A(\mathcal{G}_f) + Ii]_{\star i} \right)^\top \left([nA(\mathcal{G}_p) - A(\mathcal{G}_f)]_{\star j} \right)$$
(8)

where $i, j \in \{1, 2, ..., |\Pi_0|\}$, i denotes the imaginary unit. Then, the characteristic matrix $\mathfrak{M}^{(0)}$ of Π_0 is given by: $\mathfrak{M}^{(0)}_{ii} = 0$ for all *i*, while for $j \neq i$:

$$\mathfrak{M}_{ij}^{(0)} = \begin{cases} 1 & \text{if } \operatorname{Re}(a_{ij}) < n \text{ and } \operatorname{Im}(a_{ij}) \neq 0; \\ \varnothing & \text{if } \operatorname{Re}(a_{ij}) < n \text{ and } \operatorname{Im}(a_{ij}) = 0; \\ 0 & \text{if } \operatorname{Re}(a_{ij}) \ge n. \end{cases}$$
(9)

where $\text{Re}(\cdot)$ and $\text{Im}(\cdot)$ denote the real and imaginary parts of a complex number.

Proof: This proof essentially shows that the initial mergeability matrix in Definition 11 can be found, using (8) based on the adjacency matrices of the fixed and parametrized parts of the extended graph.

Case $Re(a_{ij}) \ge n$: This result indicates that multiple parametrized edges enter the same node from both SIMUGs, which results in a nonmergeable '0' entry in the matrix. The distinction between parametrized and fixed edges is made due to the scaling factor n, which leads to

$$\operatorname{Re}(a_{ij}) = x_1 n^2 - x_2 n + x_3 \ge n, \tag{10}$$

with $x_1, x_2, x_3 \in \mathbb{N}$ denoting the occurrences of respectively common parametrized edges to the same node, i.e., $x_1 = |Z_{ij}|$, a parametrized and fixed edge to the same node or common fixed nodes. Note that due to the number of nodes the following constraint holds

$$x_1 + x_2 + x_3 \le n - 2,\tag{11}$$

since $[nA(\mathcal{G}_p) - A(\mathcal{G}_f)]_{ii} = 0$ by exclusion of self-loops. Then, we show that $\operatorname{Re}(a_{ij}) \ge n$ if and only if $x_1 > 0$, since for $x_1 = 0$ we have $\max_{x_2, x_3} - x_2n + x_3 = n - 2$ with $x_2 = 0$ and $x_3 = n - 2$ by (11). Now, also any solution for $x_1 > 0$ implies that (10) holds, since x_2 is constrained by $x_2 \le n - 2 - x_1$. Therefore, $\operatorname{Re}(a_{ij}) \ge n$ results in $\mathfrak{M}_{ij} = 0$, because the union of \mathcal{T}_i and \mathcal{T}_i is clearly not a SIMUG since a node has $|\mathcal{P}_i| > 1$.

Case $Re(a_{ij}) \leq n \& Im(a_{ij}) \neq 0$: Here, $x_1 = 0$ in (10), so the constraint $|\mathcal{P}_j| \leq 1$ holds for the union of \mathcal{T}_i and \mathcal{T}_j , while the imaginary axis is used to encode whether the 2-nd condition in Definition 10 is satisfied. This ensures that in case of merging \mathcal{T}_i to \mathcal{T}_j the root(s) of \mathcal{T}_j still have a path to all nodes in \mathcal{T}_i . The root node(s) in \mathcal{T}_i is/are encoded by i in (8), which has a path to all its nodes. Then, if an edge exists from a node in \mathcal{T}_j to the root denoted by i in \mathcal{T}_i , there also exists a path from the root of \mathcal{T}_i to all the nodes in \mathcal{T}_i , so $\mathfrak{M}_{ij} = 1$.

Case $Re(a_{ij}) \leq n \& Im(a_{ij}) = 0$: In this case the same reasoning holds, but the result is not mergeable, since there is not a path from the root(s) of \mathcal{T}_j to the nodes in \mathcal{T}_i . Therefore, the result is $\mathfrak{M}_{ij} = \emptyset$, which concludes the proof.

With Lemma 2 the characteristic matrix of the initial covering can directly be calculated on the basis of the adjacency matrices $A(\mathcal{G}_p)$ and $A(\mathcal{G}_f)$.

C. Merging Procedure

Next we will represent a merging operation on a SIMUG covering through an equivalent operation of the characteristic matrix. In view of this we define $\mathfrak{M} \in \mathbb{S}^{|\Pi_0| \times |\Pi_0|}$, and let $\mathfrak{M}_{i\star}$ and $\mathfrak{M}_{\star i}$ be the *i*-th row and *j*-th column of \mathfrak{M} , respectively.

For a given disjoint SIMUG covering Π with $|\Pi| = n$ and a set $\mathbb{N} := \{1, 2, ..., n\}$, we then define the following function $\mathcal{F} : \mathbb{S}^{n \times n} \times \mathbb{N} \times \mathbb{N} \to \mathbb{S}^{(n-1) \times (n-1)}$, and $\hat{\mathfrak{M}} = \mathcal{F}(\mathfrak{M}, i, j)$ is a reduction of \mathfrak{M} obtained by merging SIMUG *i* into SIMUG *j* by the following algebraic operations: 1) $\hat{\mathfrak{M}} = \mathfrak{M}; 2$) Row merging: $\hat{\mathfrak{M}}_{j\star} = \mathfrak{M}_{i\star} \otimes \mathfrak{M}_{j\star}; 3$) Column merging: $\hat{\mathfrak{M}}_{\star j} =$ $\mathfrak{M}_{\star i} \odot \mathfrak{M}_{\star j}; 4$) Remove the *i*-th row and column of $\hat{\mathfrak{M}}$.

The row merging operator \otimes and the column operator \odot are given as follows. First, the column operator \odot is commutative

defined to describe the merging feature algebraically: $c = a \odot$ $b = b \odot a$, with $a, b, c \in \mathbb{S}$, which follow the rules

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$$\odot 1 = 1, 1 \odot 0 = 0, 1 \odot \emptyset = 1,$$
 (12)

$$0 \odot 0 = 0, \varnothing \odot 0 = 0, \varnothing \odot \varnothing = \varnothing, \tag{13}$$

Then, the row merging operator \otimes is similar and adheres to the same rules as (12)-(13), but differs in the rule $1 \odot \emptyset = 1$, where it is also not commutative. Instead of $1 \odot \emptyset = 1$ the following rules apply:

$$\emptyset \otimes 1 = 1, 1 \otimes \emptyset = \emptyset. \tag{14}$$

Now, we extend the operators to entrywise vector operations, for which we let $\rho, \mu \in \mathbb{S}^n$ be two column (or row) vectors. The following operations $\rho \odot \mu$ and $\rho \otimes \mu$ are entrywise operations that return a new column (or row) vector, whose *i*-th element is given by $\rho_i \odot \mu_i$ or $\rho_i \otimes \mu_i$, respectively.

Theorem 2: Consider a directed graph $\hat{\mathcal{G}}$, and let Π be a disjoint SIMUG covering of $\hat{\mathcal{G}}$ represented by the characteristic matrix \mathfrak{M} . If in Π , the *i*-th SIMUG is mergeable to the *j*-th one, then a new SIMUG covering $\hat{\Pi}$ of $\hat{\mathcal{G}}$ is obtained by merging the *i*-th SIMUG into the *j*-th one, where $|\hat{\Pi}| = |\Pi| - 1$ and the characteristic matrix of $\hat{\Pi}$ is given by $\mathfrak{M} = \mathcal{F}(\mathfrak{M}, i, j)$.

Proof: This proof shows that, given a SIMUG covering, merging two SIMUGs and finding subsequent mergeable SIMUGs for the new covering is equivalent to applying the rules in (12)-(14) to the mergeability matrix. First, the rules for the column merging is treated, followed by the row merging operator. Suppose \mathcal{T}_i is mergeable to \mathcal{T}_j , then, the row operator defines for the reduced covering $\hat{\Pi}$ to which SIMUGs the union of \mathcal{T}_i and \mathcal{T}_j is mergeable, while the column operator defines which SIMUGs are mergeable to the union of \mathcal{T}_i and \mathcal{T}_j . Therefore, the following statements hold for the column operator \odot and the row operator \otimes in (12)-(13):

- If either *T_i* or *T_j* can not merge to or be merged to *T_x* due to a '0' entry in the mergeability matrix, then, *T_x* can not merge to or be merged to the union of *T_i* and *T_i*. Hence, the rules 0 ⊙ 0 = 0, Ø ⊙ 0 = 0 and 1 ⊙ 0 = 0.
- 2) If \mathcal{T}_x is mergeable to both \mathcal{T}_i and \mathcal{T}_j , then, \mathcal{T}_x is also mergeable to the union of \mathcal{T}_i and \mathcal{T}_j and vice versa, i.e., $1 \odot 1 = 1$.
- 3) If $\mathfrak{M}_{xi} = \emptyset$, then, \mathcal{T}_x is not mergeable to \mathcal{T}_i , but if \mathcal{T}_x is mergeable to \mathcal{T}_j , then, \mathcal{T}_x is also mergeable to the union of \mathcal{T}_i and \mathcal{T}_j or vice versa, since the condition for a SIMUG that $|\mathcal{P}_l| \le 1 \forall l \in V(\mathcal{T}_i \cup \mathcal{T}_j \cup \mathcal{T}_x)$ will still be satisfied. i.e., $\emptyset \odot 1 = 1$. Moreover, if $\mathfrak{M}_{xj} = \emptyset$ too, then, the same condition is satisfied, so $\emptyset \odot \emptyset = \emptyset$.

Then, the row operator \otimes has one logical difference with respect to the column operator \odot , i.e., (14), which is shown in the following statements:

- For the rule: Ø⊗1 = 1, for which M_{ix} = Ø and M_{jx} = 1, T_i is merged to T_j, so the root(s) of the union of T_i and T_j is/are the root(s) of T_j. Since T_j was mergeable to T_x and T_i does not pose a problem to merge to T_x, the union of T_i and T_j is mergeable to T_x.
- 2) Now for the rule: $1 \otimes \emptyset = \emptyset$ for which $\mathfrak{M}_{ix} = 1$ and $\mathfrak{M}_{jx} = \emptyset$, \mathcal{T}_i is merged to \mathcal{T}_j , so the root(s) of the union of \mathcal{T}_i and \mathcal{T}_j is/are the root(s) of \mathcal{T}_j . However, the root(s) of the union of \mathcal{T}_i and \mathcal{T}_j do not necessarily have a path to all nodes in \mathcal{T}_x .

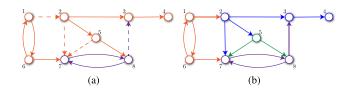


Fig. 3. (a) A disjoint SIMUG covering of a network with 2 SIMUGs; fixed modules are indicated with dashed lines, parametrized modules with solid lines. (b) A pseudotree covering of the same network with all modules parametrized.

Following these statements, the operators that are used in the function $\mathcal{F}(\mathfrak{M}, i, j)$ are shown to be equivalent to merging two SIMUGs in the covering. The function $\mathcal{F}(\mathfrak{M}, i, j)$ therefore reduces the mergeability matrix and updates it according to the merged SIMUGs.

On the basis of the above merging operation we can now follow the same merging algorithm as presented in [11], where we first find the row of the characteristic matrix with a unique '1' entry, on the basis of which the SIMUG related to this row number is merged. Then we continue with merging SIMUGs related to '1' entries in nodes, giving priority to those rows that have the most ' \emptyset ' entries. For more details and motivations we refer to [11, Algorithm 1]. Note that, like in [11] there is no formal guarantee that we arrive at a minimal number of SIMUGs covering the network.

VI. ALLOCATING EXCITATION SIGNALS

With the result of Theorem 1 we can guarantee generic network identifiability if we make sure that in an edge-disjoint SIMUG covering of the network graph, one root in every SIMUG is excited by an external r or e-signal. Having such a SIMUG covering obtained in the previous Section, we can now detect those SIMUGs that do not have such an excitation yet from existing r and e signals, and allocate additional r signals to the root nodes of these SIMUGs.

In comparison with the situation of all-parametrized modules [11], there are two distinctive situations to be mentioned:

- For a SIMUG for which all roots have only outgoing links that are fixed, the external excitation does not necessarily need to be added to the root.
- SIMUGs that are composed of fixed edges only, do not require excitation, and they do not need to be merged to other nodes. However they can serve in the role of interconnecting two other SIMUGs that do require excitation, and for which merging with the fixed SIMUG can reduce the number of excitation signals required.

After having allocated a sufficient number of r signals conditions, a final check is made for necessity of all allocated r signals, by individually removing them and verifying the vertex disjoint path condition of Lemma 1.

VII. EXAMPLE

The presented algorithm is applied to an 8-node network depicted in Figure 3 that shows typical locations of fixed modules that introduces conservatism in the methods in [11]. For generic network identifiability external (excitation) signals are added to a root node of each SIMUG, in this case, one of the nodes $\{w_1, w_6\}$, and one of the nodes $\{w_7, w_8\}$.

This result can be compared against the pseudotree covering in [11] in two ways. First, the approach where the fixed edges are excluded to cover the parametrized modules only does not consider the connection between node w_1 and w_2 . Hence, another pseudotree is required with node w_2 as root. Second, if in this network all present modules would be parametrized, the resulting pseudotree covering is depicted in Figure 3(b). In this situation we arrive at four pseudotrees leading to additional (excitation) signals to be added at nodes w_2 and w_5 .

VIII. CONCLUSION

A graphical method for allocating external signals to achieve generic identifiability of a dynamic network model set, has been extended to the situation where network modules can be fixed. To this end, a pseudotree covering of the network graph has been generalized to a covering based on a special type of multi-rooted graph (SIMUG). The related merging algorithm also has been generalized, aiming at the allocation of a minimum number of external excitation signals for achieving generic identifiability. While minimality of the number of allocated signals cannot be guaranteed, including the property that some modules might be known, and thus fixed, reduces the number of external signals that guarantee identifiability.

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