

Analysis and simulation of a syndrome decoder for a constraint length $K=5$, rate $R=1/2$ binary convolutional code

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I. INTRODUCTION.

The principle of syndrome decoding for $R=\frac{1}{2}$ convolutional codes will be explained using the binary code generated by the encoder of fig (1).

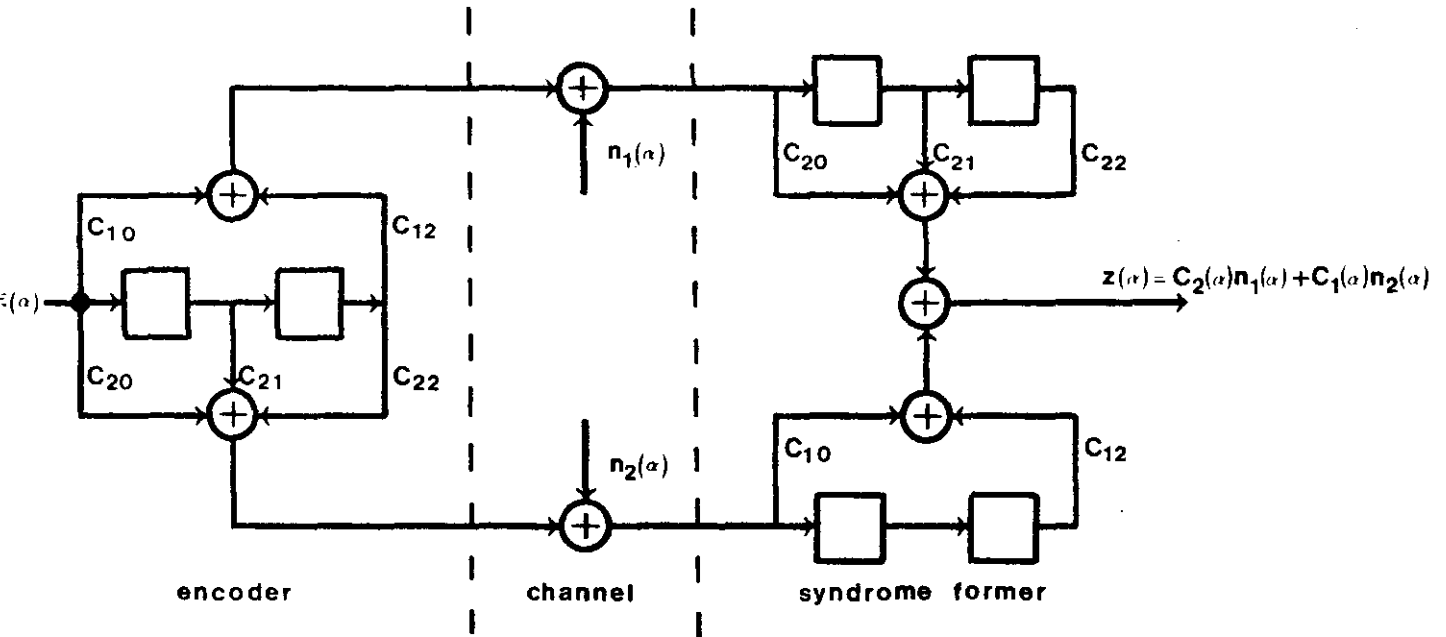


Fig. 1. Encoding and syndrome forming for a $R=\frac{1}{2}$ code

The encoder has connection polynomials $C_1(\alpha)$ and $C_2(\alpha)$. Hence, the encoder outputs are $C_1(\alpha)x(\alpha)$ and $C_2(\alpha)x(\alpha)$. The syndrome $z(\alpha)$ only depends on $n_1(\alpha)$ and $n_2(\alpha)$, for

$$\begin{aligned} z(\alpha) &= C_2(\alpha) [C_1(\alpha)x(\alpha) + n_1(\alpha)] + C_1(\alpha) [C_2(\alpha)x(\alpha) + n_2(\alpha)] = \\ &= C_2(\alpha)n_1(\alpha) + C_1(\alpha)n_2(\alpha). \end{aligned}$$

Having formed the syndrome $z(\alpha)$, a recursive algorithm described in [1], determines the noise sequence pair $[\hat{n}_1(\alpha), \hat{n}_2(\alpha)]$ of minimum Hamming weight that can be a possible cause of this syndrome. For a non catastrophic code $C_1(\alpha)$ and $C_2(\alpha)$ are relatively prime. Hence, by Euclids algorithm there exist polynomials $D_1(\alpha)$ and $D_2(\alpha)$ such that

$$D_1(\alpha)C_1(\alpha) + D_2(\alpha)C_2(\alpha) = 1.$$

The estimate $\hat{x}(\alpha)$ of the data sequence $x(\alpha)$ can be written as

$$\hat{x}(\alpha) = [D_1(\alpha)y_1(\alpha) + D_2(\alpha)y_2(\alpha)] + \omega(\alpha)$$

where

$$\omega(\alpha) = D_1(\alpha)\hat{n}_1(\alpha) + D_2(\alpha)\hat{n}_2(\alpha)$$

and

$$y_i(\alpha) = C_i(\alpha)x(\alpha) + n_i(\alpha); \quad i=1,2.$$

The syndrome $z(\alpha)$ can be thought of as generated by the syndrome former of fig. 2.

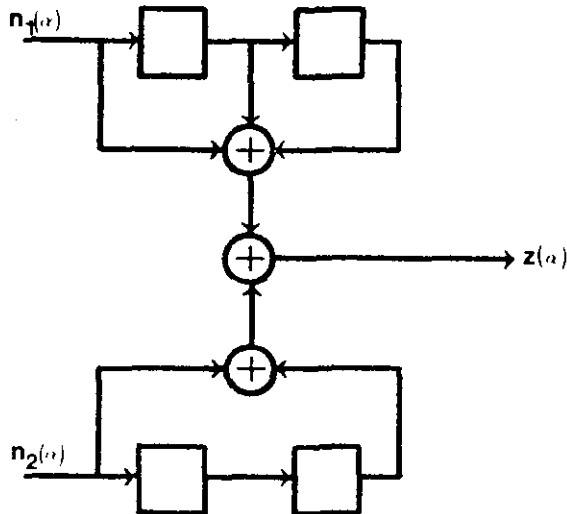


Fig. 2. The syndrome decoder.

If the binary content on time t^- is called the state of the syndrome former, then, after shifting in a bitpair (n_1, n_2) the new state is determined by the content of the v stages on time t^- , and the incoming bitpair (n_1, n_2) . From [1] we have 2^v classes of states, each containing 2^v equivalent states. Equivalent states are states which cause the same syndrome digits in response to a given input bitsequencepair. The state in which each of the v stages of the topregister contain a 0, is taken as the representative for a class of equivalent states.

If we shift in the bitpair $(0,0)$ or $(0,1)$, the new state will be $(2 * (\text{old state}))$ resp. $(2 * (\text{old state}) + 1)$, whereas to the bitpair $(1,0)$ or $(1,1)$ we may always add a "0-equivalent" state bit by bit to the content of the registers, without affecting $z(\alpha)$, thus again filling the topregister with a binary zero content. The "0-equivalent" state for the syndrome former of fig. 2 is given in $(1,1)$.

$$\begin{aligned} \text{So:} &= \begin{matrix} 1,0 & & 1,1 \\ & & 1,1 \end{matrix} \end{aligned}$$

The D polynomials are

$$\begin{aligned} D_1(\alpha) &= 1 + \alpha & 1.2 \\ D_2(\alpha) &= \alpha \end{aligned}$$

and the state table is given in table 1.

	bitpair (n_1, n_2)				n_1, n_2				\hat{n}_1, \hat{n}_2			
state	00	01	11	10	00	01	11	10	00	01	11	10
	new state				syndrome z				ω			
0	0	1	2	3	0	1	0	1	0	0	1	1
1	2	3	0	1	0	1	0	1	1	1	0	0
2	0	1	2	3	1	0	1	0	0	0	1	1
3	2	3	0	1	1	0	1	0	1	1	0	0

Table 1.

With each state we associate a metric $M_j(k)$, $j=0,1,2,3$; $k=0,1,2,\dots$, that equals the minimum Hamming weight of a path $[\hat{n}_1(\alpha), \hat{n}_2(\alpha)]^{(\delta)}$, leading from state $j=0$ at time $k=0$ to that particular state.

The metric $M_j(k+1)$ at time $(k+1)$ can be determined recursively, for example, for the syndrome decoder of fig. 2:

$$M_2(k+1) = \bar{z}_k \min [M_1(k), M_0(k)+2] + z_k \min [M_3(k), M_2(k)+2] \quad (1.3)$$

Given the value z_k , i.e. $z_k=0$ or $z_k=1$, each $(k+1)$ -state can be reached from two k -states, for each of these two k -states add to the metric the Hamming weight of the respective transitions, i.e. of $[\hat{n}_{k1}, \hat{n}_{k2}]$, to the particular $(k+1)$ -state.

The minimum of the two values is $M_j(k+1)$. In case of a tie we may choose the survivor at random.

For state 1 and 3 we obtain that:

$$M_1(k+1) = \bar{z}_k \min [M_2(k)+1, M_3(k)+1] + z_k \min [M_0(k)+1, M_1(k)+1] \quad (1.4)$$

$$M_3(k+1) = \bar{z}_k \min [M_2(k)+1, M_3(k)+1] + z_k \min [M_0(k)+1, M_1(k)+1] \quad (1.5)$$

And consequently $M_1(k+1) = M_3(k+1)$. If there is no tie, the path registers for state 1 and state 3 are equal, except for the first stage of the two pathregisters, which are filled with 0 and 1 respectively. In case of a tie we can force equality of the pathregisters, without increasing error probability.

The decoding procedure is given in fig. 3.

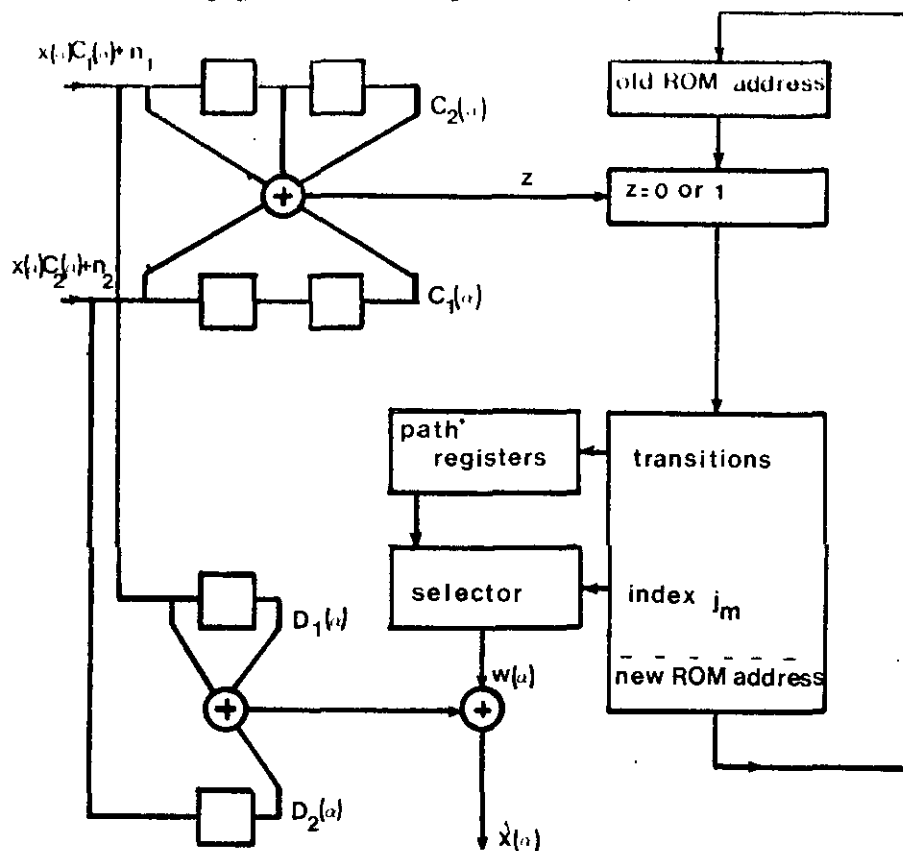


Fig. 3.

Knowledge of the successive survivors for each state, and the state index $j_m(k)$ of the state with minimum metric value suffices to determine the key sequence $w^{(k)}$. This information can be stored in a ROM, in which each address corresponds with a certain metric combination.

For both $z=0$ and $z=1$, the current ROM address contains information about the survivor, the index j_m , and the new ROM address, respectively. The path registers are shuffled and a selector mechanism determines which bit has to be decoded, according to j_m .

The next section describes the differences between Viterbi and syndrome decoding as far as the path registers and metric calculations are concerned. It also justifies the selection of a particular $k=5$ code.

II. Selection of a $v=4$ decoder.

First some differences between Viterbi- and syndrome decoding, as far as pathregister organization and metric calculation are concerned, will be explained.

The $R=1/2$ syndrome decoder, has a more complicated pathregister organization, as can be seen from fig. 4 and fig. 5.

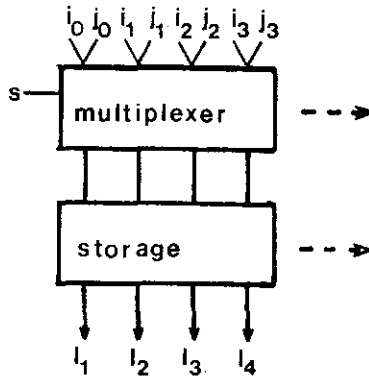


Fig. 4.

Viterbi pathregister organization.

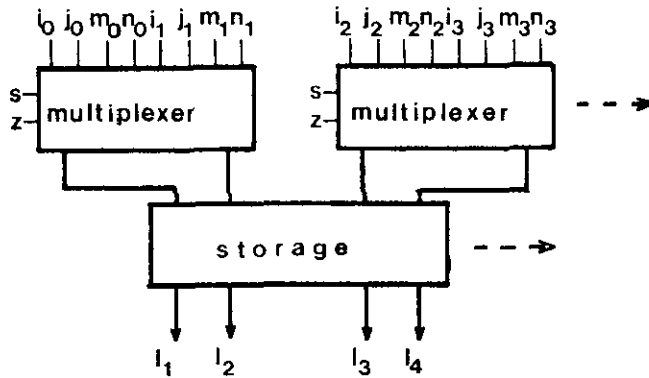


Fig. 5.

Syndrome decoder pathregister organization.

In Viterbi decoding, there are two possible choices for the survivor for each state, whereas for the syndrome decoder there are four alternatives, i.e. two for $z=0$ and two for $z=1$. This leads to more complicated pathregisters. However, in many cases the syndrome decoder requires fewer pathregisters than the Viterbi decoder does, thus easily offsetting the above complication.

An additional advantage of syndrome decoding is a smaller number of metric combinations, and an easier metric calculation.

In Viterbi decoding, we have two incoming states for each state with usually a complementary bitpair.

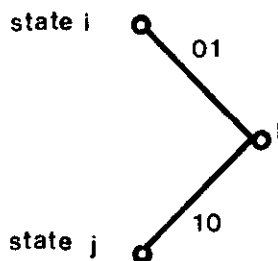


Fig. 6.

After receiving two bits, the new metric for state l is

$$M_l(k+1) = \min [M_i(k) + d\{(r_1, r_2) - (0, 1)\}, M_j(k) + d\{(r_1, r_2) - (1, 0)\}],$$

(r_1, r_2) being the received data pair.

So we have to determine the distance between (r_1, r_2) and $(0, 1)$.

In syndrome decoding, we have after receiving z_k ,

$$M_l(k+1) = z_k \min [M_i(k) + c_i, M_j(k) + 2 - c_i] +$$

$$+ \bar{z}_k \min [M_m(k) + c_m, M_n(k) + 2 - c_m].$$

We then determine the minimum of two metrics offset by some constant.

When selecting a decoder one has to look for a code with a small number of distinct metric combinations, that can be realized using few pathregisters. In addition the code must have a good error

correcting capability. Therefore a computer program has been developed to determine for the $v=4$ codes,

- 1) the number of bit errors in the paths at d_{\min} and $d_{\min} + 1$, according to [2],
- 2) the number of distinct metric combinations,
- 3) the bit error probability for the decoders with PR-length 16 and 11,
- 4) the minimum possible number of pathregisters.

Three $v=4$ codes with minimum distance 7 are listed in table 2.

Code n^0	Connection polynomials	Number of metric combinations	Minimum number of pathregisters	Distance	Number of paths	Total number of bit errors
1	10011	1817	9	7	3	7
	10111			8	3	10
2	11001	1784	12	7	2	4
	11011			8	4	12
3	11001	13641	12	7	2	4
	10111			8	4	12

Table 2.

The state- and metric calculation tables for these three codes are given below.

Code 1.

The "0-equivalent" state is

$$S_0 = 1, 0, 0, 0$$

$$1, 0, 1, 1$$

The D-polynomials are

$$D_2 = 1 + \alpha + \alpha^2$$

$$D_1 = \alpha + \alpha^2$$

State	new state				z				$\bar{\omega}$			
	n_1, n_2				n_1, n_2				\hat{n}_1, \hat{n}_2			
	00	01	11	10	00	01	11	10	00	01	11	10
0	0	1	12	13	0	1	0	1	0	1	1	0
1	2	3	14	15	0	1	0	1	1	0	0	1
2	4	5	8	9	1	0	1	0	1	0	0	1
3	6	7	10	11	1	0	1	0	0	1	1	0
4	8	9	4	5	1	0	1	0	0	1	1	0
5	10	11	6	7	1	0	1	0	1	0	0	1
6	12	13	0	1	0	1	0	1	1	0	0	1
7	14	15	2	3	0	1	0	1	0	1	1	0
8	0	1	12	13	1	0	1	0	0	1	1	0
9	2	3	14	15	1	0	1	0	1	0	0	1
10	4	5	8	9	0	1	0	1	1	0	0	1
11	6	7	10	11	0	1	0	1	0	1	1	0
12	8	9	4	5	0	1	0	1	0	1	1	0
13	10	11	6	7	0	1	0	1	1	0	0	1
14	12	13	0	1	1	0	1	0	1	0	0	1
15	14	15	2	3	1	0	1	0	0	1	1	0

Table 3.

According to state table 3 we can make the metric calculation table for this code.

Metric	$z_k=0$	$z_k=1$
$M_p(k+1)$	$\min [M_i(k) + c_i, M_j(k) + 2-c_i]$	$\min [M_m(k) + c_m, M_n(k) + 2-c_m]$
M_0	$M_0, M_6 + 2$	$M_8, M_{14} + 2$
M_1	$M_8 + 1, M_{14} + 1$	$M_0 + 1, M_6 + 1$
M_2	$M_1, M_7 + 2$	$M_9, M_{15} + 2$
M_3	$M_9 + 1, M_{15} + 1$	$M_1 + 1, M_7 + 1$
M_4	$M_{10}, M_{12} + 2$	$M_2, M_4 + 2$
M_5	$M_2 + 1, M_4 + 1$	$M_{10} + 1, M_{12} + 1$
M_6	$M_{11}, M_{13} + 2$	$M_3, M_5 + 2$
M_7	$M_3 + 1, M_5 + 1$	$M_{11} + 1, M_{13} + 1$
M_8	$M_{12}, M_{10} + 2$	$M_4, M_2 + 2$
M_9	$M_2 + 1, M_4 + 1$	$M_{10} + 1, M_{12} + 1$
M_{10}	$M_{13}, M_{11} + 2$	$M_5, M_3 + 2$
M_{11}	$M_3 + 1, M_5 + 1$	$M_{11} + 1, M_{13} + 1$
M_{12}	$M_6, M_0 + 2$	$M_{14}, M_8 + 2$
M_{13}	$M_8 + 1, M_{14} + 1$	$M_0 + 1, M_6 + 1$
M_{14}	$M_7, M_1 + 2$	$M_{15}, M_9 + 2$
M_{15}	$M_9 + 1, M_{15} + 1$	$M_1 + 1, M_7 + 1$

Table 4.

From table 3 and 4 it follows that except for the first bit

$$PR 1 \equiv PR 13$$

$$PR 3 \equiv PR 15$$

$$PR 5 \equiv PR 9$$

$$PR 7 \equiv PR 11$$

and except for the second bit,

$$PR 7 \equiv PR 15$$

$$PR 3 \equiv PR 11$$

$$PR 10 \equiv PR 2$$

$$PR 6 \equiv PR 14$$

so the first two bits excluded,

$$PR 3 \equiv PR 15 \equiv PR 7 \equiv PR 11.$$

From the above we can see that for this code, we need 9 different pathregisters.

If we analyse the transitions to each state, we can deduce the first two bits for each pathregister, as listed in table 5.

pathregisters	first two bits
0,6,11,13	00
3,5,8,14	01
1,7,10,12	10
2,4,9,15	11

Table 5.

Candidates as survivor for state 0 are the states 0,6,8 and 14. The first two bits of the corresponding pathregisters are given in table 5, hence, no storage elements are needed for these stages. The organization for pathregister 0 is then given in fig. 7.

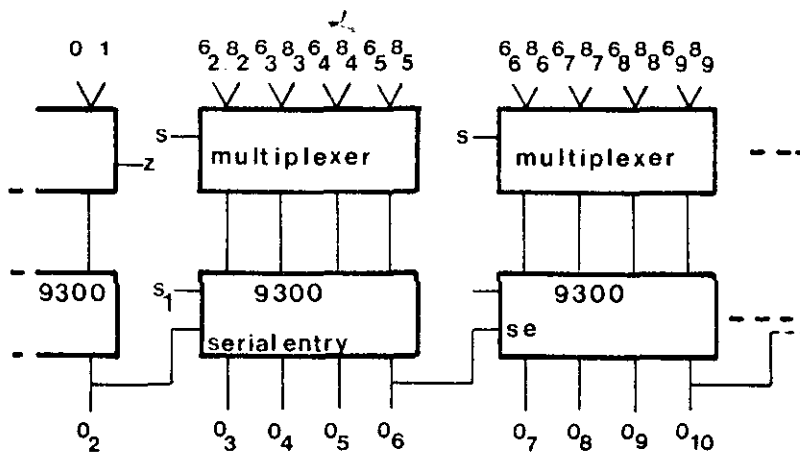


Fig. 7.

Instead of the one out of four multiplexers, we can use here quad-two multiplexers, because $PR\ 6 \equiv PR\ 14$, and if state 0 is selected as a survivor, we can simply shift the contents of pathregister 0 one place to the right. Note that 0_2 is 0 or 1, respectively, according to whether $z = 0$ or 1.

The same kind of organization is valid of PR 4 and PR 15. So there is no additional hardware needed for these pathregisters. For PR(1,2,5,6,8,12), we need one out of three multiplexers. The total amount of hardware, required for the 9 pathregisters, is much less than with classical Viterbi decoding.

Code 2.

The "0-equivalent" state is

$$S_0 = 1,0,0,0$$

$$1,1,1,1$$

The D-polynomials are

$$D_2 = \alpha^3$$

$$D_1 = 1 + \alpha + \alpha^2 + \alpha^3$$

The state table is given in table 6.

State	new state				z				\bar{w}			
	n_1, n_2				n_1, n_2				\bar{n}_1, \bar{n}_2			
	00	01	11	10	00	01	11	10	00	01	11	10
0	0	1	14	15	0	1	0	1	0	0	1	1
1	2	3	12	13	1	0	1	0	0	0	1	1
2	4	5	10	11	0	1	0	1	0	0	1	1
3	6	7	8	9	1	0	1	0	0	0	1	1
4	8	9	6	7	1	0	1	0	1	1	0	0
5	10	11	4	5	0	1	0	1	1	1	0	0
6	12	13	2	3	1	0	1	0	1	1	0	0
7	14	15	0	1	0	1	0	1	1	1	0	0
8	0	1	14	15	1	0	1	0	0	0	1	1
9	2	3	12	13	0	1	0	1	0	0	1	1
10	4	5	10	11	1	0	1	0	0	0	1	1
11	6	7	8	9	0	1	0	1	0	0	1	1
12	8	9	6	7	0	1	0	1	1	1	0	0
13	10	11	4	5	1	0	1	0	1	1	0	0
14	12	13	2	3	0	1	0	1	1	1	0	0
15	14	15	0	1	1	0	1	0	1	1	0	0

Table 6.

According to state table 6 we can make a metric calculation table.

Metric	z = 0	z = 1
$M_p(k+1)$	$\min [M_i(k) + c_i, M_j(k) + 2 - c_i]$	$\min [M_m(k) + c_m, M_n(k) + 2 - c_m]$
M_0	$M_0, M_7 + 2$	$M_8, M_{15} + 2$
M_1	$M_8 + 1, M_{15} + 1$	$M_0 + 1, M_7 + 1$
M_2	$M_9, M_{14} + 2$	$M_1, M_6 + 2$
M_3	$M_1 + 1, M_6 + 1$	$M_9 + 1, M_{14} + 1$
M_4	$M_2, M_5 + 2$	$M_{10}, M_{13} + 2$
M_5	$M_{10} + 1, M_{13} + 1$	$M_2 + 1, M_5 + 1$
M_6	$M_{11}, M_{12} + 2$	$M_3, M_4 + 2$
M_7	$M_3 + 1, M_4 + 1$	$M_{11} + 1, M_{12} + 1$
M_8	$M_{12}, M_{11} + 2$	$M_4, M_3 + 2$
M_9	$M_4 + 1, M_3 + 1$	$M_{11} + 1, M_{12} + 1$
M_{10}	$M_5, M_2 + 2$	$M_{13}, M_{10} + 2$
M_{11}	$M_{10} + 1, M_{13} + 1$	$M_2 + 1, M_5 + 1$
M_{12}	$M_{14}, M_9 + 2$	$M_6, M_1 + 2$
M_{13}	$M_6 + 1, M_1 + 1$	$M_9 + 1, M_{14} + 1$
M_{14}	$M_7, M_0 + 2$	$M_{15}, M_8 + 2$
M_{15}	$M_8 + 1, M_{15} + 1$	$M_7 + 1, M_0 + 1$

Table 7.

From tables 6 and 7 it follows that except for the first bit

$$PR(1) \equiv PR(15)$$

$$PR(3) \equiv PR(13)$$

$$PR(5) \equiv PR(11)$$

$$PR(7) \equiv PR(9)$$

So we need 12 different pathregisters.

It is obvious that the organization for the pathregisters is more complicated than for the first code. The advantage of this code is that there are only 1784 metric combinations.

Code 3.

The "0-equivalent" state is

$$S_0 = 1,0,0,0$$

$$1,1,0,0$$

The D-polynomials are:

$$D_2 = \alpha$$

$$D_1 = 1 + \alpha$$

The state table is given in table 8.

State	new state				z				\bar{w}			
	n_1, n_2				n_1, n_2				\hat{n}_1, \hat{n}_2			
	00	01	11	10	00	01	11	10	00	01	11	10
0	0	1	2	3	0	1	0	1	0	0	1	1
1	2	3	0	1	0	1	0	1	1	1	0	0
2	4	5	6	7	1	0	1	0	0	0	1	1
3	6	7	4	5	1	0	1	0	1	1	0	0
4	8	9	10	11	1	0	1	0	0	0	1	1
5	10	11	8	9	1	0	1	0	1	1	0	0
6	12	13	14	15	0	1	0	1	0	0	1	1
7	14	15	12	13	0	1	0	1	1	1	0	0
8	0	1	2	3	1	0	1	0	0	0	1	1
9	2	3	0	1	1	0	1	0	1	1	0	0
10	4	5	6	7	0	1	0	1	0	0	1	1
11	6	7	4	5	0	1	0	1	1	1	0	0
12	8	9	10	11	0	1	0	1	0	0	1	1
13	10	11	8	9	0	1	0	1	1	1	0	0
14	12	13	14	15	1	0	1	0	0	0	1	1
15	14	15	12	13	1	0	1	0	1	1	0	0

Table 8.

According to table 8 we can make the metric calculation table.

Metric	z = 0	z = 1
$M_P(k+1)$	$\min [M_i(k) + c_i, M_j(k) + 2 - c_i]$	$\min [M_m(k) + c_m, M_n(k) + 2 - c_m]$
M_0	$M_0, M_1 + 2$	$M_8, M_9 + 2$
M_1	$M_8 + 1, M_9 + 1$	$M_0 + 1, M_1 + 1$
M_2	$M_1, M_0 + 2$	$M_9, M_8 + 2$
M_3	$M_8 + 1, M_9 + 1$	$M_0 + 1, M_1 + 1$
M_4	$M_{10}, M_{11} + 2$	$M_2, M_3 + 2$
M_5	$M_2 + 1, M_3 + 1$	$M_{10} + 1, M_{11} + 1$
M_6	$M_{11}, M_{10} + 2$	$M_3, M_2 + 2$
M_7	$M_2 + 1, M_3 + 1$	$M_{10} + 1, M_{11} + 1$
M_8	$M_{12}, M_{13} + 2$	$M_4, M_5 + 2$
M_9	$M_4 + 1, M_5 + 1$	$M_{12} + 1, M_{13} + 1$
M_{10}	$M_{13}, M_{12} + 2$	$M_5, M_4 + 2$
M_{11}	$M_4 + 1, M_5 + 1$	$M_{12} + 1, M_{13} + 1$
M_{12}	$M_6, M_7 + 2$	$M_{14}, M_{15} + 2$
M_{13}	$M_{14} + 1, M_{15} + 1$	$M_6 + 1, M_7 + 1$
M_{14}	$M_7, M_6 + 2$	$M_{15}, M_{14} + 2$
M_{15}	$M_{14} + 1, M_{15} + 1$	$M_6 + 1, M_7 + 1$

Table 9.

From tables 8 and 9 it follows that except for the first bit

$$PR(1) \equiv PR(3)$$

$$PR(5) \equiv PR(7)$$

$$PR(9) \equiv PR(11)$$

$$PR(13) \equiv PR(15)$$

So we need 12 different pathregisters.

As for code 1 we can now determine the first three bits for each PR, which is listed below.

PR	first three bits.
0,1	0 0 0
8,9	0 0 1
4,5	0 1 0
12,13	0 1 1
6,7	1 1 0
14,15	1 1 1
2,3	1 0 0
10,11	1 0 1

Table 10.

Conclusions.

The code which yields the syndrome decoder with the minimum amount of hardware is code 1, because the number of pathregisters is only 9 and the metric table contains 1817 entries.

This code has a good error correcting capability as can be seen from fig. 8 , and the amount of hardware of the syndrome decoder is significantly smaller than that of the classical Viterbi decoder.

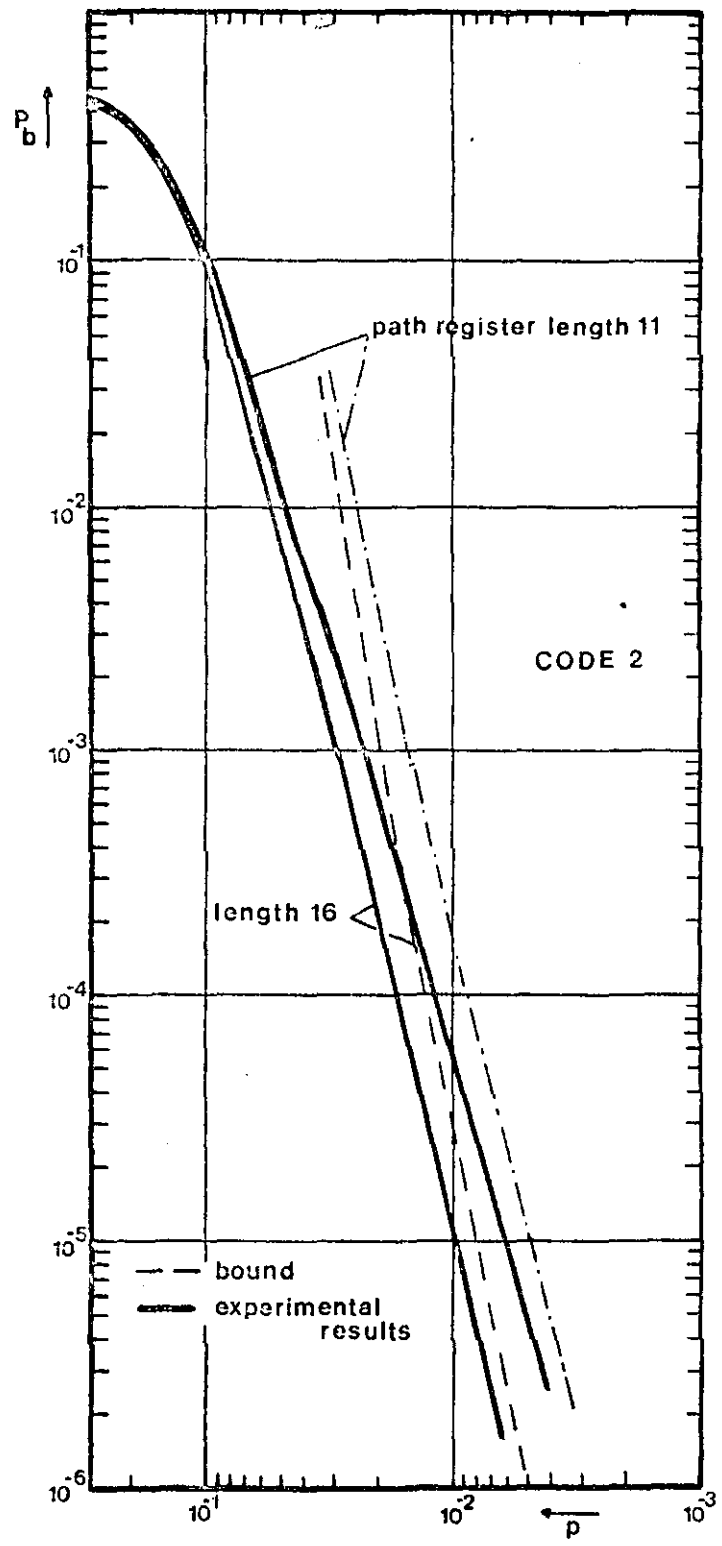
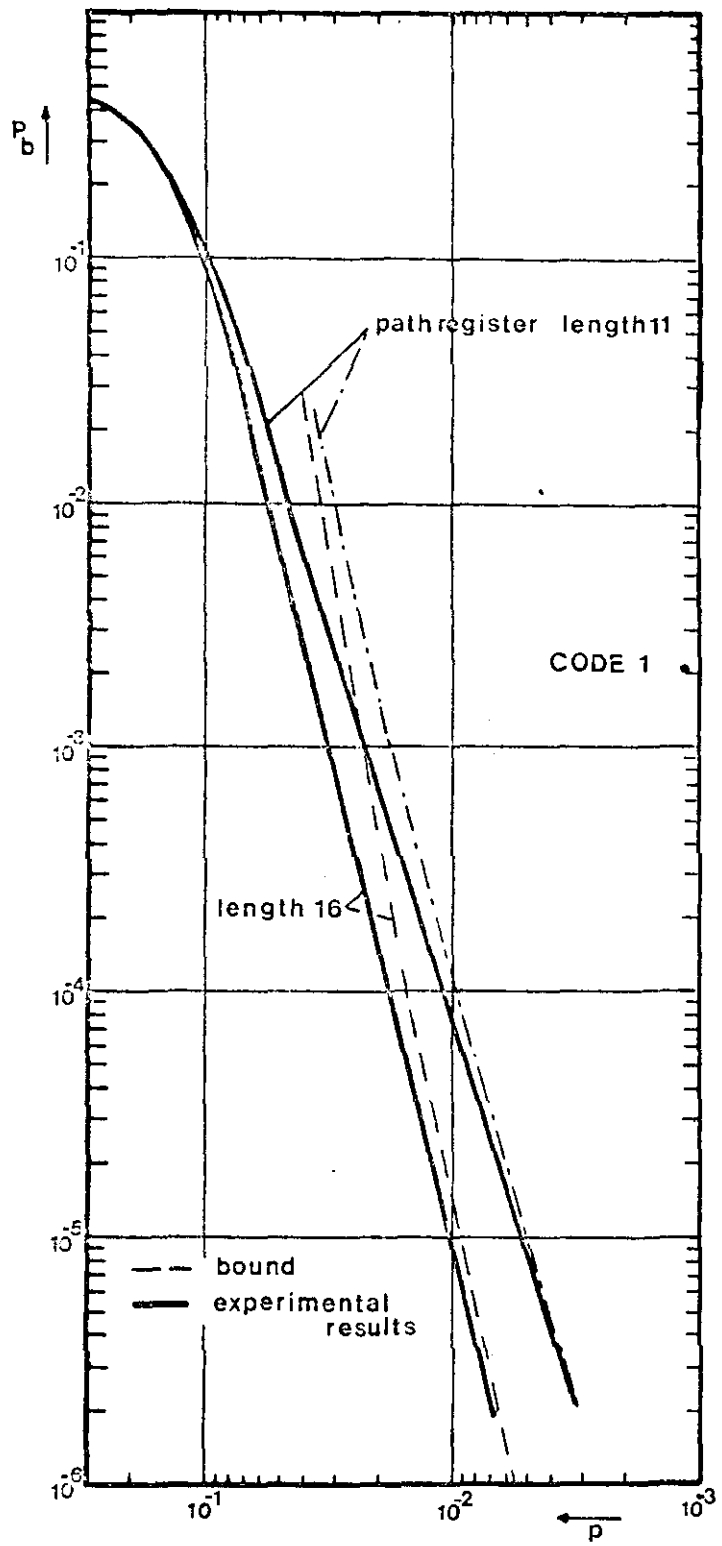


Fig. 8. Bit error rate P_b versus channel transition probability p .

REFERENCES

1. J.P.M.Schalkwijk and A.J.Vinck, "Syndrome decoding of convolutional codes", IEEE Trans. on Communications, to be published.
2. L.v.d.Meeberg, "A tightened upper bound on the error probability of binary convolutional codes with Viterbi decoding", IEEE Trans. Inform. Theory, vol. IT-20, pp. 389-391, May 1974.

Appendix A: computer simulation

```

*
* SINSERT MACVIT
*
* THIS IS A COMPUTER SIMULATION FOR A K=5 SYNDROME DECODER
* *****
*   GENERAL COMMENT
* *****
* THIS PROGRAM IS BASED ON 12 PATHREGISTERS
* TO HAVE A PROGRAM WHICH CAN BE USED FOR THE OTHER K=5 ENCODERS
  REL
*
* THE CONNECTION POLYNOMIALS ARE      10011 AND 10111
*
* THERE ARE 1817 DISTINCT METRIC COMBINATIONS
* MINIMUM DISTANCE IS 7
* TOTAL NUMBER OF BITERRORS IN THE PATHS ON DISTANCE 7 AND 8 IS
*   17
* THE '0 EQUIVALENT STATE' IS          1000
*                                       1011
*
* THE D POLYNOMIALS ARE                D2= 111
*                                       D1= 011
* A TABLE HAS BEEN MADE, IN WHICH THE ROWNUMBERS CORRESPONDEND WITH
* A METRICCOMBINATION
* ALSO THE INFORMATION FOR THE PATHREGISTERFILLING AND NEW MINIMUM
* IS PRESENT IN THIS TABLE
*
* *****
*
* *****
START NOP
* WHAT IS THE CHANNEL ERROR PROBABILITY
BEGL  NOP
      CALL VINI
      DAC PIET
      DAC RNDI
      DAC INSTV
      DAC EINDV
      DAC HUL
      DAC TOTAL
      DAC O
* A SUBROUTINE IS CALLED TO GIVE THE DESIRED ERROR PROBABILITY
* AND THE NUMBER OF BITERRORS THAT HAS TO BE MADE IN THE DECODING
* PROCEDURE
* THIS IS A STARTING PROCEDURE TO HAVE A DECREASING ERROR PROBABILITY
* WITH STARTING VALUE PIET AND STEPS INSTV UNTIL THE VALUE
* EINDV IS REACHED
* TOTAL IS THE NUMBER OF BITERRORS THAT HAS TO BE MADE EACH RUN
* RNDI AND HUL ARE VARIABLES FOR THE RANDOM GENERATOR
* *****

```

* GET THE TABLES FOR THE PATHREGISTER FILLING AND METRIC COMBINATIONS

*

CALL PR0G
DAC Z0ITE
CALL PR01
DAC Z1ITE
CALL PR02
DAC PVOTE
CALL PR03
DAC PVITE

* IN Z0ITE ARE THE NEW RZNUMBERS FOR Z=0

* IN Z1ITE ARE THE R0NUMBERS FOR Z=1

* IN PVOTE IS STORED THE PATHREGISTER FILLING INFORMATION FOR Z=0

* THE LAST FOUR BITS OF EACH WORD INDICATE THE NEW MINIMUM

* THE SAME FOR Z=1 IN PVITE

BEG NZP

* GET A CHANNEL NOISE BIT

CALL RNDG
DAC RNDM
DAC PIET
DAC NOISE
DAC CNT
DAC 0

*

* A RANDOM SEQUENCE IS CALLED

* TO GIVE A NOISY BIT WITH PROBABILITY PIET

* THE BIT IS STORED IN NOISE

* A CONTROL IS MADE IF SOMETHING IS WRONG WITH THE GENERATOR

* THATS WHY A VARIABLE CNT IS USED

* RNDM IS A CONSTANT NEEDED TO FORM THE RANDOM SEQUENCE

* FORM THE CORRUPTED INPUT N1

LDA NOISE

* THE LAST BIT OF WORD NOISE IS ONE OR ZERO WITH PROBABILITY PIET

* SO IT HAS THE FORM 000000000000000X

ALL 5

* NOISE SHIFTED 5 PLACES TO THE LEFT GIVES

000000000X00000

* IN THE SYNDROME FORMER N1 TAKES THE 5 RIGHT PLACES

LIKE

0000000000XXXXX

* NOW ADD SHIFTED NOISE BIT TO HAVE

0000000000XXXXXX

ADD N1

* SHIFT ONE PLACE TO THE RIGHT TO HAVE THE CORRECT CONTENT

* OF THE SYNDROME REGISTER WHICH IS

0000000000XXXXX

ARL 1

STA N1

* STORE N1 FOR THE NEXT TRANSMISSION

*

* FORM THE PRODUCT N1C2

ANA C2

STA N1C2

* SAVE THE RESULT

```
* GET A CHANNEL NOISE BIT
CALL RND5
DAC RNDM
DAC PIET
DAC NOISE
DAC CNT
DAC 0
```

```
*****
```

```
* FORM THE INPUT N2
```

```
LDA NOISE
ALL 5
ADD N2
```

```
* NOW FORM THE PRODUCT N2C1
```

```
APL 1
STA N2
ANA C1
```

```
* FORM THE EXCLUSIVE OR OF N1C2+N2C1
```

```
ERA N1C2
STA N1C2
```

```
* LOOK IN TABLE PART IF THE SYNDROME Z=1 OR 0.
```

```
* IN TABLE PART THE PARITY OF 5 BITS ARE MADE
```

```
* BECAUSE THIS IS NOT POSSIBLE WITH THE COMPUTER IN AN EASY WAY
```

```
*
```

```
LDX N1C2
LDA PART,1
STA Z
```

```
*****
```

```
* SYNDROME Z IS FORMED
```

```
*****
```

```
* NOW FORM THE PRODUCT N2D2
```

```
LDA N2
ANA D2
STA N2D2
```

```
* NOW FORM THE PRODUCT N1D1
```

```
LDA N1
ANA D1
```

```
* FORM THE EXCLUSIVE OR OF N1D1+N2D2
```

```
ERA N2D2
STA N2D2
```

```
* LOOK IN TABLE PART IF W=0 OR W=1
```

```
LDX N2D2
LDA PART,1
```

```
* SHIFT W IN W1 TO HAVE A DECODING DELAY
```

```
IAB
LDA W1
IAB
LRL 1
IAB
STA W1
```

```
*****
```

```
* N1D1+N2D2 IS FORMED
```

```
*****
```

```

*
* GET NEW METRIC AND PATHREGISTER INFORMATION
*
  LDA Z
  SAS 16
  JMP P0
* INFORMATION FOR Z=1
*
* OLD ROW NUMBER IS STORED IN BMET
* LOAD X REG WITH BMET
  LDX BMET
* LOOK IN TABLE ZIITE ON ROW BMET TO HAVE NEW ROWNUMBER
  LDA ZIITE,1
  STA NWMET
* IN NWMET IS NOW THE NEW ROWNUMBER
* LOOK IN TABLE PVITE TO HAVE THE CORRESPONDING
* PATHREGISTER INFORMATION
* STORE THIS IN PADDR
  LDA PVITE,1
  STA PADDR
  JMP P1
P0  LDX BMET
* SAME COMMENT AS FOR Z=1
*
* INFORMATION FOR Z=0
*
  LDA ZOITE,1
  STA NWMET
  LDA PVOTE,1
  STA PADDR
P1  LDA NWMET
  STA BMET
*
* IN BMET IS NOW THE NEW ROWNUMBER FOR THE NEXT TRANSMISSION
*
*****
* NEW ROWNUMBER(METRICCOMBINATION)AND PATH REGISTER FILLING INFORMATION
* IS READY
*****

```

* REFILL PATH REGISTERS

```
LDA Z
SAS 16
JMP ZIS0
```

*

* THE PATH REGISTERS MUST BE REFILLED FOR Z = 1
 * THE INFORMATION IS STORED IN PADRV
 * FOR INSTANCE PADO IS FILLED WITH PATH 8 OR PATH 14
 * THIS DEPENDS ON THE FIRST BIT IN PADRV
 * JUMP + 2 MEANS THAT THE FIRST BIT OF PADO
 * IS 0, JUMP +1 MEANS THAT THIS BIT IS A 1
 * THE CONTENT OF PADO IS TEMPORARELY STORED IN PADH0
 * BECAUSE THE OLD INFORMATION OF PADO IS NEEDED IN THE
 * FOLLOWING PATHREGISTER FILLINGS

*

*

* THE SAME GOES FOR THE OTHER PATHREGISTERS
 PRV AFTER SKIP ON BIT 1 THE CONTENT ;
 OF PADH0 IS PAD8 OR PAD14 AND JUMP +2

*

*

PRV AFTER SKIP ON BIT 2 THE CONTENT ;
 OF PADH1 IS PADO OR PAD6 AND JUMP +1

*

```
SSP
STA PADH13
```

*

* PATH 1 IS EQUAL TO PATH 13 EXCEPT FOR THE FIRST BIT WHICH IS A C

*

*

PRV AFTER SKIP ON BIT 3 THE CONTENT ;
 OF PADH2 IS PAD9 OR PAD15 AND JUMP +1

*

*

PRV AFTER SKIP ON BIT 4 THE CONTENT ;
 OF PADH3 IS PAD1 OR PAD7 AND JUMP +2

*

```
SSM
STA PADH15
```

*

```

*
PRV AFTER SKIP ON BIT 5 THE CONTENT ;
OF PADH4 IS PAD2 OR PAD4 AND JUMP +1
*
*
PRV AFTER SKIP ON BIT 6 THE CONTENT ;
OF PADH5 IS PAD10 OR PAD12 AND JUMP +2
*
SSM
STA PADH9
*
*
PRV AFTER SKIP ON BIT 7 THE CONTENT ;
OF PADH6 IS PAD3 OR PAD5 AND JUMP +2
*
*
PRV AFTER SKIP ON BIT 8 THE CONTENT ;
OF PADH7 IS PAD11 OR PAD13 AND JUMP +1
*
SSP
STA PADH11
*
*
PRV AFTER SKIP ON BIT 9 THE CONTENT ;
OF PADH8 IS PAD4 OR PAD2 AND JUMP +2
*
*
PRV AFTER SKIP ON BIT 10 THE CONTENT ;
OF PADH10 IS PAD5 OR PAD3 AND JUMP +1
*
*
PRV AFTER SKIP ON BIT 11 THE CONTENT ;
OF PADH12 IS PAD14 OR PAD8 AND JUMP +1
*
*
PRV AFTER SKIP ON BIT 12 THE CONTENT ;
OF PADH14 IS PAD15 OR PAD9 AND JUMP +2
*
*
JMP HAN
*
* THE PATHREGISTER FILLING FOR Z=1 IS DONE
*

```

*
*
* Z=0 FILL THE PATH REGISTERS
*

ZIS0 NOP

PRV AFTER SKIP ON BIT 1 THE CONTENT ;
OF PADH0 IS PAD0 OR PAD6 AND JUMP +2

*
*

PRV AFTER SKIP ON BIT 2 THE CONTENT ;
OF PADH1 IS PAD8 OR PAD14 AND JUMP +1

*

SSP
STA PADH13

*
*

PRV AFTER SKIP ON BIT 3 THE CONTENT ;
OF PADH2 IS PAD1 OR PAD7 AND JUMP +1

*
*

PRV AFTER SKIP ON BIT 4 THE CONTENT ;
OF PADH3 IS PAD9 OR PAD15 AND JUMP +2

*

SSM
STA PADH15

*
*

PRV AFTER SKIP ON BIT 5 THE CONTENT ;
OF PADH4 IS PAD10 OR PAD12 AND JUMP +1

*
*

PRV AFTER SKIP ON BIT 6 THE CONTENT ;
OF PADH5 IS PAD2 OR PAD4 AND JUMP +2

*

SSI
STA PADH9

*
*

PRV AFTER SKIP ON BIT 7 THE CONTENT ;
OF PADH6 IS PAD11 OR PAD13 AND JUMP +2

*
*

PRV AFTER SKIP ON BIT 8 THE CONTENT ;
OF PADH7 IS PAD3 OR PAD5 AND JUMP +1

*

SSP
STA PADH11

*
*

PRV AFTER SKIP ON BIT 9 THE CONTENT ;
OF PADH8 IS PAD12 OR PAD10 AND JUMP +2

*
*

PRV AFTER SKIP ON BIT 10 THE CONTENT ;
OF PADH10 IS PAD13 OR PAD11 AND JUMP +1

*
*

PRV AFTER SKIP ON BIT 11 THE CONTENT ;
OF PADH12 IS PAD6 OR PAD0 AND JUMP +1

*
*

PRV AFTER SKIP ON BIT 12 THE CONTENT ;
OF PADH14 IS PAD7 OR PAD1 AND JUMP +2

*

* FILLING OF ORIGINAL PATHREGISTERS
 * BECAUSE THE PATH REGISTERS ARE STORED IN HELP PATHS
 * WE NOW HAVE TO CHANGE THIS TO MAKE THE PATHREGISTERS READY FOR
 * THE NEXT TRANSMISSION

```
HAN IMA PADH0
    IMA PAD0
    IMA PADH1
    IMA PAD1
    IMA PADH2
    IMA PAD2
    IMA PADH3
    IMA PAD3
    IMA PADH4
    IMA PAD4
    IMA PADH5
    IMA PAD5
    IMA PADH6
    IMA PAD6
    IMA PADH7
    IMA PAD7
    IMA PADH8
    IMA PAD8
    IMA PADH9
    IMA PAD9
    IMA PADH10
    IMA PAD10
    IMA PADH11
    IMA PAD11
    IMA PADH12
    IMA PAD12
    IMA PADH13
    IMA PAD13
    IMA PADH14
    IMA PAD14
    IMA PADH15
    IMA PAD15
```

* PATHS ARE REFILLED

* LOOK FOR BITERROR

* LOCATE MINIMUM, STORED IN PADRV IN LAST 4 BITS

```
LDA PADRV
ANA MASK
AOA
STA HULP
LDX HULP
```

*
 * HULP PLACES BEHIND PADA THE PATH WHICH HAS TO BE THE MINIMUM
 * IS STORED

*

```
LDA PADA+1
```

* COMPARE WITH ORIGINAL W1 FOR CONTROL ON BITERROR

```
ERA W1
ANA LTSTE
SAS II
JMP END
```

* A BITERROR IS MADE SO INCREASE ABITF

```
LDA ABITF
AOA
STA ABITF
```

* DECODING IS DONE

* ORGANIZATION PART FOR NUMBER OF TRANSMISSIONS
* AND POSSIBLY NEW CHANNEL ERROR PROBABILITY

CAS TOTAL

*
* IF THE TOTAL NUMBER OF BITERRORS IS EQUAL TO THE NUMBER IN TOTAL
* WE HAVE TO START A NEW VALUE FOR THE CHANNEL ERROR PROBABILITY
* ELSE GO ON WITH THE PRESENT CHANNEL ERROR PROBABILITY
* FOR A NEW CHANNEL ERROR PROBABILITY JMP TO NWESE
*

JMP NWESE
JMP NWESE
JMP GGD
END NOP
GGD LDA CNT

*
* A CONTROL IS MADE IF SOMETHING IS WRONG WITH THE RANDOM GENERATOR
* IF SO, WE START A NEW CHANNEL ERROR PROBABILITY
*

SAS 16
JMP IRSAT
JMP NWESE
IRSAT LDA ATRSM
AOA
STA ATRSM
CAS TIEND
JMP HOGOP
JMP HOGOP

* IF THE NUMBER OF TRANSMISSIONS IS GREATER THAN TIEND WE INCREMENT TIE

D

* AND MAKE ATRSM ZERO
* IN TIND IS NOW THE NUMBER OF TRANSMISSIONS TIMES TIEND
* THE REST IS IN ATRSM
*

JMP BEG
HOGOP IRS TIND
ORA
STA ATRSM
JMP BEG
NWESE NOP
CALL VBI
DAC PIET
DAC TIND
DAC ATRSM
DAC ABITF
DAC O

* A SUBROUTINE IS CALLED TO GET THE OUTPUT
* THE OUTPUT IS 1) THE CHANNEL ERROR PROBABILITY
* 2) THE TOTAL NUMBER OF TRANSMISSIONS
* 3) THE NUMBER OF BITERRORS TO BE MADE
*

*
*
* A SUBROUTINE IS CALLED TO MAKE A NEW CHANNEL
* ERROR PROBABILITY
* A CONTROL IS MADE IF THE VALUE UNTIL WHICH THE PROBABILITY HAS TO GO
* IS REACHED
*

CALL NUEN
DAC PIET
DAC EINDV
DAC INSTV
DAC RUDM
DAC HUL
DAC CONTR
DAC O
CRA

* RESET THE VALUES OF ATRSM, TIND, ABITF
*
* MAKE THE CONTROL FOR THE LAST PROBABILITY
*

STA ATRSM
STA TIND
STA ABITF
LDA CONTR
CAS NUL
JMP LEO
JMP BEG
JMP BEG
NOP
JMP LES

* DATA DEFINITIONS , END OF PROGRAM

COUNT DATA 0
N1 DATA 0
N2 DATA 0
C2 DATA '23
N1C2 DATA 0
MIN DATA 0
C1 DATA '27
Z DATA 0
D2 DATA '34
D1 DATA '14
N2D2 DATA 0
VI DATA 0
EMET DATA 1
HULP DATA 0
TOTAL DATA 0
TIEND DATA 10000
TIND DATA 0
EINDV DATA 1.EE-3
INSTV DATA 1.EE-3
HUL DATA 1.EE-3
CONTR DATA 0
NUENET DATA 0
PADRV DATA 0
NULSE DATA 0
HUP DATA -30000
BAS DATA -30000
PART DATA 0
*

*

DATA '1
DATA '1
DATA '0
DATA '1
DATA '0
DATA '0
DATA '1
DATA '1
DATA '0
DATA '0
DATA '1
DATA '0
DATA '1
DATA '1
DATA '0
DATA '1
DATA '0
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DATA '1
DATA '0
DATA '1
DATA '1
DATA '0
DATA '0
DATA '1
DATA '1
DATA '0
DATA '1
DATA '0
DATA '0
DATA '1

PADH0 DATA 0
PADH1 DATA 0
PADH2 DATA 0
PADH3 DATA 0
PADH4 DATA 0
PADH5 DATA 0
PADH6 DATA 0
PADH7 DATA 0
PADH8 DATA 0
PADH9 DATA 0
PADH10 DATA 0
PADH11 DATA 0
PADH12 DATA 0
PADH13 DATA 0
PADH14 DATA 0
PADH15 DATA 0
PADA DATA 0
PAD0 DATA 0
PAD1 DATA 0
PAD2 DATA 0

```
*
PAD3 DATA 0
PAD4 DATA 0
PAD5 DATA 0
PAD6 DATA 0
PAD7 DATA 0
PAD8 DATA 0
PAD10 DATA 0
PAD12 DATA 0
PAD14 DATA 0
PAD9 DATA 0
PAD11 DATA 0
PAD13 DATA 0
PAD15 DATA 0
MASK DATA '17
LTSTE DATA '40
Z1 DATA 0
HLP1 DATA 0
NUL DATA 0
CØNT DATA 0
RNDM DATA 5.EE-1
PIET DATA 1.EE-3
TØT DATA 0
X DATA 0.0
ALPHA DATA 0
MASK1 DATA 0
ABITE DATA 0
ATRS1 DATA -0
NETTAR DATA 0
ZOITE BSZ 1325
ZIITE BSZ 1325
PVOTE BSZ 1325
PVITE BSZ 1325
LEØ CALL EXIT
      END START
BOTTOM
```

THIS IS A MODEL FOR THE PATHREGISTER FILLING
 *PADHA IS FILLED WITH PADX OR PADY
 *THE FIRST BIT IS SET OR RESET, WHICH DEPENDS ON THE
 * VARIABLE GAMMA.

```

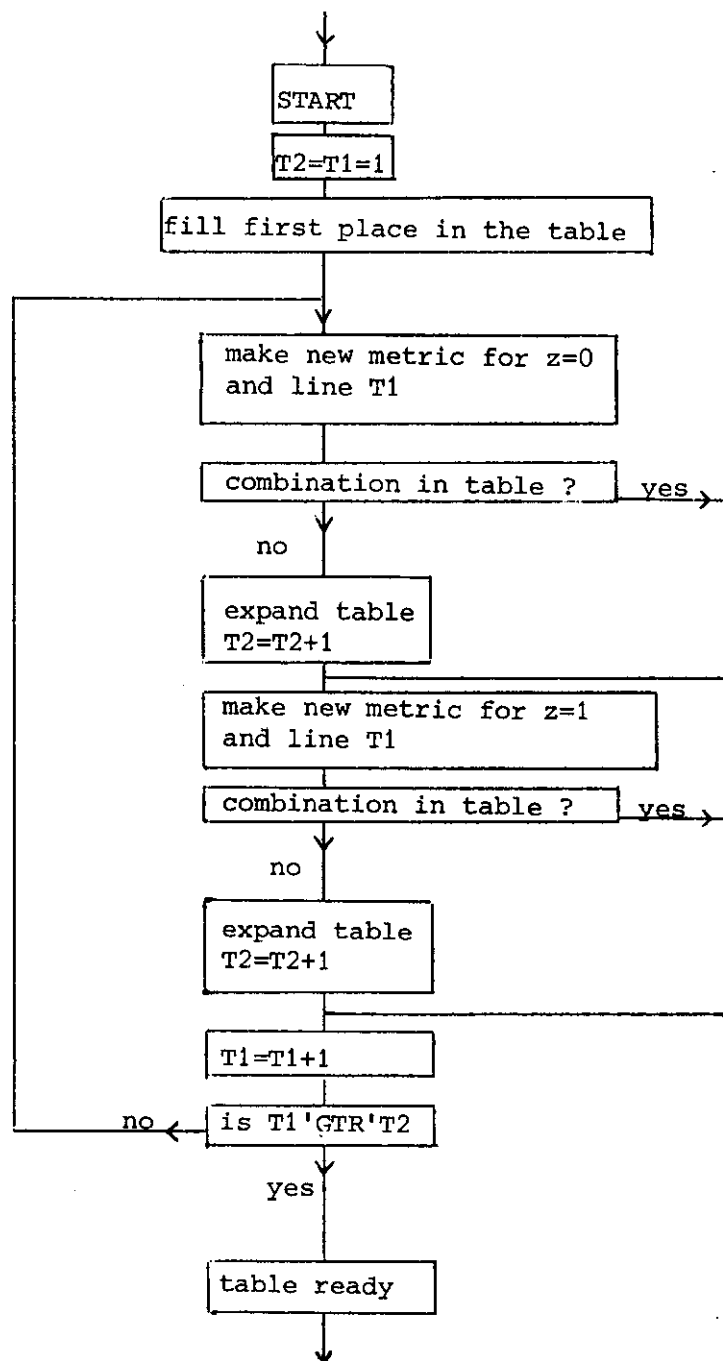
*
*
*****
*
*   MACRO VITL:  MACVIT
*
*****
*
*   MACRO NAME  :  PRV  (PAD REGISTER FOLLING)
*   MACRO CALL  :  PRV AFTER SKIP ON BIT BETA THE CONTENT
*                 OF PADHA IS PADX OR PADY AND JUMP GAMMA.
*   PARAMETERS  :  BETA, PADHA, PADX, PADY, GAMMA.
*   DUMMY WORDS :  AFTER, SKIP, ON, THE, CONTENT, AND.
*   ARG IDENTIFER: BIT, OF, IS, OR, JUMP.
*
*
*
PRV MAC (BIT)=1, (OF)=5, (IS)=2, (OR)=5, ;
      (JUMP)=4, AFTER, SKIP, ON, THE, ;
      CONTENT, AND
      NLSM
      LDA  PADRV
      SAS  <1>
      JMP  &AA
      LDA  <2>
      JMP  &AB
&AA   LDA  <3>
&AB   LRL  1
      JMP  *<4>
      SSM
      STA  <5>
      ENDM

```

* THIS SIMULATION IS DONE WITH A PRIME-300 MINI COMPUTER

Appendix B: Generation of syndrome metric table

Block scheme for the generation of the syndrome table.



E / E C A / S Y N O R D M E T A B L E
 = = = = =

```
'BEGIN' 'FILE' 'I'(KIND=READER),OUT:
'INTEGER' AA,BB,C,J,V,T,X,K,L;
'INTEGER' T2,T1;
'REAL' P,PP;
'INTEGER' G,H,J,M,F;
'INTEGER' I;
'REAL' 'ARRAY'                                011:199  ];
'INTEGER' 'ARRAY' S[0:11],D[0:11]              .AB[0:15];
'INTEGER' 'ARRAY'                               A[0:11],BA[0:15];
'INTEGER' SS;
'INTEGER' 'PROCEDURE' METRIC0(S,D,AB);
  'INTEGER' 'ARRAY' S[0],D[0],AB[0];
'BEGIN' 'INTEGER' AA,BB,C,CC,J,V,FF;
      'INTEGER' 'PROCEDURE' M(AA,BB,FF);
'VALUE' AA,BB; 'INTEGER' AA,BB,FF;
'BEGIN' 'INTEGER' P;
P:=AA;
'IF' BB<P 'THEN' P:=BB;
M:=P;
'END';
FF:=1;
S[0]:=M(D[0],D[1]+2,FF);
S[1]:=M(D[6]+1,D[7 ]+1,FF);
S[2]:=M(D[1],D[0]+2,FF);
S[3]:=M(D[8],D[7]+2,FF);
S[4]:=M(D[1 ]+1,D[2]+1,FF);
S[5]:=M(D[7],D[8]+2,FF);
S[6 ]:=M(D[9] ,D[10]+2,FF);
S[7 ]:=M(D[3 ]+1,D[4]+1,FF);
S[8 ]:=M(D[10] ,D[9]+2,FF);
S[9 ]:=M(D[5 ] ,D[4]+2,FF);
S[10]:=M(D[10]+1,D[11]+1,FF);
S[11]:=M(D[4 ] ,D[5 ]+2,FF);
C:=S[0]; 'FOR' U:=1 'STEP' 1 'UNTIL' 11 'DO' 'BEGIN'
'IF' S[U] 'LESS' C 'THEN'          C:=S[U];
'END';
'FOR' V:=0 'STEP' 1 'UNTIL' 11 'DO' S[V]:=S[V]-C;
'END';
'INTEGER' 'PROCEDURE' METRIC1(S,D,BA);
  'INTEGER' 'ARRAY' S[0],D[0],BA[0];
'BEGIN' 'INTEGER' AA,BB,C,J,V,FF,CC;
      'INTEGER' 'PROCEDURE' M(AA,BB,FF);
'VALUE' AA,BB; 'INTEGER' AA,BB,FF;
'BEGIN' 'INTEGER' P;
P:=AA;
'IF' BB<P 'THEN' P:=BB;
M:=P;
'END';
FF:=1;
S[0 ]:=M(D[6 ] ,D[7 ]+2,FF);
S[1 ]:=M(D[0 ]+1,D[1 ]+1,FF);
```

```

S[3 ]:=M(D[2 ] ,D[1 ]+2,FF);
S[4 ]:=M(D[7 ]+1,D[8 ]+1,FF);
S[5 ]:=M(D[1 ] ,D[2 ]+2,FF);
S[6 ]:=M(D[3 ] ,D[4 ]+2,FF);
S[7 ]:=M(D[9 ]+1,D[10]+1,FF);
S[8 ]:=M(D[4 ] ,D[3 ]+2,FF);
S[9 ]:=M(D[11] ,D[10]+2,FF);
S[10]:=M(D[4 ]+1,D[5 ]+1,FF);
S[11]:=M(D[10] ,D[11]+2,FF);
C:=S[0]:'FOR'U:=1'STEP'1'UNTIL'11'DO''BEGIN'
'IF'S[U]'LSS' C'THEN'          C:=S[U];
'END';
'FOR' V:=0'STEP'1'UNTIL'11'DO'S[V]:=S[V]-C;
'END';
'REAL' 'PROCEDURE' OPZOEKEN(O,T,P);
'VALUE'T,P;
'REAL' 'ARRAY'U[1];'INTEGER'T ;
'REAL'P;
'BEGIN''INTEGER' AAA,BBB,CCC;
BBB:=T;
'FOR'AAA:=1'STEP'1'UNTIL' BBB'DO'
'BEGIN''IF'O[AAA]=P'THEN''BEGIN'BBB:=0;CCC:=1'END'
'ELSE'CCC:=2;
'END';
OPZOEKEN:=CCC;
'END';
'REAL''PROCEDURE'GAR(A,P);
'VALUE'P;'REAL'P;'INTEGER''ARRAY'A[0];
'BEGIN''INTEGER'1,J;
'FOR'I:=1'STEP'1'UNTIL'12
'DO' A[I-1]:=P.[4*I-1:4];
'END';
'REAL''PROCEDURE'ARP(PP,A);
'INTEGER''ARRAY'A[0];
'REAL' PP;
'BEGIN''INTEGER'T;PP:=0;
'FOR'T:=1'STEP'1'UNTIL'12'DO'
PP:=PP&A[T-1][4*T-1:4];
'END';
'FOR'T1:=0'STEP'1'UNTIL'11'DO' READ(IN,/,A[T1]);
ARP(U[1],A);
T1:=T2:=1;
WRITE(OUT,<///,
"OLD-METRICCOMB----NEW-COMB-FOR-Z=0----NEW-COMB-FOR-Z=1">);
'FOR'T1:=1'STEP'1'UNTIL'T2'DO'
'BEGIN'
P:=O[T1];
GAR(A,P);
WRITE(OUT,</,"->);
'FOR'K:=0'STEP'1'UNTIL' 11'DO' WRITE(OUT,<11>,A[K]);
WRITE(OUT,< "-----">);
METRICO(S,A,AB);
'FOR'K:=0'STEP'1'UNTIL' 11'DO' WRITE(OUT,<11>,S[K]);
WRITE(OUT,< "-----">);
ARP(PP,S);
'IF'OPZOEKEN(O,T2,PP)=1
'THEN'
'ELSE''BEGIN'T2:=T2+1;O[T2]:=PP;'END';

```

```

METRIC(S,A,DA);
'FOR'K:=0'STEP'1'UNTIL' 11'DO' WRITE(OUT,<11>,S(K));
ARP(PP,S);
'IF'OPZOEKEN(0,T2,PP)=1
'THEN'
'ELSE''BEGIN'T2:=T2+1;O(T2):=PP;'END';
'IF'T2>19 7'THEN'T2:=0;
'END';
'END'.

```

first lines of table for code 3

```

OLD-METRICCOMB-----NEW-COMB-FOR-Z=0-----NEW-COMB-FOR-Z=1
-052434444443-----052434444443-----303143342233
-303143342233-----240214223344-----314230133343
-240214223344-----123201213240-----232032241424
-314230133343-----321323334042-----012230132203
-123201213240-----122331214110-----221322230012
-232032241424-----122022301122-----234223032432
-321323334042-----342423032332-----222031201123
-012230132203-----021223230012-----113231213210
-122331214110-----011220030002-----221222323021
-221322230012-----232022031222-----122001201120
-122022301122-----112130112232-----220212022232
-234223032432-----102132322221-----032433242233
-342423032332-----203122222221-----042234442233
-222031201123-----212130112133-----230212022322
-021223230012-----032022031322-----102001101120
-113231213210-----010210120002-----221321223021
-011220030002-----011022030012-----012111212210
-221222323021-----232322032222-----221021101021
-232022031222-----102022101121-----032223032232
-122001201120-----112130112110-----220212020012
-112130112232-----121221223032-----010110022102
-220212022232-----101101112120-----032032231223
-102132322221-----130212222223-----312230133132
-032433242233-----032234243343-----103122322232
-203122222221-----230212222222-----212230132132
-042234442233-----052234233443-----303123122232
-212130112133-----221221123042-----010110012202
-230212022322-----102101211120-----032032231222
-032022031322-----012133312232-----012223032232
-102001101120-----110110112110-----110210020012
-010210120002-----021012020011-----112011211210
-221321223021-----232322032122-----121021201011
-011022030012-----011022011222-----012111012231
-012111212210-----021221221111-----211221121021
-232322032222-----102122121121-----032233332232
-221021101021-----212120012122-----130112012122
-102022101121-----110110112222-----110210022132
-032223032232-----012233233332-----012233232233
-112130112110-----121221121012-----121221123011
-220212020012-----212012021221-----032012211221
-121221223032-----132322032132-----111021101112
-010110022102-----011212120011-----012031111210
-101101112120-----120211112110-----111120120012
-032032231223-----032133212233-----213223032332
-130212222223-----022101111120-----222032231322
-312230133132-----321323133032-----012130112102
-032234243343-----032334334443-----103132132233
-103122322232-----130212223232-----312230132233
-230212222222-----122101111120-----232032231222
-212230132132-----221223133032-----012120112102
-052234233443-----032333434443-----102133142233

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