

## Enforcing nondeterminism via linear time temporal logic specifications

*Citation for published version (APA):* Kuiper, R. (1987). *Enforcing nondeterminism via linear time temporal logic specifications*. (Computing science notes; Vol. 8705). Technische Universiteit Eindhoven.

Document status and date: Published: 01/01/1987

#### Document Version:

Publisher's PDF, also known as Version of Record (includes final page, issue and volume numbers)

#### Please check the document version of this publication:

• A submitted manuscript is the version of the article upon submission and before peer-review. There can be important differences between the submitted version and the official published version of record. People interested in the research are advised to contact the author for the final version of the publication, or visit the DOI to the publisher's website.

• The final author version and the galley proof are versions of the publication after peer review.

• The final published version features the final layout of the paper including the volume, issue and page numbers.

Link to publication

#### General rights

Copyright and moral rights for the publications made accessible in the public portal are retained by the authors and/or other copyright owners and it is a condition of accessing publications that users recognise and abide by the legal requirements associated with these rights.

- · Users may download and print one copy of any publication from the public portal for the purpose of private study or research.
- You may not further distribute the material or use it for any profit-making activity or commercial gain
  You may freely distribute the URL identifying the publication in the public portal.

If the publication is distributed under the terms of Article 25fa of the Dutch Copyright Act, indicated by the "Taverne" license above, please follow below link for the End User Agreement:

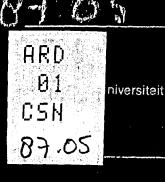
www.tue.nl/taverne

#### Take down policy

If you believe that this document breaches copyright please contact us at:

openaccess@tue.nl

providing details and we will investigate your claim.



5

Faculteit der Miskunde en informatica

Enforcing Nondeterminism via Linear Time Temporal Logic Specifications

Ruurd Kuiper

March 1987

87.05

Enforcing Nondeterminism

via

Linear Time Temporal Logic Specifications

Ruurd Kuiper

March 1987 87.95

#### COMPUTING SCIENCE NOTES

· · .

This is a series of notes of the Computing Science Section of the Department of Mathematics and Computing Science of Eindhoven University of Technology. Since many of these notes are preliminary versions or may be published elsewhere, they have a limited distribution only and are not for review.

Copies of these notes 'are available from the author or the editor.

n an struktur som en som skiller and som en som en som en som som som en som en som en som en som en som en so An en som en s An en som en s

en en en en la finita de la companya de la company Antenen de la companya de la company La companya de la comp

	and the second of the second states and the		
Realities and the	Eindhoven University of Technology —Department—of—Mathematics_and Computing_So	cience	
	P.O. Box 513	,	
	56 00 MB EINDHOVEN		
n General and a second	The Netherlands All rights reserved		
	editor: F.A.J. van Neerven		

### Enforcing Nondeterminism via Linear Time Temporal Logic Specifications

#### Ruurd Kuiper

Department of Mathematics and Computing Science, Eindhoven University of Technology, P.O. Box 513, 5600 MB Eindhoven, The Netherlands.

#### March 1987

#### ABSTRACT

525

It is shown how some amount of nondeterminism can be enforced via linear time temporal logic. This is achieved through extending the notion of specification rather than changing the logic, i.e., no recourse is taken to branching time. The treatment is compared, both in intent and with respect to realization to a similar approach using predicate transformers.

#### 1. Introduction

A specification describes requirements which further developments or implementations must fulfill in order to satisfy it. Usually, many decisions are deliberately left open to be filled in at later stages. Consequently, specifications usually contain nondeterminism which will, perhaps only in part, be resolved later.

For example, if production of either of the actions a, b, c or d will satisfy the user, a component S might, for the moment using without further explanation an intuitively obvious notation, be specified by

#### S sat $a \lor b \lor c \lor d$ .

المتعيد الأراهة يرتوتعها والعاورة

The customary interpretation of such a specification is to allow S to be implemented by any process of which the output is in the set  $\{a, b, c, d\}$ . For instance, by a process  $\underline{a}$ , which always produces  $\underline{a}$  when activated, but also by  $\underline{a \lor c}$ , which produces either an  $\underline{a}$  or a c upon different activations.

. . . . .

This kind of nondeterminism, say *allowed* nondeterminism, is not required of the implementation at all and only leaves some freedom to the implementor due to, deliberate, vagueness in the specification. A completely different kind of nondeterminism, say required nondeterminism is the nondeterminism which the implementation should possess.

For example, a random number generator should not always generate the same number when activated. Yet a specification like

S sat  $x := x'(x' \in \mathbb{N})$ ,

interpreted similarly as above as containing allowed nondeterminism, does not guarantee this. An implementation which always assigns, say, 5 to x would perfectly satisfy this specification.

Usually, specification methods make use of the first kind of nondeterminism to allow general specifications, but cannot handle the second kind. Branching time temporal logic, which describes behaviour as sets of trees is one of the few exceptions. Linear time temporal logic, describing behaviour as sets of sequences does, in its usual form, not have this expressive ability. There are, however, many different considerations which at present leave the debate as to which of the two is the most suitable, wide open.

We will present and discuss a way to enable in the context of linear time temporal logic specification of a modest amount of required nondeterminism. The idea is to limit the extent to which the allowed nondeterminism may be resolved, by additionally specifying a lower bound. This enforces implementations to possess a degree of nondeterminism between the bounds set by the required and the allowed nondeterminism.

For the above examples such lower bounds might be, respectively,  $a \lor c$  and  $x := x'(x' \in \{1,...,100\})$ .

In section 2 we briefly discuss the (only) approach similar to ours we know of, namely [Fr77]. This is carried out in the context of predicate transformers and safety properties, but it will be seen that a more general idea underlies his approach. In section 3 we show how this can be used for linear time temporal logic specifications. The interaction with development is discussed in the next section. In section 5 a brief look is taken at the situation for branching time temporal logic. The last section contains some discussion.

#### 2. A precursor: required nondeterminism and predicate transformers

In [Fr77], Francez addresses specifying required nondeterminism using predicate transformers. We look at the example given above, S sat  $a \lor b \lor c \lor d$  with the extra aim to specify some required nondeterminism.

Let the specification of S be given as  $\{\phi\} S \{\psi\}$ .

In the usual weakest precondition approach, only considering allowed nondeterminism, this

means that S has to satisfy

(i)  $\phi \Rightarrow wp(S, \psi)$  where, in this example,

 $\phi = \text{true}$  $\psi = a \lor b \lor c \lor d.$ 

This only gives an upper bound to the allowed nondeterministic behaviour of S and allows implementations like, e.g.,  $S = \underline{b}$ .

The idea in [Fr77] now is, to enforce  $\psi$  as a lower bound on required nondeterminism as well, again using weakest preconditions. The extra part of the satisfaction notion is, that S should also satisfy

(ii)  $\forall \psi^* \neq \psi[(\psi^* \Rightarrow \psi) \Rightarrow \neg (\phi \Rightarrow wp(S, \psi^*))],$ where again in this example

 $\phi = true$ 

$$\Psi = a \vee b \vee c \vee d.$$

It can be easily seen, that together these requirements limit the implementations to  $a \vee b \vee c \vee d$  only.

In this example, lower and upper bound coincide. The words lower and upper suggest, although [Fr77] does not claim this, noncoinciding bounds, allowing a range of implementations in between them. This might, for instance, be denoted by

 $\{\phi\} S \{\overline{\psi}, \psi\}$ , where  $\overline{\psi}$  is the upper and  $\psi$  the lower bound.

1 11.2

· . . .

Intuitively, expressed in terms of an obvious semantics of i/o pairs, the lower /upper bound approach, in our view, aims at achieving the following kind of constraints.

Let  $\langle i, a \rangle$  denote: on any input, produce a. Take as lower and upper bound requirements respectively

$$\overline{\psi} = a \lor b \lor c \lor d$$
$$\psi = a \lor c.$$

Then the desired constraint on S would be

 $\{\langle i, a \rangle, \langle i, c \rangle\} \subseteq [[S]] \subseteq \{\langle i, a \rangle, \langle i, b \rangle, \langle i, c \rangle, \langle i, d \rangle\},\$ 

i.e., allowing the implementations  $\underline{a \lor c}$ ,  $\underline{a \lor b \lor c}$ ,  $\underline{a \lor c \lor d}$  and  $\underline{a \lor b \lor c \lor d}$ . Unfortunately, using (ii) with  $\underline{\psi}$  as  $\psi$  does not give the desired result. Namely (ii) now is of the form

$$\forall \Psi^* \neq \Psi[(\Psi^* \Rightarrow a \lor c) \Rightarrow \neg (\mathsf{true} \Rightarrow wp(S, \Psi^*))].$$

Consider the implementation  $S = \underline{b}$ . As S produces only b, S does not satisfy  $wp(S, a \lor c)$ , which will remain the case if  $a \lor c$  is strengthened. So S is, contrary to the intuition, allowed as an implementation of  $\{\phi\} S \{\overline{\psi}, \psi\}$ . Hence, the approach in [Fr77] is limited to coinciding lower and upper bounds.

- 4 -

In the next section, the lower/upper bound approach will be adapted to linear time temporal logic specifications and extended to enable the use of lower and upper bounds that do not coincide.

#### 3. Enforcing required nondeterminism in linear time temporal logic

· · · · ·

In linear time temporal logic (LTL) we take both the specification,  $\psi$ , and the semantics, [[S]], of an implementation S to be an LTL formula. Such a formula in turn can be interpreted as characterizing a set of (state) sequences, namely those for which it is true.

The customary satisfaction relation when considering only allowed nondeterminism is then straightforward:

S sat 
$$\psi \triangleq \llbracket S \rrbracket \Rightarrow \psi$$
.

Intuitively this means that the set of sequences that can be generated by S is included in the set allowed by  $\phi$ . It is clear that any less nondeterministic implementation S', meaning that the set of sequences it can generate is smaller, which in turn means that  $[S'] \Rightarrow [S]$ , satisfies  $\psi$  as well. So the implication makes it impossible to specify required fairness. Establishing a lower bound is the solution and, in the LTL framework, can be easily incorporated in a manner reflecting the intuitive set inclusion as mentioned in the previous section.

Define

$$S \text{ sat } \langle \psi, \overline{\psi} \rangle \triangleq \psi \Rightarrow \llbracket S \rrbracket \Rightarrow \overline{\psi}$$

The specification of the example, in the formal notation as used in [BKP84], i.e. assuming sequences to have labels indicating environment (E) steps and component  $(\Pi)$  steps, then becomes:

S sat 
$$\langle \underline{\Psi}, \overline{\Psi} \rangle$$
,

where

 $\underline{\Psi} = E U(\Pi \wedge (a \vee c)) C fin,$ 

(which informally states: starting with environment steps E, after which the component stops.)

and

 $\overline{\psi} = E U(\Pi \land (a \lor b \lor c \lor d)) C fin.$ 

#### Remarks

(i) An alternative way to enable specifying required nondeterminism may seem to change the implication to equivalence (this, in fact, is the situation in [Fr77]):

وبالد والمتحدون والمالونيات

 $P \text{ sat } \Psi \triangleq \llbracket P \rrbracket = \Psi.$ 

This indeed fulfills the aim, but does not possess the lower and upper bound flexibility. Consequently, extra allowed nondeterminism can now only be obtained by explicitly listing the allowed alternatives, e.g., via exclusive or notation:

> $S \quad sat \quad \psi_1 \oplus \psi_2 \oplus \cdots \oplus \psi_n \triangleq$  $S \quad sat \quad \psi_1 \oplus S \quad sat \quad \psi_2 \oplus \cdots \oplus S \quad sat \quad \psi_n.$

This is unfortunate, as usually when giving a specification one only has a rough idea about what one wants to allow, but certainly not a full grasp of all possible alternatives. Furthermore, if infinitely many alternatives for implementation exist, as in the case of the random number generator example, it is not possible to list all of these unless infinite  $\oplus$  is allowed. In that case, although the first objection remains, both extensions are equivalent.

(ii) In, e.g., [Pn85] a strong notion of expressivity is defined for specification methods: A method is expressive  $\triangleq$  for all S there is a characteristic specification,  $spec_c$  such that:

(i) For all S', S' sat  $spec_c \iff (\llbracket S \rrbracket = \llbracket S' \rrbracket)$ 

(ii) For all spec, S sat spec  $\Leftrightarrow$  (spec<sub>c</sub>  $\Rightarrow$  spec)

This property usually does not hold; it is obtained for [BKP84] when extended as above.

#### 4. Development

and the second second

One part of development is concerned with decomposition into subspecifications. The extension of the notion of specification is such, that adapting of this part of existing methods is straightforward. For instance, a compositional specification method dealing with required nondeterminism can be obtained by using an existing one like described in [BKP84] and just redefining the notion of specification as above and adapting the proof rules as follows.

For the decomposition part, the essential rules are those concerned with syntactical combinators, e.g., sequential and parallel composition, enabling to derive properties of components from properties of their syntactic subcomponents. These rules reflect the semantics of such operators and are of the form

$$S_1 \text{ sat } \psi_1$$

$$S_2 \text{ sat } \psi_2$$

$$C(S_1,S_2) \text{ sat } C'(\psi_1,\psi_2)$$

where C is a syntactical combinator on components and C' the corresponding syntactical combinator on specifications.

The translation then is

4 \* N 4

$$S_1 \text{ sat } < \underline{\psi}_1, \overline{\psi}_1 >$$

$$S_2 \text{ sat } < \underline{\psi}_2, \overline{\psi}_2 >$$

$$C(S_1, S_2) \text{ sat } < C'(\underline{\psi}_1, \underline{\psi}_2), C'(\overline{\psi}_1, \overline{\psi}_2) >$$

A concrete example, for sequential composition, using the temporal logic operator C (chop) is

 $S_1 \text{ sat } < \psi_1, \overline{\psi}_1 >$   $S_2 \text{ sat } < \psi_2, \overline{\psi}_2 >$   $\overline{S_1; S_2 \text{ sat } < \psi_1 C \psi_2, \overline{\psi}_1 C \overline{\psi}_2 >}.$ 

Another part of development is concerned with extending the requirements on the behaviour. In the context of LTL this intuitively means further narrowing down the sets of sequences allowed by the specification. In the  $\psi \Rightarrow [S] \Rightarrow \overline{\psi}$  framework, this amounts to weakening (!)  $\psi$  and strengthening  $\overline{\psi}$ . This gives rise to the following rule:

· · · · · ·

and the second second

· . . .

1 . .

. . . .

the second

1. . :

1. 1. 1. 1. A.S.

$$S \quad sat < \underline{\phi}, \overline{\phi} >$$

$$\Psi \Rightarrow \Phi$$

$$\overline{\phi} \Rightarrow \overline{\psi}$$

$$S \quad sat < \underline{\psi}, \overline{\psi} >$$

Again turning to the previously used example, this means that it can be derived that from

S sat  $\langle a \lor c \lor d, a \lor c \lor d \rangle$ it follows that

#### S sat $\langle a \lor c, a \lor b \lor c \lor d \rangle$

This corresponds to the intuition, as the first specification only allows the implementation  $S = a \lor c \lor d$ . This is, as has been seen previously, one of the various implementations allowed by the second specification.

#### Remark

There is a rather subtle problem in the treatment of required nondeterminism in development. Of variables about which at a certain stage in the development nothing has yet been decided, usually nothing is required, i.e., all sequences are allowed as regards their values.

However, if nothing is required in  $\Psi$  about such a variable, this should remain so during further development, because, as seen from the rules,  $\Psi$  may only be weakened. Intuitively, as seen from the example, if straightforward strengthening of already mentioned variables is involved, there is no problem, because required nondeterminism for this variable was explicitly stated.

For the decomposition case there is a problem, as one would like, but cannot, formulate that for as yet unused variables no lower bound is yet established. A possible solution for this case is to argue that a decomposition step causes a lower level of abstraction to be used. New variables added to the interface can be viewed as visible only to the subcomponents.

Requirements, especially required nondeterminism, pertaining to these variables can then also be seen as limited to this level only.

The problem then disappears, as  $\psi$  on a higher level of specification cannot impose requirements on these variables. This approach may be formalized by introducing an explicit interface for each level of specification. (See, e.g., [BK83].)

5. Branching time temporal logic

15.9.1

In branching time temporal logic (BTL), the formulae are interpreted not as characterizing sets of sequences, but sets of trees.

It is then obvious, that because sets of such trees are involved, a completely analogous treatment as for the LTL case is, in principle, possible. Whether this is desirable depends on one's view about which objects are more natural as behaviour of programs in certain circumstances.

Consider, for example, required nondeterminism, say  $a \vee b$ . If one feels, that only a set containing at least a sequence with a and one with b on it is a correct representation of this requirement, then a similar extension as to LTL is needed for BTL. The reason is, that although sequences can be viewed as trees, when required nondeterminism is imposed via sets of these, the same problems with resolving allowed nondeterminism too far as in LTL apply to BTL. If however, one allows this to be expressed via the requirement that each tree has at least a branch with a and one with b on it, standard BTL is expressive enough already.

As yet, apart from many other arguments about which of these basic varieties is the most suitable (or when), about this particular choice there seems to be no consensus. For more information on BTL, see, e.g., [EL85].

#### 6. Discussion

We presented a way to enforce some amount of required nondeterminism via LTL specifications. It is sometimes argued that specifying required nondeterminism is meaningless, as no test will be able to falsify a claim like, e.g.,  $\Psi = a \lor b$ . The idea is, that even after repeated testing with consistently result a, b might still occur at some future test.

One remark here is, that exactly the same argumentation applies to fairness requirements like: eventually b will occur. This concept however now seems quite well accepted.

More direct counter arguments are the following:

- (i) When designing a system, it is natural that initially some properties are underdefined. During development these may be strengthened to falsifiable ones, which is certainly the only way in which they can be implemented.
- (ii) An implementation will come together with a proof that its specification is met, so testing is not required.

A fortunate consequence of the fact that the extension made to the notion of specification retains the interpretation as a pure LTL formula and does not alter the logic is, that existing decision procedures (see, e.g., [Go83]) can still be used.

An open problem is, whether existing devices that contain nondeterminism, like random number generators, will satisfy abstract specifications of this property. Furthermore, if this is the case, how can this be proven? The link between the formulation of the practical and the theoretical properties seems not obvious.

#### Acknowledgements

Many thanks to Ron Koymans and Rob Gerth for comments and help at various stages and especially to Willem-Paul de Roever, who provided the link to reference [Fr77]. I am very grateful to Edmé van Thiel for Elastic Time Typing.

#### References

[EL85]	Emerson, E.A., Chin-Laung Lei, Modalities for Model Checking: Branching Time Logic Strikes Back, POPL 1985.
[Fr77]	Francez, N., A Case for a Forward Predicate Transformer, Inf. Proc. Letters IEEE 6:6, 1977.
[Go83]	Gough, G.D., M.Sc. Thesis, Decision Procedures for Temporal Logic, Univ. of Manchester.
[BK83]	Barringer, H., Kuiper, R., Towards the Hierarchical, Temporal Logic, Specification of Concurrent Systems, LNCS 207.
[BKP84]	Barringer, H., Kuiper, R., Pnueli, A., Now You May Compose Temporal Logic Specifications, STOC 1984.
[Pn85]	Pnueli, A., Linear and Branching Structures in the Semantics and Logics of Reac- tive Systems, ICALP 1985.

na sense a sens A sense a sense

Second State and State

#### COMPUTING SCIENCE NOTES

# In this series appeared :

No.	Author(s)	Title
85/01	R.H. Mak	The formal specification and
الأرجر أأراسي	1. a	derivation of CMOS-circuits
85/02	W.M.C.J. van Overveld	On arithmetic operations with M-out-of-N-codes
85/03	W.J.M. Lemmens	Use of a computer for evaluation of flow films
85/04	T. Verhoeff H.M.J.L. Schols	Delay insensitive directed trace structures satisfy the foam
		rubber wrapper postulate
86/01	R. Koymans	Specifying message passing and real-time systems
86/02	G.A. Bussing K.M. van Hee M. Voorhoeve	ELISA, A language for formal specifications of information systems
86/03	Rob Hoogerwoord	Some reflections on the implementation of trace structures
86/04	G.J. Houben J. Paredaens K.M. van Hee	The partition of an information system in several parallel systems
86/05	Jan L.G. Dietz Kees M. van Hee	A framework for the conceptual modeling of discrete dynamic systems
86/06	Tom Verhoeff	Nondeterminism and divergence
22 M 12	en e	created by concealment in CSP
		- On proving communication
	L. Shira	closedness of distributed layers

86/08	R. Koymans	Compositional semantics for
	R.K. Shyamasundar	real-time distributed
	W.P. de Roever	computing (Inf.&Control 1987)
	R. Gerth	1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1
	S. Arun Kumar	
86/09	C. Huizing	Full abstraction of a real-time
	R. Gerth	denotational semantics for an
	W.P. de Roever	OCCAM-like language
<b>86/</b> 10	J. Hooman	A compositional proof theory
		for real-time distributed
		message passing
86/11	W.P. de Roever	Questions to Robin Milner - A
		responder's commentary (IFIP86)
86/12	A. Boucher	A timed failures model for
	R. Gerth	extended communicating processes
86/13	R. Gerth	Proving monitors revisited: a
	W.P. de Roever	first step towards verifying
		object oriented systems (Fund.
		Informatica IX-4)
86/14	R. Koymans	Specifying passing systems
	•	requires extending temporal logic
87/01	R. Gerth	On the existence of sound and
		complete axiomatizations of
		the monitor concept
87/02	Simon J. Klaver Chris F.M. Verberne	Federatieve Databases
	Units i in verberne	
87/03	G.J. Houben	A formal approach to distri-
	J.Paredaens	buted information systems
87/04	T.Verhoeff	Delay-insensitive codes -
		An overview
	:	

,

	87/05	R.Kuiper	Enforcing non-determinism via linear time temporal logic specification.
87/07 R.Koymans Specifying message passing and real-time systems with real-time temporal logic.	87/06	-	Temporele logica specificatie van message passing en real-time systemen (in Dutch).
<ul> <li>Systems with real-time temporal logic.</li> </ul>	×		
<ul> <li>A. S. S.</li></ul>	87/07	-	
<ul> <li>Ander Anderson (Maria Construction of the Constructio</li></ul>			
<ul> <li>Ander Ander Ander</li></ul>	·* •	1 Mar	
		· A.	an a
	۰. ۰		
<ul> <li>A second second</li></ul>			
<ul> <li>An and the second second</li></ul>			
<ul> <li>Martine Martine M</li></ul>			
<ul> <li>A second s</li></ul>	1 · 4 · .	·· · · · · · · · · · · · · · · · · · ·	
The set of the second sec			
Transform of the second first of the second	1. Jan 3. au		
an an air air an		$\mathcal{L}_{\mathcal{A}} = \mathcal{M}_{\mathcal{A}} = \mathcal{M}_{\mathcal{A}}$	
	Tree? "		

- M. Freening. T. Within the Same on the Reeven

State of the second second second

. .

# Available Reports from the Theoretical Computing Science Group

\_\_\_\_

-----

\_\_\_\_

- -

	Author(s)	Title	Class EUT	ification DESCARTES
TTR82.1	R. Kuiper, W.P. de Roever	Fairness Assumptions for CSP in a Tem- poral Logic Framework		
TIR83.1	R. Koymans, J. Vytopil, W.P. de Roever	Real-Time Programming and Synchronous Message passing (2nd ACM PODC)		
TIR83.2	H. Barringer, R. Kuiper	Towards the Hierarchical, Temporal Logic, Specification of Concurrent Systems		
TIR84.1	R. Gerth, W.P. de Roever	A Proof System for Concurrent Ada Pro- grams (SCP4)		
TIR84.2	R. Gerth	Transition Logic - how to reason about tem- poral properties in a compositional way (16th ACM FOCS)		
TIR84.3	H.Barringer, R. Kuiper, A. Pnucli	Now you may compose Temporal Logic Specifications (Proc. STOC84)		
TIR84.4	H. Barringer, R. Kuiper	Hicrarchical Development of Concurrent Systems in a Temporal Logic Framework		
TIR85.1	W.P. de Roever	The Quest for Compositionality - a survey of assertion-based proof systems for con- current progams, Part I: Concurrency based on shared variables (IFIP85)		
TIR85.2	O. Grünberg, N. Francez, J. Makowsky, W.P. de Roever	A proof-rule for fair termination of guarded commands (Inf.& Control 1986)		

TIR85.3	F.A. Stomp, W.P. de Roever, R. Gerth	The $\mu$ -calculus as an assertion language for fairness arguments (Inf.& Control 1987)		
TIR85.4	R. Koymans, W.P. de Roever	Examples of a Real-Time Temporal Logic Specification (LNCS207)		
TIR85.5	H. Barringer, R. Kuiper, A. Pnucli	A Compositional Approach to a CSP-like Language		
TIR86.1	R. Koymans	Specifying Message Passing and Real-Time Systems (extended abstract)	CSN86/01	
TIR86.2	J. Hooman, W.P. de Roever	The Quest goes on: A Survey of Proof Sys- tems for Partial Correctness of CSP (LNCS227)	-	
TIR86.3	R. Gerth, L. Shira	On Proving Communication Closedness of Distributed Layers (LNCS236)	CSN86/07	
TIR86.4	R. Koymans, R.K. Shyamasundar, W.P. de Roever, R. Gerth, S. Arun Kumar	Compositional Semantics for Real-Time Distributed Computing (Inf.&Control 1987)	CSN86/08	
TIR86.5	C. Huizing, R. Gerth, W.P. de Roever	Full Abstraction of a Real-Time Denota- tional Semantics for an OCCAM-like Language		PE.01
TIR86.6	J. Hooman	A Compositional Proof Theory for Real- Time Distributed Message Passing	CSN86/10	TR.4-1-1(1)
TIR86.7	W.P. de Roever	Questions to Robin Milner - A Responder's Commentary (IFIP86)	CSN86/11	
TIR86.8	R. Gerth, A. Boucher	A Timed Failures Model for Extended Communicating Processes	CSN86/12	TR.4-4(1)
TIR86.9	R. Gerth, W.P. de Roever	Proving Monitors Revisited: a first step towards verifying object oriented systems (Fund. Informatica IX-4)		
TIR86.10	R. Koymans	Specifying Message Passing Systems Requires Extending Temporal Logic	CSN86/14	PE.02

TIR86.11	H. Barringer, R. Kuiper, A. Pnucli	A Really Abstract Temporal Logic Seman- tics for Concurrency (Proc. POPL86)
TIR87.1	R. Gerth	On the existence of sound and complete CSN87/01 axiomatizations of the monitor concept
TIR87.2	R. Kuiper	Enforcing Nondeterminism via Linear Time CSN87/05 Temporal Logic Specifications
TIR87.3	R. Koymans	Temporele Logica Specificatie van Message CSN87/06 Passing en Real-Time Systemen (in Dutch)
TIR87.4	R. Koymans	Specifying Message Passing and Real-Time CSN87/07 PE.03 Systems with Real-Time Temporal Logic