



30th Eurosensors Conference, EUROSENSORS 2016

Reliable and Inexpensive Solar Irradiance Measurement System Design

C. Orsetti, M. Muttillio, F. R. Parente, L. Pantoli*, V. Stornelli, G. Ferri

*Department of Industrial and Information Engineering and Economics,
University of L'Aquila, 67100, L'Aquila, Italy*

Abstract

In this work, we present an innovative low cost sensor and algorithm for the monitoring and measurement of solar irradiance. This parameter is usually estimated using pyranometers, often based on thermopile. They are quite expensive, also because need additional hardware for data acquisition and manipulation as well as non-negligible installation costs. The system architecture and novel algorithm here proposed employ small PhotoVoltaic (PV) cells and a digital sensor interface. Moreover, the logic section permits to tilt the sensor allowing to track the sun with improved accuracy.

© 2016 The Authors. Published by Elsevier Ltd. This is an open access article under the CC BY-NC-ND license (<http://creativecommons.org/licenses/by-nc-nd/4.0/>).

Peer-review under responsibility of the organizing committee of the 30th Eurosensors Conference

Keywords: Differential capacitance sensors; bridge-based circuits; analog circuits; Wheatstone bridge; electronic interface.

1. Introduction

Solar irradiance is an important parameter to be measured for both atmospheric science and renewable energy system design. Since the technology is rapidly developing, a large number of pyranometers have been proposed in the last years (e.g. [1-5]), but both their cost and the maintenance requirements tend to be expensive. Users need a monitoring instrument that is robust, accurate, less expensive and capable of unattended long time operations. In this scenario, the system here addressed is able to provide a cheap solution for the measurements of solar irradiance, by means of PV cells and a microcontroller-based data acquisition and manipulation system implementing a dedicated algorithm for the measured data extrapolation. In the first prototype, an open-source electronic prototyping platform

* L. Pantoli. Tel.: +39 0862 434444; fax: +39 0862 434403.
E-mail address: leonardo.pantoli@univaq.it

has been used for signal management, with the aim to demonstrate the potentiality of the proposed solution.

2. The proposed system

The PV cells, whose dimensions are 45x45mm, are series coupled and directly connected with a shunt resistor useful for sampling the voltage generated by the sunlight (Fig.1). The time dependent analog signal is directly dependent on the intensity of sunlight and is the input of the following digital sensor interface. The system is able to evaluate the solar irradiance (IRR_S) with a dedicated algorithm implementing the constitutive relation:

$$IRR_S = \left(e^{V_{out} \cdot A} - B \right) \cdot \frac{T}{T_R}$$

The A parameter in the equation can be defined as a conversion coefficient, while the B parameter is an offset factor; both of them take into account the physical characteristics of the PV elements and are determined experimentally. Moreover, T in the formula is the PV cells temperature, while T_R is the reference temperature used for system calibration. The functionality and reliability of the system is provided by the additional circuitry. The temperature sensors are placed below the PV cells, while four photoresistors define two orthogonal directions (Fig. 2) used by the algorithmic implemented in the microcontroller (Fig. 3) for alignment purposes. The tracking functionality of the PV cells is allowed by two servo-motors installed on the structure (Fig. 4) and driven by the same microcontroller (Fig. 3).

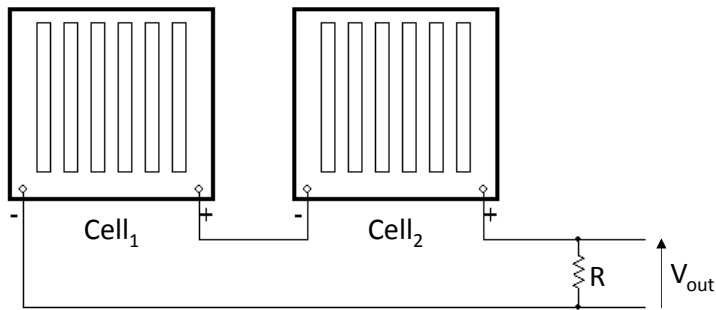


Fig. 1. The solar PV cells connection scheme.

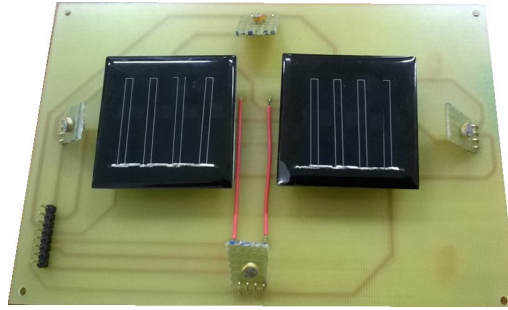


Fig. 2. Top view of the prototype board.

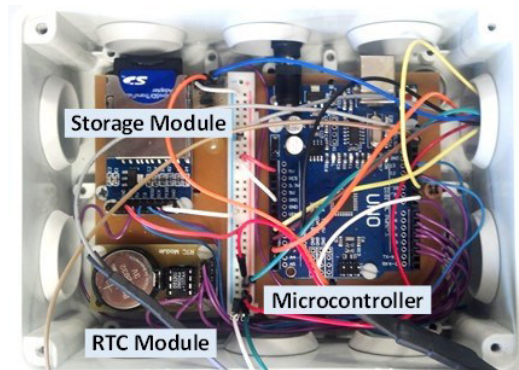


Fig. 3. Digital sensor interface.

3. Results

On field test and measurements have been performed showing a good accuracy and high sensitivity so validating the proposed algorithm and system hardware. In order to check the overall performance of the system, results have been compared with those provided by the *DPA053*, a typical thermopile pyranometer (Fig. 5), resulting in a good agreement. A compact version of the prototype is currently under construction with improved electronics, by taking advantage of the current mode approach and low voltage low power techniques [6-11], and even better performance.

4. Conclusions

We have here presented we present an innovative low cost sensor and algorithm for the monitoring and measurement of solar irradiance employing PV cells and a digital sensor interface that shows satisfactory performances.

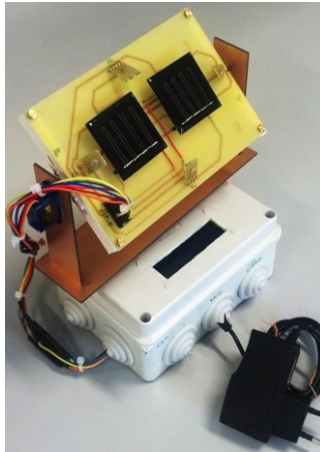


Fig. 4. The complete prototype system.

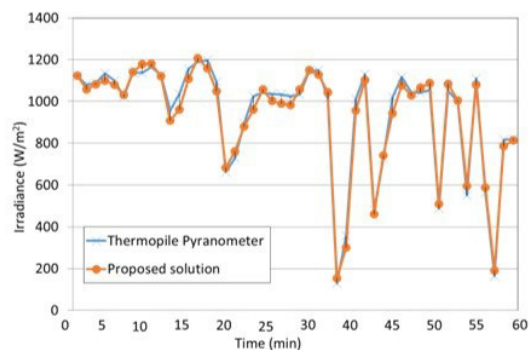


Fig. 5. Measured results compared with a thermopile pyranometer

References

- [1] F. S. A. Aziz, S. I. Sulaiman and H. Zainuddin, "A prototype of an integrated pyranometer for measuring multi-parameters," Signal Processing and its Applications (CSPA), 2013 IEEE 9th International Colloquium on, Kuala Lumpur, pp. 73-77, 2013.
- [2] P. Bharadwaj and V. John, "Design, fabrication and evaluation of solar irradiation meter," Power Electronics, Drives and Energy Systems (PEDES), 2014 IEEE Intern. Confer. on, Mumbai, pp. 1-6, 2014. V. Stornelli, "Low voltage Low Power Fully Differential Buffer", Journal of Circuits, Systems, and Computers, vol. 18, no.3, pp. 497-502, 2009.
- [3] E. C. Kern and M. C. Russell, "Rotating shadow band pyranometer irradiance monitoring for photovoltaic generation estimation," Photovoltaic Specialists Conference, 1991., Conference Record of the Twenty Second IEEE, Las Vegas, NV, 1991, pp. 541-546 vol.1.
- [4] R. H. G. Tan, P. L. J. Tai and V. H. Mok, "Solar irradiance estimation based on photovoltaic module short circuit current measurement," Smart Instrumentation, Measurement and Applications (ICSIMA), 2013 IEEE International Conference on, Kuala Lumpur, 2013, pp. 1-4.
- [5] P. Bharadwaj and V. John, "Design, fabrication and evaluation of solar irradiation meter," Power Electronics, Drives and Energy Systems (PEDES), 2014 IEEE International Conference on, Mumbai, 2014, pp. 1-6.
- [6] L. Pantoli, V. Stornelli and G. Leuzzi, "Class AB tunable active inductor," in Electronics Letters, vol. 51, no. 1, pp. 65-67, 1 8 2015.
- [7] G. Ferri, V. Stornelli, A. Di Simone, "A CCII-based high impedance input stage for biomedical applications", Journal of circuits, systems, and computers, Volume 20, Issue 8, Pages 1441-1447, December 2011.
- [8] Pantoli, L., Stornelli, V., & Leuzzi, G., A low-voltage low-power 0.25 μm integrated single transistor active inductor-based filter, Analog integrated circuits and signal processing (2016), 87(3), 463–469.
- [9] P. Branchi, L. Pantoli, V. Stornelli, G. Leuzzi, "RF and microwave high-Q floating active inductor design and implementation", International Journal of Circuit Theory and Applications, 43 (8), 1095-1104, 2015;
- [10] V. Stornelli, G. Ferri, "A 0.18 μm CMOS DDCCII for portable LV-LP filters", Radioengineering, vol. 22, is. 2, 2013, pp. 434-439, 2013.
- [11] G. Ferri, V. Stornelli, A. Celeste, "Integrated rail-to-rail low-voltage low-power enhanced DC-gain fully differential operational transconductance amplifier", ETRI Journal, vol. 29, is. 6 Pages 785-792, 2007.