Dislocation formation in silicon implanted at different temperatures

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Abstract

The formation of pre-amorphization damage, i.e. dislocations formed by the agglomeration of silicon interstitials, requires a minimum amount of implant damage. The amount of damage can be altered by changing the implant temperature or current density, which can influence dislocation formation. We studied this using cross-sectional transmission electron microscopy for boron and indium implants at kiloelectronvolt and megaelectronvolt energies respectively. Dislocation formation for boron implants, where only simple cascade densities are generated, does not depend on implant temperature or current density. For 1 MeV indium implants, where the implant damage consists mainly of amorphous zones, an increase in critical dose for dislocation formation by a factor of approximately 3 is observed if the implant temperature is raised. This is attributed to the interaction of point defects with the amorphous zones during the elevated temperature implant. Implants of 150 keV indium at room temperature result in complete amorphization before the critical amount of crystal damage is reached. Here, end-of-range loops (EOR-loops) form after annealing. Increasing the implant temperature suppresses amorphization, and pre-amorphization damage is observed if a critical amount of crystal damage has been generated. EOR-loop formation results from the agglomeration of silicon interstitials from the amorphous-crystalline transition region. If the number of interstitials in this region is lowered by carrying out the implant at low temperature, EOR-loop formation can be suppressed. This is shown by comparing amorphizing germanium implants done at room and liquid nitrogen temperatures.

1. Introduction

Ion implantation is commonly used for introducing dopants into silicon. The ions enter the substrate and come to rest in typically 10^{-13} s by nuclear and electronic stopping mechanisms [1]. The nuclear interactions give rise to displacements of silicon host atoms. For low mass implants, the displacement cascade densities are low and generally only simple vacancyand interstitial-related defects are created. These point defects are not stable at room temperature (RT), thus they either recombine or form larger point defect clusters [2, 3]. Such clusters also form for high mass implants, but in addition amorphous zones (a-zones) are created by the dense cascades [4]. The configuration of defect complexes and a-zones remaining after the implant is called the primary damage.

Annealing of primary damage below the amorphization threshold results in the formation of category I dis-

locations, also known as pre-amorphization damage, if a critical amount of primary damage has been exceeded [5]. This criterion for dislocation formation has been demonstrated for RT implants of ions ranging from low mass (11B) to high mass (115In, 121Sb). The critical amount of primary damage corresponds to a number of displaced silicon atoms of approximately 10^{16} cm⁻² for boron and approximately 10^{17} cm⁻² for indium implants, according to Rutherford backscattering spectrometry (RBS) and channeling analysis [5]. This difference in number was attributed to the difference in primary damage morphology for the boron (simple point defect complexes) and indium (in addition a-zones) implants. The displaced silicon atoms inside the a-zones do not contribute to dislocation formation because the a-zones recrystallize upon annealing.

Changing the amount of primary damage could result in a change in dislocation formation. The amount

and type of primary damage is influenced by several parameters. (1) Increasing the mass of the implanted ion results in a change from only point defect clusters for low mass ions to a damage morphology mainly consisting of a-zones for high mass ions [5, 6]. (2) Implanting at higher energy distributes the primary damage over a larger depth. For high mass implants, the critical amount of primary damage is then obtained before the amorphization threshold is reached [5]. (3) Performing the implant at an elevated temperature enhances annealing during the implant. For 1 MeV silicon implants, an increase in implant temperature of 30 °C can reduce the amount of primary damage by 90% [7]. High mass implants, which normally result in amorphous surface layers for RT implants, only generate crystalline damage if the implant is performed at higher temperatures [8]. (4) The influence of the implant dose on the amount of primary damage is strongly dependent on the type of primary damage formed. A linear dependence on implant dose is observed for high mass implants done at RT [8]. Implants of low mass ions or elevated temperature implants result in a damage build-up that is strongly non-linear with dose [6-8]. (5)Different ion fluxes give rise to different defect production rates in silicon [7]. For a high defect production rate, it is easy for the point defects to interact and form larger complexes. For a low defect production rate, the point defects anneal before they can interact and form more stable defect complexes. Therefore, the highest amount of primary damage results from implants carried out with the highest flux. From the above, it is concluded that there are several ways to lower the amount of primary damage. Increasing the implant temperature or decreasing the ion flux may therefore help reduce dislocation formation.

For kiloelectronvolt high mass implants carried out at RT, the amorphization threshold is attained before the critical amount of primary damage is reached. However, amorphization is avoided if the implant is performed at a sufficiently high substrate temperature [8, 9]. The primary damage then consists only of crystal damage. Again, a critical amount of crystal damage should be required to form dislocations during thermal treatment.

Annealing an amorphous layer results in the formation of end of range loops (EOR-loops) near the original amorphous-crystalline (a-c) transition region [6, 10]. The extrinsic loops result from the agglomeration of silicon interstitials which are positioned in the a-c transition region [11]. For a certain implant dose, the amount of crystal damage in this region can be lowered if the implant is done at a lower temperature [12, 13]. If the amount of damage is lower than a critical number, EOR-loop formation might be avoided [11]. Servidori and Vecchi indeed showed that for

 $2\times10^{15}~\text{cm}^{-2}~100~\text{keV}$ phosphorus implants, dislocations were observed for RT implants, in contrast to implants performed at liquid nitrogen temperature [LN₂)[14].

In the first part of this paper, we report on dislocation formation for low (boron) and high (indium) mass implanted silicon. The primary damage for boron implants was altered by changing the implant temperature and the implant flux. Only smaller point defect clusters are formed for these boron implants, hence only some annihilation and coarsening of the point defect clusters is expected. Cross-sectional transmission electron microscopy (XTEM) analysis will show the influence of this change in primary damage on dislocation formation. For comparison, the primary damage for indium implants was also changed by performing the implants at different temperatures. Here, a larger influence on dislocation formation is expected, since a-zones are not stable at higher temperatures and could influence the point defect configuration. By comparing the results for the boron and indium implants, the influence of a-zones on dislocation formation can be determined.

The second part of this paper discusses the dislocation formation for low energy indium implants. EOR-loops result from annealing the RT implants. Amorphization is avoided when the implant is performed at $300\,^{\circ}\mathrm{C}$ and pre-amorphization damage should be observed when the amount of crystal damage exceeds the critical amount.

Finally, in the third part, it is shown that EOR-loop formation can be avoided. This is demonstrated for 75 keV germanium implants.

2. Experimental details

Implants of ¹¹B, ⁷³Ge, and ¹¹⁵In with energies between 75 keV and 1 MeV were done in 5-15 Ω cm, p-type float zone (100) silicon in a random direction. A current density lower than 20 nA cm⁻² was used, unless otherwise specified. The samples (approximately 15 × 15 mm²) were mounted on a molybdenum or copper block cooled by liquid nitrogen or heated to temperatures up to 500 °C by a tungsten filament. The temperature was monitored with a Pt100 resistor mounted in the molybdenum block or with a thermocouple mounted on the copper block. The number of displaced silicon atoms in the as-implanted samples was determined using 2 MeV He+ RBS in the channeling geometry [15]. The implanted samples were annealed in a vacuum furnace (base pressure approximately 10⁻⁷ Torr). Cross-sectional and plan-view TEM were done in the bright-field mode using a Siemens Elmiskop 101 microscope.

3. Pre-amorphization damage for low (boron) and high (indium) mass implants

3.1. Boron implants

Dislocation formation was investigated for 200 keV and 1 MeV ¹¹B implants. Figure 1 shows XTEM images after 15 min, 900 °C anneals of samples implanted with 200 keV boron for different doses and implant temperatures. No dislocations are observed for a room temperature (RT) implant of 0.7×10^{14} B cm⁻², but they are seen for 1.0×10^{14} B cm⁻². Hence, the latter dose is the threshold dose which generates the critical amount of damage needed for dislocation formation for RT implants. If raising the implant temperature helped suppress dislocation formation, the threshold dose should increase for higher implant temperatures. However, dislocations are still observed for 1.0×10^{14} B cm⁻² implants at either 200-400 °C. The concentration of dislocations increases for the 400 °C implant if the dose is raised to 2.0×10^{14} B cm⁻². Thus, the dose required for dislocation formation for 200 keV boron implants is not influenced if the implant temperature is raised from 25 to 400 °C.

Implants of 5×10^{14} cm⁻² 1 MeV boron were performed at temperatures ranging from 25 to 400 °C. The beam current density on the target was 15 nA cm⁻². This does generates around eight times the critical amount of damage required for dislocation formation for 1 MeV boron implants [5]. RBS channeling spectra of the samples are shown in Fig. 2. Also shown is the spectrun of unimplanted silicon. The dechanneling is highest for the RT implant and decreases for increasing temperature. Therefore, the RBS-measured primary damage is reduced if the implant is done at an elevated temperature.

XTEM images of annealed samples implanted at 25, 200, and 400 °C are presented in Fig. 3. The anneal was done at 900 °C for 15 min. A band of elongated dislocation loops with lengths up to 1.0 μ m positioned at a depth of around 1.6 μ m is observed for the RT implant. The micrographs for the 200 and 400 °C implants do not show any reduction in dislocation formation. Again, raising the implant temperature does not influence the dislocation formation for boron implants, even though the primary damage levels measured by RBS were different.

Implants of 5×10^{14} cm⁻² 1 MeV boron at 400 °C were also performed at higher current densities of 150 and 1500 nA cm⁻². RBS channeling measurements of these samples are presented in Fig. 4. The sample implanted at the lowest current density results in the lowest dechanneling yield. The highest yield, and thus the highest amount of primary damage, is found for the sample implanted at a current density of 1500 nA cm⁻².

XTEM images of these samples (Fig. 5) all show a band of elongated dislocations positioned at a depth of about 1.6 μ m with no observable difference in dislocation size or density. Thus, changing the primary damage by changing the current density of the boron implant does not seem to influence dislocation formation.

The only number which is important for dislocation formation for the boron implants is the total number of silicon atoms displaced during the implant. The structure of defect complexes formed during implantation, which was altered by changing either the implant temperature or the flux, is apparently not so critical.

3.2. Indium implants

Implants of high mass ions at RT result in high cascade densities which lead to the formation of amorphous zones (a-zones). By performing implants of indium at elevated temperatures, the a-zones will decrease in size or may not form at all. This will also influence the diffusion and population of point defects, and a large change in primary damage is expected. By comparing results for both boron and indium implants, the influence of a-zones on the point defect population, and therefore on dislocation formation, can be investigated.

Figure 6 shows XTEM images after annealing of samples implanted with 1 MeV 115 In for different doses and implant temperatures. For implants at -85 °C, no dislocations are observed for a dose of 0.5×10^{13} In cm $^{-2}$ after 900 °C, 15 min annealing. Only one dislocation was found in the XTEM sample for a dose of 1.0×10^{13} In cm $^{-2}$, while a band of dislocation loops at a depth of $0.4~\mu m$ is observed for a dose of 1.5×10^{13} In cm $^{-2}$. In contrast, dislocations for RT implants only begin to appear between 1.5 and 2.0×10^{13} In cm $^{-2}$, in agreement with previous results [5]. The concentration of loops further increases for 3.0×10^{13} In cm $^{-2}$. For implants at 400 °C, dislocations are not observed until a dose of 3.0×10^{13} In cm $^{-2}$.

The results for temperatures ranging from -85 °C to 500 °C are summarized in Fig. 7. The indium threshold dose for dislocation formation is represented by the drawn line and increases by a factor of approximately 3 across this temperature range, saturating at around 400 °C.

Figure 8 shows RBS channeling spectra for indium doses closest to the critical dose at several implant temperatures. The primary damage in the silicon is highest for an implant at -85 °C and consists of 1.9×10^{17} cm⁻² displaced silicon atoms. This number is calculated by subtracting the dechanneling contribution from the total yield [15]. The number of displaced silicon atoms estimated from the RBS spectra decreases with increasing implant temperature, see Table 1. The

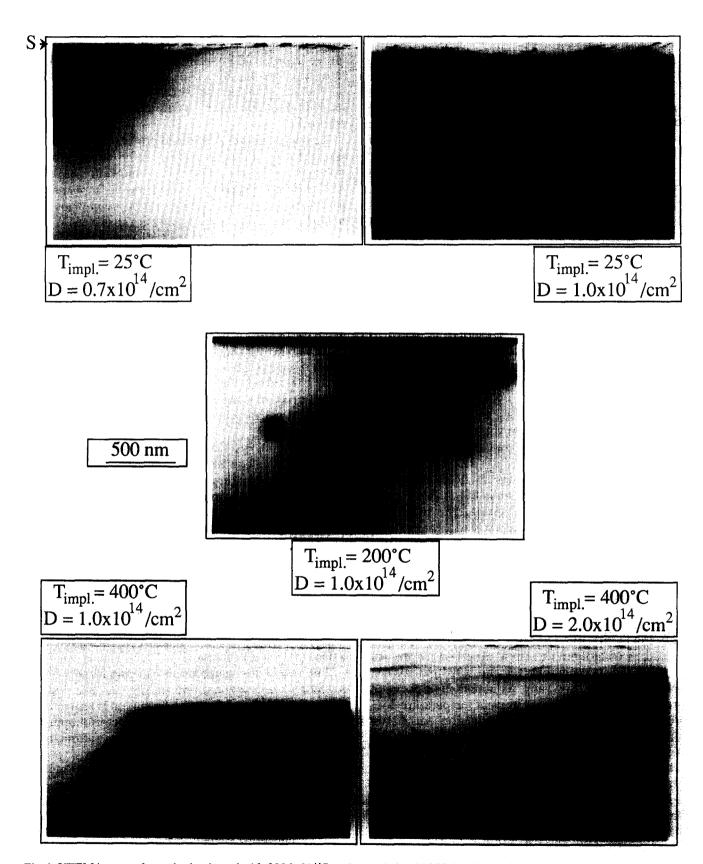


Fig. 1. XTEM images of samples implanted with $200~keV^{\ 11}B$ and annealed at 900~C for 15~min. Implant doses and temperatures are denoted in the figure.

critical number needed for dislocation formation was determined by combining the RBS and XTEM results. For RT implants, this number is 1.9×10^{17} cm⁻², similar to previous results [5]. The critical number

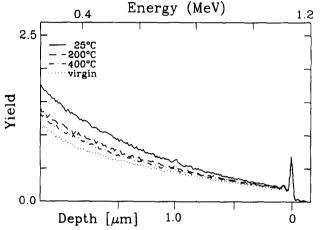


Fig. 2. RBS channeling spectra of silicon implanted with 5×10^{14} cm⁻² 1 MeV boron at 25, 200, or 400 °C. A spectrum of unimplanted silicon is shown for comparison.

decreases by a factor of about 40 with increasing substrate temperature below 10^{16} cm⁻² for implants done at 500 °C. This number is close to the critical number found for boron implants, in which case no a-zones are created [5].

At implant temperatures of about 400 °C, isolated a-zones anneal out [9, 16]. Such annealing of amorphous silicon may influence the point defect population, and thereby dislocation formation. Annealing an indium implanted sample at 400 °C prior to the high temperature treatment may also influence dislocation formation. Figure 9 shows RBS channeling spectra for 3×10^{13} cm⁻² 1 MeV indium implants at 25 and 400 °C. Part of the RT implanted sample was subsequently annealed at 400 °C for 3 h, much longer than the 0.5 h needed to perform the implants. The highest dechanneling, found for the sample implanted at RT, peaks at a depth of 0.4 µm and corresponds to approximately 3.6×10^{17} cm⁻² displaced silicon atoms (Table 1). Annealing this sample at 400 °C for 3 h substantially reduces the measured number of displaced silicon atoms to 4.7×10^{16} cm⁻². However, an even

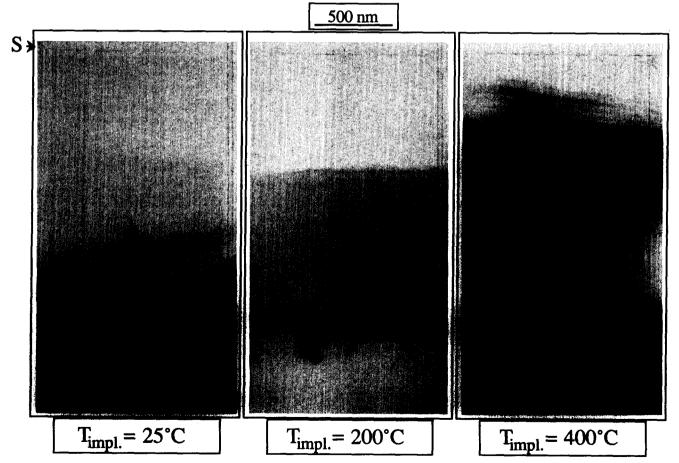


Fig. 3. XTEM images of silicon implanted with 5×10^{14} cm⁻² 1 MeV boron at 25, 200, or 400 °C. Annealing was done at 900 °C for 15 min.

lower dechanneling is observed for the sample *implanted* at 400 °C, where only 1.2×10^{16} cm⁻² displaced silicon atoms are detected. This is in agreement with earlier results for 40 keV antimony implants, which

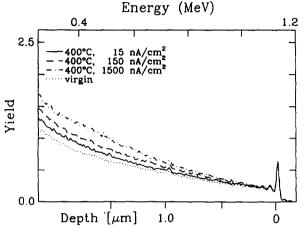


Fig. 4. RBS channeling spectra of silicon implanted with 5×10^{14} cm $^{-2}$ 1 MeV boron at 400 °C for current densities of 15, 150, or 1500 nA cm $^{-2}$. A channeling spectrum of unimplanted silicon is shown for comparison.

also show a greater reduction in disorder for implants *done* at elevated temperature, as opposed to samples only annealed at higher temperatures [17].

XTEM images of these samples after 15 min, 900 °C anneals are shown in Fig. 10. A band with a high concentration of dislocation loops at a depth of $0.4~\mu m$ is observed for both RT implanted samples. Thus, the pre-anneal at 400 °C for 3 h does not influence dislocation formation. However, XTEM analysis of the sample *implanted* at 400 °C shows a much lower concentration of dislocation loops. Hence, only *performing* the 1 MeV indium implant at elevated temperatures alters the dislocation formation. This implies that annealing of the a-zones during the implant reduces the point defect population, and thereby helps suppress dislocation formation.

3.3. Combined boron and indium implants

For the 1 MeV indium implants, a dependence of the critical dose for dislocation formation on implant temperature was observed. This is in contrast with the results for the boron implants, where the only number relevant for dislocation formation was the total number

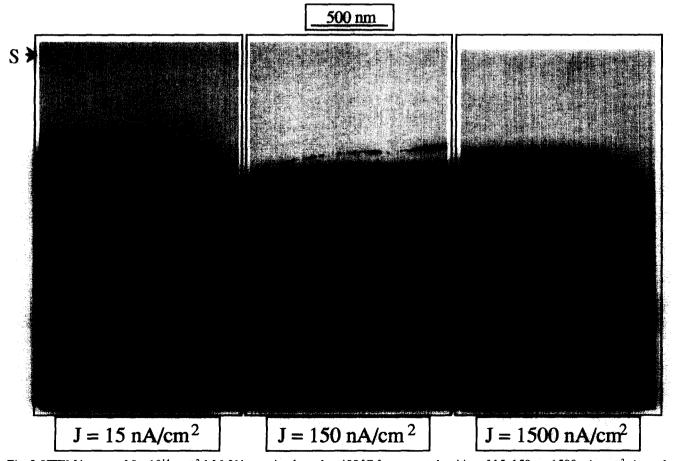


Fig. 5. XTEM images of 5×10^{14} cm⁻² 1 MeV boron implanted at 400 °C for current densities of 15, 150, or 1500 nA cm⁻². Annealing was carried out at 900 °C for 15 min.

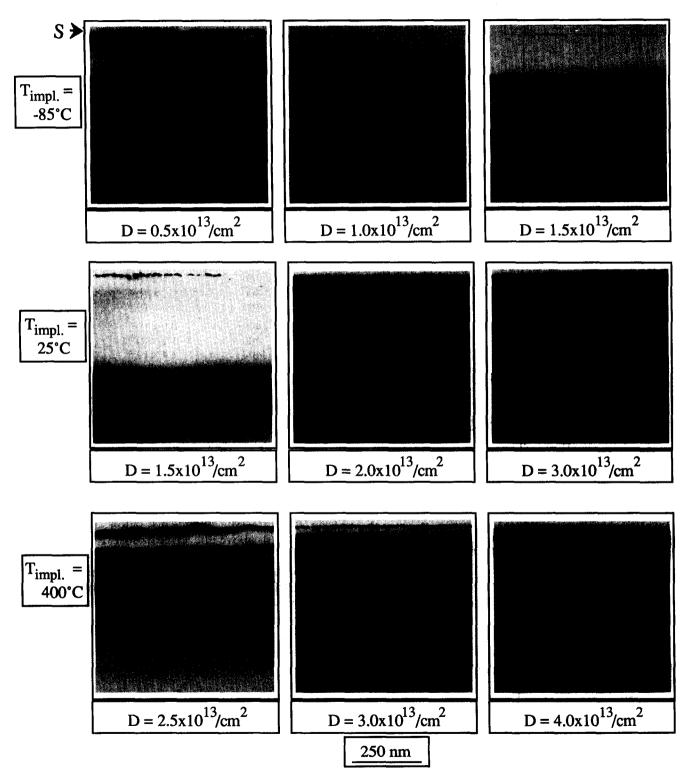


Fig. 6. XTEM images of silicon implanted with 1 MeV indium at -85, 25, or 400 °C for different doses. Annealing was done at 900 °C for 15 min.

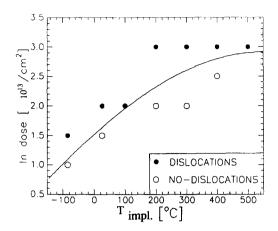


Fig. 7. Summary of XTEM results for 1 MeV indium implants. The full drawn line gives the estimated critical dose for dislocation formation.

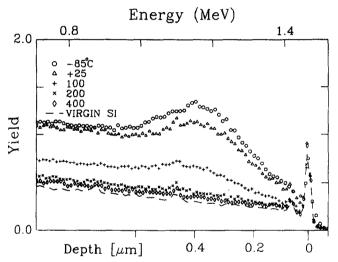


Fig. 8. RBS channeling spectra for indium doses closest to the critical dose for dislocation formation.

of silicon atoms displaced during the implant. The damage generated by indium implants differs from that induced by boron implants by the formation of a-zones, which apparently influences the point defect population and consequently dislocation formation. Sadana et al. suggested that a-c interfaces can act as traps for silicon interstitials, leaving a lower number of interstitials for forming dislocations [18]. By analogy, a-zones could also act as interstitial traps. To test this, combined boron and indium implants were carried out to see whether (regrowing) a-zones trap interstitials. Indium was implanted at 1 MeV at RT to a dose of 0.75×10^{13} cm⁻², half the dose required for dislocation formation (Fig. 7). A significant fraction of the damage generated by this implant is in the form of a-zones. Next, 200 keV boron was implanted to doses of either 0.7 to 1.0×10^{14} cm⁻². These boron implants should

TABLE 1. Overview of the implant doses and temperatures for the 1 MeV indium implants, including the number of displaced silicon atoms estimated from RBS and information concerning dislocation formation (– no dislocations, + dislocations)

$\begin{array}{c} \text{Indium dose} \\ (10^{13}\text{cm}^{-2}) \end{array}$	Implant temperature (°C)	Silicon atoms displaced (cm ⁻²)	Dislocations
0.5	-85	$6.0 \times 10^{16} \\ 1.9 \times 10^{17}$	_
1.0	-85		_/+
1.5	-85	3.6×10^{17}	+
1.5	25	1.5×10^{17}	-
2.0	25	1.9×10^{17}	+
3.0	25	3.6×10^{17}	+
2.0	100	5.0×10^{16}	-/+
3.0	100	8.1×10^{16}	+
4.0	100	9.5×10^{16}	+
2.0	200	2.4×10^{16} 3.4×10^{16} 3.5×10^{16}	-
3.0	200		+
4.0	200		+
2.0	300	1.1×10^{16}	-
3.0	300	1.1×10^{16}	+
4.0	300	1.4×10^{16}	+
2.5	400	0.9×10^{16} 1.2×10^{16} 1.4×10^{16}	-
3.0	400		+
4.0	400		+
3.0	500	0.5×10^{16} 0.4×10^{16} 0.8×10^{16}	-
4.0	500		+
6.0	500		+
3.0	25	3.6×10^{17}	+
3.0	25 and 400, 3 h	4.7×10^{16}	+
3.0	400	1.2×10^{16}	+

only add simple point defects. (From Fig. 1, it can be seen that 0.7×10^{14} cm⁻² is just below, and 1.0×10^{14} cm⁻² is just above the critical dose for dislocation formation for single implants.) If the a-zones generated by the indium implant trapped a significant number of the interstitials created by the boron implant, then dislocations would not form for either implant. Parts of these samples first received an anneal at 400 °C for 3 h. The largest a-zones will not regrow during this anneal [4], while the interstitials will be highly mobile. Hence, interstitials should be able to diffuse and interact with the a-zones.

XTEM images of the samples after the final 900 °C anneal for 15 min are presented in Fig. 11. Dislocations are observed not only for the sample which was implanted with 1.0×10^{14} B cm⁻², but also for the sample with the sub-critical boron dose of 0.7×10^{14} B cm⁻². The number of defects found is highest for the boron dose of 1.0×10^{14} B cm⁻². If a pre-anneal at 400 °C is carried out, the same density of dislocations is observed. Therefore, even if a-zones are in the vicinity of highly mobile interstitials, they have no influence

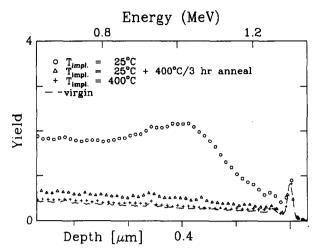


Fig. 9. RBS channeling spectra of silicon implanted with 3×10^{13} cm⁻² 1 MeV indium at RT or 400 °C. Part of the RT implanted sample received an anneal at 400 °C for 3 h.

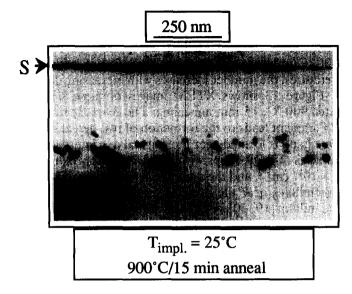
on dislocation formation. From these results it is concluded that a-zones are not efficient traps for interstitials created by its own, or another implant.

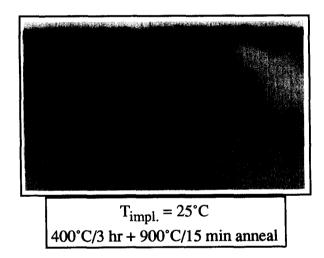
Implanting indium at elevated temperatures affects the formation of amorphous silicon and therefore has a strong influence on damage formation. In the complicated build-up and dynamic annealing of primary damage for high mass implants, the point defect population can also be altered, which consequently influences dislocation formation. For elevated temperature implants of boron, the interaction of damage is less complicated and dislocation formation is not influenced by increasing the implant temperature.

4. Pre-amorphization damage vs. EOR-loop formation

For RT implants of 150 keV indium, amorphization takes place before the critical number of silicon atoms needed for dislocation formation is displaced, and end-of-range dislocation loops (EOR-loops) are observed after annealing [5, 6]. However, if these implants are carried out at an elevated temperature, amorphization should be suppressed and pre-amorphization damage may result instead.

Figure 12 shows RBS channeling spectra of 150 keV indium implants done at 25 and 300 °C for doses of 3×10^{14} , 6×10^{13} , and 3×10^{13} cm⁻². The implant of 3×10^{14} In cm⁻² at RT creates a 110 nm thick amorphous surface layer. If the indium dose is lowered to 6×10^{13} cm⁻², only a buried amorphous layer is formed. In the case of the lowest dose, 3×10^{13} cm², the dechanneling yield does not reach the random level, indicating that a highly damaged silicon region





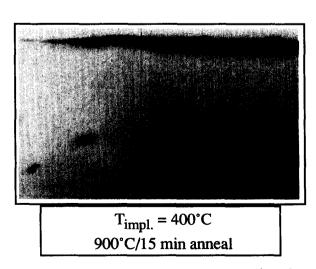


Fig. 10. XTEM images of silicon implanted with 3×10^{13} cm $^{-2}$ 1 MeV indium after final annealing at 900 °C for 15 min. Implant and anneal temperatures are denoted in the figure.

has formed. In contrast, for the 300 °C implants, only small direct scattering peaks are observed by RBS. The numbers of displaced silicon atoms for the 25 and 300 °C implants are given in Table 2. For both temperatures, the number increases with increasing dose.

Figure 13 shows XTEM images of these samples after 900 °C, 15 min annealing. The 3×10^{14} In cm⁻² RT implant leads to the formation of two defect bands. One band is located near the original a-c interface and consists of EOR-loops with a density of approximately 2×10^{10} cm⁻², and the second consists of small indium precipitates near the projected range ($R_p = 40$ nm) of the indium [19, 20]. XTEM of the as-implanted sample for an indium dose of 6×10^{13} cm⁻² shows the forma-

tion of a 30 nm thick buried amorphous layer. Heavily damaged crystalline silicon is observed above and below this amorphous layer, resulting in dislocation formation during the anneal at 900 °C. XTEM of the sample after the 3×10^{13} In cm⁻² RT implant shows a damaged crystalline region, in agreement with the RBS measurement. Annealing of this structure also results in dislocation formation. XTEM after annealing of the sample implanted at 300 °C with 3×10^{14} In cm⁻² shows the formation of a dislocation network near $R_{\rm p}$, along with several small precipitates (Fig. 13). These dislocations are identified as pre-amorphization damage and are much larger and more complicated than the EOR-loops observed for the RT implant.

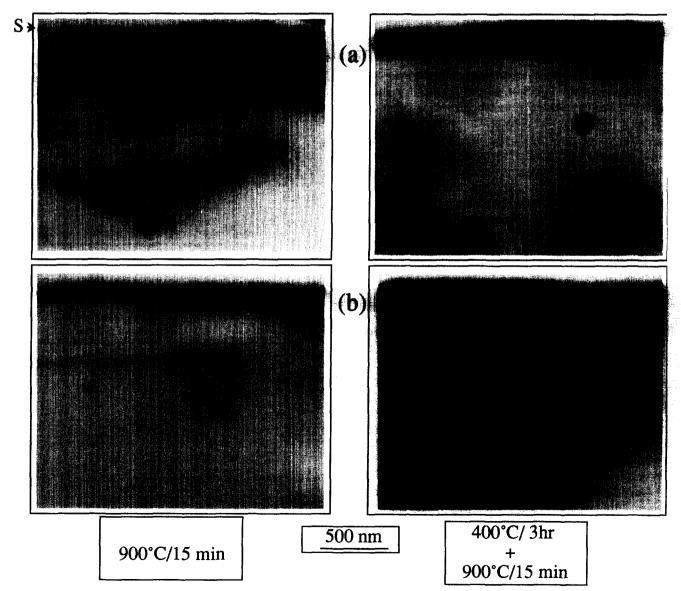


Fig. 11. XTEM images after 900 °C, 15 min annealing of samples implanted with 0.75×10^{13} cm⁻² 1 MeV indium and 200 keV boron to doses of (a) 0.7×10^{14} cm⁻² or (b) 1.0×10^{14} cm⁻². Part of the samples first received an anneal at 400 °C for 3 h.

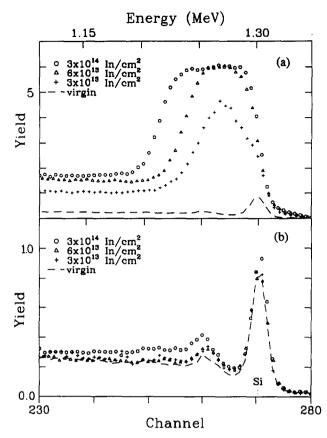


Fig. 12. RBS channeling spectra before annealing for 150 keV indium implants done at RT or 300 °C. Doses and implant temperatures are indicated in the figure.

TABLE 2. Overview of the implant doses and temperatures for the 150 MeV indium implants, including the number of displaced silicon atoms estimated from RBS and information concerning dislocation formation (– no dislocations, + dislocations)

	se Implant ²) temperature (°C	Silicon atoms) displaced (cm ⁻²)	Dislocations
3	25	2.8 × 10 ¹⁷	+
6	25	4.6×10^{17}	+
30	25	6.2×10^{17}	+
3	300	3.2×10^{15}	_
6	300	3.5×10^{15}	+
30	300	9.9×10^{15}	+

Thus, suppressing amorphization by implanting at higher temperatures can indeed result in the formation of pre-amorphization damage. The micrograph of the as-implanted structure for the indium dose of 6×10^{13} cm⁻² shows the presence of crystal damage, which again gives rise to dislocation formation during the furnace anneal. If the indium dose is lowered to 3×10^{13} cm⁻², crystal damage is still observed. How-

ever, no dislocations remain after the anneal of this sample, which is in contrast with the RT implant. The amount of crystal damage for the 300 °C implant must have decreased to such a value that stable dislocations could not form during annealing.

A reduction in dislocation formation was observed for the 1 MeV indium implants at elevated temperature (Section 3.2). In this section, it was shown that elevated temperature implants of 150 keV indium for moderate doses can also reduce or suppress dislocation formation. However, for higher doses, increasing the implant temperature changes the secondary damage from the simple EOR-loops to a complicated dislocation network.

5. EOR-loop formation for germanium implanted at RT and LN_2

Pre-amorphization damage is avoided if the amount of crystal damage generated by the implant is below a critical value. Annealing an amorphous layer results in the formation of EOR-loops from agglomeration of silicon interstitials which are positioned in the a-c transition region. If the amount of crystal damage in the transition region is also reduced below a critical value by performing implants at low temperature, EOR-loop formation may be avoided. To investigate this, amorphizing implants of germanium were performed at RT and LN₂.

Figure 14 presents RBS measurements of samples implanted with 75 keV 73 Ge for different doses and implant temperatures. A surface amorphous layer with a thickness of 100 nm is obtained for a 5×10^{14} cm⁻² RT implant. For a lower dose $(4 \times 10^{14}$ cm⁻²) implant at LN₂, the amorphous layer is almost as thick (90 nm). The germanium implanted structures were annealed at 400 °C for 1 h to avoid the formation of hairpin dislocations [6], at 600 °C for 1 h to induce regrowth of the amorphous layer, and at 900 °C for 15 min to anneal any remaining crystalline damage.

Cross-section and plan-view TEM images of the annealed samples are presented in Fig. 15. A band of EOR-loops at a depth of 100 nm is observed for the RT implant. The diameter of these loops is approximately 25 nm and their concentration is about 1×10^{10} cm⁻². In sharp contrast, no dislocations are observed for the LN₂ implant. This means that the concentration of dislocations is lower than 1×10^5 cm⁻², the detection limit for dislocations in plan-view TEM in our microscope.

The LN₂ implanted sample was also directly annealed at 900 °C for 15 min. This anneal was, as for each anneal described in this paper, performed in a vacuum furnace where it takes some time for the sample to reach the final temperature. Hence, the

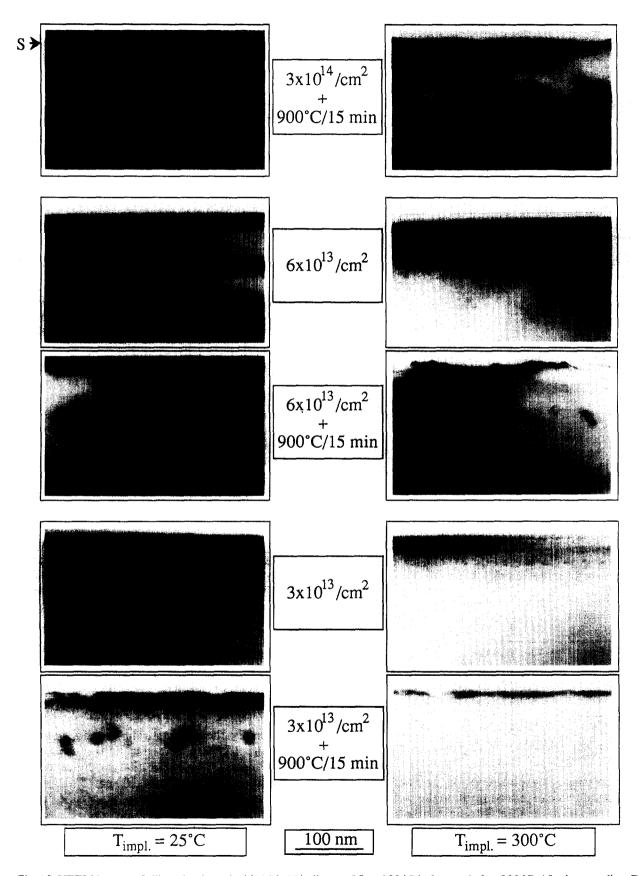


Fig. 13. XTEM images of silicon implanted with 15 keV indium at 25 or 300 $^{\circ}$ C before and after 900 $^{\circ}$ C, 15 min annealing. Doses are indicated in the figures.

amorphous layer will have regrown before the end temperature of 900 °C is reached. TEM analysis again shows no dislocations, so performing the one-step anneal at 900 °C was sufficient.

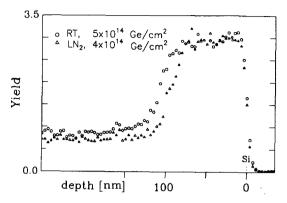


Fig. 14. RBS spectra before annealing of silicon implanted with 75 keV germanium. The RT implanted sample received a dose of 5×10^{14} cm⁻² whereas the LN₂ implanted sample received a dose of 4×10^{14} cm⁻².

Elimination of EOR-loops was observed for the germanium implanted samples if the implant was performed at LN₂. It is known that silicon amorphizes more readily if the implant is performed at low temperatures and the amorphous layer will be thicker for the same implanted dose [8, 12]. This thickness increase results in a lower amount of crystal damage beyond the a-c interface [12, 13]. The amount of crystal damage remaining after the LN₂ implant must have been smaller than the amount needed for dislocation formation.

6. Conclusions

The formation of pre-amorphization damage was investigated for boron and indium implants. For boron implants, the only number which is significant for dislocation formation is the total number of silicon atoms displaced during the implant. The structure of defect complexes formed during implantation (the primary

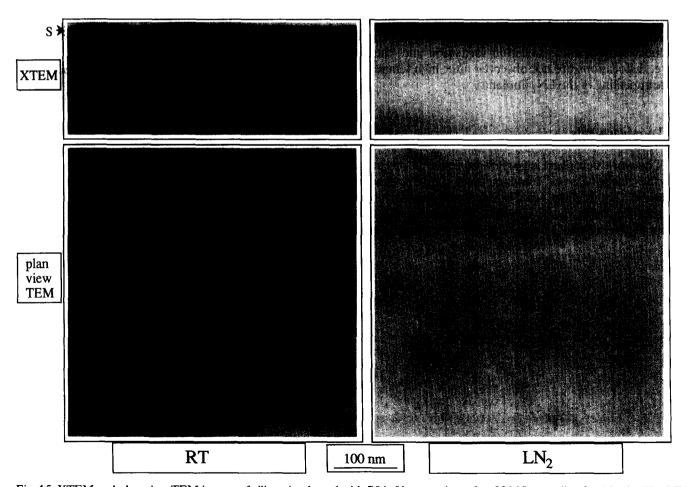


Fig. 15. XTEM and plan-view TEM images of silicon implanted with 75 keV germanium after 900 °C annealing for 15 min. The RT implanted sample received a dose of 5×10^{14} cm⁻² and the LN₂ implanted sample received a dose of 4×10^{14} cm⁻².

damage), which is altered by changing the implant temperature or the current density, is not so critical. In the case of indium implants, where a-zones are a major part of the primary damage, the critical number of displaced silicon atoms needed for dislocation formation decreases by a factor of about 40 with increasing substrate temperature. For implants done above 300 °C, the critical number is approximately 10^{16} cm⁻², close to the number found for boron implants for which no a-zones are created. The elevated temperature implants not only affect the formation of amorphous silicon but also the point defect population.

Implants of 150 keV indium at RT result in complete amorphization before the critical amount of crystal damage is reached, so EOR-loops form after annealing. Increasing the implant temperature suppresses amorphization, and the formation of preamorphization damage is observed. If the implant dose is lowered, a sub-critical amount of crystal damage, less than 10^{16} displaced silicon atoms per square centimeter, is generated for the elevated temperature implant and dislocation formation is avoided.

Performing amorphizing germanium implants at LN_2 instead of RT suppresses EOR-loop formation. The number of interstitials in the a-c transition region for the LN_2 implant is probably lower than the critical number needed for dislocation formation and, therefore, dislocations are not observed after high temperature annealing of the LN_2 implant.

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