Magnetic Tunnel Junctions With Co:TiO₂ Magnetic Semiconductor Electrodes

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Spin-polarized tunneling is investigated in magnetic tunnel junctions containing an ultrathin interfacial layer of $Co:TiO_2$ magnetic semiconductor. The $Co:TiO_2$ layers (0 to 1 nm thick) are inserted at the $SrTiO_3/Co$ interface in $La_{0.67}Sr_{0.33}MnO_3/SrTiO_3/Co$ tunnel junctions. For all junctions we find a negative tunnel magnetoresistance, which decreases upon the insertion of $Co:TiO_2$, while the junction resistance increases strongly. This suggests that the ultrathin $Co:TiO_2$ is a paramagnetic insulator that acts as an additional tunnel barrier, in contrast to thick (180 nm) layers grown under comparable conditions, which exhibit metallic impurity band conduction and room-temperature ferromagnetism.

Index Terms—Cobalt-doped TiO2, La0.67 Sr0.33 MnO3, magnetic semiconductor, magnetic tunnel junction, spintronics.

I. INTRODUCTION

■ HE reported ferromagnetism in wide band gap oxides and nitrides has been of interest in the quest for a room-temperature dilute magnetic semiconductor (DMS), which is desired for the realization of practical spintronic devices. However, the origin of the reported room-temperature ferromagnetism in transition metal doped oxide semiconductors is still quite controversial. The initial theoretical prediction [1] concerns p-type material, with the sp-d exchange interaction as a key ingredient for magnetism mediated by mobile charge carriers. However, the doped oxides exhibiting ferromagnetism contain oxygen vacancies and are n-type semiconductors. It seems unlikely that the originally proposed theory is applicable to such systems. Other proposed models include ferromagnetic exchange interaction between magnetic ions via a donor impurity band [2], and hybridization between the conduction band and the magnetic ions [3]. There are also many reports claiming that the observed magnetism in these oxides is not charge carrier mediated, but of extrinsic origin [4]. In order to address this issue, cobalt-doped titanium dioxide (Co:TiO₂) is a good material to investigate in detail since it has been shown to exhibit room-temperature ferromagnetism [5], anomalous Hall effect (AHE) [6], [7], optical magnetic circular dichroism [8], and impurity band conduction [9].

One of the consequences of carrier-mediated ferromagnetism is that the carriers should be spin polarized. Here, we aim to investigate the spin polarization of the charge carriers in anatase Co:TiO_2 via spin-polarized tunneling in a magnetic tunnel junction (MTJ). Ultrathin Co:TiO_2 layers (Co concentration 1.4%) are inserted between the SrTiO_3 (STO) tunnel barrier and the Co metal electrode of $\text{La}_{0.67}\text{Sr}_{0.33}\text{MnO}_3/\text{SrTiO}_3/\text{Co}$ magnetic tunnel junctions. The epitaxial $\text{La}_{0.67}\text{Sr}_{0.33}\text{MnO}_3$ (LSMO) bottom electrode in combination with the STO barrier functions as a spin analyzer with a given positive tunnel

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spin polarization (TSP), allowing one to probe the TSP of the interface on the opposite side of the STO barrier containing the Co:TiO₂. This provides insight into the origin of magnetism in Co:TiO₂ and the possible role of the carriers [10]. The Co:TiO₂ thin films were grown in anatase phase under conditions for which room-temperature ferromagnetism, AHE, and metallic impurity band conduction were previously observed [7], [9]. We investigate how the sign of the tunnel magnetoresistance (TMR) and its bias dependence change upon insertion of the Co:TiO₂ layers, and compare the results with La_{0.67}Sr_{0.33}MnO₃/SrTiO₃/Co junctions without the Co:TiO₂ studied previously [11].

II. EXPERIMENT

We have grown LSMO (8.5 nm)/STO (3.1 nm)/Co (11 nm)/Au (5 nm) heterostructures onto STO(001) single crystal substrates by pulsed laser deposition using a stoichiometric ceramic target for LSMO and a single crystalline target for STO. The substrates were chemically treated and annealed at 950°C to obtain a TiO₂ termination [12]. Perovskites were grown at 750°C and 1 Hz laser repetition rate under O₂ pressures of 0.35 mbar and 0.30 mbar, respectively, for LSMO and STO. After deposition of the STO barrier, the O₂ pressure was increased to 1 bar and kept at this value during cooling to room temperature, in order to obtain proper O content. Metal Co counter electrodes and Au capping layers were deposited at room temperature without O₂ gas present. Junctions fabricated according to this process are referred to as "standard junctions."

To study the tunnel spin polarization of STO/Co:TiO₂ interfaces, a series of MTJ structures was fabricated with ultrathin Co:TiO₂ films deposited onto the STO tunnel barriers. Since the growth conditions for ferromagnetic Co:TiO₂ (i.e., 9×10^{-5} mbar O₂ and substrate temperature of 550°C) are radically different from the conditions used to grow the LSMO and STO perovskites, we adopted the following procedure. After growth of the STO tunnel barrier, the samples were cooled to room temperature in 1 bar O₂ as described above. Then, the layer stack was heated again to 550°C in 9×10^{-5} mbar O₂ for the Co:TiO₂ deposition, followed by cooling to room temperature in the same 9×10^{-5} mbar O₂ pressure, and deposition of the Co and Au metals at room temperature. These conditions avoid having a high oxygen content in the Co:TiO₂ layer, which

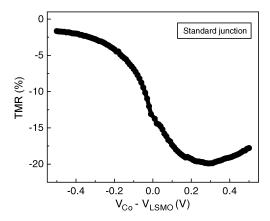


Fig. 1. TMR versus bias voltage for a standard LSMO/STO/Co magnetic tunnel junction at 10 K. Positive bias corresponds to electrons tunneling from the LSMO into the Co.

would lead to insulating behavior and loss of room-temperature ferromagnetism [9]. However, the process may lead to oxygen deficiency in the STO barrier (and in the underlying LSMO), which is known to affect the TMR of the structures [11]. Therefore, we also fabricated reference LSMO/STO/Co MTJs without the insertion of Co:TiO₂, but going through the same process as for the junction with Co:TiO₂. That is, these junctions were heated to 550°C at 9×10^{-5} mbar O₂ for the exact same time as is required for Co:TiO₂ deposition, but without depositing any Co:TiO₂. Such samples will be referred to as "reference junctions" and are expected to have the same oxygen content in the STO barrier as junctions with the Co:TiO₂.

Standard lithographic techniques are used to define junctions with circular active areas of 100 μ m, as previously described [11]. DC current-voltage measurements were carried out in a four-point cross geometry. The TMR ratio is defined as $(R_{ap}-R_p)/R_p$, where R_{ap} and R_p are the resistances for antiparallel and parallel magnetization of the two electrodes, respectively. For all results presented here, the condition is satisfied that the junction resistance is at least 10 times the electrode square resistance [13].

III. RESULTS AND DISCUSSION

Fig. 1 shows the variation of the TMR in the standard junction with bias voltage. The TMR is negative and asymmetric with respect to bias polarity, with a maximum TMR (absolute value) of about 20% at +300 mV, and vanishing TMR for high negative bias. These results are similar to that described in previous reports [11], [14]. The negative TMR is indicative of a negative tunneling spin polarization at the STO/Co interface [11], [14]. Fig. 2 shows the dependence of the resistance R_p of the standard junction on temperature (T). The resistance has a maximum at about 260 K and decreases at low temperature. Such temperature dependence was also observed in similar structures [11], [15], and is usually attributed to a reduced effective ordering temperature of the LSMO, accompanied by a metal-insulator transition, at the LSMO/STO interface [16].

Next, we studied the effect of Co:TiO_2 insertion on the MTJ characteristics, for junctions with, respectively, 0.5 and 1 nm

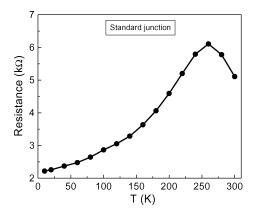


Fig. 2. Tunnel resistance versus temperature T for a standard LSMO/STO/Co magnetic tunnel junction with parallel magnetization configuration and $+\,100~\text{mV}$ bias.

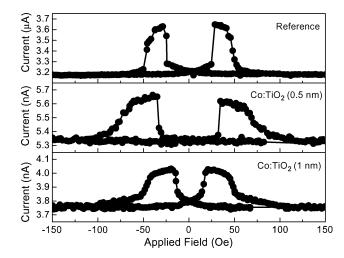


Fig. 3. Tunnel current versus applied magnetic field for the LSMO/STO/Co reference junction (top panel), a LSMO/STO/Co MTJ with 0.5 nm of Co:TiO $_2$ inserted at the STO/Co interface (middle panel), and a LSMO/STO/Co junction with 1.0 nm of Co:TiO $_2$ inserted at the STO/Co interface (bottom panel). All data taken at 10 K and for + 100 mV bias.

Co:TiO₂ added between STO and Co. As shown in Fig. 3, a sizeable and negative TMR is observed for MTJs with 0.5 nm and 1 nm Co:TiO2 inserted, and also for the reference junction (Co:TiO₂ deposition conditions mimicked but no Co:TiO₂ deposited). The TMR with 1 nm of Co:TiO2 is slightly higher than with 0.5 nm, but the difference is only small and within the junction-to-junction variation of the TMR that is always present. Fig. 4 shows the bias dependence of the TMR. All junctions show an asymmetric bias dependence as for the standard junction, but with different TMR maxima: at +50 mV (+40 mV) for MTJs with 0.5 nm (1 nm) Co:TiO2 inserted, and at +150 mV for the reference MTJ. Moreover, the TMR decreases rapidly with bias beyond the maximum, which is similar to that of LSMO/STO/Co junctions with oxygen deficient barriers [11]. In contrast, standard LSMO/STO/Co MTJs feature a broad maximum at +300 mV, with a slow decay of the TMR as the bias voltage is increased to higher positive values (see Fig. 1). For the junctions with Co:TiO₂, the magnitude of the TMR is significantly smaller as compared to that of the reference junction, and the TMR maximum becomes more narrow and shifts toward

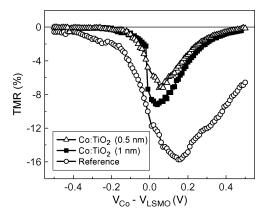


Fig. 4. TMR versus bias for the reference junction (circles), an MTJ with 0.5 nm of Co:TiO $_2$ inserted (triangles), and an MTJ with 1 nm of Co:TiO $_2$ inserted (squares). All data taken at $10~\rm K$.

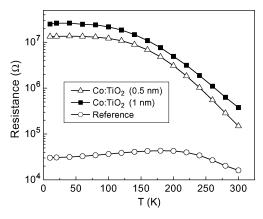


Fig. 5. Tunnel resistance versus temperature T with parallel magnetization configuration and +100 mV bias for the reference junction (circles), an MTJ with 0.5 nm of Co:TiO₂ inserted (triangles), and an MTJ with 1 nm of Co:TiO₂ inserted (squares).

zero bias. In addition, the junction resistance increases significantly upon insertion of the Co:TiO₂ by about 3 orders of magnitude at 10 K (see Fig. 5). We can exclude that this is due to the loss of oxygen in the tunnel barrier, as this should also be present for the reference junction. Comparing Fig. 2 and Fig. 5, we observe that the reference junction has an order of magnitude higher junction resistance in comparison with the standard MTJ. This difference is attributed to the change in oxygen content of the STO barrier due to heating of the structure to 550°C under oxygen poor conditions $(9 \times 10^{-5} \text{ mbar})$.

The strong increase of the junction resistance upon insertion of the Co:TiO₂, accompanied by a decrease of the TMR, provides some clues about the electronic and magnetic properties of the ultrathin Co:TiO₂ films. The higher junction resistance suggests an increased effective barrier thickness, which in turn points to insulating behavior of the inserted Co:TiO₂ layers. This is somewhat surprising since thicker films of Co:TiO₂ (180 nm), grown under comparable conditions (550°C at 7×10^{-5} mbar O₂) on STO substrates, exhibit impurity band conduction and are (semi-)conducting with a carrier density of 5×10^{18} cm⁻³ and mobility of about 20 cm²/Vs at room temperature, and resistivity of the order of 0.1 Ω cm in the range between 10 and 300 K [9]. Hence, it was expected that the

inserted Co:TiO₂ would become part of the electrode, rather than act as a tunnel barrier. An ultrathin film of only a nm thick (the unit cell of anatase Co:TiO₂ along the c-axis growth direction is 0.95 nm) may have different electronic properties and behave as a 2-D system which does not exhibit impurity band formation under these growth conditions. Alternatively, the film properties may be affected by the contact with the metal Co electrode on top of it. For instance, this may lead to carrier depletion in the Co:TiO₂, leaving it insulating.

If the Co:TiO₂ is indeed insulating and acts as an additional tunnel barrier, the tunneling electrons, and the associated negative TMR, originate from the Co metal at the interface with the Co:TiO₂. Since thick, more resistive Co:TiO₂ films grown under oxygen rich conditions are found to be paramagnetic at room temperature [9], the reduced TMR is also readily explained. For, the spin-polarized tunneling electrons originating from the Co would then experience spin-flip scattering by paramagnetic Co ions present in the Co:TiO₂ part of the tunnel barrier, which is known [17], [18] to reduce the TMR. Note that in previous work [11], the introduction of a pure TiO₂ layer (without Co doping) was found not to change the TMR, which indicates that the reduction of the TMR observed here is not due to changes in the Co metal electrode or its interface with the oxides, but due to the Co doping in the TiO₂.

IV. CONCLUSION

It is found that the TMR and junction resistance of epitaxial LSMO/STO/Co magnetic tunnel junctions changes significantly upon the insertion of ultrathin layers of Co:TiO₂ magnetic semiconductor at the STO/Co interface. The magnitude of the TMR decreases but remains negative, while the junction resistance increases strongly. This is consistent with an effectively insulating and paramagnetic Co:TiO₂ adding to the tunnel barrier, with the tunneling electrons originating mostly from the Co:TiO₂/Co interface, and experiencing spin-flip scattering by paramagnetic Co in the Co:TiO₂. Because the properties of the ultrathin Co:TiO₂ in contact with a metal Co electrode appear different from thicker films grown under similar conditions, the experiments unfortunately do not confirm or rule out a finite spin polarization of carriers in thicker Co:TiO₂ films.

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