# Ideal RESURF Geometries

Alessandro Ferrara, *Student Member, IEEE*, Boni K. Boksteen, *Student Member, IEEE*, Raymond J. E. Hueting, *Senior Member, IEEE*, Anco Heringa, Jurriaan Schmitz, *Senior Member, IEEE*, and Peter G. Steeneken

Abstract-In order to maximize the OFF-state breakdown voltage (BV) of semiconductor devices, the slope of the electric field in the drift extension along the current flow direction  $(E_x \text{ field})$  should be zero. This is achieved using the reduced surface field (RESURF) effect. This paper demonstrates a method to construct devices that obey Poisson's equation and satisfy the ideal RESURF condition giving zero slope in  $E_x$  throughout the 2-D device region. The designs are obtained by shaping the device geometry and the boundaries and by applying the proper potentials at the boundaries. Using this method, ideal designs of the drift extension have been derived for devices based on graded doping, graded thickness, and graded fieldplate potential. In addition, 2-D solutions have been derived for periodic superjunction device geometries. A solution for devices that combine several types of field shaping is demonstrated. Finally, the effect of nonideal geometries on the BV in more realistic geometries is discussed.

*Index Terms*—Charge balance, electric field, field plate, high voltage, power MOSFET, reduced surface field (RESURF), Silicon-on-insulator, superjunction.

#### I. INTRODUCTION

**B**REAKDOWN of semiconductor devices occurs due to an avalanche of charge carriers initiated by high electric fields in reverse biased junctions [1], [2]. In order to increase the breakdown voltage (BV) of high-voltage semiconductor devices, a relatively long drift extension is, therefore, used to reduce the electric fields [2]. It is commonly known [3] that the OFF-state BV is maximized by ensuring that the electric field in the drift extension along the current flow direction (*x*-direction) satisfies

$$\frac{\partial E_x(x, y)}{\partial x} = 0. \tag{1}$$

This is defined as the ideal RESURF condition for the  $E_x$  field. For most device designs, the solution of Poisson's equation

Manuscript received June 1, 2015; accepted July 14, 2015. Date of current version September 18, 2015. This work was supported in part by the Dutch Point-One Program and in part by Agentschap NL, an agency of the Dutch Ministry of Economic Affairs. The review of this paper was arranged by Editor R. M. Todi.

A. Ferrara, B. K. Boksteen, R. J. E. Hueting, and J. Schmitz are with the MESA+ Institute for Nanotechnology, University of Twente, Enschede 7522 NB, The Netherlands (e-mail: a.ferrara@utwente.nl; b.k.boksteen@utwente.nl; r.j.e.hueting@utwente.nl; j.schmitz@utwente.nl).

A. Heringa, retired, was with NXP Semiconductors, Eindhoven 5656 AE, The Netherlands (e-mail: a.heringa@hi.nl).

P. G. Steeneken is with NXP Semiconductors, Eindhoven 5656 AE, The Netherlands, and also with the Delft University of Technology, Delft 2628 CD, The Netherlands (e-mail: peter.steeneken@nxp.com).

Color versions of one or more of the figures in this paper are available online at http://ieeexplore.ieee.org.

Digital Object Identifier 10.1109/TED.2015.2460112

a) 1D (no-RESURF)  $E_x(x)$   $\rho$   $E_x(x+dx)$   $E_x(x+dx)$   $E_x(x+dx)$   $E_x(x,y)$   $E_x(x,y)$   $E_x(x+dx,y)$   $E_x(x+dx,y)$  $E_x(x+dx,y)$ 

Fig. 1. Schematics explaining the RESURF principle by considering an infinitesimal part of the drift region containing a constant charge density  $\rho$  and having dielectric constant  $\varepsilon$ . (a) 1-D device in the absence of the RESURF effect. Since  $(\partial E_y/\partial y) = 0$ , according to Poisson's equation (2), the electric field  $E_x$  has a slope  $(\partial E_x/\partial x) = (\rho/\varepsilon)$ . (b) Ideal field condition can be satisfied by applying an electric field in the y-direction with gradient  $(\partial E_y/\partial y) = (\rho/\varepsilon)$ , such that  $(\partial E_x/\partial x) = 0$ , and  $E_x$  is constant.

does not obey (1) throughout the 2-D geometry. As will be shown, an ideal RESURF geometry requires the boundaries to be shaped in a special manner, an aspect that has not been dealt with in [4]–[10]. If the ideal field condition is not satisfied everywhere in the xy plane, the BV of these devices is lower than for those satisfying (1) at a given drift extension length L. For this reason, the investigation of device designs that provide the solutions of Poisson's equation obeying (1) is worthwhile. Exact solutions of (1) are also useful for the performance optimization of other semiconductor devices, such as FinFETs, nanowire or gate-all-around FETs and deep submicrometer transistors [11].

The Reduced Surface Field (RESURF) [12], [13] principle is based on reducing the field in the current flow direction (x-direction) by introducing a perpendicular field gradient (in the y-direction), as shown in Fig. 1. In the absence of RESURF [Fig. 1(a)], with zero  $E_y$ -field gradient, the  $E_x$  field has a gradient ( $\partial E_x/\partial x$ ) =  $(\rho/\varepsilon)$ , where  $\rho$  is the charge density and  $\varepsilon$  is the dielectric constant. In Fig. 1(b), the RESURF effect is implemented using a ( $\partial E_y/\partial y$ )-field gradient in the drift region. Since according to Poisson's equation, ( $\partial E_x/\partial x$ ) + ( $\partial E_y/\partial y$ ) =  $(\rho/\varepsilon)$ , it follows that for ( $\partial E_y/\partial y$ ) =  $(\rho/\varepsilon)$ , the gradient ( $\partial E_x/\partial x$ ) becomes zero, and the ideal RESURF condition (1) is satisfied. The graphs in Fig. 1 (right) illustrate that at identical maximum field and BV, a 2-D drift extension needs

0018-9383 © 2015 IEEE. Personal use is permitted, but republication/redistribution requires IEEE permission. See http://www.ieee.org/publications\_standards/publications/rights/index.html for more information.



Fig. 2. Schematic potential distribution  $\psi(x, y)$  in the drift extension of ideal RESURF devices. Only half of the symmetric structure is shown. The device is mirror symmetric in the boundary at y = 0, and a field plate (at potential  $V_{\text{FP}}$ ) or periodic boundary condition is placed at  $y = t_1 + t_2$ .

only half of the length compared with a 1-D one. The BV is equal to the gray area under the graphs.

An additional advantage of the application of RESURF is that a higher doping in the drift region can be used. While increasing the doping would lead to a reduction of the BV in a 1-D device [Fig. 1(a)], the RESURF effect can be used to balance the increase in charge density by a higher vertical field gradient  $(\partial E_y/\partial y)$ . This further improves the specific ON-resistance  $R_{ON}A$ -BV tradeoff of power devices [14].

This paper focuses on constructing device designs that satisfy the ideal RESURF [Fig. 1(b)] throughout their 2-D geometry. For the sake of generality, the x-direction and the y-direction in this paper, respectively, refer to the direction of the current flow and any of the two directions perpendicular to it, independently of the device orientation with respect to the wafer plane. Therefore, the proposed designs can be implemented in both lateral and vertical devices with RESURF fields acting either in or out of the wafer plane.

The rest of this paper is organized as follows. Section II describes ideal RESURF geometries in 2-D that are derived by an exact analytical solution of Poisson's equation. Two types of geometries are discussed: 1) geometries that use field plates to generate the field gradient  $(\partial E_y/\partial y)$  and 2) periodic geometries that use superjunctions to satisfy the ideal field condition. Section III shows implementation examples of the different types of a combined RESURF device. Section V discusses the effect of deviations from the ideal case on the BV. Finally, the conclusions are presented in Section VI.

#### II. DERIVATION OF THE IDEAL RESURF SOLUTION

In this section, analytical solutions of Poisson's equation are derived that satisfy the ideal RESURF condition  $(\partial E_x/\partial x) = 0$ . The assumed geometry and boundary conditions are shown in Fig. 2. The structure consists of two domains, with charge densities  $\rho_{1,2}$  and dielectric constants  $\varepsilon_{1,2}$ . The interface between the domains is assumed to be ideal, i.e., free of interface charges. At this interface, the perpendicular displacement field  $D_{\perp}$ , the parallel field  $E_{\parallel}$ , and the potential  $\psi$  need to be continuous. Domain 1 is made of a semiconductor material that has the purpose to block current at high voltages in the OFF-state and to conduct current in the ON-state. The main purpose of domain 2 is to generate the field gradient  $(\partial E_v / \partial y)$  needed for the RESURF effect in the current blocking state. Domain 2 is either made of a dielectric without fixed charge ( $\rho_2 = 0$ ) or of a semiconductor material with opposite charge density compared with that in domain 1  $(\rho_2 \neq 0)$ . Since the structure is intended for blocking voltages up to the BV, the right boundary is at a potential  $\psi = BV$ , whereas the left boundary is at the ground ( $\psi = 0$ ). The geometry, and potential  $\psi(x, y)$ , is symmetrical with respect to the reflection in the x-axis  $[(\partial \psi(x, y)/\partial y)|_{y=0} = 0]$ . The nonperiodic geometries are terminated by a field plate at the boundary  $y = t_1 + t_2$ , which is held at a field-plate potential  $\psi(x) = V_{\rm FP}(x)$ . The periodic geometries are also symmetric with respect to reflection in the line  $y = t_1 + t_2$ , and therefore require  $(\partial \psi / \partial y) = 0$  at this interface. The dielectric constants in both domains  $\varepsilon_{1,2}$  are assumed to be position independent, since the spatial inhomogeneities due to process variations in a slab of semiconductor or dielectric material have negligible effects on the field distribution. A continuous significant grading of  $\varepsilon_2$  for achieving optimal RESURF, suggested in [15], requires custom technology not readily available today, and therefore has not been considered in this paper.

For constant  $\varepsilon$ , Poisson's equation needs to be obeyed

$$\nabla^2 \psi(x, y) = \frac{\partial^2 \psi(x, y)}{\partial x^2} + \frac{\partial^2 \psi(x, y)}{\partial y^2} = -\frac{\rho(x, y)}{\varepsilon}.$$
 (2)

Potential distributions of the form  $\psi(x, y) = -(\alpha x + \beta)f(y)/\varepsilon$  are considered, which satisfy both (1) and (2) if  $(\alpha x + \beta)(\partial^2 f(y)/\partial y^2) = \rho(x, y)$ . Since charge distributions with a zero or constant charge gradient are the most relevant for RESURF devices [4], [15]–[19], this paper focuses on the solutions of the potential distribution  $\psi(x, y)$  with  $(\partial^2 f(y)/\partial y^2) = 1$ , such that  $\rho(x, y)$  is a linear function of x and is not a function of y

$$\rho(x, y) = \rho(x) = \alpha x + \beta \tag{3}$$

with  $\alpha$  and  $\beta$  being constants.

The function  $\psi(x, y)$  satisfying (1) and (2), the conditions  $\psi_1(0, 0) = 0$  and  $\psi_1(L, 0) = BV$ , and (3) for any (x, y) is found to be

$$\psi(x, y) = \frac{\mathrm{BV}}{L}x - \frac{\rho(x)y^2}{2\varepsilon} + c_1y + c_2 \tag{4}$$

where  $c_1$  and  $c_2$  are the integration constants. In other words, (4) describes an exact ideal RESURF solution of Poisson's equation for charge distributions  $\rho(x)$  that are either constant or have a linearly graded doping concentration in the *x*-direction. This solution needs to be extended to both the domains and suitable boundary conditions need to be found.

Using the symmetric boundary condition from Fig. 2,  $(\partial \psi_1 / \partial y) = 0$  at y = 0, the solution  $\psi_1(x, y)$  inside domain 1 is found to be

$$\psi_1(x, y) = \frac{BV}{L}x - \frac{\rho_1(x)}{2\varepsilon_1}y^2.$$
 (5)

This solution can be extended to domain 2 using the continuity conditions at the interface between domain 1 and domain 2. These conditions ensure the continuity of the displacement field  $D_{\perp}$  perpendicular to the boundary and the electric field  $E_{\parallel}$  parallel to the boundary (see Fig. 2). When the interface is parallel to the *x*-axis (i.e.,  $t_1$  does not vary with *x*), the potential  $\psi_2$  in domain 2 is given by

$$\psi_{2}(x, y) = \frac{BV}{L}x - \frac{\rho_{1}(x)}{2\varepsilon_{1}}t_{1}\left[t_{1} + 2\frac{\varepsilon_{1}}{\varepsilon_{2}}(y - t_{1})\right] - \frac{\rho_{2}(x)}{2\varepsilon_{2}}(y - t_{1})^{2}.$$
 (6)

It thus has been shown that (5) and (6) satisfy both Poisson's equation and the ideal field condition throughout both domains. Moreover, they satisfy the boundary conditions along the line y = 0 and the continuity equations at the interface  $y = t_1$ .

For (5) and (6), to represent actual solutions of Poisson's equation, two challenges remain. First, the outer boundary conditions at  $y = t_1 + t_2$  need to be satisfied. This will be discussed in Section II-A for nonperiodic and field-plate-assisted structures and in Section II-B for periodic structures. Next, the conditions  $\psi = 0$  and  $\psi = BV$ , respectively, on the left and right boundaries need to be satisfied for all y. This can be achieved by geometry shaping of these boundaries, as described in Section II-C.

#### A. Field-Plate-Assisted RESURF

In nonperiodic structures, the RESURF gradient in the y-direction is created using a field plate. The field plate symmetrically terminates the structure at the top and the bottom of the drift extension. In order to satisfy the boundary condition, the potential on the field-plate  $V_{\rm FP}$  needs to match that of the solution in (6), such that  $\psi(x, y)|_{y=\pm(t_1+t_2)} = V_{\rm FP}(x)$ . Thus, for a field plate located at  $y = \pm(t_1 + t_2)$ , the field-plate potential  $V_{\rm FP}(x)$  is obtained by substituting  $y = \pm(t_1 + t_2)$  in (6) resulting in

$$V_{\rm FP}(x) = \frac{\rm BV}{L}x - \frac{\rho_1(x)}{2\varepsilon_1}t_{\rm eq}(x)^2 - \frac{\rho_2(x)}{2\varepsilon_2}t_2(x)^2$$
(7)

where the equivalent thickness  $t_{eq}$  [15], [18] can be expressed in terms of  $t_1$  and  $t_2$  using

$$t_{\rm eq}(x) = \sqrt{t_1 \left[ t_1 + \frac{2\varepsilon_1}{\varepsilon_2} t_2(x) \right]}.$$
 (8)

# B. Periodic RESURF

As an alternative to field plates, semiconductor domains with opposite charge density can be used to generate the gradient  $(\partial E_y/\partial y)$ . These periodic structures can be created by alternating domains 1 and 2 in an infinite stack of domains with constant thicknesses  $2t_1$  and  $2t_2$ . Due to the repetitive, periodic nature of the superjunction stack, it is sufficient to show that the solution satisfies the boundary and continuity conditions at y = 0,  $y = t_1$ , and  $y = t_1 + t_2$ .

If the structure is periodic in the y-direction, the boundary conditions at  $y = \pm (t_1 + t_2)$  do not only require the



Fig. 3. Schematic of an example ideal RESURF structure that can be derived from Fig. 2 using the geometry shaping procedure in Section II-C. The left and right boundaries need to follow the shape of the equipotential lines in order to keep the potential distribution ideal.

continuity of the potential  $\psi(x, y)|_{y=\pm(t_1+t_2)}$  but also of its *y*-derivative. From symmetry considerations, this is only possible if  $(\partial \psi / \partial y)|_{y=\pm(t_1+t_2)} = 0$ . Applying this condition to (6) yields the well-known charge balance condition [6]

$$\rho_1(x)t_1 + \rho_2(x)t_2 = 0. \tag{9}$$

This shows that in order to satisfy the ideal field condition, the charge distributions in the periodic domains need to satisfy (9), which requires the sign of the doping charge to alternate between the n-type and the p-type. If domain 2 is an ideal dielectric with zero fixed charge, such as in periodic dielectric RESURF (DIELER [20]), charge balance cannot be achieved, and (1) cannot be satisfied.

Equations (5) and (6) provide the RESURF solution in the field-plate-assisted case together with (7), and describe the ideal potential distribution in the periodic domains for superjunction devices together with (9). After having satisfied the interface boundary conditions, the geometry shaping procedure in Section II-C will be applied in order to satisfy the boundary conditions at the left and right terminals for both the periodic and nonperiodic devices.

## C. Geometry Shaping of the Left and Right Boundaries

The left and right boundaries of the structure can be described by a parametric equation  $(x, y) = (x_b(s), y_b(s))$ . The potential  $\psi_b$  as a function of the position parameter *s* at the boundaries is given by (5) and (6)

$$\psi_b(s) = \psi(x_b(s), y_b(s)). \tag{10}$$

The potential  $\psi_b$  along the left  $(x_{b0}, y_{b0})$  and right  $(x_{bBV}, y_{bBV})$  boundaries of domain 1 is imposed to be a constant. In order to obtain boundary designs that satisfy this condition, a geometry shaping procedure is applied, which ensures that the boundaries run along an equipotential line of (5), as shown schematically in Fig. 3. Thus, the shape of the left and right boundaries is found according to (10) by finding the equipotential lines  $(x_b, y_b)$ that satisfy the following equations:

$$\psi(x_{b0}(s), y_{b0}(s)) = 0$$
 (a)  
 $\psi(x_{bBV}(s), y_{bBV}(s)) = BV$  (b).
(11)

Some degrees of freedom are present in the choice of the shape on the left and right boundaries of domain 2. Suitable

shapes can be chosen depending on the type of RESURF. If the structure cannot be terminated on equipotential boundaries, as in (11), a nonzero potential gradient on the boundaries will be present. In any case, ideal solutions are obtained as long as the boundary is shaped, such that the potential along the boundary obeys (10). It should be emphasized that the shaping procedure described in this section is important for inducing 2-D ideal RESURF solutions. Devices with straight boundaries at constant potential will usually not exactly obey (1) for nonzero doping  $\rho_1$  (see Section V-A). After having derived ideal RESURF solutions, Section III will focus on providing examples of their implementation in several important cases.

# III. PROCEDURE AND EXAMPLES OF IDEAL RESURF STRUCTURES

In this section, several examples of ideal RESURF device architectures are given (see also [15]). In order to design a structure that yields an exact solution of Poisson's equation and satisfies the ideal RESURF condition (1), the following procedure is taken.

- 1) Choose BV and device length L, making sure that (BV/L) is smaller than the critical field  $E_{\text{crit}}$  (see Section V-A) to prevent the breakdown.
- 2) Choose the semiconductor thickness  $t_1$ .
- 3) For field-plate RESURF:
  - a) choose one graded parameter  $\rho_1$ ,  $t_2$ , or  $V_{\text{FP}}$ ;
  - b) fix the other two (nongraded and constant) parameters making sure that junction, vertical and dielectric breakdown are prevented (see Section V-C);
  - c) determine the gradient (*x*-dependence) of the graded parameter using (7).
- 4) For Periodic (superjunction) RESURF:
  - a) fix  $\rho_1(x)$ .
  - b) choose  $t_2$  and  $\rho_2(x)$  to satisfy (9).
- 5) Shape the left and right boundaries according to (11) making use of (5) and (6).

This procedure is demonstrated for several cases in this section. The procedure captures most practical cases, but does not yield all possible types of solutions, since one might also consider to grade two parameters, or to grade  $t_1$  [21] or the dielectric constants  $\varepsilon$  [7]. It is noted that the analytical solutions of the potential (5) and (6) are found to be in exact agreement with numerical solutions of Poisson's equations using COMSOL [22] within numerical accuracy. A further validation of the model has been proposed in [15] using TCAD simulations [23] for solving the semiconductor equations.

## A. Field-Plate-Assisted RESURF

By choosing one graded parameter in (7) [Step 3a)], the ideal RESURF solutions are demonstrated in some significant cases: 1) graded doping in domain 1  $\rho_1$  (Fig. 4); 2) graded field-plate potential  $V_{\text{FP}}$  (Fig. 5); and 3) graded thickness  $t_2$  of domain 2 (Fig. 6). In each of the three figures, the solutions are shown for dielectric RESURF [ $\rho_2 = 0$ , subfigure (a)] and for p-n junction RESURF [ $\rho_2 \neq 0$ , subfigure (b)]. As can be seen, the equipotential lines are



Fig. 4. Potential distribution  $\psi(x, y)$  in a RESURF device with graded charge. (a) Dielectric RESURF ( $\varepsilon_1 = 11.7\varepsilon_0, \varepsilon_2 = 3.9\varepsilon_0, \rho_1 = 1.5 \text{ C/cm}^4 \cdot x, \text{ and } \rho_2 = 0$ ). (b) p-n junction RESURF ( $\varepsilon_1 = \varepsilon_2 = 11.7\varepsilon_0, \rho_1 = 4.9 \text{ C/cm}^4 \cdot x, \text{ and } \rho_2 = -0.32 \cdot 10^{-3} \text{ C/cm}^3$ ). Device dimensions:  $t_1 = t_2 = 2.5 \ \mu\text{m}$  and  $L = 5 \ \mu\text{m}$ . Boundary potentials:  $V_{\text{left}} = 0 \text{ V}, V_{\text{right}1} = \text{BV} = 136.2 \text{ V}, \text{and } V_{\text{right}2} = 54.4 \text{ V/}\mu\text{m} \cdot (t_1 + t_2 - y)$  in (a) and  $V_{\text{right}2} = 1.5 \text{ V/}\mu\text{m}^2 \cdot (t_1 + t_2 - y)^2 + 50.6 \text{ V/}\mu\text{m} \cdot (t_1 + t_2 - y)$  in (b),  $V_{\text{top-bottom}} = 0 \text{ V}$ .



Fig. 5. Potential distribution  $\psi(x, y)$  in a RESURF device with graded field plate potential. (a) Dielectric RESURF ( $\varepsilon_1 = 11.7\varepsilon_0$ ,  $\varepsilon_2 = 3.9\varepsilon_0$ ,  $\rho_1 = 8 \cdot 10^{-3}$  C/cm<sup>3</sup>, and  $\rho_2 = 0$ ). (b) p-n junction RESURF ( $\varepsilon_1 = \varepsilon_2 = 11.7\varepsilon_0$ ,  $\rho_1 = 8 \cdot 10^{-3}$  C/cm<sup>3</sup>, and  $\rho_2 = -3.2 \cdot 10^{-3}$  C/cm<sup>3</sup>). Device dimensions:  $t_1 = t_2 = 0.5 \ \mu$ m and  $L = 5 \ \mu$ m. Boundary potentials:  $V_{\text{left}} = 0 \ \text{V}$ ,  $V_{\text{right}} = \text{BV} = 136.2 \ \text{V}$ , and  $V_{\text{top-bottom}} = 27.2 \ \text{V/}\mu \text{m} \cdot (x - 2.5 \ \mu\text{m})$  in (a) and  $V_{\text{top-bottom}} = 27.2 \ \text{V/}\mu \text{m} \cdot (x - 0.93 \ \mu\text{m})$  in (b).

equidistant in the x-direction, showing that the ideal RESURF condition (1) is obeyed throughout the 2-D region in all the cases.

## B. Periodic RESURF

An example of periodic superjunction RESURF is shown in Fig. 7. As discussed in Step 4 of the procedure in Section III, no graded parameters are needed for ideal periodic RESURF. The charge densities  $\rho_1$  and  $\rho_2$ , thicknesses, and field-plate potentials are, therefore, taken to be constant.

#### **IV. COMBINED RESURF STRUCTURES**

Besides achieving ideal RESURF by grading one parameter or exploiting periodicity, one can also create



Fig. 6. Potential distribution  $\psi(x, y)$  in a RESURF device with graded domain 2. (a) Dielectric RESURF ( $\varepsilon_1 = 11.7\varepsilon_0, \varepsilon_2 = 3.9\varepsilon_0, \rho_1 = 0.4 \cdot 10^{-2}$  C/cm<sup>3</sup>, and  $\rho_2 = 0$ ). (b) p-n junction RESURF ( $\varepsilon_1 = \varepsilon_2 = 11.7\varepsilon_0, \rho_1 = 0.4 \cdot 10^{-2}$  C/cm<sup>3</sup>, and  $\rho_2 = -0.4 \cdot 10^{-3}$  C/cm<sup>2</sup>). Device dimensions:  $t_1 = 1 \ \mu\text{m}, t_2 = 0.24 \cdot (x - 0.71 \ \mu\text{m})$  in (a) and  $t_2 = 467.2 \ \mu\text{m}^{-1} \cdot (x - 0.71 \ \mu\text{m})^2 + 0.66 \cdot (x - 0.71 \ \mu\text{m})$  in (b) and  $L = 5 \ \mu\text{m}$ . Boundary potentials:  $V_{\text{left}} = 0 \ \text{V}, V_{\text{right}1} = \text{BV} = 136.2 \ \text{V}, \text{and } V_{\text{right}2} = 126 \ \text{V}/\mu\text{m} \cdot (t_1 + t_2 - y)$ in (a) and  $V_{\text{right}2} = 1.94 \ \text{V}/\mu\text{m}^2 \cdot (t_1 + t_2 - y)^2 + 21.2 \ \text{V}/\mu\text{m} \cdot (t_1 + t_2 - y)$ in (b),  $V_{\text{top-bottom}} = 0 \ \text{V}$ .



Fig. 7. Potential distribution  $\psi(x, y)$  in a periodic superjunction RESURF device (three periods are shown). Device parameters:  $\varepsilon_1 = \varepsilon_2 = 11.7\varepsilon_0$ ,  $\rho_1 = 3.2 \cdot 10^{-3}$  C/cm<sup>3</sup>,  $\rho_2 = -8 \cdot 10^{-4}$  C/cm<sup>3</sup>, and  $L = 1 \ \mu$ m. Boundary potentials:  $V_{\text{left}} = 0$  V,  $V_{\text{right}} = \text{BV} = 34.2$  V, and  $V_{\text{top,bottom}} = 34.2$  V/ $\mu$ m · ( $x - 0.02 \ \mu$ m).

combined devices. Combining different methods can be useful when the grading of only one parameter requires values that are difficult to achieve in real devices. Fig. 8 shows a semiconductor-dielectric structure (with  $\rho_2 = 0$ ), where the graded-field plate, graded charge, and graded dielectric thickness methods have been combined. The compound device has been constructed by the methods discussed in Section III,



Fig. 8. Exact solution of the 2-D potential distribution  $\psi(x, y)$  in a compound dielectric RESURF device consisting of three regions (I, II, and III). Region I ( $L_{\rm I} = 3 \ \mu {\rm m}$ ) has a linearly graded field-plate potential from  $V_{\rm FP} = 0$  V to  $V_{\rm FP} = 67.2$  V,  $N_D = 5 \cdot 10^{15}$  cm<sup>-3</sup>, and  $t_1 = t_2 = 0.5 \ \mu {\rm m}$ . Region II ( $L_{\rm II} = 4 \ \mu {\rm m}$ ) has a linearly graded doping from  $N_D = 5 \cdot 10^{15}$  cm<sup>-3</sup> to  $N_D = 7.8 \cdot 10^{16}$  cm<sup>-3</sup>,  $V_{\rm FP} = 67.2$  V, and  $t_1 = t_2 = 0.5 \ \mu {\rm m}$ . Region III ( $L_{\rm III} = 3 \ \mu {\rm m}$ ) has a linearly graded domain 2 thickness from  $t_2 = 0.5 \ \mu {\rm m}$  to  $t_2 = 1 \ \mu {\rm m}$ ,  $t_1 = 0.5 \ \mu {\rm m}$ ,  $N_D = 7.8 \cdot 10^{16}$  cm<sup>-3</sup>, and  $V_{\rm FP} = 67.2$  V. Left-right boundary potentials:  $V_{\rm left} = 0$  V,  $V_{\rm right1} = {\rm BV} = 246.7$  V, and  $V_{\rm right2} = 67.2$  V + 179.4 V/ $\mu {\rm m} \cdot (t_1 + t_2 - y)$ .

making sure that the potential is continuous at the interfaces I–II and II–III. Thus, the device satisfies (1) and (2) throughout the 2-D region.

## V. DISCUSSION

In Section III, examples of ideal semiconductor-dielectric structures satisfying the RESURF condition have been shown. Deviations from ideal RESURF and other issues occurring in real devices are discussed in this section, which focuses on the shape of the boundaries (Section V-A), linearly graded potentials (Section V-B) and premature breakdown (Section V-C).

# A. Effect of Deviations From Ideal RESURF on BV

The curved design of the left and right boundaries needed for ideal RESURF increases the total device length along the x axis. In Fulop's approximation [24] breakdown occurs at a critical field  $E_{\rm crit} = [1/(Af^1/7L^1/7)]$ , where  $A_f = 1.8 \cdot 10^{-35} \text{ cm}^6/\text{V}^7$  is the silicon ionization coefficient, and L is the device length. The BV of an ideal device with curved boundaries is then  $BV_{curved} = E_{crit}L = (L^{6/7}/A_f^{1/7})$ . If the device has straight boundaries, however, for the same length L, the device will have a lower BV, because the boundaries do not have the ideal shape. The BV with straight boundaries is given by  $BV_{straight} = \eta BV_{curved}$ , where  $\eta =$ (BV<sub>straight</sub>/BV<sub>curved</sub>) is defined as the ratio of BVs of a device with straight boundaries and that of a device with ideally curved boundaries with the same length L. The parameter  $\eta$ depends on the device length, geometry, and doping profile and can be extracted in simulations by calculating the ionization integral along the symmetry line at y = 0 (where the  $E_x$  field is maximum). In Fig. 9,  $\eta$  is shown for a device with graded field-plate voltage and  $\rho_2 = 0$  as a function of the length L for different values of the fixed charge  $\rho_1$  in Fig. 9(a) and of the equivalent thickness  $t_{eq}$  in Fig. 9(b). It can be seen that  $\eta$  reduces for increasing  $\rho_1$  and for increasing thicknesses  $t_{eq}$ (assuming  $t_1 = t_2$ ). The reduced values of  $\eta$  in these cases are



Fig. 9. Ratio between the straight and the curved  $E_x$  BV [ $\eta = (BV_{straight}/BV_{curved})$ ] for a dielectric RESURF device with graded fieldplate potential, as in Fig. 5(a), having (a) different doping charges  $\rho_1 = qN_D$ and (b) different thicknesses  $t_{eq}$  (with  $t_1 = t_2$ ).

caused by the more pronounced curvature of the equipotential lines, which increases the difference between the curved and straight device designs and the BV. As the device length increases,  $\eta$  approaches unity, because boundary-shape-related effects become less significant for  $L \gg t_1$ . The value of  $\eta$  is also close for very short devices, since in the limit of  $t_1 \gg L$ , the electric field will behave as in a parallel plate capacitor, and the effect of curvature on BV becomes small. As a result, for the simulated devices in Fig. 9, the parameter  $\eta$  reaches a minimum for a device length  $L \approx 1-5 \mu m$ .

## B. Boundary Conditions With Graded Potential

In non-periodic devices, a gradient is needed to achieve the optimal RESURF condition using the methods shown in Section III. Grading the potential at the field plate and dielectric boundaries and/or at the left and right boundaries is possible. In practice, graded boundary potentials are not easily implemented. A possibility is to split the graded potential in a number of constant voltage field plates [25], or to use conductive resistive elements [26]. It is also possible to exploit structural symmetries to achieve a graded potential on a boundary without a field plate. For example, the periodicity in the y-direction is exploited in superjunctions resulting in the charge balance condition (9). Similarly, a symmetric design around the x = L line can be used to mimic a linearly graded potential on the right boundary.

## C. Junction, Vertical, and Dielectric Breakdown

Besides breakdown caused by the  $E_x$  field, device breakdown might also be caused by the  $E_y$  field [15]. In order to arrive at an optimized device design, it is, therefore, necessary to design the RESURF device, such that the ionization integral along the electric field lines in the y-direction at x = Ldoes not exceed unity. Using the Fulop's approximation [24], this condition yields

$$\rho_1(x=L) \cdot t_1(x=L)^{7/8} \le \left(\frac{8}{A_f}\right)^{1/8} \varepsilon_1^{7/8}$$
(12)

with  $A_f = 1.8 \cdot 10^{-35} \text{ cm}^6/\text{V}^7$  in silicon. In addition, avalanche breakdown of the p-n junction comprising the p-well (for an nMOS) of the power transistor and the drift extension has to be considered. As shown in [15], the condition

 $N_D(x = 0) \cdot t_1 \leq 10^{12} \text{ cm}^{-2}$  [12] limiting the RESURF dose at x = 0 can be used for this purpose in silicon-based devices. In the presence of dielectrics, the magnitude of the electric field should not exceed the critical value for dielectric breakdown. Finally, it is worth mentioning that for electric field magnitudes exceeding 70 (V/ $\mu$ m) in silicon band-to-band tunneling also limits the BV [17], [27].

# VI. CONCLUSION

In this paper, 2-D analytical solutions of Poisson's equation have been derived that satisfy the ideal RESURF condition (1). Several examples of how these solutions can be used for ideally shaping the geometry of field-plate and periodic RESURF devices have been demonstrated. The analytical solutions provide insight in the physics and facilitate device optimization. The different geometry shaping procedures demonstrate the degrees of freedom available for optimizing RESURF devices. Moreover, the derivation of the shape of idealized structures allows the analysis of deviations from the ideal shape in practical devices. For instance, it is shown that devices with curved boundaries can have a higher breakdown than devices with straight boundaries. With sufficient technological control over device dimensions and doping profiles, the demonstrated device geometries with curved boundaries and field plates can lead to optimized RESURF drift extensions.

#### REFERENCES

- [1] S. M. Sze, *Physics of Semiconductor Devices*. New York, NY, USA: Wiley, 1969.
- [2] B. J. Baliga, Fundamentals of Power Semiconductor Devices. New York, NY, USA: Springer-Verlag, 2008.
- [3] A. W. Ludikhuize, "A review of RESURF technology," in *Proc. 12th* ISPSD, 2000, pp. 11–18.
- [4] S. Merchant, "Analytical model for the electric field distribution in SOI RESURF and TMBS structures," *IEEE Trans. Electron Devices*, vol. 46, no. 6, pp. 1264–1267, Jun. 1999.
- [5] A. Popescu, F. Udrea, R. Ng, and W. I. Milne, "Analytical modelling for the RESURF effect in JI and SOI power devices," *IEE Proc.-Circuits, Devices, Syst.*, vol. 149, no. 56, pp. 273–284, 2002.
- [6] T. Fujihira, "Theory of semiconductor superjunction devices," Jpn. J. Appl. Phys., vol. 36, no. 10, pp. 6254–6262, 1997.
- [7] X. Luo, B. Zhang, and Z. Li, "A new structure and its analytical model for the electric field and breakdown voltage of SOI high voltage device with variable-k dielectric buried layer," *Solid-State Electron.*, vol. 51, no. 3, pp. 493–499, 2007.
- [8] S.-K. Chung and S.-Y. Han, "Analytical model for the surface field distribution of SOI RESURF devices," *IEEE Trans. Electron Devices*, vol. 45, no. 6, pp. 1374–1376, Jun. 2002.
- [9] A. G. M. Strollo and E. Napoli, "Power superjunction devices: An analytic model for breakdown voltage," *Microelectron. J.*, vol. 32, nos. 5–6, pp. 491–496, 2001.
- [10] S.-K. Chung, "An analytical model for breakdown voltage of surface implanted SOI RESURF LDMOS," *IEEE Trans. Electron Devices*, vol. 47, no. 5, pp. 1006–1009, May 2000.
- [11] J.-P. Colinge, *FinFETs and Other Multi-Gate Transistors*. New York, NY, USA: Springer-Verlag, 2008.
- [12] J. A. Appels and H. M. J. Vaes, "High voltage thin layer devices (RESURF devices)," in *Proc. IEDM*, 1979, pp. 238–241.
- [13] Y. S. Huang and B. J. Baliga, "Extension of RESURF principle to dielectrically isolated power devices," in *Proc. 3rd ISPSD*, Apr. 1991, pp. 27–30.
- [14] R. P. Zingg, "On the specific on-resistance of high-voltage and power devices," *IEEE Trans. Electron Devices*, vol. 51, no. 3, pp. 492–499, Mar. 2004.
- [15] B. K. Boksteen, A. Ferrara, A. Heringa, P. G. Steeneken, G. E. J. Koops, and R. J. E. Hueting, "Design optimization of field-plate assisted RESURF devices," in *Proc. 25th ISPSD*, May 2013, pp. 237–240.

- [16] S. Mahalingam and B. J. Baliga, "A low forward drop high voltage trench MOS barrier Schottky rectifier with linearly graded doping profile," in *Proc. ISPSD*, Jun. 1998, pp. 187–190.
- [17] B. K. Boksteen *et al.*, "On the degradation of field-plate assisted RESURF power devices," in *Proc. IEDM*, Dec. 2013, pp. 13.4.1–13.4.4.
- [18] B. K. Boksteen, A. Ferrara, A. Heringa, P. G. Steeneken, and R. J. E. Hueting, "Impact of interface charge on the electrostatics of field-plate assisted RESURF devices," *IEEE Trans. Electron Devices*, vol. 61, no. 8, pp. 2859–2866, Aug. 2014.
- [19] J. He *et al.*, "Linearly graded doping drift region: A novel lateral voltage-sustaining layer used for improvement of RESURF LDMOS transistor performances," *Semicond. Sci. Technol.*, vol. 17, no. 7, pp. 721–728, 2002.
- [20] J. Sonsky and A. Heringa, "Dielectric resurf: Breakdown voltage control by STI layout in standard CMOS," in *Proc. IEDM*, Dec. 2005, pp. 376–379.
- [21] X. Luo *et al.*, "A new SOI high-voltage device with a step-thickness drift region and its analytical model for the electric field and breakdown voltage," *Semicond. Sci. Technol.*, vol. 23, no. 3, pp. 035028-1–035028-6, 2008.
- [22] COMSOL Multiphysics (Version: 3.4), COMSOL, Inc., Burlington, MA, USA, 2007.
- [23] *ATLAS (Version: 5.18.3.R)*, Silvaco, Inc., Santa Clara, CA, USA, 2012. [24] W. Fulop, "Calculation of avalanche breakdown voltages of silicon p-n
- junctions," *Solid-State Electron.*, vol. 10, no. 1, pp. 39–43, 1967.
- [25] M. J. Kumar and R. Sithanandam, "Extended-p<sup>+</sup> stepped gate LDMOS for improved performance," *IEEE Trans. Electron Devices*, vol. 57, no. 7, pp. 1719–1724, Jul. 2010.
- [26] G. A. M. Hurkx and R. van Dalen, "Semiconductor device with voltage divider for increased reverse blocking voltage," Patent EP 1 169738 A1, Jan. 9, 2002.
- [27] G. A. M. Hurkx, D. B. M. Klaassen, and M. P. G. Knuvers, "A new recombination model for device simulation including tunneling," *IEEE Trans. Electron Devices*, vol. 39, no. 2, pp. 331–338, Feb. 1992.



Alessandro Ferrara (S'12) received the master's (*cum laude*) degree in electronic engineering from the Federico II University of Naples, Naples, Italy, in 2011. He is currently pursuing the Ph.D. degree with the University of Twente, Enschede, The Netherlands, in collaboration with NXP Semiconductors, Eindhoven, The Netherlands.



**Raymond J. E. Hueting** (S'94–M'98–SM'06) received the M.Sc. (*cum laude*) and Ph.D. degrees in electrical engineering from the Delft University of Technology, Delft, The Netherlands.

He joined the Semiconductor Components Group, University of Twente, Enschede, The Netherlands, in 2005, where he is also involved in semiconductor device physics and modeling.

**Anco Heringa** received the M.Sc. degree in technical physics from the University of Groningen, Groningen, The Netherlands, in 1977.

He was a Consultant in process/device modeling with Philips, Eindhoven, The Netherlands, until 2002. He was with NXP, Eindhoven, where he was involved in integrated high-voltage devices until his retirement in 2014.



**Jurriaan Schmitz** (M'02–SM'06) received the M.Sc. and Ph.D. degrees, in 1990 and 1994, respectively.

He is currently a Full Professor of Semiconductor Components with the University of Twente, Enschede, The Netherlands, where he is also the Head of the Department of Electrical Engineering.



**Boni K. Boksteen** (S'10) received the M.Sc. (*cum laude*) degree in electrical engineering from the University of Twente, Enschede, The Netherlands, in 2010, where he is currently pursuing the Ph.D. degree.

He is also involved in RESURF-based power MOS optimization and reliability with the University of Twente in collaboration with NXP Semiconductors, Eindhoven, The Netherlands.



**Peter G. Steeneken** received the M.Sc. (*cum laude*) and Ph.D. degrees in physics from the University of Groningen, Groningen, The Netherlands, in 1997 and 2002, respectively.

He has been with Philips, Eindhoven, The Netherlands, and NXP Semiconductors, Eindhoven, since 2002. He is currently a part-time Professor of Applied Nanophysics with the Delft University of Technology, Delft, The Netherlands.