# Software Defined Radio

Vincent J. Arkesteijn, Roel Schiphorst, Fokke W. Hoeksema, Eric A.M. Klumperink, Bram Nauta and Cornelis H. Slump faculty of Electrical Engineering University of Twente PO Box 217, 7500 AE Enschede the Netherlands v.j.arkesteijn@el.utwente.nl http://icd.el.utwente.nl/ http://www.sens.el.utwente.nl/



### **Aim**

- a flexible (programmable/configurable) mobile radio
- focus is on the analog + digital front-end

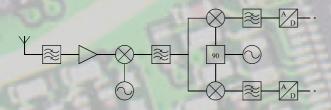
### **Motivation**

- Convenient: true global roaming
- Cheap: only one device for all standards (economy of scale)
- Short TTM: no new hardware necessary to support new standard
- High performance and long battery life: high performance when necessary, low energy consumption when possible

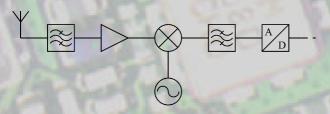
## Some architectures

Super heterodyne

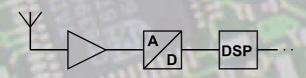
# energy efficiency



- + proven concept
- + low power consumption
- inflexible
- Digital IF



- + more flexible
- seperate front-ends necessary for each standard
- Utopical software radio



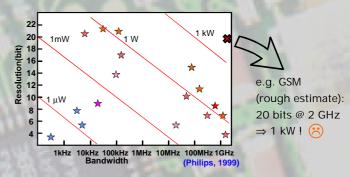
- + flexible
- unfeasible due to technology constraints

# **Technology constraints**

A/D conversion

Trade-off between resolution, sampling frequency and power (see figure below for some A/D-convertors)

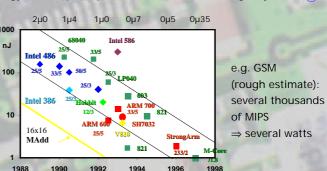
Progress is very slow



So: the ideal software radio will remain a utopy for a long time.

Digital processing

Energy consumption per instruction is decreasing very fast:



• The ideal software radio will remain a utopy for a long time, so another solution is required.

Year of Introduction

- As the energy efficiency of digital hardware progresses fast, more and more functionality will be implemented digitally.
- An architectural solution depends on analog and digital technology, so an approach from both analog and digital perspective is essential.

# Challenge

- To find an architecture that is both flexible and feasible.
  - what architecture to choose
  - how to partition analog/digital parts
  - how to partition flexibility (ASIC/FPGA/DSP)

#### **Acknowledgment**

This research is supported by the PROGram for Research on Embedded Systems & Software (PROGRESS) of the Dutch organization for Scientific Research NWO, the Dutch Ministry of Economic Affairs and the technology foundation STW.