AUTOMATIC TUNING OF QUALITY FACTORS FOR VHF CMOS FILTERS

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Abstract

A novel circuit for tuning both the quality factors and the cut-off frequency of an integrated filter is described. The Q-tuning circuit itself has no signal carrying nodes and is therefore suitable for filters at very high frequencies [5]; up to 100 MHz.

Introduction

To correct the frequency response of an integrated filter for process and temperature variations, tuning of the cut-off frequency [1] (f-tuning) is generally applied. Several filters are also provided with automatic tuning of the quality factors (Q-tuning) [2]. Combined f- and Q-tuning can be applied with either a master VCF [2] (Voltage Controlled Filter). or a master VCO [3] (Voltage Controlled Oscillator). In fig. 1 the method using a master VCO is illustrated. Consider first the Q-tuning loop. The Q-loop controls the amplitude of the VCO in such a way that it will oscillate with a constant amplitude. If the Q of the VCO is infinite, then the VCO will oscillate harmonically with a constant amplitude (the poles are exactly on the j ω axis of the complex plane). If the To correct the frequency response of an integrated

exactly on the j ω axis of the complex plane). If the voltage used for tuning the Q of the two integrators in the master VCO is copied to the (matched) integrators

in the slave filter, the quality factors of the filter will automatically be correct. The amplitude of the VCO signal is uncritical as long as the integrators in the

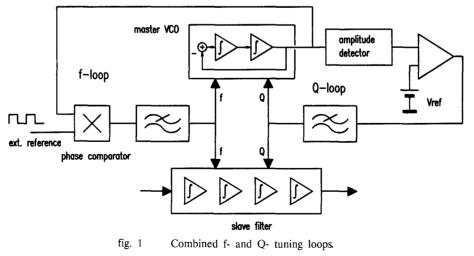
VCO operate in their linear region. The f-control loop is a well known phase locked loop (PLL) which locks the oscillating frequency to an external reference frequency. The voltage used for tuning the frequency of the VCO is copied to the slave filter

filter. The combination of f- and Q-tuning is possible if the f- and Q-control loops are independent. This is difficult in practice. If the Q-tuning loop is much faster than the f- tuning loop, the f- tuning loop will be quasi static and then the f- and Q-loops become practically independent. For VHF filters the Q-loop must be fast enough to tune the VCO which oscillates at least at the cut-off frequency of the filter, which can be up to 110 MHz [5]. The Q-tuning loop must therefore be very fast. be very fast.

This paper describes a Q-tuning technique without a physical loop, so that it is very fast and therefore suitable for VHF.

A VHF filter approach

Recently, an approach has been introduced for VHF transconductance-C filters [4] which makes use of a



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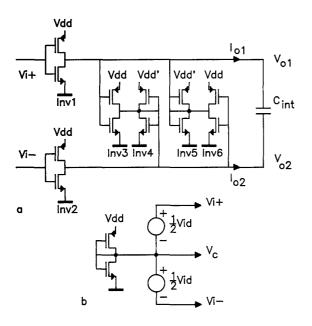


fig. 2 Li ear VHF transconductance element [4].

transconductance element without internal nodes. This transconductance element is given in figure 2a. The basic V-I conversion is performed by two CMOS inverters Inv1 and Inv2. If these inverters are driven by a differential input voltage V_{id} , balanced around the common mode level V_C (see fig. 2b), the differential output current will be linear with the differential input voltage. This can be shown as follows.

If the drain currents of an N-channel and a P-channel MOS transistor in strong inversion and saturation are written as:

$$I_{dn} = -\frac{\beta_n}{2} (V_{gs} - V_{tn})^2 \text{ and } I_{dp} = -\frac{\beta_p}{2} (V_{gs} - V_{tp})^2 , (1)$$

the differential output current of the two inverters can be written as:

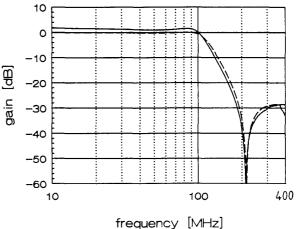
$$\mathbf{I}_{od} = \mathbf{I}_{o2} - \mathbf{I}_{o1} = \mathbf{V}_{id} (\mathbf{V}_{dd} - \mathbf{V}_{tn} - |\mathbf{V}_{tp}|) \sqrt{\beta_n \cdot \beta_p} = \mathbf{V}_{id} \cdot gm_d \quad (2)$$

The transconductance gm_d can be tuned by means of V_{dd} . The common mode level of the output voltages V_{o2} and V_{o1} is controlled by the four inverters Inv3-Inv6. Inv4 and Inv5 are shunted as resistors $1/gm_4$ and $1/gm_5$. The output currents of Inv3 and Inv6, $gm_3(V_c \cdot V_{o1})$ and $gm_6(V_c \cdot V_{o2})$ respectively, are injected into these resistors.

resistors. For common input signals the result is that the "V_{o1}" node is loaded with a resistor $1/(gm_s+gm_6)$ and the "V_{o2}" node with a resistor $1/(gm_3+gm_4)$. For differential signals the "V_{o1}" node is loaded with a resistor $1/(gm_5-gm_6)$ and the "V_{o2}" node is loaded with a resistor $1/(gm_4-gm_3)$. If the inverters have the same supply voltage and geometry, all the gm's will ideally be equal. Thus the network Inv3-Inv6 forms a low-ohmic load for differential signals, resulting in a controlled common mode voltage level of the outputs.

mode voltage level of the outputs. The DC-gain of the transconductor-C integrator is increased by loading the transconductor with a negative resistance for differential signals. By choosing $gm_3 > gm_4$, $gm_5 = gm_4$ and $gm_6 = gm_3$ this negative resistance, 1/ $\Delta gm = 1/(gm_4 - gm_3) = 1/(gm_5 - gm_6)$, is simply implemented, without adding extra nodes to the circuit: The width of the transistors in Inv4 and Inv5 is designed slightly smaller than those of Inv3 and Inv6. In order to make Q tuning possible, the DC-gain of the integrators is fine-tuned during operation with a separate supply voltage V_{dd} for Inv4 and Inv5. Increasing V_{dd} with constant V_{dd} results in a lower DC-gain of the integrator and therefore in lower quality factors of the filter. Thanks to the absence of internal nodes and the tunable output resistance, the phase error of the transconductance-C integrator will be very small up to very high frequencies. With this transconductor, filters at very high frequencies can be made. Figure 3, for example, shows the measured frequency response of a 100 MHz third order elliptic low-pass filter [5]. The f-tuning was done by means of V_{dd} and Q tuning was done by means of V_{dd} . Both the f- and Q- tuning were performed manually. In the next section an automatic Q-tuning technique for the transconductance element of fig. 2 is presented.

- measured ---- ideal



Frequency response of a 100MHz third order

elliptic low-pass filter [5].

Automatic Q-tuning

With the transconductor described above the master VCO of fig. 4 can be made. If the Q of the VCO is infinite, it will oscillate at a frequency determined by V_{dd} . For every value of V_{dd} there is only one value of V_{dd} resulting in correct Q. The inverse is also true: for each V_{dd} there is only one value of V_{dd} so that the Q is correct. It follows that the frequency can also be tuned with V_{dd} ' if the Q-loop controls V_{dd} . V_{dd} and V_{dd} will then be related correctly. This is very important for the Q-tuning circuit presented here.

Consider the VCO is oscillating harmonically with an amplitude V_a at a frequency ω . Using eq (1) the supply current I_{dd} is calculated (see figure 4). This results in:

fig. 3

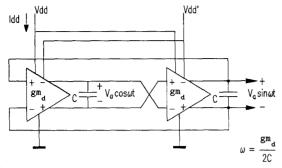


fig. 4 Voltage Controlled Oscillator.

$$\mathbf{I}_{dd} = 2\beta_{p} \left(2 \left[\frac{\sqrt{\beta_{n} / \beta_{p}}}{1 + \sqrt{\beta_{n} / \beta_{p}}} (\mathbf{V}_{dd} - \mathbf{V}_{tn} - |\mathbf{V}_{tp}|) \right]^{2} + \frac{1}{4} \mathbf{V}_{a}^{2} (\sin^{2} \omega t + \cos^{2} \omega t) \right]$$
(3)

and since $\sin^2 \omega t + \cos^2 \omega t = 1$, this can be written as:

$$I_{dd} = 2\beta_{p} \left[2 \left[\frac{\sqrt{\beta_{n} / \beta_{p}}}{1 + \sqrt{\beta_{n} / \beta_{p}}} (V_{dd} - V_{tn} - |V_{tp}|) \right]^{2} + \frac{1}{4} V_{a}^{2} \right]$$
(4)

If $V_a=0$, therefore no oscillation, then I_{dd} consists of the quiescent current of 8 inverters, biased in their linear region. In the case of oscillation, $V_a \neq 0$, the current is larger but remains constant. If the node V_{dd} is supplied by means of a *DC current source*, with a value given by eq. (4), instead of a *voltage source* with value V_{dd} , the oscillator will oscillate with a constant and well controlled amplitude V_a . This can be seen as follows:

1) Suppose the amplitude V_a tends to increase (poles of the VCO in the right half plane). With a constant I_{dd} this implies from eq.(4) that V_{dd} must decrease. With a (quasi static) constant V_{dd} ' this implies that gm_3 , and gm_6 (fig. 2) decrease while gm_4 and gm_5 remain constant so that the oscillation is damped. The amplitude will therefore decrease.

2) Suppose the amplitude V_a tends to decrease (poles in the left half plane). With a constant I_{dd} this implies that V_{dd} must increase. With a (quasi static) constant V_{dd} this implies that gm_3 , and gm_6 increase while gm_4 and gm_5 remain constant so that the oscillation is undamped. The amplitude will therefore increase.

The result of this mechanism is that, for a given V_{dd} , V_{dd} is controlled in such a way that the poles of the VCO will always be on the imaginary axis; the Q factor

of the VCO is then infinite. If the resulting voltage V_{dd} of the master VCO is copied to the filter by means of a buffer, the quality factors of the slave filter will automatically be correct

The problem now is how to realize the current source I_{dd} with the value given by eq. (4). This can be done

 I_{dd} with the value given by eq. (4) This can be done as follows. Usually $V_{dd} \approx V_{dd}$, with this in mind the current I_{dd} can be made from V_{dd} , which in turn is determined by the frequency control loop. This is shown in figure 5. The current I_o is determined by V_{dd} , V_b , and the inverter parameters. The inverter in fig. 5 is equal to those in the VCO and all P-MOS and N-MOS transistors have equal geometries. The current I_o can be calculated, this results in:

$$I_{o} = \frac{1}{2} \beta_{p} \left[2 \left[\frac{\sqrt{\beta_{n} / \beta_{p}}}{1 + \sqrt{\beta_{n} / \beta_{p}}} (V_{dd}^{*} - V_{tn}^{*} - |V_{tp}|) \right]^{2} + 2V_{b}^{2} \right]$$
(5)

Comparing eqs. (4) and (5) it can be seen that if:

$$V_{b} = \frac{1}{4}\sqrt{2} V_{a}, \qquad (6)$$

the current I_o has only to be multiplied with a factor four to obtain the current given by eq. (4), as long as $V_{dd} \approx V_{dd}$. This multiplication is simply performed with an 1:4 current mirror. The voltage buffer copies the voltage V_{dd} to the slave filter. Normally the transistors deviate from ideal square law behavior, the result of this is that the current I_{dd} of the VCO is not exactly constant. I_{dd} will contain higher harmonics of the oscillation frequency ω . The capacitance C_{dd} however will drain these currents, so that the ripple in V_{dd} remains very small. The capacitance C_{dd} is the N-well to substrate capacitance of the P-MOS transistors, which will be on chip. If necessary the buffer can in addition be preceded by a simple low pass filter. filter.

Note, that if the circuit of fig. 5 , the VCO and the

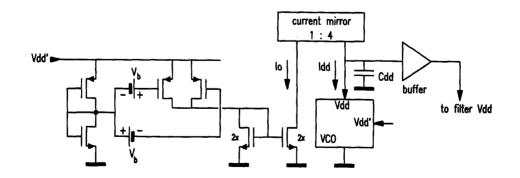


fig. 5 Circuit to generate the current I_{dd}.

slave filter all have the same temperature, temperature effects are compensated.

The advantage of this Q-tuning circuit is that it needs no fast amplitude detectors or rectifiers, owing to the intrinsic wideband amplitude detection provided by the transconductance element of fig. 2 (see also eq.(3)). The circuit of fig. 5 has no signal carrying nodes. All nodes have a (quasi static) DC voltage. For this reason the circuit is suitable for very high frequencies. The only signal carrying nodes are the outputs of the two integrators inside the VCO. Furthermore the circuit is extremely simple; it only consists of one - quasi static - current source, two current mirrors and a buffer.

Experimental results

The circuit of figure 5 and the VCO have been realized on breadboard, using commercially available CA3600 CMOS arrays. The voltage V_b was chosen 0.5V. Using eq. (6), the amplitude V_a of the VCO is expected to be $0.5^* 2\sqrt{2} = 1.4V$.

 $0.5^{-2}V^{2} = 1.4 v$. The VCO oscillates at frequencies up to 7MHz. The results are plotted in figure 6. The voltage V_{dd} varies with V_{dd} ' in such a way that the VCO oscillates with a constant amplitude of almost 1.4 V -as expected. The frequency varies almost linearly with V_{dd} as predicted by eq. (2).

frequency varies almost linearly with V_{dd} as predicted by eq. (2). Simulations indicate that an on-chip realization of the circuit will be able to operate at very high frequencies; up to 100 MHz.

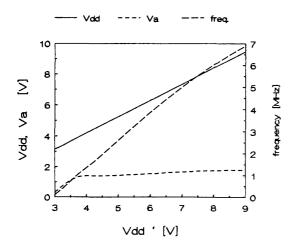


fig. 6 Experimental results obtained from a breadboard realization: V_a (amplitude), $V_{dd,}$ and frequency of the VCO versus V_{dd} '. The voltage V_b was chosen 0.5V.

Conclusions

A VHF transconductance-C filter technique with automatic tuning of the quality factors and the cut-off frequency, based on a VCO, was presented. The Q-tuning circuit is very simple and contains no signal carrying nodes resulting in a well controlled Q up to very high frequencies (100 MHz). Experimental results of a breadboard realization of the Q-tuning circuit were presented, giving good results.

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