

Passivation of Bi₂Te₃ Topological Insulator by Transferred CVD-Graphene: Toward Intermixing-Free Interfaces

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The investigation, and ultimate application, of topological insulators, typically involve exposure to ambient conditions or their integration with metals, which lead to surface oxidation or material intermixing. X-ray photoelectron spectroscopy (XPS) measurements that demonstrate passivated and intermixing-free interfaces in the topological insulator Bi₂Te₃ by means of dry-transferred CVD graphene are reported. After air exposure, no traces of Bi₂Te₃ oxidation are found. Furthermore, it is demonstrated that graphene acts as a very efficient metal and chalcogen diffusion barrier in Bi₂Te₃/graphene/permalloy (Py) heterostructures, which are relevant for spintronics. Such results are in stark contrast with the significant surface degradation observed in bare Bi₂Te₃ under ambient conditions and the deep Bi–Te bonding disruption that occurs in Bi₂Te₃/Py heterostructures. These findings provide a new approach to control and engineer topological insulator interfaces for spintronic applications and a new platform to investigate the combined use of graphene and topological insulator Dirac states.

1. Introduction

Device miniaturization and progress in nanotechnology have evidenced the crucial role of interfaces toward the performance of devices. Interface properties are relevant in all current spintronic technologies, as well as in many novel, potentially useful, spintronic phenomena and materials.^[1–3] This is the case for topological insulators (TIs), which are characterized by insulating bulk and conductive topologically protected surface (or boundary) states (TSS). The TSS are of particular interest because of their spin-momentum locking, which constrains the electron spin orientation perpendicular to the electron momentum and represents an effective route for charge-to-spin interconversion (CSI). When a current is applied, the TSS generate a non-equilibrium spin density that can be used to manipulate the

magnetization orientation of a ferromagnet (FM) by means of spin-orbit torques (SOTs), a key phenomenon for next-generation magnetic memory technologies.^[2] Recent reports using TIs have demonstrated large SOT efficiencies.^[4] However, to identify the microscopic mechanism of CSI and SOTs, as well as to maximize their efficiency, a deep understanding of the properties of the TI/FM interface is needed.^[5–11]

Interface phenomena are complex, involving charge transfer, hybridization, intermixing, or alloying. More so when air-sensitive or reactive elements are involved, in which case, oxidation and intermixing are particularly relevant. Different strategies have been used to protect the surface of TIs from oxidation, from intercalation of Rb atoms^[12] to Te capping (and latter decapping) for ex situ transfers of the TI.^[13] In the case of TI/Py (Py = Ni₈₀Fe₂₀) heterostructures, an added challenge is the significant intermixing present: nickel was observed to diffuse into Bi₂Se₃, leading to phase separation,^[7] and tellurium was found to diffuse out of Bi₂Te₃ deep into Py.^[8] In these experiments, interface diffusion was reduced by intercalating thin nonmagnetic (metallic) spacers,^[8] which were grown onto the TI before depositing the FM. However, it has been further established that interface modifications can alter the nature of the CSI mechanism, leading to large variations in the spin torque^[8] and the CSI efficiency.^[9,14,15]

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Despite increasing efforts, and a variety of metal spacers investigated, interface control in TI/FM structures is still limited, while to our knowledge intermixing has never been completely suppressed. Because $(\text{Bi,Sb})_2(\text{Se,Te})_3$ family of TIs are van der Waals (vdW) materials, using another vdW material as a spacer represents an attractive alternative to the metals used so far. vdW materials are atomically smooth and, as part of a heterostructure, they tend to interact weakly, through vdW interaction, with minimal element intermixing. Among vdW materials, graphene is an excellent candidate as a diffusion barrier, given its closely packed hexagonal lattice, outstanding mechanical and thermal properties, and chemical stability. Graphene is known to be impermeable to standard gases, including helium,^[16] and has been used to block the diffusion of metals, such as in Si/Cu,^[17] Al/Si, and Au/Ni interfaces.^[18] It has also been used to passivate Ni^[19] and Cu and CuNi alloy,^[20] although its effective long-term passivation of Cu is controverted.^[21–25] Some studies report a lack of passivation or even increased oxidation,^[22,23] possible explanations including the presence of defects in graphene^[24,26] or reversible oxygen intercalation at the graphene-Cu interface.^[21] Long-term passivation of FMs such as Ni and Co by direct chemical vapor deposition (CVD) of graphene has also been reported,^[27] and used as electrodes in magnetic tunnel junctions.^[28–30] In fact, the interaction between the graphene and the FM leads to hybridization (chemisorption or physisorption), both potentially useful for spintronic applications.^[31] In addition, although TI/graphene/FM heterostructures have not been investigated experimentally, giant CSI and large SOTs have been predicted.^[32]

In this work, we fabricated Bi_2Te_3 /graphene and Bi_2Te_3 /graphene/FM heterostructures in order to investigate the performance of graphene as both an oxidation and diffusion barrier. By means of X-ray photoelectron spectroscopy (XPS) we demonstrate that graphene prevents Bi_2Te_3 from oxidizing, even after exposure to ambient air and moisture. A Py film was then grown under ultra-high vacuum (UHV), forming a Bi_2Te_3 /graphene/Py heterostructure. The chemical states of Te and Bi in the Bi_2Te_3 film were investigated by XPS and compared with results obtained with Bi_2Te_3 /Py and Bi_2Te_3 . Our systematic analysis found no trace of intermixing when graphene is intercalated, demonstrating its superior diffusion barrier performance for Ni and Te.

2. Results and Discussion

2.1. TI/Graphene Heterostructure

Figure 1 shows a schematic of the developed approach to fabricate TI/graphene heterostructures (see the Experimental Section for details on each step). The topological insulator, Bi_2Te_3 , was grown by molecular beam epitaxy^[33] (MBE) on a $\text{BaF}_2(111)$ substrate and transferred, using a UHV suitcase, to a glovebox with inert Ar gas, where the O_2 and the H_2O concentrations were below 1 ppm. In order to achieve a large-area scalable process, graphene was grown by CVD on a Cu foil.^[34] Dry graphene stamps were prepared using a bilayer of polypropylene carbonate (PPC) and polydimethylsiloxane (PDMS),

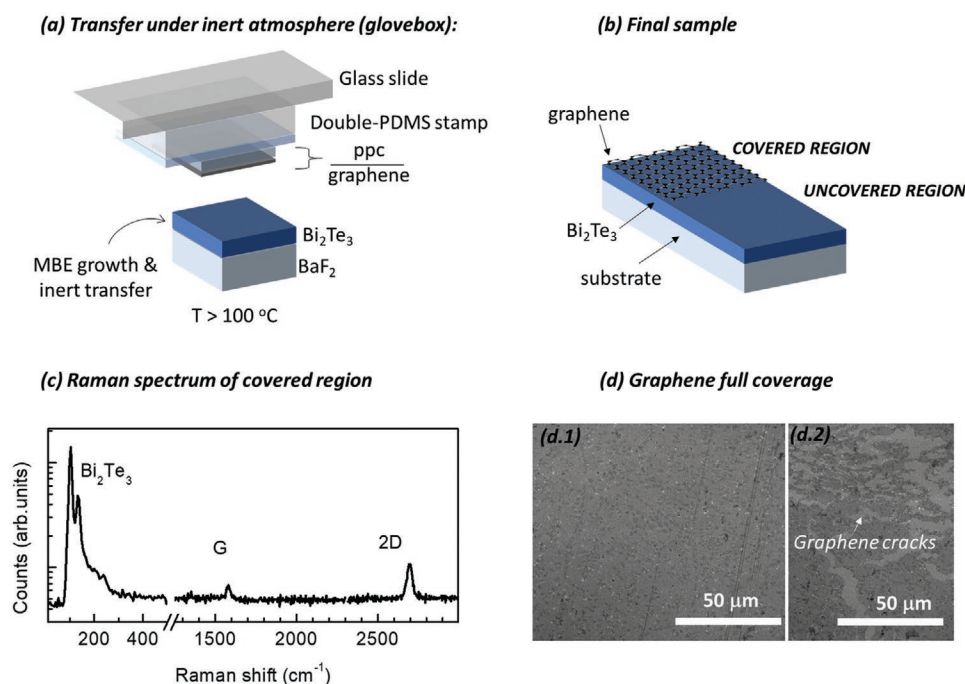


Figure 1. Sketch of the sample fabrication process and basic characterization. a) A dry stamp of PDMS/PPC/graphene was introduced into the glovebox and pressed onto the sample (graphene facing down) while the latter was heated to over 100°C to melt the PPC; b) after cleaning the resist, the final sample allowed for comparison between regions with and without graphene coverage. c) Raman spectrum confirmed the presence of graphene on the covered region, using a laser wavelength of 488 nm. d) SEM images reveal graphene full coverage in the majority of the transferred area (as in 1d1) as opposed to visible graphene discontinuities (cracks) formed during non-optimized transfer (shown as reference in 1d2).

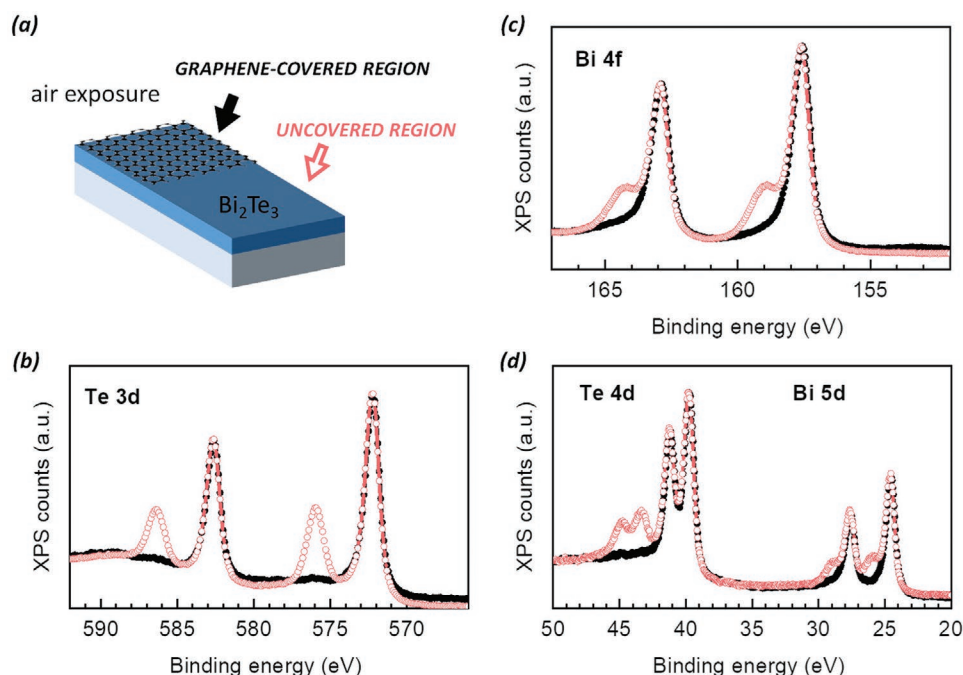


Figure 2. XPS measurements comparing the regions with and without graphene coverage (closed black and open red symbols, respectively). Te 3d, Bi 4f, Bi 5d, and Te 4d peaks are shown in the panels (b–d) as labeled. In all cases, large oxide peaks are present in the areas without graphene. Such peaks are absent for the graphene-capped area. Spectra intensities have been shifted and normalized to the value of the maximum of the largest peak.

followed by the removal of the Cu foil by means of wet etching. The graphene stamp and Bi_2Te_3 were put in contact using an optical microscope and micro-positioners (Figure 1a). The whole stack was then heated to over 100°C to melt the PPC and transfer the graphene/PPC onto Bi_2Te_3 . Up to this point, the process was carried out in the glovebox to avoid oxidation of the Bi_2Te_3 surface. Afterwards the sample was taken out of the glove box, the PPC was removed with acetone and isopropanol, and then the bilayer was exposed to air for over an hour. In the finished sample, graphene covered Bi_2Te_3 only partially to enable a direct comparison between regions with and without graphene coverage in the same Bi_2Te_3 film (Figure 1b).

Poor contrast using an optical microscope makes it difficult to distinguish the quality of the graphene, or even its presence, on the Bi_2Te_3 . However, this can be readily achieved with Raman spectroscopy. The Raman spectrum presented in Figure 1c shows the two characteristic peaks for Bi_2Te_3 at low frequencies (Raman shift $< 400\text{ cm}^{-1}$), while the presence of graphene is confirmed by means of the G and 2D peaks, visible at larger frequencies ($\approx 1600\text{ cm}^{-1}$ and $\approx 2700\text{ cm}^{-1}$). The faint D peak at $\approx 1350\text{ cm}^{-1}$ demonstrates the successful transfer of good-quality graphene with a relatively low density of defects. Scanning electron microscope (SEM) images also confirm full continuous graphene coverage over the majority of the transferred area when using an optimized transfer process (Figure 1d.1). This contrasts with the visible graphene cracks present in regions of samples for which the transfer process was not optimal (Figure 1d.2).

2.2. Graphene as Oxidation, Passivation Barrier

Figure 2 compares XPS measurements for Bi_2Te_3 over regions with and without graphene coverage (black-solid and red-open symbols, respectively). The direct comparison of the two regions (represented in Figure 2a) allows us to eliminate any characterization artifacts associated with air-exposure time and ambient conditions, as well as any possible variation in the morphology of the grown Bi_2Te_3 films.

The XPS spectra reveal peaks at the Te 3d (Figure 2b), Bi 4f (Figure 2c), and Te 4d and Bi 5d (Figure 2d) edges. Several peaks in Figure 2b,c are directly associated to Bi_2Te_3 , located at 572.2 eV ($\text{Te } 3d^{5/2}$), 582.6 eV ($\text{Te } 3d^{3/2}$), 157.6 eV ($\text{Bi } 4f^{7/2}$) and 162.9 eV ($\text{Bi } 4f^{5/2}$). Additional peaks are observed only in bare Bi_2Te_3 at larger binding energies (575.9 and 586.4 eV for Te and 158.8 and 164.1 eV for Bi) and are indicative of substantial oxidation. By fitting the areas of the Bi, Te and their corresponding oxides, and considering electron escape depths of $\lambda_{\text{Bi}} = 2.3\text{ nm}$ ^[35] and $\lambda_{\text{Te}} = 2.1\text{ nm}$,^[36] we estimate the oxide thickness to be $\approx 1.3\text{ nm}$ and 0.7 nm for Bi and Te, respectively (see Experimental Section for more details). This is in stark contrast with the Bi_2Te_3 /graphene region, in which no oxidation is discernible. An equivalent analysis, yielding the same conclusion, can be carried out in the Te 3d and Bi 5d edges (Figure 2d). Note that the spot size of the XPS is in the few nm range, confirming that the graphene transfer has been successful and that the TI passivation is effective over a large area.

Additional angle-resolved photoemission spectroscopy measurements (see Supplementary Information) on the

measurements, we show that the chemical bonding of the Bi₂Te₃ remains intact after depositing Py on top of graphene. This result contrasts with the deep Bi–Te bonding disruption observed in Bi₂Te₃/Py heterostructures, or even when selected metallic interlayers are inserted between Bi₂Te₃ and Py.

Our heterostructure fabrication method does not require substrate-dependent growth optimization and allows for large-scale samples. Even though the passivation of the surface and intermixing-free interfaces are demonstrated for Bi₂Te₃, we expect similar results for the whole family of (Bi,Sb)₂(Se,Te)₃ TI materials. Considering the increasing interest in quantum transport in TIs and graphene, in proximity effects, and in spintronic applications relying on TI/FM interfaces, our results offer important prospects for fundamental progress and for future spintronics. Regarding spintronics, future work should focus on the properties of the ferromagnet, which is grown on the TI/graphene bilayer, and, for example, the efficiency of the spin-orbit torques in the finished heterostructure.

4. Experimental Section

TI Film Growth: Bi₂Te₃ films with a thickness of ten quintuple layers (10 nm) were grown by co-evaporation of Bi and Te in an MBE chamber with a base pressure of 2×10^{-10} mbar. The growth rate was ≈ 0.25 nm min⁻¹. The fluxes were previously measured with a quartz crystal microbalance. The substrates used, BaF₂ (111) purchased at PIKEM, were previously outgassed in UHV at 800 °C for 1 h (in the same chamber) and the growth was done at 380 °C and monitored by in situ reflection high energy electron diffraction. Films were transferred, using a vacuum suitcase ($\approx 10^{-8}$ mbar), to a glovebox under Ar atmosphere, where the O₂ and the H₂O concentrations were below 1 ppm. There, graphene was transferred on top, as described in the main text. The sample temperature given here was the temperature measured with a thermocouple located between the heater and the sample holder, leading to an overestimation of the real temperature, which correspond to ≈ 575 °C for the degassing and 260 °C for the growth.

Graphene Growth and Stamp Preparation: Graphene was grown on a 25 μm thick Cu foil (99.8% or 99.999% Alfa Aesar) by CVD using a Lindberg/Blue tube furnace. The copper foil was first cleaned in acetone and isopropanol and then electropolished in an electrochemical cell by applying 3 V for 90 s and using the copper foil as anode. The electrolyte solution was a mixture of 150 mL of water, 75 mL of phosphoric acid, 75 mL of ethanol, 15 mL of isopropanol, and 1.5 g of urea.^[53] After that, the polished Cu foil was annealed in Ar (450 sccm) and H₂ (50 sccm) for 45 min under low pressure (base pressure of 20 mbar). Graphene was then grown using a CH₄ flow of 30 sccm at 1000 °C for 10 min and keeping the same flow conditions for H₂ and Ar as in the annealing step. These growth conditions produce a full multidomain graphene monolayer and also occasionally small domains of bilayer graphene at the center of the individual graphene domains.^[54] For graphene transfer process, four layers of polypropylene carbonate (PPC) (at a concentration of 0.1 gr mL⁻¹) were spin-coated on top of the graphene and heated to 80 °C during 1 min after each layer. Finally, a thicker polydimethylsiloxane (PDMS) film was attached to the surface of the PPC, to later provide support with the handling of the stamp. Cu was wet-etched with diluted ammonium persulfate (0.25 M) and rinsed with water as standard procedure^[55]. The PDMS stamp with graphene was left to dry overnight and entered into the glovebox.

Py/Al deposition: After the characterization of the Bi₂Te₃/graphene surface, the sample was reintroduced in the MBE chamber. The sample was outgassed at 280 °C for 1 h in UHV and cooled down to liquid nitrogen temperature for the evaporation of Py (2 nm) and Al

(2 nm) with effusion cells at growth rates of ≈ 0.24 and 0.15 nm min⁻¹, respectively. The thickness was monitored with a calibrated quartz crystal microbalance. After growth, the sample was taken out to air for the oxidation of the Al into AlO_x.

Raman measurements: Raman spectra were taken using a Witec Alpha300 R confocal Raman spectrometer and the 488 nm laser line, using a power of 0.8 mW. The spectra were measured with 100× magnification. The grating used for probing the graphene peaks was 600 lines mm⁻¹, while a grating of 1800 lines mm⁻¹ was used for higher resolution to investigate the spectra for Bi₂Te₃.

XPS measurements: After exposure to air, samples were introduced into a UHV chamber (10^{-10} mbar), and XPS was carried out using a SPECS PHOIBOS hemispherical analyzer with a monochromatized X-ray source (Al K_α line of 1486.6 eV, 300 W). Calibration was done using the C 1s peak as reference. For the measurement of the non-oxidized Bi₂Te₃ surface, the film was grown in the MBE and capped by a Te layer (15 nm) deposited in situ below 100 °C. Inside the XPS chamber, the Te capping layer was desorbed by heating the sample up to 190 °C for 10 min. For the sample with and without graphene coverage (Bi₂Te₃/graphene), no previous annealing was done prior to the XPS measurements. No change was observed in the XPS peak positions after annealing at 300 °C for 30 min, just an increase in their intensity. To estimate the oxide thickness, it was noted that Bi₂Te₃ was much thicker than the electron

escape depth. This led to $\frac{I_{ox}}{I_{non-ox}} \approx 1 - \exp\left(-\frac{t_{ox}}{\lambda}\right)$, where I_{ox} and I_{non-ox} are the peak areas corresponding to the oxidized and non-oxidized elements (Te or Bi), respectively. As stated in the main text, $\lambda = 2.3$ nm (2.1 nm) was taken at the Bi 4f (Te 3d) energy.^[35] The analysis performed separately at the Bi 4f and Te 3d lines yielded oxide thicknesses of 1.3 nm and 0.7 nm for Bi and Te, respectively.

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

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Conflict of Interest

The authors declare no conflict of interest.

Data Availability Statement

The data that support the findings of this study are available from the corresponding author upon reasonable request.

Keywords

Bi₂Te₃, graphene–topological insulator interface, intermixing, passivation, XPS

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