

# TECHNICAL RESEARCH REPORT

## Model Reduction for a Tungsten Chemical Vapor Deposition System

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**T.R. 98-28**



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# Model Reduction for a Tungsten Chemical Vapor Deposition System

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**Abstract:** A model of a tungsten chemical vapor deposition (CVD) system is developed to study the CVD system thermal dynamics and wafer temperature nonuniformities during a processing cycle. We develop a model for heat transfer in the system's wafer/susceptor/guard ring assembly and discretize the modeling equation with a multiple-grid, nonlinear collocation technique. This weighted residual method is based on the assumption that the system's dynamics are governed by a small number of modes and that the remaining modes are slaved to these slow modes. Our numerical technique produces a model that is effectively reduced in its dynamical dimension, while retaining the resolution required for the wafer assembly model. The numerical technique is implemented with only moderately more effort than the traditional collocation or pseudospectral techniques. Furthermore, by formulating the technique in terms of a collocation procedure, the relationship between temperature measurements made on the wafer and the simulator results produced with the reduced-order model remain clear.

**Keywords:** Model reduction, Chemical vapor deposition, Weighted residual methods

## 1. Introduction

This paper presents a distributed parameter system model reduction technique developed in the context of simulating a commercial Chemical Vapor Deposition (CVD) system. In the system studied, single semiconductor wafers are processed in a cold-walled chamber with wafer heating provided by one ring-shaped incoherent lamp bank.

Numerous simulation studies aimed at understanding or controlling wafer temperature uniformity in radiant-heated systems have been conducted (Breedijk et al., 1993, Cole et al., 1994, Dilhac et al., 1995; Merchant et al., 1996; Sorrell et al., 1992). Because of the complexity of some of the simulators developed and the computational expenses of performing simulations for process optimization and control, interest in developing reduced-order models from the high fidelity simulations has emerged (Aling et al., 1996, 1997; Theodoropoulou et al., 1998). These models are based on collecting "snapshots" of the RTP system at points in time during processing cycles designed to excite as many spatial temperature modes as possible, and processing them with the Proper Orthogonal Decomposition method (Sirovich, 1987) to determine

the dominant temperature spatial modes. Because the original modeling equations are projected onto these modes with Galerkin's method, the reduced-order models have a predictive capability not found in models generated from purely empirical model identification schemes.

In this paper, we develop a model representing the thermal dynamics of a commercial CVD system. We propose a multiple-grid, nonlinear collocation technique in the context of developing a reduced-order simulator, where the goal of the reduction procedure is to minimize the number of dynamic modes required for accurate dynamic simulations of this system. The proposed method is implemented using numerical procedures similar to the traditional collocation and pseudospectral techniques. An additional advantage of the technique is that the collocation formulation retains a direct connection between temperature measurements made on the wafer and simulation results produced with the reduced-order model.

## 2. ULVAC System for Tungsten CVD

Tungsten (W) is used both as a contact plug material and as a first-level metal for interconnects in microelectronic devices. In the selective W CVD processes, W selectively grows on Si but not on SiO<sub>2</sub> sur-

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<sup>1</sup>This research was supported by the National Science Foundation under grant NSF EEC 95-27576. The authors also thank Dr. John Kidder for his hospitality in the LAMP lab.

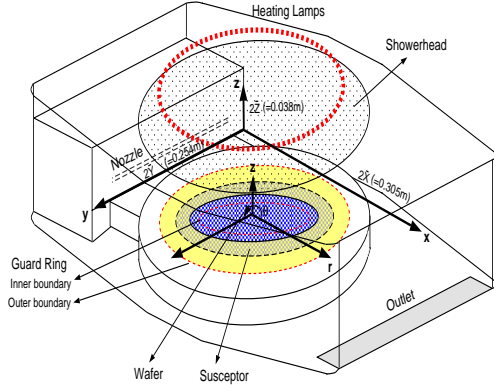


Figure 1: *Geometry of the tungsten CVD reactor.*

faces, thus can be used to fill via contacts in a single step. In blanket deposition processes, a metal nucleation layer is first deposited on the entire wafer. This allows W to be blanket-deposited over the wafer surface, filling the contact holes. A subsequent reactive ion etchback step is then used to complete the plugs.

Our modeling research focuses on the BTU-ULVAC ERA-1000 selective tungsten deposition system, shown schematically in Fig. 1. Reactant gases are introduced into the reactor from two sources: a gas mixture of  $\text{SiH}_4$  and  $\text{WF}_6$  is injected through a slit-like nozzle on the side wall, and  $\text{H}_2$  is pumped in through a showerhead at the top of reactor chamber. Gases mix in the chamber and react at the surface of a 4 inch diameter wafer, which is supported by a slowly rotating quartz susceptor. A portion of the wafer near its outer edge is covered by a quartz guard ring. The wafer is heated to 600 K by a ring of incoherent tungsten-halogen lamps through the transparent showerhead. The CVD runs last for approximately 5 minutes after the operating temperature is reached.

Initial modeling of the ULVAC tungsten CVD system has focused on the gas phase flow field and heat transfer, and the wafer thermal dynamics. Because of the short gas residence time relative to the wafer thermal dynamics, the gas phase flow field and temperature can be described by the steady-state conservation of total mass, momentum, and energy, and take the form

$$\begin{aligned}\nabla \cdot (\rho \mathbf{v}) &= 0 \\ -\nabla \cdot \mu (\nabla \mathbf{v}) + \nabla P &= 0 \\ \nabla \cdot (\rho C_p \mathbf{v} T_g) - \nabla \cdot (\kappa \nabla T_g) &= 0\end{aligned}$$

where  $\rho$ ,  $\mathbf{v}$ ,  $P$ ,  $\mu$ ,  $C_p$ ,  $\kappa$  represent gas density, velocity, pressure, viscosity, heat capacity, and thermal conductivity, respectively (Kleijn and Werner, 1993). A Galerkin discretization technique was used with globally defined trial functions to solve the gas flow field equations under the assumption of fully-developed,

laminar flow and subject to no-slip boundary conditions at the chamber walls. A two-dimensional eigenfunction expansion method combined with a one-point spanwise collocation discretization was used to compute the gas temperature profile, subject to the flow field calculated above and gas temperature boundary conditions defined by representative operating conditions (Chang and Adomaitis, 1997). Typical gas temperature contours and wafer/gas energy transfer rates are displayed in Fig. 2.

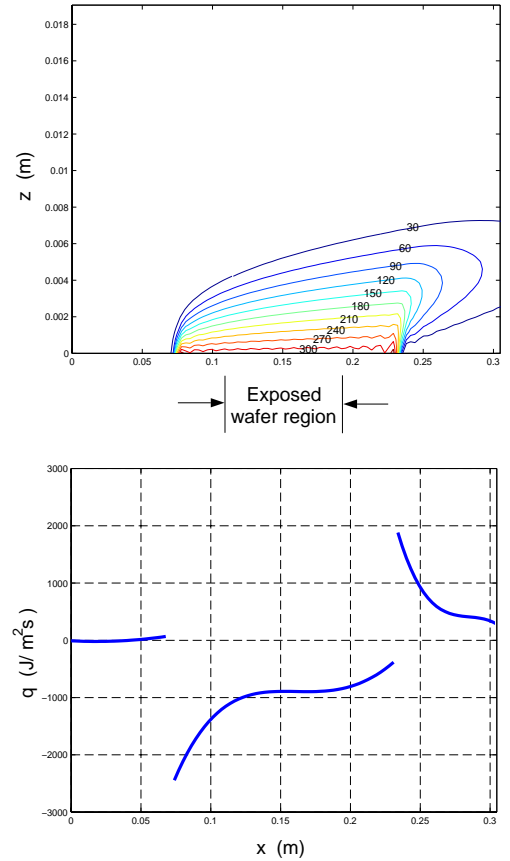


Figure 2: *Simulation of gas temperature field and wafer/gas heat transfer rate at the centerline of reactor chamber with  $Re = 1167.6$ . Gas inlet temperature is  $25^\circ\text{C}$  and the wafer temperature is  $323^\circ\text{C}$ .*

### 3. Wafer Assembly Temperature Model

The wafer/susceptor/guard ring assembly model is based on assuming the assembly is thin relative to its diameter ( $\Delta_z \ll$  guard ring radius) and is derived by a differential element energy balance. This assumption implies a uniform temperature distribution across assembly thickness. Since there are different geometric combinations of the guard ring, wafer, and susceptor in the radial direction, an assembly

thickness parameter ( $\Delta_z$ ) and mass-averaged, effective material properties are defined for each section (see Figure 3) making these parameters functions of  $r$ . Assuming no azimuthal temperature variations exist, the wafer temperature governing equation in cylindrical coordinates is

$$\Delta_z(r)\rho(r)C_p(r)\frac{\partial T_w}{\partial t} = \frac{1}{r}\frac{\partial}{\partial r}\left(k(r)\Delta_z(r)r\frac{\partial T_w}{\partial r}\right) - \Delta Q$$

where the energy flux from the top and bottom of the assembly is defined as

$$\Delta Q = Q_{cd} + Q_{cv} + Q_{em} - Q_{lp}$$

These quantities are listed in Table 1 for the different regions. Note that the  $q_{lp}$  function represents the lamp radiant flux distribution and the lamp power input is  $u(t)$ . The temperature of chamber floor and electrode surface (located under the wafer and used as part of the chamber plasma cleaning cycle) is assumed to be equal to the wall temperature by the design of cooling system.

Because the quartz showerhead will absorb most of the infrared component of the light emitted by the heating lamp, we assume the primary component of radiant energy that reaches the assembly is in the visible portion of the spectrum, meaning that little will be absorbed by the susceptor or guard ring. However, the radiation emitted by the heated wafer will have primarily longer-wavelength components and so will be readily absorbed by the quartz guard ring, susceptor, and chamber walls.

The lamp radiation intensity at specific points on the wafer surface will depend on the lamp power output, the distance from the wafer point to each bulb, and the angle of incidence the ray makes with the wafer surface. The procedures for computing the flux intensity profile generated by the 6kW bulb ring is given in Chang (1997).

The temperature equation is made dimensionless by dividing the radial coordinate by the guard ring radius  $R_{gr}$ ; the assembly thickness function  $\Delta_z(r)$  by the wafer thickness,  $Z_{waf}$ ; and time by the time constant  $\tau = (\rho_{Si}C_{pSi}R_{gr})/\kappa_{Si}$ . The effective property functions (except for the heat transfer coefficients) are made dimensionless by dividing each function by the corresponding thermophysical property of silicon evaluated at ambient conditions (see the curves in Fig. 3 and the constants listed in the center figure legend). The constants  $C_{cd}$ ,  $C_{cv}$ ,  $C_{em}$ , and  $C_{lp}$  result from the nondimensionalization. The resulting wafer assembly temperature model is as follows

$$\rho(r)C_p(r)\frac{\partial T_w}{\partial t} = \frac{1}{\Delta_z(r)}\frac{1}{r}\frac{\partial}{\partial r}\left(k(r)\Delta_z(r)r\frac{\partial T_w}{\partial r}\right) + C_{cd}\frac{h_{cv}(r)}{\Delta_z(r)}(T_{wall} - T_w)$$

$$\begin{aligned} &+ C_{cv}\frac{h_{cv}(r)}{\Delta_z(r)}(T_g + 1 - T_w) \\ &+ C_{em}\frac{\epsilon(r)}{\Delta_z(r)}(T_{wall}^4 - T_w^4) \\ &+ C_{lp}\frac{\alpha(r)}{\Delta_z(r)}q_{lp}(r)u(t) \end{aligned} \quad (1)$$

subject to homogeneous boundary conditions

$$\partial T_w / \partial r = 0 \quad \text{at } r = 0, 1 \quad (2)$$

with initial condition  $T_w(r, t = 0) = T_0(r)$  at  $r < 1$ . The dimensionless physical and geometric constants used in calculations are plotted in Figure 3. The dimensional values in SI units are obtained by multiplying corresponding factors listed in the legend box.

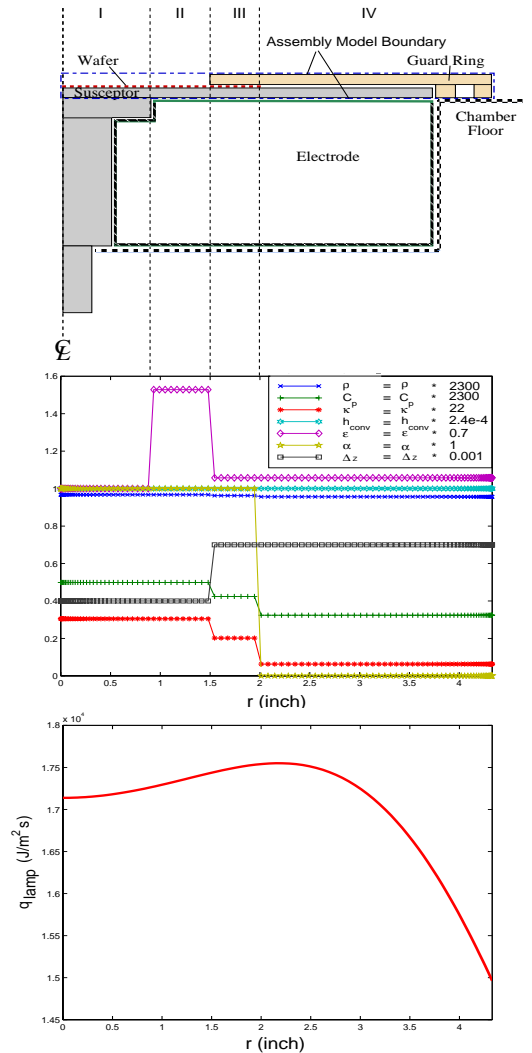


Figure 3: Detailed sketch of the wafer/guard ring/susceptor assembly (top), the corresponding physical properties of the different regions (center), and radiant energy flux distribution (bottom).

Table 1: Heat flux components defined in different regions.

	I	II	III	IV
$Q_{cd}$	$h_{cd2s}(T_w - T_{wall})$	$h_{cd2g}(T_w - T_{wall})$	$h_{cd2g}(T_w - T_{wall})$	$h_{cd2g}(T_w - T_{wall})$
$Q_{cv}$	$h_{cv2g}(T_w - T_g)$ <sup>a</sup>	$h_{cv2g}(T_w - T_g)$	$h_{cv2g}(T_w - T_g)$	$h_{cv2g}(T_w - T_g)$
$Q_{em}$	$\epsilon_{Si}\sigma(T_w^4 - T_{wall}^4)$	$(\epsilon_{Si} + \epsilon_{SiO_2})\sigma(T_w^4 - T_{wall}^4)$	$2\epsilon_{SiO_2}\sigma(T_w^4 - T_{wall}^4)$	$2\epsilon_{SiO_2}\sigma(T_w^4 - T_{wall}^4)$
$Q_{lp}$	$\alpha_{Si}q_{lp}u(t)$	$\alpha_{Si}q_{lp}u(t)$	$\alpha_{Si}q_{lp}u(t)$	$\alpha_{SiO_2}q_{lp}u(t)$

**a.** Average heat transfer coefficient to gas phase, calculated in Chang and Adomaitis (1997).

#### 4. Interior Collocation Solution

We represent solutions to the wafer assembly thermal dynamics model by the trial function expansion

$$T_w(r, t) = \sum_{j=1}^{\infty} a_j(t) \psi_j(r). \quad (3)$$

where the trial functions  $\psi_j(r)$  are chosen as the normalized Bessel functions of the first kind of order 0:

$$\psi_j(r) = \frac{J_0(\gamma_j r)}{\sqrt{\langle J_0(\gamma_j r), J_0(\gamma_j r) \rangle}}.$$

The  $\gamma_j$  are computed as solutions to  $J_1(\gamma) = 0$ ; this choice of trial functions means that the guard-ring edge no-flux boundary condition and the condition of symmetry at  $r = 0$  are satisfied by each trial function. The trial functions form an orthonormal sequence under the inner product  $\langle f, g \rangle = \int_0^1 f g r dr$ .

##### 4.1. Interior Collocation Formulation

We now write the wafer temperature in terms of the orthonormal function series  $\psi_j$ , truncated after the  $N$ th term, in vector notation  $T_w^N = \boldsymbol{\psi}^{1 \times N} \mathbf{a}^{N \times 1}$ . The transformation array  $\mathbf{Q}^{N \times N}$  is defined by  $Q_{i,j} = \psi_j(r_i)$  which equates mode amplitudes  $\mathbf{a}^{N \times 1}$  to  $N$  discrete temperature points on the wafer  $\mathbf{T}_w^{N \times 1}$  by  $\mathbf{T}_w = \mathbf{Q} \mathbf{a}$ . Temperature derivatives and the heat equation operator  $\nabla^2$  can be written in terms of these discrete temperature values through the use of the differentiation arrays  $\mathbf{A}$  and  $\mathbf{B}$  defined by

$$\mathbf{A} = \left[ \frac{d\mathbf{Q}}{dz} \right] \mathbf{Q}^{-1} \quad \mathbf{B} = [\nabla^2 \mathbf{Q}] \mathbf{Q}^{-1}.$$

The  $N$  interior collocation points are chosen as the roots of the  $\psi_{N+1}$  trial function (Villadsen and Stewart, 1967), and the discretized nonlinear modeling equation can be written as

$$\begin{aligned} \frac{dT_{w_j}}{dt} = & \{ [\mathbf{A}\boldsymbol{\Theta}]_j + \kappa_j \Delta_{z_j} \mathbf{B}_j \mathbf{T}_w \\ & + C_{em} \epsilon_j (T_{wall}^4 - T_{w_j}^4) \\ & + C_{cd} h_{cd} (T_{wall} - T_{w_j}) \\ & + C_{cv} h_{cv} (T_g + 1 - T_{w_j}) \end{aligned} \quad (4)$$

$$\begin{aligned} & + C_{lp} \alpha_j q_{lp} u(t) \} / \rho_j C p_j \Delta_{z_j} \\ = & \mathbf{F}(\mathbf{T}_w) \end{aligned}$$

subject to initial conditions  $T_{w_j} = 1$ ,  $j = 1, \dots, N$ . Note that  $[\mathbf{A}\boldsymbol{\Theta}]_j = [\mathbf{A}_j(\boldsymbol{\kappa}\boldsymbol{\Delta}_z)][\mathbf{A}_j \mathbf{T}_w]$ ,  $j = 1, \dots, N$  in the above equation, and that  $\mathbf{A}_j$  denotes the  $j$ th row of the discretization array  $\mathbf{A}$ .

#### 5. Nonlinear Weighted Residual Methods

The wafer temperature modeling equation (1) can be rewritten as

$$\dot{T}_w(t, r) = G(T_w). \quad (5)$$

Solutions are sought in the form of the truncated trial function expansion  $T_w^N = \boldsymbol{\psi} \mathbf{a}$ . In the Galerkin solution procedure, the residual, defined by substituting  $T_w^N$  into (5), is projected onto the trial functions to obtain the same number of ordinary differential equations in time:

$$\langle T_w^N, \psi_n \rangle = \langle G(T_w^N), \psi_n \rangle. \quad (6)$$

By writing out (6) explicitly, we obtain

$$\begin{aligned} \dot{a}_l^s &= g_l(\mathbf{a}^s, \mathbf{a}^f) \quad l = 1, \dots, L, \quad L < N \\ \dot{a}_n^f &= h_n(\mathbf{a}^s, \mathbf{a}^f) \quad n = L + 1, \dots, N \end{aligned}$$

and can make a distinction between the relatively “fast” dynamical modes  $a_n^f$  and the slower modes  $a_l^s$ . The fast modes are associated with the highly-oscillatory (spatial) trial functions  $\psi_n$  which dissipate more quickly by heat conduction through the wafer relative to the slower, less spatially-oscillatory modes. If we assume the system’s dynamics are governed by the slower modes, the fast modes can be slaved to the slow by setting  $\dot{a}_n^f = 0$ . This means solutions  $T^N(t, r)$  are constrained to the manifold in phase space defined by the nonlinear equations

$$0 = h_n(\mathbf{a}) \quad n = L + 1, \dots, N. \quad (7)$$

Integrating the remaining ordinary differential equations in time subject to (7) defines the *nonlinear* Galerkin procedure (Aling, et al. 1997; Deane, et al.,

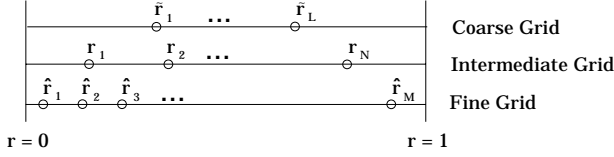


Figure 4: *The three spatial discretization grids.*

1991; Dettori, et al., 1995; Graham, and Kevrekidis 1996; Sirovich, 1991).

At this point we note there are no obvious connections between the collocation-discretized equations and the combined slow and fast dynamics sets of ordinary differential equations in time produced by the Galerkin procedure. In other words, there is no guarantee that the fast modes would be slaved to the slow if  $\dot{T}_{w_n}$  were set to zero at  $N-1-L$  interior collocation points. Moreover, no obvious selection procedure for choosing these points is apparent in this formulation.

### 5.1. Nonlinear Collocation

At the start of each time step during numerical integration of the  $N$ -mode discretized wafer temperature equation, we would like to take a smaller number  $L$  of points on the solution curve  $T_w^N(r, t)$  and compute the time derivatives at these points based on the temperature profile  $\mathbf{T}_w$  defined on the intermediate grid  $\mathbf{r}$  that is generated when the (spatially) higher-frequency modes are slaved to the slow in a quasi steady-state manner (see Fig. 4). These dynamic modes are defined using the most-coarse grid  $\tilde{r}_l$  (typically determined as the roots of  $\psi_{L+1}$ ) with  $l = 1, \dots, L$ . The discretized equations defining the time derivatives at the coarse-scale grid points are

$$\frac{d\tilde{\mathbf{T}}_w}{dt} = \tilde{\mathbf{Q}}\mathbf{Q}^{-1} [\mathbf{F}(\mathbf{T}_w)]$$

with  $\tilde{Q}_{i,j} = \psi_j(\tilde{r}_i)$ ,  $i = 1, \dots, L$ ,  $j = 1, \dots, N$ , and  $\mathbf{F}(\mathbf{T}_w)$  defined by (4). The vector  $\mathbf{T}_w$  corresponds to values of  $T_w^N(r, t)$  defined on the intermediate-resolution grid  $r_n$ ,  $n = 1, \dots, N$ . The  $N$  solution points  $T_{w_n}$  defining this temperature profile are computed from the  $N$  equations:

$$[L \text{ equations:}] \quad T_w^N(r, t) \text{ passes through the points } (\tilde{r}_l, \tilde{T}_{w_l}), l = 1, \dots, L;$$

$$[N - L \text{ equations:}] \quad \dot{a}_n = 0 \text{ for } n = L + 1, \dots, N.$$

This can be done in the physical space by the following  $N$  equations for  $\mathbf{T}_w$  at the start of each time-step:

$$\begin{aligned} \mathbf{0} &= \tilde{\mathbf{T}}_w - \tilde{\mathbf{Q}}\mathbf{Q}^{-1}\mathbf{T}_w \\ \mathbf{0} &= \tilde{\mathbf{P}} \left[ \hat{\mathbf{F}}(\mathbf{T}_w) \right]. \end{aligned}$$

In this set of nonlinear equations,  $\hat{\mathbf{F}}(\mathbf{T}_w)$  is the residual produced by the  $N$ -mode collocation solution evaluated on the finest grid  $\hat{\mathbf{r}}$  and is defined as

$$\begin{aligned} \hat{F}_j(\mathbf{T}_w) &= \{ [\hat{\mathbf{A}}\hat{\Theta}]_j + \hat{\kappa}_j \hat{\Delta}_{z_j} \hat{\mathbf{B}}_j \mathbf{T}_w \\ &+ C_{em} \epsilon_j (T_{wall}^4 - \hat{T}_{w_j}^4) \\ &+ C_{cd} h_{cd} (T_{wall} - \hat{T}_{w_j}) \\ &+ C_{cv} h_{cv} (T_g + 1 - \hat{T}_{w_j}) \\ &+ C_{lp} \hat{\alpha}_j \hat{q}_{lp_j} u(t) \} / \hat{\rho}_j \hat{C} p_j \hat{\Delta}_{z_j} \end{aligned}$$

with  $j = 1, \dots, M$ . The  $\hat{f}_j$  notation refers to the  $r$ -dependent parameter and state variables evaluated on the finest grid. The points  $\hat{r}_m$  are determined as the roots of  $\psi_{M+1}(r)$ ;  $[\hat{\mathbf{A}}\hat{\Theta}]_j$  are elements of the  $M \times 1$  vector produced by the term-by-term product of the vectors  $\hat{\mathbf{A}}\hat{\kappa}\hat{\Delta}_z$  and  $\hat{\mathbf{A}}\mathbf{T}_w$ .

Using this finer grid ( $M > N$ ) allows for residual calculations that are more accurate than would be possible using the intermediate-resolution grid. Therefore, a more accurate discrete analog to the Galerkin projection can be developed to satisfy the  $N - L$  equations:  $\dot{a}_n = 0$  for  $n = L + 1, \dots, N$ .

To compute the fine-grid residual values corresponding to a solution defined on the original, intermediate-resolution grid  $\mathbf{r}$ , we define the interpolation array  $\hat{\mathbf{T}}_w^{M \times 1} = \hat{\mathbf{Q}}^{M \times N} \mathbf{a}^{N \times 1} = \hat{\mathbf{Q}}\mathbf{Q}^{-1}\mathbf{T}_w$  with  $\hat{Q}_{m,n} = \psi_n(\hat{r}_m)$ ,  $n = 1, \dots, N$ , and  $m = 1, \dots, M$ . This defines the transformation array  $\hat{\mathbf{Q}}\mathbf{Q}^{-1}$  that takes the function  $T_w(r)$  defined by the intermediate-discretization grid ( $\mathbf{r}$ ) values of  $T_{w_n}$  and exactly interpolates the function to the fine grid. Similarly, we can generate the non-square discretization array necessary for computing the residual by

$$\hat{\mathbf{A}} = \left[ \frac{d\hat{\mathbf{Q}}}{dr} \right] \mathbf{Q}^{-1} \quad \text{and} \quad \hat{\mathbf{B}} = \left[ \frac{1}{r} \frac{d}{dr} r \frac{d\hat{\mathbf{Q}}}{dr} \right] \mathbf{Q}^{-1}.$$

The discrete Galerkin projection is implemented by defining the nonsquare array  $\tilde{\mathbf{P}}^{(N-L) \times M}$  by rows  $L + 1$  through  $N$  rows of  $\tilde{\mathbf{P}}^{-1}$ , in other words,  $\tilde{P}_{n,m} = [\tilde{\mathbf{P}}^{-1}]_{n,m}$ ,  $n = L + 1, \dots, N$ , and  $m = 1, \dots, M$  with  $\tilde{P}_{i,j} = \psi_j(\hat{r}_i)$  with  $i, j = 1, \dots, M$ .

## 6. Results and Conclusions

Representative results in the form of snapshots showing the wafer assembly temperature profiles during the startup and soak phases of a processing run are presented in Fig. 5. We conclude that the numerical technique developed produces accurate, low-order simulation results, especially at the collocation points of the coarse grid  $\tilde{\mathbf{r}}$  which is most relevant to comparisons with experimental measurements. Further

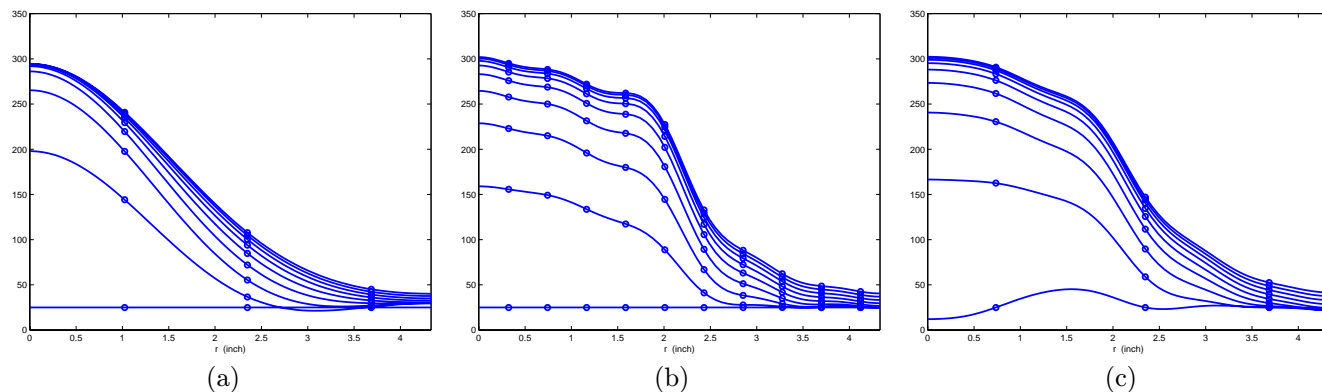


Figure 5: Wafer temperature profiles ( $^{\circ}\text{C}$ ) computed with the different collocation methods. (a) Interior collocation with 3 modes. (b) Interior collocation with 10 modes. (c) Multiple grid, nonlinear collocation with 3 dynamic modes.

refinements to the numerical technique are currently under investigation, such as using the “acceleration” POD modes in place of the Bessel’s functions  $\psi_n$  (Aling, et al. 1997; Sirovich, 1991).

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