

Heterogeneous Integration of GaSb-based epitaxy on silicon-on-insulator: towards mid-infrared photonic integrated circuits for environmental and bio-medical applications

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The interest in environmental and health care monitoring has increased significantly in recent years. This application typically requires the detection of substances which have characteristic absorption features in the mid-infrared wavelength region. Photonic integrated circuits can potentially be used to realize this functionality in a compact and low-cost fashion. This encourages the development of opto-electronic devices based on GaSb technology. Silicon-on-insulator (SOI) on the other hand is a very promising platform for passive optical functions, given the high omni-directional refractive index contrast of the so-called photonic wires, allowing for compact photonic integrated circuits, which can be fabricated using CMOS fabrication tools. The integration of GaSb active devices with SOI passive waveguide circuits would therefore allow for the realization of fully integrated mid-infrared photonic integrated circuits. This could accelerate the development of a number of applications in the environmental and medical field, such as spectroscopic gas sensors and implantable blood glucose sensors. In this paper we report on the heterogeneous integration of GaSb-based epitaxy on SOI waveguide circuits and show initial results on the realization of a GaSb-based photodetector integrated on SOI.

The heterogeneous integration is realized by epitaxial layer transfer through a die-to-wafer bonding process using the polymer DVS-BCB as a bonding agent. Before the bonding process, the GaSb epitaxy is cleaned with acetone and IPA. The SOI die is cleaned using a standard clean-1 solution (NH₄OH:H₂O₂:H₂O). The DVS-BCB polymer is diluted using mesitylene (to achieve a thin bonding layer) and then spin-coated on the SOI die. The sample is then baked on a hot plate at 150 °C for 3 minutes. The bonding is performed at this temperature, after which the stack is cured at 250 °C for 1 hour. After the bonding process, the GaSb substrate is removed using a combination of mechanical grinding and wet etching using a mixture of CrO₃ and HF until an InAsSb etch stop layer is reached. Figure 1 shows a SEM cross-section image of the GaSb layer transferred to an SOI waveguide circuit. A III-V/SOI separation of 150nm is easily achievable, which is sufficient for the optical coupling between the SOI waveguide circuit and GaSb-based opto-electronic components.

Based on such a heterogeneous GaSb-SOI layer stack, heterogeneous GaSb/SOI waveguide photodetectors were designed as a first step towards a mid-infrared photonic integrated circuit. In this device, depicted in figure 2, the mid-infrared light is evanescently coupled from the silicon waveguide layer to the GaSb absorption layer. Simulation results are shown in figure 3. At the conference, we will elaborate on the device performance.

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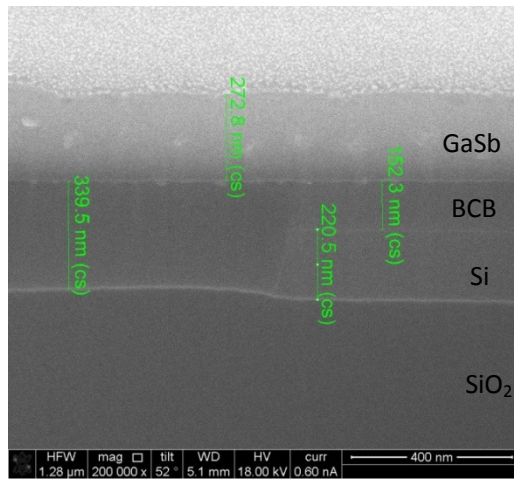


Figure 1: SEM cross-section image of the GaSb epitaxial layer stack (after GaSb substrate removal) transferred to a silicon-on-insulator waveguide circuit

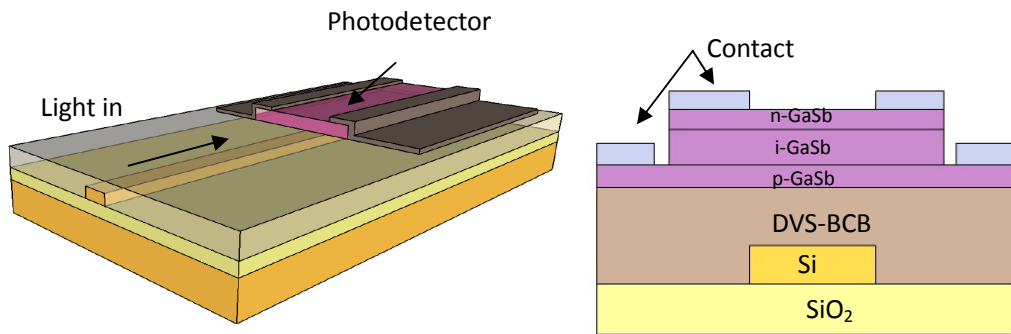


Figure 2: Photodiode device layout and schematic cross-section of the heterogeneous layer stack

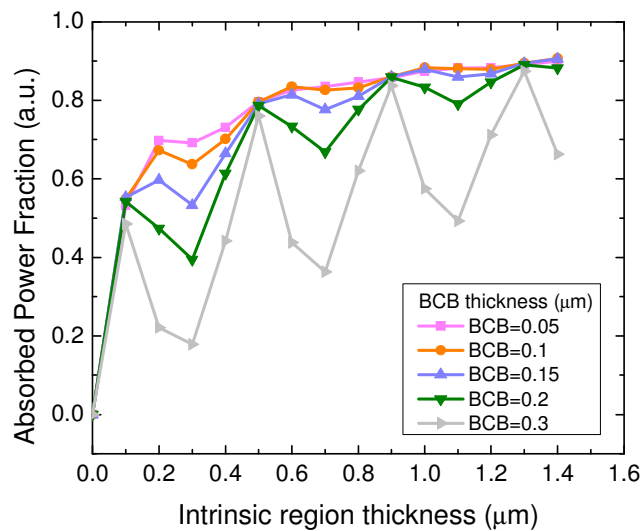


Figure 3: Absorbed power fraction as a function of the intrinsic region thickness and BCB layer thickness for a 20 μm long photodetector