8x14Gb/s Si Ring WDM Modulator Array with Integrated **Tungsten Heaters and Ge Monitor Photodetectors**

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Abstract: An 8x14Gb/s wavelength-division multiplexed Si ring modulator array is presented with uniform channel performance. Tungsten heaters and Ge monitor photodetectors at the ring modulator drop ports are co-integrated to track and control the modulation quality. OCIS codes: (200.4650) Optical interconnects; (230.4110) Modulators; (230.0250) Optoelectronics

1. Introduction

Silicon carrier depletion ring modulators are attractive for optical interconnects because they require low operating energy and they can support high bit-rates, while occupying small footprint [1, 2]. They have therefore the potential to enable significant bandwidths in wavelength division multiplexed (WDM) configurations, at reasonable cost. However, due to their resonant nature, ring modulators suffer from narrow operating bandwidth. Resonance wavelength tunability is necessary to maintain error-free modulation during temperature fluctuations and to compensate for fabrication variability. Doped silicon or metal heaters have been demonstrated on ring modulators to achieve wavelength tuning [1-3]. In [3] we presented tungsten (W) heaters integrated in a standard CMOS BEOL flow, for the thermal control of photonic waveguides. In [4], we demonstrated the co-integration of ring modulators with Ge photodetectors (PDs) for monitoring resonance shifts. In this paper we present an 8-channel WDM transmitter comprised of ring modulators with drop ports, W-heaters and a Ge photodetector at each drop port. The metal heaters enable wavelength tuning of the ring modulator, while the Ge photodetectors can be used in a feedback loop as the monitoring device to trigger resonance tuning when needed. We show 0.19nm/mW heater tuning efficiency on all channels, 3dB bandwidths of 8.5GHz at 0V for the ring modulators and 3dB bandwidths of 18GHz at -1V for the Ge photodetectors. Large-signal modulation with open eye diagrams at 14Gb/s is also demonstrated using a 1.5V peak-to-peak (Vpp) RF drive signal corresponding to only 7fJ energy/bit per ring modulator, excluding heater power consumption. Operation at fixed wavelength is studied when the heater power is varying from 2.5-5mW, and it is found that optimum eye diagrams corresponding to minimum transmitter penalty (TP) can be correlated to Ge PD photocurrent.

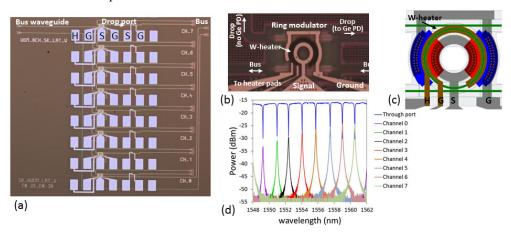


Fig. 1. Top view of (a) the 8 channel WDM transmitter with integrated W-heaters and Ge photodiodes, and (b) focus on the ring modulator. (c) Detail of the modulator design. (d) Transmission spectra of 8 cascaded ring modulators with drop ports.

2. Device description and DC performance

Fig. 1(a) shows a top view of the fabricated array of 8 ring modulators with drop ports, W-heaters and Ge photodetectors at the drop port. A zoom-in of the ring modulators and a detail from the ring design are also shown in Fig. 1(b) and Fig. 1(c) respectively. In this design, when light is input at the left side of the bus waveguide of the

cascaded rings, the drop ports can be characterized with optical transmission measurements, while when light is input at the right side of the bus the response of the Ge photodetectors to the light in the drop ports can be measured

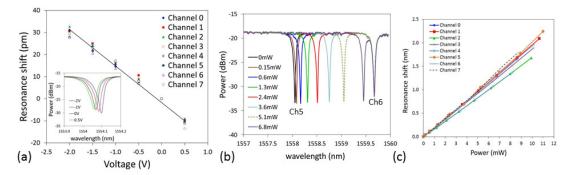


Fig. 2. (a) Resonance shift versus reverse bias for the 8 ring modulators and example of transmission spectra with applied bias. (b) Wavelength tuning of modulator resonance with applied heater power. (c) Ring resonance shift versus heater power for 8 channels.

The silicon ring waveguides had approximately 7.5 μ m radius and they were formed by etching 150nm out of 220nm crystalline silicon on 2 μ m buried oxide. A p-n diode (doped at 4e18cm⁻³) was implemented only in about 50% of the ring circumference, as shown in Fig. 1(c), resulting in ~12fF ring capacitance. The ring radius was slightly varied from ring to ring in the cascaded configuration along the same waveguide, resulting in an average channel spacing of 1.6 \pm 0.12nm along with a free spectral range (FSR) of ~12.8nm, as shown in Fig. 1(d). A reduction of ~7dB in the drop port transmission with increasing propagation distance along the bus waveguide was measured, which is attributed to increasing loss in waveguide transitions from deep-to-shallow etched waveguides. This could be improved in a future optimized design. An example of the ring response with applied DC bias is shown in the inset of Fig. 2(a). The modulation efficiency of all rings was ~16pm/V, as shown in Fig. 2(a). The Q-factor of the rings at 0V bias was about 18500 \pm 400 for all rings and it was the main limitation of the rings' small-signal (S₂₁) frequency response to f_{3dB} <10.5GHz. By optimizing the design it is expected that the bandwidth can be increased to 20GHz, enabling 28Gb/s operation.

The W-heaters were formed about $1\mu m$ on top of the ring modulators, after the W-contact holes and before the Cu metallization of the metal connections, as described in [3]. Fig. 2(b) shows an example of the resonance shift with applied heater power, while Fig. 2(c) shows the resonance shift with heater power for all 8 channels. The heating efficiency was $190\pm15 pm/mW$, or about 67.4mW for one FSR of a single ring. This translates to approximately 8.5mW needed for shift of the resonance within the ~1.6nm of the channel spacing. No degradation of the extinction ratio was observed with heater power, as shown in Fig. 2(b). Local substrate undercut is expected to substantially improve the power efficiency of the heaters [3].

The Ge photodetectors were lateral p-i-n diodes, $15\mu m$ long, formed by selective Ge epitaxial growth on top of the crystalline silicon and before the contact module. The dark current of these devices was about 100nA at -1V and the responsivity was above 0.55A/W at 1555nm for all channels.

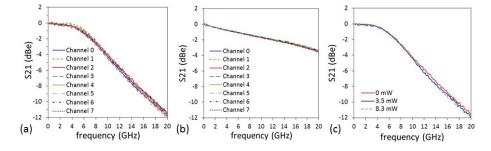


Fig. 3. Frequency response of (a) silicon ring modulators at 0V, and (b) Ge photodetectors at -1V for all 8 channels. (c) Frequency response of the ring modulator at channel 6 at different W-heater powers.

3. High-speed characterization

Small-signal S_{21} parameter characterization was performed using an Agilent network analyzer at -8dBm RF power. The frequency response of the silicon ring modulators and the Ge photodetectors is shown in Fig. 3(a) and 3(b) respectively. A 3-dB bandwidth of 8.6GHz was measured for all ring modulators at 0V. The Ge photodetectors had

a 3-dB bandwidth of 18GHz at -1V. The effect of heater power on the frequency response of the modulators is shown in Fig. 3(c). No difference in the small-signal performance of the ring modulators was measured for up to 8.3mW W-heater power.

The transmitter penalty (TP) can be defined as:

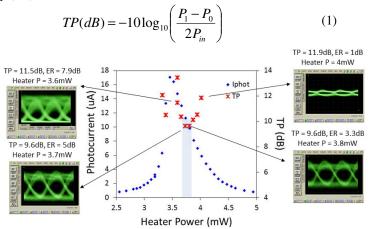


Fig. 4. Dynamic transmitter penalty and Ge photocurrent measured at 14Gb/s on channel 3 at fixed wavelength of 1555.4nm, with varying power on the W-heater. 14Gb/s eye diagrams measured at different W-heater power are also shown for comparison. Best eye diagrams at this wavelength for channel 3 were obtained at heater power indicated by the shaded area –this best operating point coincides with minimum TP.

The transmitter penalty is a useful figure of merit to determine the best wavelength of operation for low-power ring modulators [4]. Here, we use TP to assess modulation quality at fixed wavelength when the ring resonance is tuned by varying the W-heater power, and we correlate TP to the Ge photocurrent. Fig. 4 shows the Ge photocurrent and the dynamic TP with increasing W-heater power, measured when the ring was operated at 14Gb/s, driven by a $1.5V_{pp}$ signal with a -1.5V reverse bias, and using a 2^{31} -1 PRBS input signal. The operating wavelength during these measurements was fixed to 1555.4nm and the optical power arriving at the modulator array was -2dBm. It is shown that as the ring resonance is red-shifted with increasing heater power, the photocurrent is also varied, because the ring acts as a filter for the power incident on the Ge photodetector. Maximum photocurrent is therefore measured at heater power when the ring resonance minimum is at 1555.4nm. Although this results in significant Extinction Ratio (ER = 7.9dB), this condition coincides also with high insertion loss (IL), which does not result in optimum eye diagram (see top left eye in Fig. 4). Significant noise on the 1-bits is detected at this operating point, when the eye is measured using an EDFA and an optical filter after the modulator. Increasing further the heater power results in optimum combination of ER and IL at 1555.4nm, or minimum TP, as shown by the bottom left and right eye diagrams of Fig. 4. This corresponds to ~10µA average photocurrent on the Ge photodetector, sufficient for signal monitoring by a feedback control circuit. Further increase of the heater power red-shifts the ring resonance away from the wavelength of 1555.4nm, increasing again TP and degrading eye quality (top right eye diagram in Fig. 4).

3. Conclusions

In summary, an 8x14Gb/s wavelength-division multiplexed Si ring modulator array was presented, co-integrated with Tungsten heaters and Ge monitor photodetectors at the ring modulator drop ports. Uniform channel performance was demonstrated in terms of ring modulation bandwidth and efficiency, heater efficiency and Ge photodetector performance. The co-integration of these various elements will support the future demonstration of actively stabilized, ultra-compact ring-based WDM silicon photonics transmitters.

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4. References

- [1] X. Zheng *et al*, "A 33mW 100Gbps CMOS Silicon Photonics WDM Transmitter Using Off-Chip Laser Sources," in Optical Fiber Communication Conference and the National Fiber Optic Engineers Conference (OFC/NFOEC), (2013), Post-deadline papers, PDP5C.9, pp. 1-3. [2] G. Li *et al*, "40Gb/s thermally tunable CMOS ring modulator," in Group IV Photonics (GFP), (2012), pp. 1-3.
- [3] A. Masood *et al*, "Comparison of heater architectures for thermal control of silicon photonic circuits," in Group IV Photonics (GFP), (2013), p. ThC2.
- [4] M. Pantouvaki *et al*, "20Gb/s Silicon Ring Modulator Co-Integrated with a Ge Monitor Photodetector," in European Conference on Optical Communication (ECOC)₂ (2013), We.3.B.2.