









INTEGRATION OF STRETCHABLE OPTICAL WAVEGUIDES WITH SOURCES AND DETECTORS

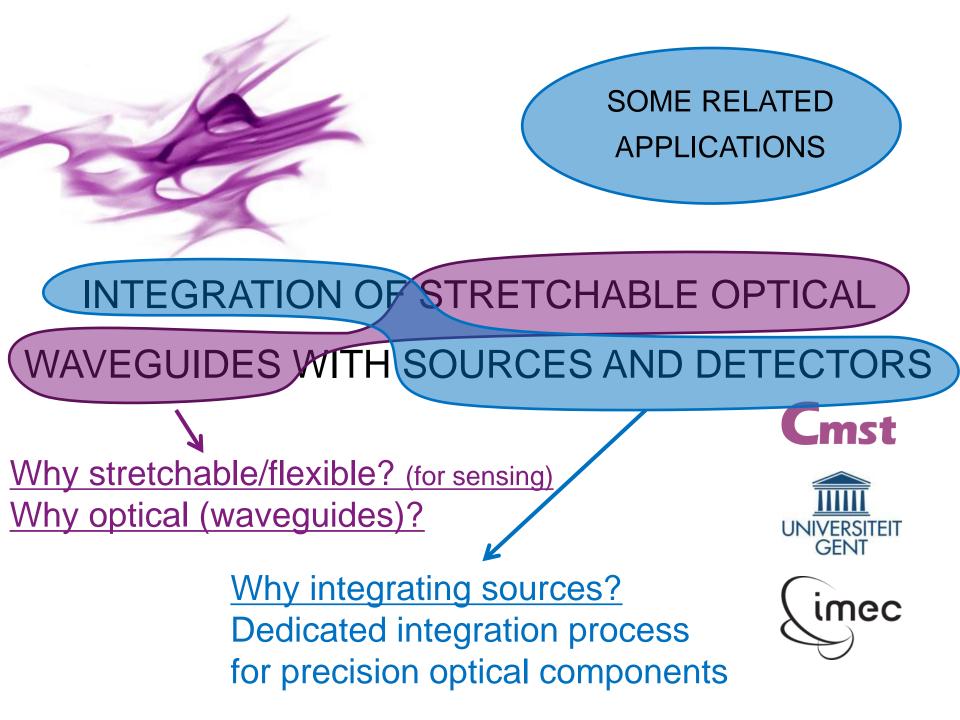
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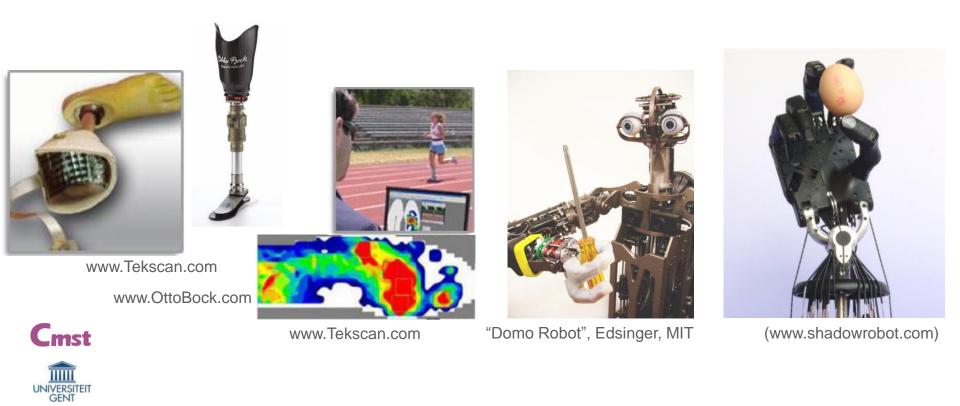
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WHY FLEXIBLE / STRETCHABLE (SENSING) SYSTEMS?

- Making "ultra-thin" (sensing) systems
- Unobtrusive systems \rightarrow wearable, "on body" applications
- Examples: artificial skin



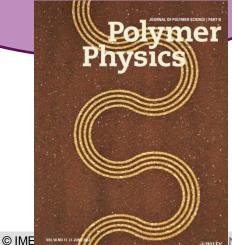


WHY FLEXIBLE / STRETCHABLE **OPTICAL SYSTEMS?**

- Stretchable electronics well-known technology
- Optical systems increasingly important
- Number of advantages (e.g. optical sensors vs. electrical)
- Therefore

Stretchable electrical interconnections

Stretchable optical interconnections (=waveguides)



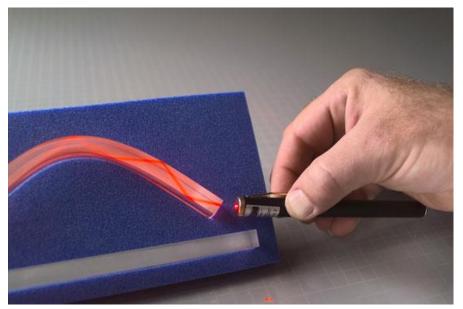




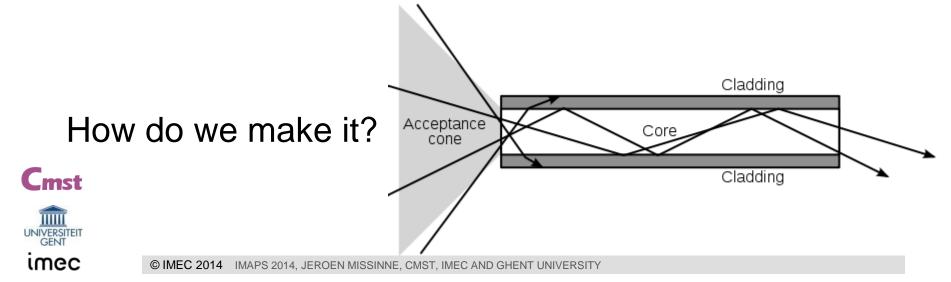
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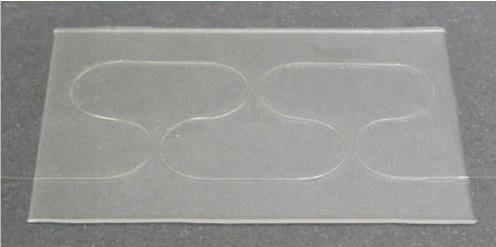
STRETCHABLE OPTICAL WAVEGUIDES: PRINCIPLE



What is an optical waveguide?



STRETCHABLE OPTICAL WAVEGUIDES: CONCEPT



"Only" flexible waveguide, but meandering

(B. Van Hoe)

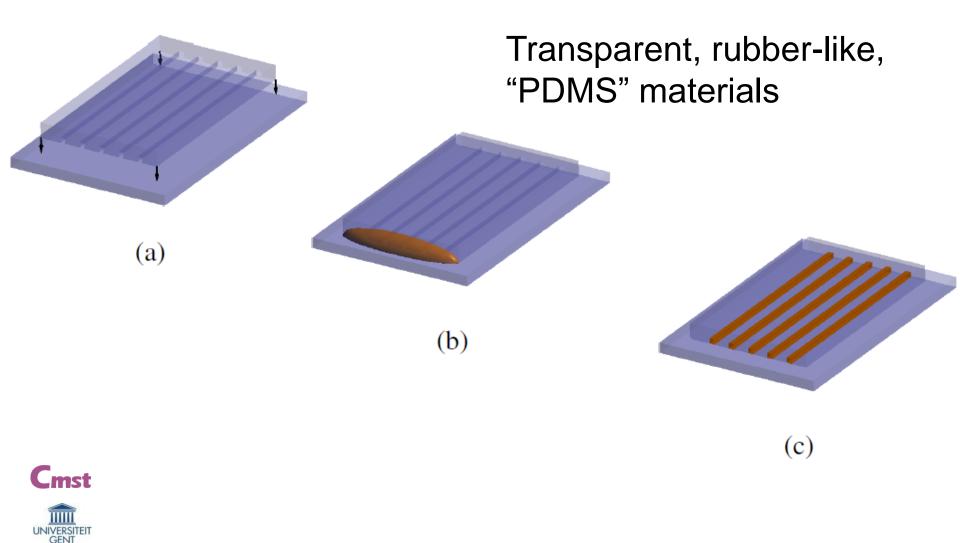
Straight waveguide channels, but from stretchable material







STRETCHABLE OPTICAL WAVEGUIDES: FABRICATION



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STRETCHABLE OPTICAL WAVEGUIDES: FABRICATION

$$NA \triangleq \sqrt{n_{core}^2 - n_{clad}^2} = 0.69$$

 \rightarrow allows small bending radii

LS-6257 (Nusil) n_{core} ≈ 1.57





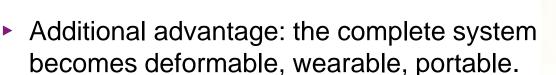
STRETCHABLE OPTICAL WAVEGUIDES: FABRICATION

The microstructures clearly guide light

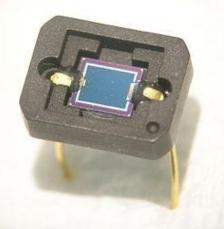


Bendability + stretchability of the final waveguide sample

- Waveguide without sources, detectors = useless
- Integration = needed for operation under deformation
- Micrometer waveguide dimensions
 Requires integration on the micrometer level
 Bulky, packaged components cannot be used
 We propose: ultra-thin, flexible OE package







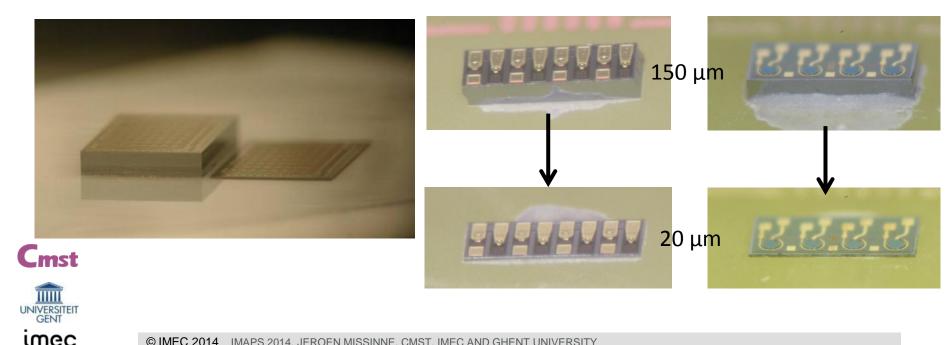


INTEGRATING SOURCES AND **DETECTORS: ULTRA-THIN OE PACKAGE**

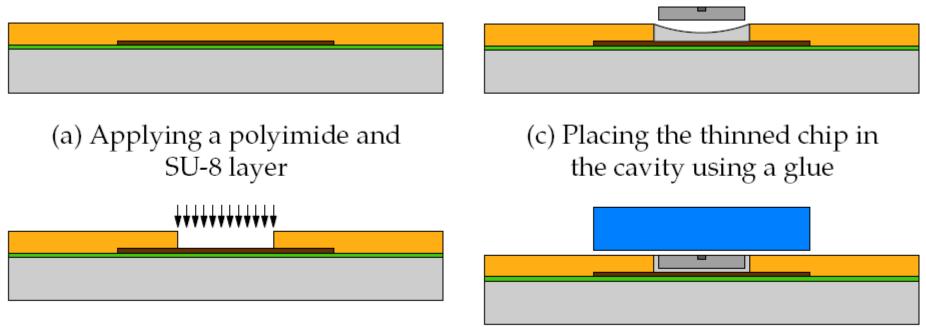
1st step: use of bare die chips + thinning

- Final (bare die) thickness ~20µm
- Lapping & polishing steps
- (re-)apply back contact if needed (single mode VCSELs)

2nd step: embedding of thinned bare dies



2nd step: embedding in polymer layers



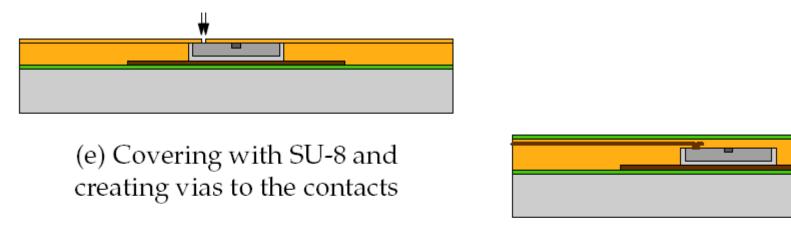
(b) Defining a cavity (via laser ablation or lithography)

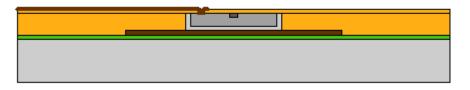
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(d) Leveling of the chip in the cavity

2nd step: embedding in polymer layers





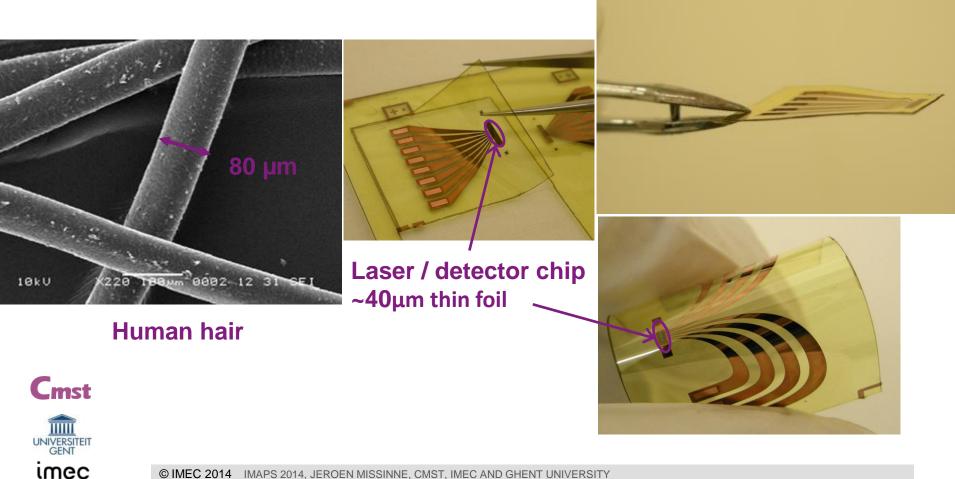
(f) Metalizing the vias





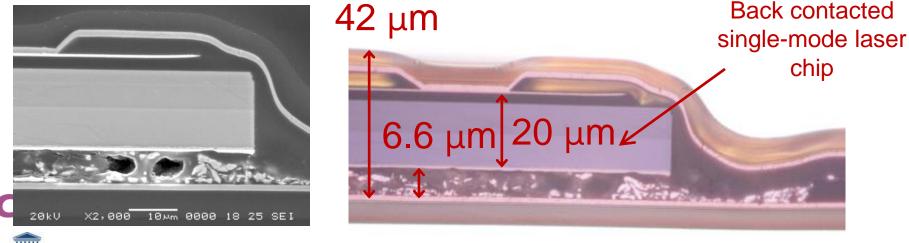
(g) Applying a covering SU-8 and polyimide layer

2nd step: embedding in polymer layers: results



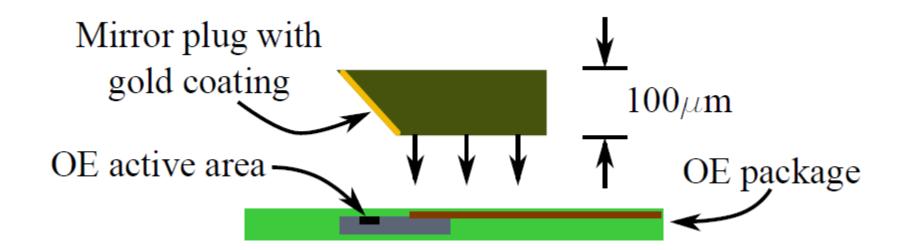
2nd step: embedding in polymer layers: results cross-section images





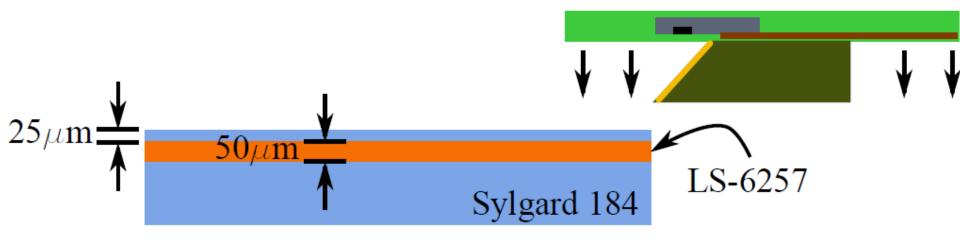
(results B. Van Hoe)

Placement of a 45° mirror-plug on the optoelectronic package



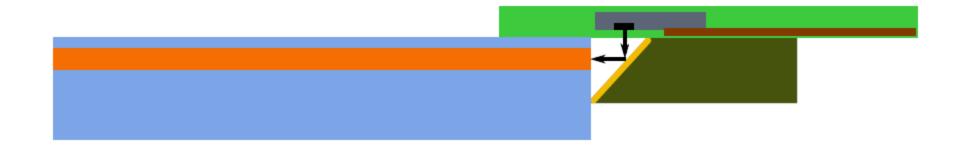


Aligning + bonding with the waveguide array



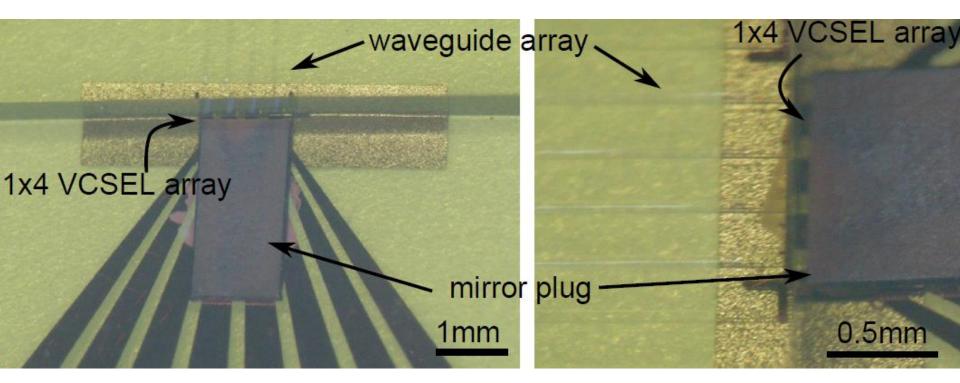


Resulting waveguides + sources / detectors



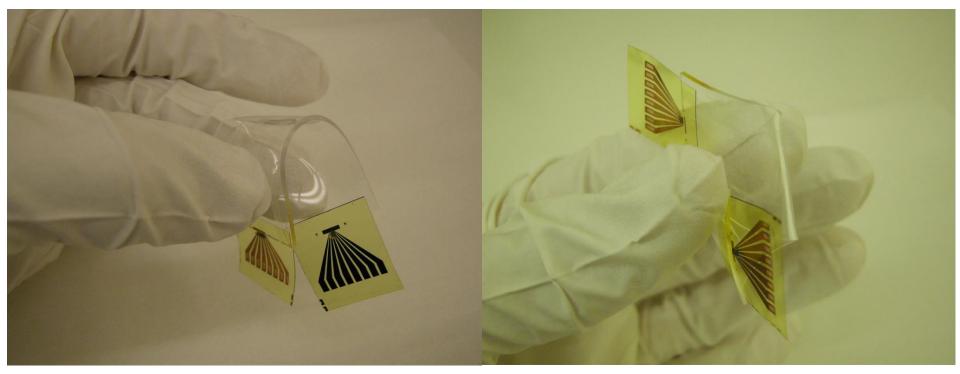


Resulting waveguides + sources / detectors





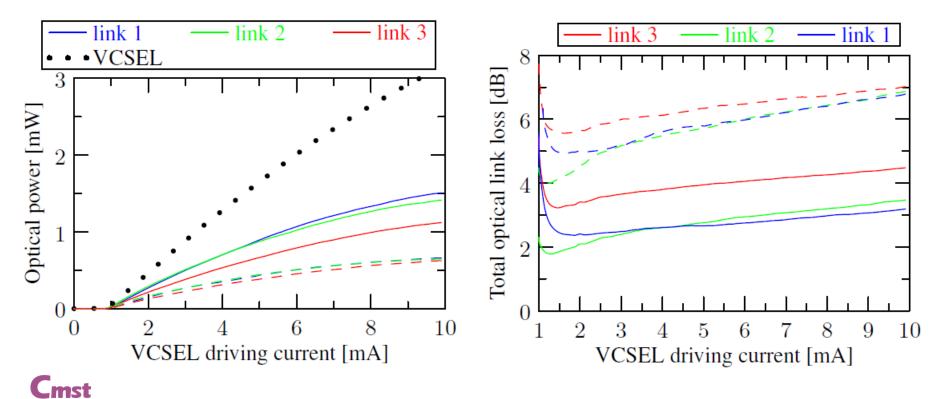
Resulting waveguides + sources / detectors





OPTICAL LINK: LOSSES?

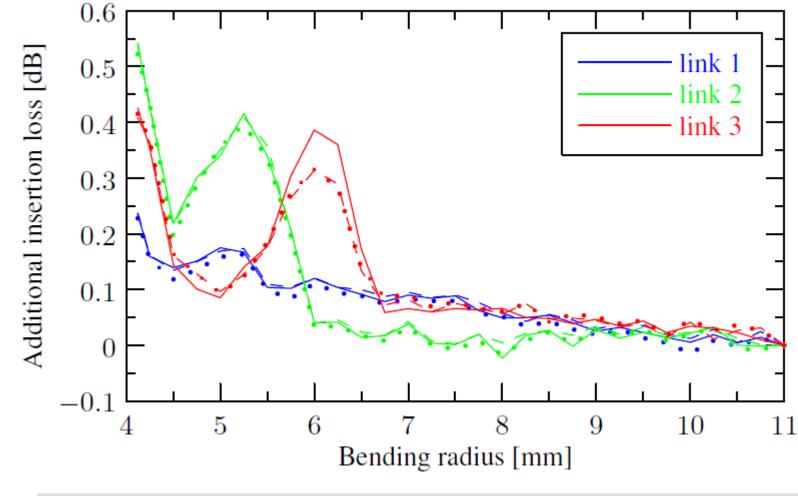
Light in \rightarrow waveguide \rightarrow light out?





OPTICAL LINK: BENDING LOSSES?



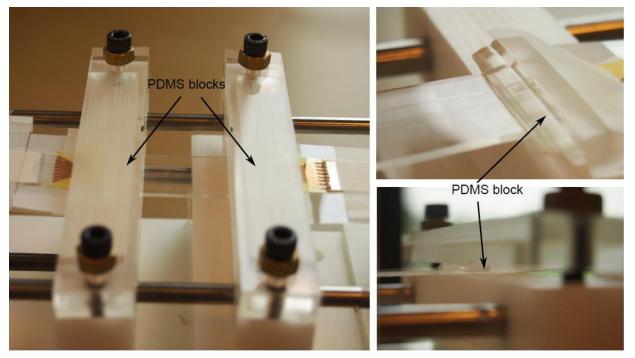


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OPTICAL LINK: STRETCH LOSSES?

Light in \rightarrow stretched waveguide \rightarrow light out?

Up to 30% elongation tested



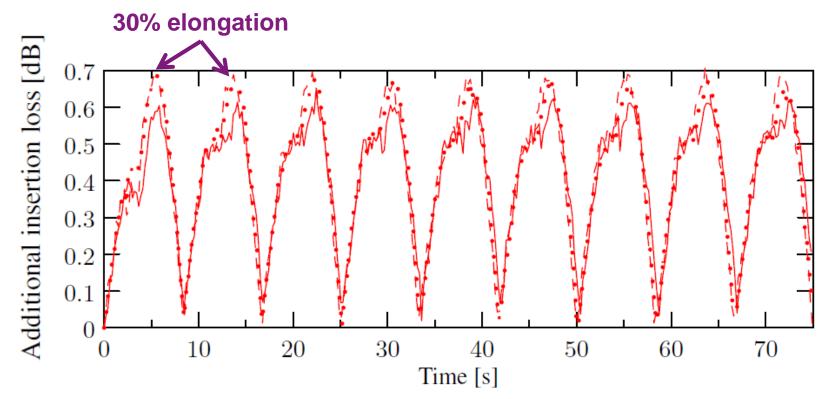


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OPTICAL LINK: STRETCH LOSSES?

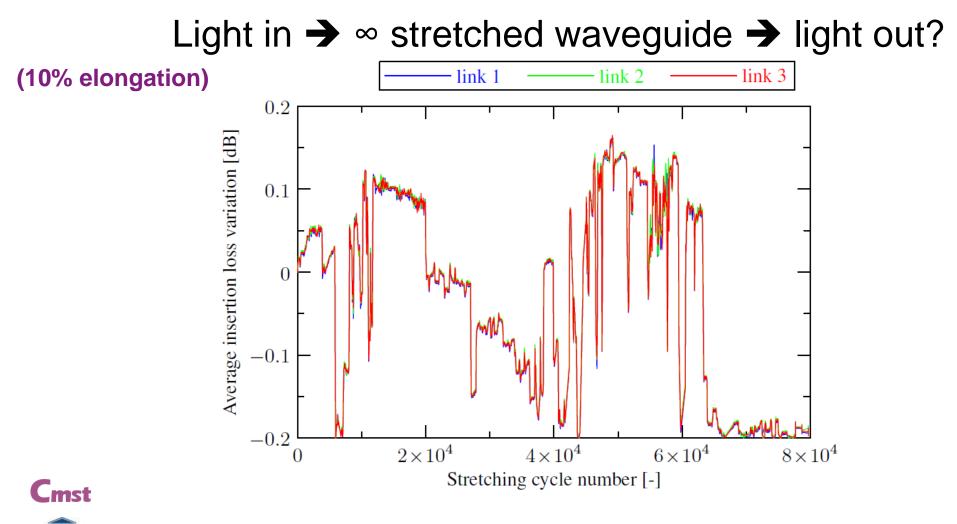
Light in → stretched waveguide → light out?



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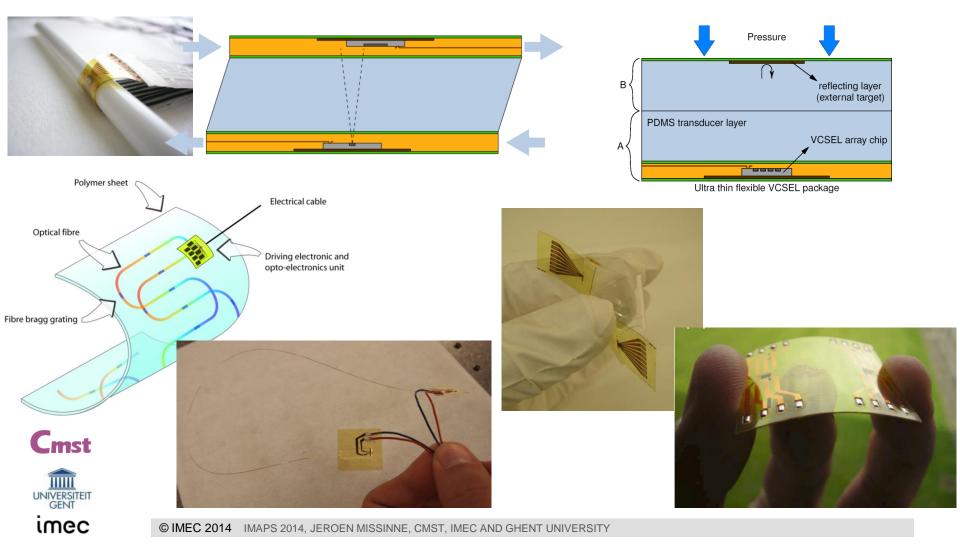
OPTICAL LINK: RELIABILITY?





ALTERNATIVE APPLICATIONS

applications: ultra-thin (sensing) systems



THANK YOU





