Computing and Informatics, Vol. 41, 2022, 1310-1336, doi: 10.31577/cai_2022_5_1310

SYNTHESIS OF LIVENESS-ENFORCING PETRI NET SUPERVISORS BASED ON A THINK-GLOBALLY-ACT-LOCALLY APPROACH AND A STRUCTURALLY MINIMAL METHOD FOR FLEXIBLE MANUFACTURING SYSTEMS

Chengzong LI, Yongyao LI, Yufeng CHEN*, Naiqi WU, Zhiwu LI

Institute of Systems Engineering, Macau University of Science and Technology Taipa, Macau 999078, China e-mail: {lczxdu, liyy30000, chyf01}@163.com, {nqwu, zwli}@must.edu.mo

Pengyu MA

Hitachi Building Technology (Guangzhou) Co. Ltd. Guangzhou 510700, China e-mail: mapengyu@hitachi-helc.com

Husam Kaid

Industrial Engineering Department, College of Engineering, Taibah University Medina 42353, Saudi Arabia e-mail: yemenhussam@yahoo.com

Abstract. This paper proposes a deadlock prevention policy for flexible manufacturing systems (FMSs) based on a think-globally-act-locally approach and a structurally minimal method. First, by using the think-globally-act-locally approach, a global idle place is temporarily added to a Petri net model with deadlocks. Then, at each iteration, an integer linear programming problem is formulated to design a minimal number of maximally permissive control places. Therefore, a supervisor with a low structural complexity is obtained since the number of control places is

^{*} Corresponding author

Keywords: Flexible manufacturing system, deadlock prevention, think-globallyact-locally approach, structurally minimal method, maximal permissiveness

Mathematics Subject Classification 2010: 93-C65

1 INTRODUCTION

A flexible manufacturing system (FMS) performs different kinds of tasks with multiple processes that compete for limited resources, such as robots, machines, automated guided vehicles, buffers, fixtures, etc. In an FMS, deadlock may be caused by the competition of system resources between different processes [1]. Generally, a deadlock makes a system blocked and inefficient, even leads to destructive results, which is highly undesirable. Therefore, a number of approaches, such as deadlock detection and recovery [2, 3], deadlock avoidance [4, 5, 6], and deadlock prevention [7, 8, 9, 10, 11, 12], have been proposed to resolve the deadlock issue. In this paper, we focus on discussing deadlock prevention policies.

Petri nets, graph theory, and automata are generally considered to deal with deadlocks in FMSs [13, 14, 15, 16]. Compared with graph theory and automata, Petri nets have some irreplaceable advantages in dealing with deadlocks [17, 18, 19, 20, 21, 22]. In an FMS modeled by Petri nets, only some simple and necessary constraints are required to prevent deadlocks, and a supervisor can be obtained offline in a static way [23, 24, 25, 26, 27]. Some major indicators are considered to design liveness-enforcing Petri net supervisors, including behavioral permissiveness, structural complexity, and computational complexity. Permissive behavior represents the legal markings of a Petri net. Hence, a supervisor is said to be maximally permissive if all legal markings are reachable. The structural complexity is always evaluated by the number of control places in a supervisor. Computational complexity indicates the efficiency of the proposed algorithm to design a supervisor.

Generally, the techniques used for synthesizing deadlock prevention policies can be divided into two types: structural analysis [28, 29, 30, 31] and reachability graph analysis [7, 10, 32, 33, 34]. Structural analysis is an effective way to deal with deadlocks for some special Petri net structures (for example siphons and resource circuits) [35]. However, it usually leads to that the resulting net model is not optimally controlled [36, 37], since the computed supervisor is so conservative that many legal behaviors of the system are prevented. Compared with structural analysis techniques, the reachability graph analysis approaches can lead to optimal or near-optimal supervisors for generalized Petri net models. Nevertheless, these methods need to enumerate all reachable markings of a system [10, 38, 39, 40].

| DZdeadlock-zoneFBMfirst-met bad markingFMSflexible manufacturing systemGPglobal idle place $G(N, M_0)$ reachability graph of net (N, M_0) I place invariantILPPinteger linear programming problemLZlive-zone M marking \mathcal{M}_L set of legal markings \mathcal{M}_L set of FBMs \mathcal{M}_{FBM} set of FBMs \mathcal{M}_{FBM}^* minimal covering set of legal markings \mathcal{M}_{FBM}^* minimal covered set of FBMs \mathcal{M}_{FBM} minimal covered set of FBMs \mathcal{M}_{FBM} minimal number of control places problem N Petri net with $N = (P, T, F, W)$ N_B Petri net with the GP \mathbb{N} set of non-negative integers $[N]$ incidence matrix (N, M_0) Petri net model p place in a Petri net p_s control place (monitor) P set of places in a Petri net PI place invariant Q big enough integer constant $R(N, M_0)$ set of reachable markings of net (N, M_0) t transition in a Petri net T set of transitions in a Petri net T set of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | В | number of tokens in the GP |
|---|--------------------------------------|--|
| FBMfirst-met bad markingFMSflexible manufacturing systemGPglobal idle place $G(N, M_0)$ reachability graph of net (N, M_0) I place invariantILPPinteger linear programming problemLZlive-zone M marking \mathcal{M}_L set of legal markings \mathcal{M}_L set of FBMs \mathcal{M}_L^* minimal covering set of legal markings \mathcal{M}_{FBM}^* minimal covered set of FBMs \mathcal{M}_{FBM}^* minimal covered set of FBMs \mathcal{M}_{FBM}^* minimal number of control places problem N Petri net with $N = (P, T, F, W)$ N_B Petri net with the GP \mathbb{N} set of non-negative integers $[N]$ incidence matrix (N, M_0) Petri net model p place in a Petri net p_s control place (monitor) P set of places in a Petri net P set of reachable markings of net (N, M_0) t transition in a Petri net T set of transitions in a Petri net T set of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | DZ | deadlock-zone |
| FMSflexible manufacturing systemGPglobal idle place $G(N, M_0)$ reachability graph of net (N, M_0) I place invariantILPPinteger linear programming problemLZlive-zone M marking \mathcal{M}_L set of legal markings \mathcal{M}_{FBM} set of FBMs \mathcal{M}_L^* minimal covering set of legal markings \mathcal{M}_{FBM}^* minimal covered set of FBMsMCPPminimal number of control places problem N Petri net with $N = (P, T, F, W)$ N_B Petri net with the GP \mathbb{N} set of non-negative integers $[N]$ incidence matrix (N, M_0) Petri net model p place in a Petri net p_s control place (monitor) P set of places in a Petri netPIplace invariantQbig enough integer constant $R(N, M_0)$ set of reachable markings of net (N, M_0) t transition in a Petri net T set of transitions in a Petr | FBM | first-met bad marking |
| GPglobal idle place $G(N, M_0)$ reachability graph of net (N, M_0) I place invariantILPPinteger linear programming problemLZlive-zone M marking \mathcal{M}_L set of legal markings \mathcal{M}_{FBM} set of FBMs \mathcal{M}_L^* minimal covering set of legal markings \mathcal{M}_{FBM}^* minimal covered set of FBMs \mathcal{M}_{FBM}^* minimal covered set of FBMsMCPPminimal number of control places problem N Petri net with $N = (P, T, F, W)$ N_B Petri net with the GP \mathbb{N} set of non-negative integers $[N]$ incidence matrix (N, M_0) Petri net model p place in a Petri net p_s control place (monitor) P set of places in a Petri netPIplace invariantQbig enough integer constant $R(N, M_0)$ set of reachable markings of net (N, M_0) t transition in a Petri net T set of transitions in a Petri net T set of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | FMS | flexible manufacturing system |
| $\begin{array}{llllllllllllllllllllllllllllllllllll$ | GP | global idle place |
| $\begin{array}{llllllllllllllllllllllllllllllllllll$ | $G(N, M_0)$ | reachability graph of net (N, M_0) |
| ILPPinteger linear programming problemLZlive-zone M marking \mathcal{M}_{L} set of legal markings \mathcal{M}_{FBM} set of FBMs \mathcal{M}_{L}^{\star} minimal covering set of legal markings \mathcal{M}_{L}^{\star} minimal covered set of FBMs \mathcal{M}_{FBM} minimal covered set of FBMsMCPPminimal number of control places problem N Petri net with $N = (P, T, F, W)$ N_B Petri net with the GP \mathbb{N} set of non-negative integers $[N]$ incidence matrix (N, M_0) Petri net model p place in a Petri net p_s control place (monitor) P set of places in a Petri netPIplace invariantQbig enough integer constant $R(N, M_0)$ set of reachable markings of net (N, M_0) t transition in a Petri net T set of transitions in a Petri netTGALthink-globally-act-locally methodTGALWthink-globally-act-locally method with weighted arcs $^{\bullet}$ postset of a node $x \in P \cup T$ | Ι | place invariant |
| LZlive-zone M marking \mathcal{M}_{L} set of legal markings \mathcal{M}_{FBM} set of FBMs \mathcal{M}_{L}^{\star} minimal covering set of legal markings $\mathcal{M}_{FBM}^{\star}$ minimal covered set of FBMs \mathcal{M}_{FBM} minimal number of control places problem \mathcal{N} Petri net with $N = (P, T, F, W)$ N_B Petri net with the GP \mathbb{N} set of non-negative integers $[N]$ incidence matrix (N, M_0) Petri net model p place in a Petri net p_s control place (monitor) P set of places in a Petri netPIplace invariantQbig enough integer constant $R(N, M_0)$ set of reachable markings of net (N, M_0) t transition in a Petri net T set of transitions in a Petri netTGALthink-globally-act-locally methodTGALWthink-globally-act-locally method with weighted arcs $^{\bullet}$ postset of a node $x \in P \cup T$ | ILPP | integer linear programming problem |
| $ \begin{array}{llllllllllllllllllllllllllllllllllll$ | LZ | live-zone |
| $ \begin{array}{llllllllllllllllllllllllllllllllllll$ | M | marking |
| $ \begin{array}{llllllllllllllllllllllllllllllllllll$ | \mathcal{M}_{L} | set of legal markings |
| $ \begin{array}{llllllllllllllllllllllllllllllllllll$ | $\mathcal{M}_{\mathrm{FBM}}$ | set of FBMs |
| $ \begin{array}{llllllllllllllllllllllllllllllllllll$ | $\mathcal{M}^{\star}_{\mathrm{L}}$ | minimal covering set of legal markings |
| MCPPminimal number of control places problem N Petri net with $N = (P, T, F, W)$ N_B Petri net with the GP \mathbb{N} set of non-negative integers $[N]$ incidence matrix (N, M_0) Petri net model p place in a Petri net p_s control place (monitor) P set of places in a Petri netPIplace invariantQbig enough integer constant $R(N, M_0)$ set of reachable markings of net (N, M_0) t transition in a Petri netTset of transitions in a Petri netTGALthink-globally-act-locally methodTGALWthink-globally-act-locally method with weighted arcs• x preset of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | $\mathcal{M}_{\mathrm{FBM}}^{\star}$ | minimal covered set of FBMs |
| NPetri net with $N = (P, T, F, W)$ N_B Petri net with the GP \mathbb{N} set of non-negative integers $[N]$ incidence matrix (N, M_0) Petri net model p place in a Petri net p_s control place (monitor) P set of places in a Petri netPIplace invariantQbig enough integer constant $R(N, M_0)$ set of reachable markings of net (N, M_0) ttransition in a Petri netTset of transitions in a Petri netTGALthink-globally-act-locally methodTGALWthink-globally-act-locally method with weighted arcs $\bullet x$ preset of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | MCPP | minimal number of control places problem |
| $\begin{array}{llllllllllllllllllllllllllllllllllll$ | N | Petri net with $N = (P, T, F, W)$ |
| Nset of non-negative integers $[N]$ incidence matrix (N, M_0) Petri net model p place in a Petri net p_s control place (monitor)Pset of places in a Petri netPIplace invariantQbig enough integer constant $R(N, M_0)$ set of reachable markings of net (N, M_0) ttransition in a Petri netTset of transitions in a Petri netTGALthink-globally-act-locally methodTGALWthink-globally-act-locally method with weighted arcs• x preset of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | N_B | Petri net with the GP |
| | \mathbb{N} | set of non-negative integers |
| $\begin{array}{llllllllllllllllllllllllllllllllllll$ | [N] | incidence matrix |
| p place in a Petri net p_s control place (monitor) P set of places in a Petri netPIplace invariantQbig enough integer constant $R(N, M_0)$ set of reachable markings of net (N, M_0) t transition in a Petri net T set of transitions in a Petri netTGALthink-globally-act-locally methodTGALWthink-globally-act-locally method with weighted arcs $\bullet x$ preset of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | (N, M_0) | Petri net model |
| p_s control place (monitor) P set of places in a Petri netPIplace invariantQbig enough integer constant $R(N, M_0)$ set of reachable markings of net (N, M_0) t transition in a Petri net T set of transitions in a Petri netTGALthink-globally-act-locally methodTGALWthink-globally-act-locally method with weighted arcs• x preset of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | p | place in a Petri net |
| Pset of places in a Petri netPIplace invariantQbig enough integer constant $R(N, M_0)$ set of reachable markings of net (N, M_0) ttransition in a Petri netTset of transitions in a Petri netTGALthink-globally-act-locally methodTGALWthink-globally-act-locally method with weighted arcs• x preset of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | p_s | control place (monitor) |
| PIplace invariantQbig enough integer constant $R(N, M_0)$ set of reachable markings of net (N, M_0) ttransition in a Petri netTset of transitions in a Petri netTGALthink-globally-act-locally methodTGALWthink-globally-act-locally method with weighted arcs• x preset of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | P | set of places in a Petri net |
| $\begin{array}{llllllllllllllllllllllllllllllllllll$ | PI | place invariant |
| $\begin{array}{lll} R(N,M_0) & \text{set of reachable markings of net } (N,M_0) \\ t & \text{transition in a Petri net} \\ T & \text{set of transitions in a Petri net} \\ TGAL & \text{think-globally-act-locally method} \\ TGALW & \text{think-globally-act-locally method with weighted arcs} \\ \bullet x & \text{preset of a node } x \in P \cup T \\ x^\bullet & \text{postset of a node } x \in P \cup T \end{array}$ | Q | big enough integer constant |
| ttransition in a Petri netTset of transitions in a Petri netTGALthink-globally-act-locally methodTGALWthink-globally-act-locally method with weighted arcs $^{\bullet}x$ preset of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | $R(N, M_0)$ | set of reachable markings of net (N, M_0) |
| Tset of transitions in a Petri netTGALthink-globally-act-locally methodTGALWthink-globally-act-locally method with weighted arcs $^{\bullet}x$ preset of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | t | transition in a Petri net |
| TGALthink-globally-act-locally methodTGALWthink-globally-act-locally method with weighted arcs• x preset of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | Т | set of transitions in a Petri net |
| TGALWthink-globally-act-locally method with weighted arcs• x preset of a node $x \in P \cup T$ x^{\bullet} postset of a node $x \in P \cup T$ | TGAL | think-globally-act-locally method |
| • x preset of a node $x \in P \cup T$ x• postset of a node $x \in P \cup T$ | TGALW | think-globally-act-locally method with weighted arcs |
| x^{\bullet} postset of a node $x \in P \cup T$ | •x | preset of a node $x \in P \cup T$ |
| | x^{\bullet} | postset of a node $x \in P \cup T$ |

Table 1. Nomenclature

In this paper, we mainly discuss methods related to the reachability graph analysis. Given a control specification, all markings of a system can be divided into two categories: legal and illegal ones. For the specification of deadlock prevention, a marking is called as a legal marking if itself or one of its succeeding markings can evolve back to the initial marking, otherwise, it is an illegal marking. A control policy is optimal if it ensures that all illegal markings are prohibited while no legal marking is prevented.

In [41, 42], a reachability graph is partitioned into a live-zone (LZ) and a deadlock-zone (DZ), and these two zones contain all legal and illegal markings, respectively. Then, a first-met bad marking (FBM) is defined as an illegal one that represents the very first entry from the LZ to the DZ. FBMs are a special kind of illegal markings related to deadlocks, since the system cannot enter into the DZ if they are all prevented. However, some legal markings may be prohibited when a set of control places (monitors) is computed to prevent all FBMs. That is to say, the obtained supervisor cannot be ensured to be behaviorally optimal and also suffers from the structural complexity problem due to too many control places obtained.

To solve the above problem, Chen et al. propose a vector covering approach by analyzing the relationship between different markings [38]. They first put forward the concept of two minimal sets: a minimal covering set of legal markings and a minimal covered set of FBMs, which can be used to design control places without considering all legal markings and all FBMs. However, there are too many control places in a supervisor since a monitor is required to be designed for each FBM in the minimal covered set of FBMs.

In [10], Chen and Li further develop an approach to design structurally minimal controller, which selects as few control places as possible. By using this method, no redundant monitor survives [7, 10]. Also, it ensures that the obtained supervisor is behaviorally optimal. Nevertheless, for a complex net model, it is impossible to compute a maximally permissive supervisor in a reasonable time by using this method due to the complexity of solving an integer linear program with too many constraints and variables.

In [44], Uzam et al. present a think-globally-act-locally method (TGAL) to prevent deadlocks in an iterative way, which can alleviate the state explosion problem. They propose a global idle place (GP), which is temporarily added to the original Petri net model but the GP does not change any of its basic properties. At each iteration, by increasing one token in the GP, the reachability graph of the related net is generated and a set of control places is computed to prevent deadlocks. Finally, all monitors designed in the iteration processes are added to the original net model such that the resulting net model is live. However, this method cannot ensure the maximal permissiveness of computed supervisors for generalized Petri net models since some legal markings may be lost in the iteration processes.

In order to improve TGAL, Uzam et al. further develop a think-globally-actlocally approach with weighted arcs (TGALW) [45]. Compared with their previous work, it can obtain control places with weighted arcs at each iteration by transforming the original Petri net into a strictly conservative form. This method ensures that the resulting controlled system has more reachable markings than the previous TGAL. However, it also faces the problem that the designed supervisor is not optimal for generalized net models.

Similarly, the method previously proposed by the authors of this work [46] improves the behavioral permissiveness of TGAL. At each iteration, a vector covering approach is applied to design a set of control places with maximally permissiveness. By using this method, the resulting Petri net models have more reachable behaviors than TGAL and TGALW, and in most of cases it is optimal. Nevertheless, it also has some drawbacks, such as the obtained supervisor has too many control places and a redundancy test is necessary to select the necessary monitors at each iteration. In this paper, we further develop a method for supervisory control based on TGAL and the structural minimization of a controller. At each iteration, the structurally minimal method is used to formulate an integer linear programming problem (ILPP). By solving this ILPP, a set of optimal control places is obtained with the number of control places being minimized. Therefore, the number of designed monitors is greatly compressed and the redundancy test is not necessary at each iteration. Finally, the resulting net model is live by adding a small number of control places. Compared with TGAL, TGALW, and our previous work [46], this approach constructs an optimal or near-optimal supervisor with fewer control places.

We organize the rest of this paper as follows. Section 2 outlines some basic concepts used in this paper, such as Petri nets [17], control place synthesis by place invariant (PI) [43], and the structurally minimal approach [10]. A deadlock prevention policy is proposed to design supervisors with simple structures in Section 3. Section 4 presents some experiment results by using the proposed method. Finally, conclusions are given in Section 5.

2 PRELIMINARIES

2.1 Petri Nets

A Petri net [17] is a four-tuple N = (P, T, F, W), where P and T are finite and non-empty sets of places and transitions, respectively. The flow relation of a net is represented by arcs with arrows from places to transitions or from transitions to places, denoted as $F \subseteq (P \times T) \cup (T \times P)$. $W : (P \times T) \cup (T \times P) \to \mathbb{N}$ is a mapping that assigns a weight to an arc: W(x, y) > 0 if $(x, y) \in F$, and W(x, y) = 0, otherwise, where $x, y \in P \cup T$ and \mathbb{N} is the set of non-negative integers. The preset and postset of a node $x \in P \cup T$ are $\bullet x = \{y \in P \cup T \mid (y, x) \in F\}$ and $x^{\bullet} = \{y \in P \cup T \mid (x, y) \in F\}$, respectively. A marking represents a mapping $M : P \to \mathbb{N}$. The number of tokens in place p at marking M is denoted as M(p). Generally, vector M can be written as $\sum_{p \in P} M(p)p$. The pair (N, M_0) is called a marked Petri net or a net system. Incidence matrix [N] of net N is a $|P| \times |T|$ integer matrix with [N](p, t) = W(t, p) - W(p, t).

A transition $t \in T$ is enabled at marking M if for all $p \in {}^{\bullet}t$, $M(p) \geq W(p, t)$. This fact is denoted as $M[t\rangle$. Once a transition t fires, it yields a marking M', denoted as $M[t\rangle M'$, where M'(p) = M(p) - W(p, t) + W(t, p), for all $p \in P$. $M_0[\rangle$ is called the set of reachable markings of a Petri net N from the initial marking M_0 , often denoted by $R(N, M_0)$. A reachability graph is a graphical representation of $R(N, M_0)$. The reachability graph of a net (N, M_0) , denoted as $G(N, M_0)$, is a directed graph whose nodes are markings in $R(N, M_0)$ and arcs are labeled by the transitions of N.

Let (N, M_0) be a net system with N = (P, T, F, W). A transition $t \in T$ is live at M_0 if there exists $M' \in R(N, M)$ such that $M'[t\rangle$, for all $M \in R(N, M_0)$. (N, M_0) is live if for all $t \in T$, t is live at M_0 . It is dead at M_0 if there does not exist $t \in T$ such that $M_0[t\rangle$.

A *P*-vector is a column vector $I : P \to \mathbb{Z}$ indexed by *P*, where \mathbb{Z} is the set of integers. *P*-vector *I* is called a place invariant (PI for short) if $I \neq \mathbf{0}$ and $I^T[N] = \mathbf{0}^T$. Let *I* be a PI of (N, M_0) and *M* be a reachable marking from M_0 . Then, $I^T M = I^T M_0$.

2.2 Analysis of Reachability Graph

For the optimal control purpose, a supervisor should ensure that all legal markings are reachable. In a Petri net model (N, M_0) , set \mathcal{M}_L consists of all legal markings, denoted as:

$$\mathcal{M}_{\rm L} = \{ M | M \in R(N, M_0) \land M_0 \in R(N, M) \}.$$
(1)

A reachability graph can be partitioned into two zones: a deadlock-zone (DZ) and a live-zone (LZ) [41, 42]. A first-met bad marking (FBM) is a special illegal marking that can be firstly met from the LZ to the DZ by firing one transition. The set of FBMs is defined as:

$$\mathcal{M}_{\text{FBM}} = \{ M \in \text{DZ} | \exists M' \in \text{LZ}, \exists t \in T, M'[t\rangle M \}.$$
(2)

2.3 Control Place Synthesis Method by Place Invariant

In [43], Yamalidou et al. propose an approach to compute a control place (or monitor) by a PI, including its initial marking and connected arcs. Let $[N_0]$ be the incidence matrix of a Petri net to be controlled with n places and m transitions. Given a control requirement, the following constraint requires to be satisfied:

$$\sum_{i=1}^{n} l_i \cdot M(p_i) \le \beta \tag{3}$$

where l_i and β are non-negative integers, and $M(p_i)$ represents the marking of place p_i . By introducing a non-negative slack variable $M(p_s)$, Equation (3) is transformed as follows:

$$\sum_{i=1}^{n} l_i \cdot M(p_i) + M(p_s) = \beta \tag{4}$$

where p_s is a monitor. In particular, at the initial marking, Equation (4) can be written as:

$$M_0(p_s) = \beta - \sum_{i=1}^{n} l_i \cdot M_0(p_i)$$
(5)

where $M_0(p_s)$ is the initial marking of the control place p_s .

2.4 Optimal Control Place Synthesis

A manufacturing-oriented Petri net (M-net for short) is proposed in [49], which is a generalization of the existing net classes that model FMS. In an M-net, there are three categories of places: idle, operation (activity), and resource places, whose sets are denoted as P^0 , P_A , and P_R , respectively [23, 48]. An idle place means a raw part before entering a production sequence. The tokens in an idle place represent the number of concurrent operations that can happen in the production sequences. An operation place indicates an operation to be processed for a part in a production sequence and initially it has no token. A resource place models a type of available resources, such as robots and machines. At the initial state, tokens in a resource place are equal to the number of available resource units.

By considering the tokens in operation places only, we construct a PI to prohibit an FBM [41]. In this paper, \mathbb{N}_A is used to denote $\{i | p_i \in P_A\}$. An FBM $M_f \in \mathcal{M}_{\text{FBM}}$ should be prevented by enforcing the following constraint:

$$\sum_{i\in\mathbb{N}_A} l_i \cdot \mu_i \le \beta \tag{6}$$

where

$$\beta = \sum_{i \in \mathbb{N}_A} l_i \cdot M_f(p_i) - 1.$$
(7)

Equation (6) is the forbidden condition. For a legal marking $M' \in \mathcal{M}_{L}$, the reachability condition is given as follows:

$$\sum_{i \in \mathbb{N}_A} l_i \cdot M'(p_i) \le \beta, \quad \forall M' \in \mathcal{M}_{\mathrm{L}}.$$
(8)

By substituting the β in Equation (7) into Equation (8), the following equation is obtained:

$$\sum_{i \in \mathbb{N}_A} l_i \cdot (M'(p_i) - M_f(p_i)) \le -1, \quad \forall M' \in \mathcal{M}_{\mathrm{L}}.$$
(9)

By solving Equation (9), we can obtain a set of feasible solutions for coefficients l_i 's. Then, an optimal PI is computed, which can ensure that an FBM is prevented while all legal markings are reachable.

In order to further reduce the number of markings in \mathcal{M}_{L} and \mathcal{M}_{FBM} , a vector covering technique [38] is proposed, and more details of this method are given as follows:

Definition 1 ([38]). Let M and M' be two markings in $R(N, M_0)$. M A-covers M' (or M' is A-covered by M) if $M(p) \ge M'(p)$, for all $p \in P_A$, which is denoted as $M \ge_A M'$ (or $M' \le_A M$).

Definition 2 ([38]). Let \mathcal{M}_{L}^{\star} be a subset of legal markings. \mathcal{M}_{L}^{\star} is called a minimal covering set of legal markings if the following two conditions are satisfied:

- 1. $(\forall M \in \mathcal{M}_{\mathrm{L}})(\exists M' \in \mathcal{M}_{\mathrm{L}}^{\star})M' \geq_{A} M$; and
- 2. $(\forall M \in \mathcal{M}_{\mathrm{L}}^{\star}) (\not \exists M' \in \mathcal{M}_{\mathrm{L}}^{\star}) M' \geq_{A} M \& M \neq M'.$

Definition 3 ([38]). Let $\mathcal{M}_{FBM}^{\star}$ be a subset of \mathcal{M}_{FBM} . $\mathcal{M}_{FBM}^{\star}$ is called a minimal covered set of FBMs if the following two conditions are satisfied:

- 1. $(\forall M_f \in \mathcal{M}_{\text{FBM}})(\exists M'_f \in \mathcal{M}^{\star}_{\text{FBM}})M_f \geq_A M'_f;$ and
- 2. $(\forall M_f \in \mathcal{M}_{FBM}^{\star}) (\not \exists M_f' \in \mathcal{M}_{FBM}^{\star}) M_f \geq_A M_f' \& M_f \neq M_f'.$

Corollary 1 ([38]). If all markings in $\mathcal{M}_{FBM}^{\star}$ are forbidden by PIs, then all FBMs are forbidden.

Corollary 2 ([38]). If any marking in \mathcal{M}_{L}^{\star} is not prevented by PIs, then all legal markings are reachable.

According to Corollaries 1 and 2, an optimal supervisor is computed by using markings in sets \mathcal{M}_{L}^{\star} and $\mathcal{M}_{FBM}^{\star}$. Finally, Equation (9) can be simplified as follows:

$$\sum_{i \in \mathbb{N}_A} l_i \cdot (M'(p_i) - M_f(p_i)) \le -1, \quad \forall M' \in \mathcal{M}_{\mathrm{L}}^{\star}.$$
 (10)

2.5 Minimal Supervisory Structure Synthesis

Similarly, we use $\mathbb{N}_{\text{FBM}}^{\star}$ to denote $\{i|M_i \in \mathcal{M}_{\text{FBM}}^{\star}\}$ in this paper. Given two FBMs M_j and M_k $(j, k \in \mathbb{N}_{\text{FBM}}^{\star}$ and $j \neq k$), a PI I_j is designed to prevent M_j . Then, M_k is forbidden if the following constraint is satisfied:

$$\sum_{i \in \mathbb{N}_A} l_{j,i} \cdot \left(M_k(p_i) - M_j(p_i) \right) \ge 0 \tag{11}$$

where $l_{j,i}$'s represent the coefficients of PI I_j .

A set of binary variables $f_{j,k}$'s $(j \neq k)$ is introduced to determine whether M_k can be prevented by PI I_j or not. That is, M_k is forbidden by PI I_j if $f_{j,k} = 1$, otherwise, $f_{j,k} = 0$. Equation (11) is transformed as follows:

$$\sum_{i \in \mathbb{N}_A} l_{j,i} \cdot (M_k(p_i) - M_j(p_i)) \ge -\mathbf{Q} \cdot (1 - f_{j,k}), \quad \forall M_k \in \mathcal{M}_{FBM}^{\star} \text{ and } j \neq k$$
(12)

where Q is an integer constant that must be big enough and $f_{j,k} \in \{0,1\}$.

Then, by introducing a set of binary variables h_j 's, we have the following constraint for PI I_j :

$$f_{j,k} \le h_j, \quad \forall j, \ k \in \mathbb{N}_{\text{FBM}}^{\star} \text{ and } j \ne k$$
 (13)

where $h_j \in \{0, 1\}$. $h_j = 1$ means that PI I_j is selected to prevent FBM M_j , otherwise, PI I_j is redundant and $h_j = 0$.

For FBM M_j , we should ensure that at least one PI is selected to forbid it and the following constraint is obtained:

$$h_j + \sum_{k \in \mathbb{N}_{\text{FBM}}^*, \ j \neq k} f_{k,j} \ge 1.$$
(14)

By grouping Equations (10), (11), (12), (13), and (14), we can formulate the following ILPP, namely the minimal number of control places problem (MCPP).

$$\min\sum_{j\in\mathbb{N}_{\mathrm{FBM}}^{\star}}h_j,$$

subject to

$$\sum_{i \in \mathbb{N}_A} l_{j,i} \cdot (M'(p_i) - M_j(p_i)) \leq -1,$$

$$\forall M' \in \mathcal{M}_{\mathrm{L}}^{\star} \text{ and } \forall M_j \in \mathcal{M}_{\mathrm{FBM}}^{\star}, \tag{15}$$

$$\sum_{i \in \mathbb{N}_A} l_{j,i} \cdot (M_k(p_i) - M_j(p_i)) \ge -\mathbf{Q} \cdot (1 - f_{j,k}),$$

$$\forall M_j, M_k \in \mathcal{M}_{FBM}^{\star} \text{ and } k \neq j,$$
(16)

$$f_{j,k} \le h_j, \forall j, k \in \mathbb{N}_{\text{FBM}}^{\star} \text{ and } j \ne k,$$
 (17)

$$h_j + \sum_{k \in \mathbb{N}_{\text{FBM}}^*, \ j \neq k} f_{k,j} \ge 1,\tag{18}$$

$$l_{j,i} = \{0, 1, 2, \dots\}, \forall i \in \mathbb{N}_A \text{ and } \forall j \in \mathbb{N}_{\text{FBM}}^\star,$$
$$f_{j,k} \in \{0, 1\}, \forall k, j \in \mathbb{N}_{\text{FBM}}^\star \text{ and } k \neq j,$$
$$h_i \in \{0, 1\}, \forall j \in \mathbb{N}_{\text{FBM}}^\star.$$

The above MCPP can obtain a behaviorally optimal and structurally minimal supervisor since all legal markings are reachable and the number of control places is minimized.

3 DEADLOCK PREVENTION POLICY

This section proposes a method based on TGAL approach and the structurally minimal technique to prevent deadlocks. At each iteration, the structurally minimal method is used to design a set of maximally permissive control places and compress the number of them. The main advantage is that it can design an optimal or nearoptimal supervisor with a small number of control places. Finally, the proposed approach is shown as follows, denoted as Algorithm 1.

In Algorithm 1, we first design a GP to be added to a Petri net model with deadlocks. The related net with the GP is denoted as N_B , where B is the number of tokens in the GP. Initially, B = 1, and the related net N_1 is selected to compute its reachability graph. If N_1 has deadlocks, the structurally minimal approach is used to design optimal control places and minimize the number of them. By adding the designed control places, the net N_1 is live. Then, we increase one token in the GP

1318

Algorithm 1 A deadlock prevention policy based on TGAL method and the structurally minimal approach

Require: Petri net model (N, M_0) for an FMS with $N = (P^0 \cup P_A \cup P_R, T, F, W)$. **Ensure:** A controlled Petri net model $(N^{\alpha}, M_0^{\alpha})$.

- 1: Compute input and output transitions of idle places, and obtain sets $T_I = {}^{\bullet}P^0$ and $T_O = P^{0\bullet}$.
- 2: Design a global idle place (GP) with $GP^{\bullet} = T_O$, ${}^{\bullet}GP = T_I$, and $M_0(GP) = B$, then the Petri net model with the GP is denoted as N_B . /* B represents the number of tokens in the GP. */
- 3: Initialize B = 1 and $V_M := \emptyset$. /* V_M is a set of control places designed for (N, M_0) . */
- 4: while $\{B \leq K\}$ do /* K means the sum of initial tokens of all idle places. */
- 5: Compute the reachability graph of N_B .
- 6: **if** $\{N_B \text{ is not live}\}$ **do**
- 7: Compute FBMs and legal markings of N_B , i.e., sets \mathcal{M}_{FBM} and \mathcal{M}_{L} , respectively.
- 8: Calculate the minimal covering sets of FBMs and legal markings, namely $\mathcal{M}_{FBM}^{\star}$ and \mathcal{M}_{L}^{\star} , respectively.
- 9: Formulate an ILPP, i.e., the MCPP developed in Section 2.5.
- 10: Solve the MCPP to find an optimal solution of h_j , and obtain the corresponding values of $l_{j,i}$ and β_j $(i \in \mathbb{N}_A, j \in \mathbb{N}_{\text{FBM}}^*)$.
- 11: foreach $\{h_j = 1\}$ do
- 12: Design a control place p_{s_j} by using the solution of $l_{j,i}$ as the PI I_j 's coefficients.

```
13: V_M := V_M \cup \{p_{s_j}\}.
```

```
14: endforeach
```

```
15: endif
```

- 16: B++;
- 17: endwhile
- 18: Add all monitors in V_M to the net model (N, M_0) , and obtain a controlled net model $(N^{\alpha}, M_0^{\alpha})$.
- 19: Output the controlled Petri net model $(N^{\alpha}, M_0^{\alpha})$.
- 20: End.

(B = 2), and repeat the above steps to make N_2 live. This process is terminated if no new reachable marking is generated by increasing the tokens in the GP. Finally, an optimal or near-optimal controlled net model is obtained, by adding a small number of control places.

Now we discuss the complexity of Algorithm 1. The proposed method needs to generate the reachability graph, in which the number of nodes increases exponentially with respect to the size of the net model. Then, an ILPP, namley the MCPP, is solved at each iteration, which is an NP-hard problem. Therefore, the complexity of this method is in theory exponential although only a partial reachability graph is computed. Compared with the technique in [10], this method reduces in general the computational overhead since only a part of reachability graph is generated at each iteration.

Theorem 1. Let N_B be a related net that is generated in the iteration processes of Algorithm 1, and h^* be an optimal solution of the MCPP that is formulated for N_B . Then, the net N_B is optimally controlled by a set of maximally permissive control places, and the number of them is minimized, if such an optimal solution h^* exists.

Proof. Suppose that h^* is an optimal solution of the MCPP. According to this solution, a set of PIs is obtained, then a set of associated control places is designed. We just need to show the obtained control places are maximally permissive and the number of them is minimized. In the MCPP, each PI should satisfy Equation (15), i.e., it can prevent an FBM in $\mathcal{M}^*_{\text{FBM}}$ while all legal markings are reachable. There does not exist a legal making prevented by PIs. By combining Equations (16) and (18), it ensures that each FBM in $\mathcal{M}^*_{\text{FBM}}$ is prevented by at least one PI. All FBMs in $\mathcal{M}^*_{\text{FBM}}$ are prevented by PIs, namely all FBMs are forbidden. Therefore, the obtained control places are maximally permissive. Equation (17) means that FBMs are prevented only by the selected PIs. Meanwhile, the objective function is applied to find the minimal number of selected PIs, i.e., the optimal solution h^* is equal to the minimal number of PIs required. Thus, the conclusion holds.

A Petri net model of an FMS from [32] shown in Figure 1 is used as an example to illustrate the proposed method. It has 11 places and eight transitions, where $P^0 = \{p_1, p_8\}, P_A = \{p_2 - p_7\}$, and $P_R = \{p_9, p_{10}, p_{11}\}$. There are 18 reachable markings, including 15 legal markings and three FBMs. A GP is designed with •GP = $T_I = \bullet P^0 = \{t_4, t_8\}$ and GP• = $T_O = P^{0\bullet} = \{t_1, t_5\}$. By introducing the GP into the net model, the related net N_B is obtained.

At the first iteration, the GP has only one token (namely B = 1). By generating the reachability graph of N_1 , we find that it has seven legal markings but no deadlocks. Then, one more token is added into the GP (B = 2) and the related net N_2 is obtained. The net N_2 has 13 legal markings and two FBMs. Then, by applying the vector covering approach, we have $\mathcal{M}_{L}^{\star} = \{p_3 + p_4, p_7 + p_8, p_2 + p_4, p_6 + p_8, p_2 + p_3, p_6 + p_7\}$ and $\mathcal{M}_{FBM}^{\star} = \{p_2 + p_7, p_2 + p_6\}$.

For FBM $M_{f_1} = p_2 + p_7$, a PI I_1 can be designed to forbid it. According to Equation (15), I_1 has to satisfy the following six constraints: $l_{1,2} \cdot (0-1) + l_{1,3} \cdot (1-0) + l_{1,4} \cdot (1-0) + l_{1,7} \cdot (0-1) \leq -1, l_{1,2} \cdot (0-1) + l_{1,7} \cdot (1-1) + l_{1,8} \cdot (0-1) \leq -1, l_{1,2} \cdot (1-1) + l_{1,8} \cdot (1-0) + l_{1,7} \cdot (0-1) \leq -1, l_{1,2} \cdot (0-1) + l_{1,6} \cdot (1-0) + l_{1,7} \cdot (0-1) + l_{1,8} \cdot (1-0) \leq -1, l_{1,2} \cdot (1-1) + l_{1,3} \cdot (1-0) + l_{1,7} \cdot (0-1) \leq -1, and l_{1,2} \cdot (0-1) + l_{1,6} \cdot (1-0) + l_{1,7} \cdot (1-1) \leq -1$. By simplifying the above constraints, we have:



Figure 1. A Petri net model from [32]

$$\begin{split} -l_{1,2}+l_{1,3}+l_{1,4}-l_{1,7} &\leq -1, \\ -l_{1,2}-l_{1,8} &\leq -1, \\ l_{1,4}-l_{1,7} &\leq -1, \\ -l_{1,2}+l_{1,6}-l_{1,7}+l_{1,8} &\leq -1, \\ l_{1,3}-l_{1,7} &\leq -1, \\ -l_{1,2}+l_{1,6} &\leq -1. \end{split}$$

Then, a variable $f_{1,2}$ $(f_{1,2} \in \{0,1\})$ is introduced to indicate whether I_1 can prevent $M_{f_2} = p_2 + p_6$ or not. By Equation (16), we have $l_{1,2} \cdot (1-1) + l_{1,6} \cdot (1-0) + l_{1,7} \cdot (0-1) \ge -\mathbf{Q} \cdot (1-f_{1,2})$, where Q is a big enough integer constant. The following constraint is obtained:

$$l_{1,6} - l_{1,7} \ge -\mathbf{Q} \cdot (1 - f_{1,2})$$

Similarly, for $M_{f_2} = p_2 + p_6$, we have the following constraints:

$$\begin{aligned} -l_{2,2} + l_{2,3} + l_{2,4} - l_{2,6} &\leq -1, \\ -l_{2,2} - l_{2,6} + l_{2,7} + l_{2,8} &\leq -1, \\ l_{2,4} - l_{2,6} &\leq -1, \\ -l_{2,2} + l_{2,8} &\leq -1, \end{aligned}$$

1322

C. Li, Y. Li, Y. Chen, N. Wu, Z. Li, P. Ma, H. Kaid

$$l_{2,3} - l_{2,6} \le -1,$$

 $-l_{2,2} + l_{2,7} \le -1,$

and

$$l_{2,7} - l_{2,6} \ge -\mathbf{Q} \cdot (1 - f_{2,1}).$$

A set of variables h_j 's $(h_j \in \{0, 1\}$ and $j \in \{1, 2\})$ is introduced to show whether PI I_j is selected to design a control place or not, i.e., $h_j = 1$ represents that PI I_j is selected, otherwise, I_j is redundant. According to Equation (17), the constraints between $f_{j,k}$ and h_j $(k, j \in \{1, 2\}$ and $k \neq j$) are obtained:

$$f_{1,2} \le h_1,$$

 $f_{2,1} \le h_2.$

On the other hand, since at least one PI is required for each FBM, we have the following constraints:

$$h_1 + f_{2,1} \ge 1,$$

 $h_2 + f_{1,2} \ge 1.$

After given the above constraints, the following objective function is designed to minimize the number of control places obtained:

$$\min = h_1 + h_2.$$

Finally, by combining the above constraints and the objective function, an MCPP is formulated. By solving this MCPP, an optimal solution is obtained, i.e., $h_1 = 1$ and $h_2 = 0$. It means that only I_1 is selected to design a control place. By $f_{1,2} = 1$, I_1 can also prevent M_{f_2} . Thus, the coefficients of I_1 , $l_{1,2} = 1$, $l_{1,6} = 1$, and $l_{1,7} = 1$, are used to design a control place p_{s_1} . We have $\mu_2 + \mu_6 + \mu_7 + \mu_{p_{s_1}} = 1$, namely $M_0(p_{s_1}) = 1$, ${}^{\bullet}p_{s_1} = \{t_2, t_7\}$, and $p_{s_1}^{\bullet} = \{t_1, t_5\}$. By adding only one control place p_{s_1} , the net N_2 is live.

Next, the monitor p_{s_1} is applied to the related net and the number of tokens in the GP is increased by one (B = 3). We find that the net N_3 is live with 15 legal markings. If the tokens in the GP are increased, no new reachable marking is generated. Then, the iteration process is terminated and the GP can be removed. By adding only one control place p_{s_1} to the original net model, the net model is optimally controlled, as shown in Figure 2. Table 2 shows the iteration processes for this example. In this table, the first column represents the tokens in the GP, and the second column gives the control places added to N_B . The third and fourth columns indicate whether N_B has deadlocks or not and the number of reachable markings of N_B , respectively. The fifth column shows the markings in the DZ and other markings in the LZ are given in the sixth column. The last column presents the control places designed.



Figure 2. An optimally controlled system of the net in Figure 1

| B | Include p_{s_i} | N_B Is Live? | Reachable Markings | DZ | LZ | p_{s_i} |
|---|-------------------|----------------|--------------------|----|----|-----------|
| 1 | | Yes | 7 | | 7 | |
| 2 | | No | 15 | 2 | 13 | p_{s_1} |
| 3 | p_{s_1} | Yes | 15 | | 15 | |

Table 2. The iteration steps for the net shown in Figure 1 by Algorithm 1

4 EXPERIMENTAL RESULTS

In this section, we present some examples to show the application of the proposed method. C++ programs are designed to compute sets $\mathcal{M}_{FBM}^{\star}$ and \mathcal{M}_{L}^{\star} , and generate MCPPs. Then, we can use Lingo to solve MCPPs.

Figure 3 shows a Petri net model from [23]. In this net model, we have $P^0 = \{p_1, p_5, p_{14}\}, P_A = \{p_2 - p_4, p_6 - p_{13}, p_{15} - p_{19}\}$, and $P_R = \{p_{20} - p_{26}\}$. It has 26750 reachable markings with 21581 legal markings and 5169 illegal markings. By applying the proposed approach to this net, Table 3 shows the iteration steps.

Table 4 compares the performance of different deadlock control policies. In this table, we can find that only five control places are required by using the method developed in [10]. However, it costs more than 40 hours to obtain an optimal solution. Compared with it, the proposed method only takes 35 seconds to obtain a sub-optimal solution in terms of the number of monitors. Meanwhile, the number of monitors designed by the proposed method is less than the one obtained by the work in [46]. It illustrates that the proposed method performs better in terms of computing time and the number of designed control places.

Finally, the obtained supervisor consists of 12 control places to optimally control the net model. The designed control places are presented in Table 5, where the first



Figure 3. A net model from [23]

| B | Include p_{s_i} | N_B Is Live? | Reachable Markings | DZ | LZ | p_{s_i} |
|----|------------------------------------|----------------|--------------------|----|-------|-----------------------------------|
| 1 | | Yes | 17 | | 17 | |
| 2 | | Yes | 132 | | 132 | |
| 3 | | No | 637 | 5 | 632 | $p_{s_1} p_{s_2} p_{s_3} p_{s_4}$ |
| 4 | $p_{s_1}p_{s_2}\dots p_{s_4}$ | No | 2106 | 2 | 2104 | $p_{s_5} p_{s_6}$ |
| 5 | $p_{s_1}p_{s_2}\dots p_{s_6}$ | No | 5192 | 2 | 5190 | p_{s_7} |
| 6 | $p_{s_1}p_{s_2}\dots p_{s_7}$ | No | 9888 | 10 | 9878 | $p_{s_8} p_{s_9}$ |
| 7 | $p_{s_1}p_{s_2}\dots p_{s_9}$ | No | 15017 | 4 | 15013 | $p_{s_{10}}$ |
| 8 | $p_{s_1} p_{s_2} \dots p_{s_{10}}$ | Yes | 18972 | | 18972 | |
| 9 | $p_{s_1} p_{s_2} \dots p_{s_{10}}$ | Yes | 20980 | | 20980 | |
| 10 | $p_{s_1} p_{s_2} \dots p_{s_{10}}$ | No | 21536 | 11 | 21525 | $p_{s_{11}}p_{s_{12}}$ |
| 11 | $p_{s_1}p_{s_2}\dots p_{s_{12}}$ | Yes | 21581 | | 21581 | |

Table 3. The iteration steps for the net shown in Figure 3 by Algorithm 1

| Parameters | [23] | [29] | [41] | [10] | The Work | Proposed |
|--------------------|-------|-------|-------|-------|----------|----------|
| | | | | | in [46] | Method |
| No. Monitors | 18 | 6 | 19 | 5 | 17 | 12 |
| No. Markings | 6287 | 6287 | 21562 | 21581 | 21581 | 21581 |
| Permissiveness (%) | 29.13 | 29.13 | 99.91 | 100 | 100 | 100 |

Table 4. Performance comparison of control policies for the net shown in Figure 3

| i | I_i | • p_{s_i} | $p_{s_i}^{\bullet}$ | $M_0(p_{s_i})$ |
|----|--|-----------------------------------|------------------------------|----------------|
| 1 | $\mu_{13} + \mu_{15} \le 2$ | t_{10}, t_{16} | t_9, t_{15} | 2 |
| 2 | $\mu_2 + \mu_8 + \mu_{15} \le 2$ | t_4, t_{13} | t_3, t_{11} | 2 |
| 3 | $\mu_{11} + \mu_{17} \le 2$ | t_8, t_{18} | t_7, t_{17} | 2 |
| 4 | $\mu_{12} + \mu_{16} \le 2$ | t_9, t_{17} | t_1, t_8, t_{16} | 2 |
| 5 | $\mu_{12} + \mu_{13} + \mu_{15} + \mu_{16} \le 3$ | t_{10}, t_{17} | t_8, t_{15} | 3 |
| 6 | $\mu_{11} + \mu_{16} \le 3$ | t_8, t_{17} | t_7, t_{16} | 3 |
| 7 | $2\mu_{11} + \mu_{12} + \mu_{13} + 2\mu_{15} + 2\mu_{16} \le 8$ | $t_8, t_{10}, 2t_{17}$ | $2t_7, 2t_{15}$ | 8 |
| 8 | $\mu_2 + \mu_3 + \mu_8 + \mu_9 + \mu_{13} + \mu_{15} + \mu_{16} \le$ | $t_5, t_{10}, t_{13}, t_{17}$ | t_3, t_9, t_{11}, t_{15} | 5 |
| | 5 | | | |
| 9 | $\mu_6 + \mu_7 + \mu_{11} + \mu_{17} + \mu_{18} \le 5$ | t_3, t_8, t_{19} | t_1, t_{17} | 5 |
| 10 | $\mu_2 + \mu_3 + \mu_8 + \mu_9 + \mu_{11} + \mu_{15} + \mu_{16} \le$ | t_5, t_8, t_{13}, t_{17} | t_3, t_7, t_{11}, t_{15} | 6 |
| | 6 | | | |
| 11 | $\mu_6 + \mu_7 + \mu_9 + \mu_{11} + \mu_{13} + \mu_{15} +$ | $t_3, t_5, t_8, t_{10}, t_{18}$ | t_1, t_4, t_9, t_{15} | 9 |
| | $\mu_{16} + \mu_{17} \le 9$ | | | |
| 12 | $2\mu_6 + 2\mu_7 + 2\mu_8 + 2\mu_9 + 2\mu_{11} + 3\mu_{12} +$ | $2t_5, 3t_{10}, 4t_{17}, 2t_{19}$ | $t_1, t_8, 4t_{15}, 2t_{18}$ | , 24 |
| | $3\mu_{13} + 4\mu_{15} + 4\mu_{16} + 2\mu_{18} \le 24$ | | | |

and second columns represent the index of the monitor and the PI I_i , respectively. The pre-transitions, post-transitions, and initial marking of control place p_{s_i} are given in the third to fifth columns, respectively.

Table 5. Control places computed for the net shown in Figure 3 by Algorithm 1

Next, a Petri net model of an FMS from [45] is considered, as shown in Figure 4. For this net model, we have $P^0 = \{p_{31}, p_{32}\}$ as idle places, $P_A = \{p_1 - p_{11}\}$ as operation places, and $P_R = \{p_{21}, p_{22}, p_{23}\}$ as resource places. It has 54 869 reachable markings with 51 506 markings in the LZ and 3 363 markings in the DZ. By applying the proposed method, the iteration processes are shown in Table 6.

Table 7 presents the performance comparison of different methods for this net model. This method can design an optimal supervisor with only five control places by taking 12 seconds. Therefore, the proposed approach obtains an optimal supervisor with a small number of monitors within less time, since the method in [10] costs more than four hours. Table 8 shows the details of designed control places.

Finally, another Petri net model of an FMS from [45] is selected to demonstrate the proposed method, as depicted in Figure 5. For this net model, we have the following place set partition: $P^0 = \{p_{11}, p_{17}, p_{18}\}, P_A = \{p_1 - p_{10}, p_{12} - p_{16}\}$, and $P_R = \{p_{22} - p_{27}\}$. It has 68 531 reachable markings of which 66 400 and 2 131 are legal and illegal markings, respectively. By using the proposed method, deadlocks are prevented in an iterative way. Table 9 shows the iteration processes of this net model.

Table 10 compares the performance of some approaches for this net model. Obviously, it indicates that the proposed method can design an optimal supervisor with a small number of control places, i.e., only eight control places are required. Similarly, by using this method, the optimal supervisor is obtained within nine



Figure 4. A Petri net model of an FMS from [45]

| B | Include p_{s_i} | N_B Is Live? | Reachable Markings | DZ | LZ | p_{s_i} |
|----------------|-------------------------------|----------------|--------------------|----|-------|-----------|
| 1 | | Yes | 12 | | 12 | |
| 2 | | No | 67 | 1 | 66 | p_{s_1} |
| 3 | p_{s_1} | Yes | 252 | | 252 | |
| 4 | p_{s_1} | Yes | 767 | | 767 | |
| 5 | p_{s_1} | Yes | 1963 | | 1963 | |
| 6 | p_{s_1} | No | 4366 | 4 | 4362 | p_{s_2} |
| $\overline{7}$ | $p_{s_1} p_{s_2}$ | No | 8574 | 4 | 8570 | p_{s_3} |
| 8 | $p_{s_1} p_{s_2} p_{s_3}$ | Yes | 14986 | | 14986 | |
| 9 | $p_{s_1} p_{s_2} p_{s_3}$ | No | 23404 | 2 | 23402 | p_{s_4} |
| 10 | $p_{s_1}p_{s_2}\dots p_{s_4}$ | No | 32740 | 23 | 32717 | p_{s_5} |
| 11 | $p_{s_1}p_{s_2}\dots p_{s_5}$ | Yes | 41162 | | 41162 | |
| 12 | $p_{s_1}p_{s_2}\dots p_{s_5}$ | Yes | 47203 | | 47203 | |
| 13 | $p_{s_1}p_{s_2}\dots p_{s_5}$ | Yes | 50363 | | 50363 | |
| 14 | $p_{s_1}p_{s_2}\dots p_{s_5}$ | Yes | 51380 | | 51380 | |
| 15 | $p_{s_1}p_{s_2}\dots p_{s_5}$ | Yes | 51506 | | 51506 | |

Table 6. The iteration steps for the net shown in Figure 4 by Algorithm 1 $\,$

Synthesis of Petri Net Supervisors Based on TGAL Approach

| Parameters | [47] | TGAL | TGALW | [10] | The Work | Proposed |
|-----------------------|-------|---------|---------|-------|----------|----------|
| | | in [44] | in [45] | | in [46] | Method |
| No. Monitors | 7 | 5 | 8 | 2 | 10 | 5 |
| No. Markings | 51386 | 48752 | 51548 | 51506 | 51506 | 51506 |
| Permissiveness $(\%)$ | 99.76 | 94.65 | 99.83 | 100 | 100 | 100 |

Table 7. Performance comparison of control policies for the net shown in Figure 4

| i | I_i | $\bullet p_{s_i}$ | $p_{s_i}^{\bullet}$ | $M_0(p_{s_i})$ |
|----------|---|-------------------------------|-------------------------------|----------------|
| 1 | $\mu_1 \leq 1$ | t_2 | t_1 | 1 |
| 2 | $2\mu_2 + 2\mu_3 + \mu_7 + 2\mu_9 \le 10$ | $2t_4, t_9, 2t_{11}$ | $2t_2, t_8, t_{10}$ | 10 |
| 3 | $3\mu_2 + 3\mu_3 + 3\mu_7 + 4\mu_9 \le 21$ | $3t_4, 3t_9, 4t_{11}$ | $3t_2, 3t_8, t_{10}$ | 21 |
| 4 | $2\mu_2 + 2\mu_3 + \mu_4 + \mu_6 + 2\mu_9 \le 13$ | $t_4, t_5, t_8, 2t_{11}$ | $2t_2, t_7, 2t_{10}$ | 13 |
| 5 | $15\mu_2 + 15\mu_3 + 16\mu_4 + 16\mu_6 +$ | $16t_5, t_8, 15t_9, 21t_{11}$ | $15t_2, 5t_4, 16t_7, 6t_{10}$ | 159 |
| | $15\mu_7 + 21\mu_9 \le 159$ | | | |

Table 8. Control places computed for the net shown in Figure 4 by Algorithm 1

seconds, but the method in [10] takes more than three hours. The eight control places are given in Table 11.

5 CONCLUSIONS

This paper develops a deadlock prevention policy to design an optimal or nearoptimal liveness-enforcing Petri net supervisor with a small number of monitors for FMSs. It prevents deadlocks in an iterative way by introducing a temporary GP. At each iteration, an ILPP (namely the MCPP) is formulated to compute a set of control places. These control places have the following characteristics:

| B | Include p_{s_i} | N_B Is Live? | Reachable Markings | DZ | LZ | p_{s_i} |
|----|---------------------------------|----------------|--------------------|----|-------|-------------------|
| 1 | | Yes | 15 | | 15 | |
| 2 | | Yes | 117 | | 117 | |
| 3 | | Yes | 618 | | 618 | |
| 4 | | No | 2398 | 1 | 2397 | p_{s_1} |
| 5 | p_{s_1} | No | 7138 | 3 | 7135 | p_{s_2} |
| 6 | $p_{s_1} p_{s_2}$ | No | 16645 | 10 | 16635 | $p_{s_3}p_{s_4}$ |
| 7 | $p_{s_1} p_{s_2} \dots p_{s_4}$ | No | 30890 | 11 | 30879 | $p_{s_5} p_{s_6}$ |
| 8 | $p_{s_1} p_{s_2} \dots p_{s_6}$ | No | 46471 | 4 | 46467 | p_{s_7} |
| 9 | $p_{s_1}p_{s_2}\dots p_{s_7}$ | No | 58480 | 6 | 58474 | p_{s_8} |
| 10 | $p_{s_1} p_{s_2} \dots p_{s_8}$ | Yes | 64485 | | 64485 | |
| 11 | $p_{s_1} p_{s_2} \dots p_{s_8}$ | Yes | 66181 | | 66181 | |
| 12 | $p_{s_1}p_{s_2}\dots p_{s_8}$ | Yes | 66400 | | 66400 | |

Table 9. The iteration steps for the net shown in Figure 5 by Algorithm 1



Figure 5. A net model of an FMS from [45]

| Parameters | TGAL | TGALW | [10] | The Work | Proposed |
|-----------------------|---------|---------|-------|-----------|----------|
| | in [44] | in [45] | | in $[46]$ | Method |
| No. Monitors | 11 | 17 | 3 | 17 | 8 |
| No. Markings | 62682 | 65888 | 66400 | 66400 | 66400 |
| Permissiveness $(\%)$ | 94.4 | 99.23 | 100 | 100 | 100 |

Table 10. Performance comparison of control policies for the net shown in Figure 5

| i | I_i | $\bullet p_{s_i}$ | $p_{s_i}^{\bullet}$ | $M_0(p_{s_i})$ |
|---|--|-----------------------------|----------------------------|----------------|
| 1 | $\mu_1 + \mu_{14} \le 3$ | t_2, t_6, t_{16} | t_1, t_{15} | 3 |
| 2 | $\mu_2 + \mu_6 + \mu_{13} \le 4$ | t_3, t_7, t_{15} | t_2, t_6, t_{14} | 4 |
| 3 | $\mu_2 + \mu_3 + \mu_6 + \mu_7 + \mu_{12} + 2\mu_{13} \le 7$ | $t_4, t_8, 2t_{15}$ | t_2, t_6, t_{13}, t_{14} | 7 |
| 4 | $\mu_3 + \mu_7 + \mu_{12} \le 5$ | t_4, t_8, t_{14} | t_3, t_7, t_{13} | 5 |
| 5 | $\mu_2 + \mu_3 + \mu_6 + \mu_7 + 2\mu_{12} \le 9$ | $t_4, t_8, 2t_{14}$ | $t_2, t_6, 2t_{13}$ | 9 |
| 6 | $\mu_1 + \mu_2 + \mu_6 + \mu_{13} + \mu_{14} \le 6$ | t_3, t_7, t_{16} | t_1, t_{14} | 6 |
| 7 | $\mu_1 + \mu_2 + \mu_3 + \mu_6 + \mu_7 + \mu_{12} + 2\mu_{13} + \mu_{12} + 2\mu_{13} + \mu_{13} + \mu_{13$ | t_4, t_8, t_{15}, t_{16} | t_1, t_{13}, t_{14} | 9 |
| | $\mu_{14} \le 9$ | | | |
| 8 | $\mu_1 + \mu_2 + \mu_3 + \mu_6 + \mu_7 + 2\mu_{12} + \mu_{14} \le 11$ | $t_4, t_8, 2t_{14}, t_{16}$ | $t_1, 2t_{13}, t_{15}$ | 11 |

Table 11. Control places computed for the net shown in Figure 5 by Algorithm 1

- 1. they can prevent all illegal markings while all legal markings are reachable, i.e., they are maximally permissive; and
- 2. the number of control places is minimized and no redundant control place survives.

Compared with the previous work in [44], [45], and [46], the proposed method can design an optimal or near-optimal supervisor with fewer control places. Meanwhile, compared with the method in [10], it is more suitable for large-scale net models, since the number of constraints and variables in the MCPP is less at each iteration, and a supervisor with a small number of monitors is obtained in a reasonable time.

However, the proposed method also has some drawbacks. First, the design of supervisors needs to solve MCPPs, which is NP-hard. Second, compared with the work in [10], this method cannot ensure that an optimal supervisor with the minimal number of control places is designed. The reason is that some control places can be further compressed, but they are obtained in different iteration steps. Our future work will consider to minimize the number of control places. A possible way is that we can formulate a new ILPP after all legal and illegal markings are obtained.

Acknowledgements

This work was supported in part by the National Key R & D Project of China under Grant 2018YFB1700104, in part by the Science and Technology Development Fund, MSAR under Grant 0064/2021/A2, in part by the Zhuhai Industry-University-Research Project with Hongkong and Macao under Grant ZH22017002210014PWC, and in part by the Guangzhou Innovation and Entrepreneurship Leading Team Project Funding under Grant 202009020008.

REFERENCES

- COFFMAN, E. G.—ELPHICK, M. J.—SHOSHANI, A.: Systems Deadlocks. ACM Computing Surveys, Vol. 3, 1971, No. 2, pp. 67–78, doi: 10.1145/356586.356588.
- [2] ZHOU, M. C.—DICESARE, F.: Adaptive Design of Petri Net Controllers for Error Recovery in Automated Manufacturing Systems. IEEE Transactions on Systems, Man, and Cybernetics, Vol. 19, 1989, No. 5, pp. 963–973, doi: 10.1109/21.44011.
- [3] WYSK, R. A.—YANG, N. S.—JOSHI, S.: Resolution of Deadlocks in Flexible Manufacturing Systems: Avoidance and Recovery Approaches. Journal of Manufacturing Systems, Vol. 13, 1994, No. 2, pp. 128–138, doi: 10.1016/0278-6125(94)90028-0.
- [4] EZPELETA, J.—VALK, R.: A Polynomial Deadlock Avoidance Method for a Class of Nonsequential Resource Allocation Systems. IEEE Transactions on Systems, Man, and Cybernetics – Part A: Systems and Humans, Vol. 36, 2006, No. 6, pp. 1234–1243, doi: 10.1109/TSMCA.2006.878963.

- [5] WU, N. Q.—ZHOU, M. C.: Modeling and Deadlock Avoidance of Automated Manufacturing Systems with Multiple Automated Guided Vehicles. IEEE Transactions on Systems, Man, and Cybernetics – Part B: Cybernetics, Vol. 35, 2005, No. 6, pp. 1193–1202, doi: 10.1109/TSMCB.2005.850141.
- [6] PARK, J.—REVELIOTIS, S.A.: Deadlock Avoidance in Sequential Resource Allocation Systems with Multiple Resource Acquisitions and Flexible Routings. IEEE Transactions on Automatic Control, Vol. 46, 2001, No. 10, pp. 1572–1583, doi: 10.1109/9.956052.
- [7] CHEN, Y. F.—LI, Z. W.—ZHOU, M. C.: Behaviorally Optimal and Structurally Simple Liveness-Enforcing Supervisors of Flexible Manufacturing Systems. IEEE Transactions on Systems, Man, and Cybernetics – Part A: Systems and Humans, Vol. 42, 2012, No. 3, pp. 615–629, doi: 10.1109/TSMCA.2011.2169956.
- [8] Hu, H. S.—ZHOU, M. C.: A Petri Net-Based Discrete-Event Control of Automated Manufacturing Systems with Assembly Operations. IEEE Transactions on Control Systems Technology, Vol. 23, 2015, No. 2, pp. 513–524, doi: 10.1109/TCST.2014.2342664.
- [9] BARKAOUI, K.—ABDALLAH, I. B.: A Deadlock Prevention Method for a Class of FMS. Proceedings of the IEEE International Conference on Systems, Man, and Cybernetics: Intelligent Systems for the 21st Century. IEEE, Vol. 5, 2015, pp. 4119–4124, doi: 10.1109/ICSMC.1995.538436.
- [10] CHEN, Y.F.—LI, Z. W.: Design of a Maximally Permissive Liveness-Enforcing Supervisor with a Compressed Supervisory Structure for Flexible Manufacturing Systems. Automatica, Vol. 47, 2011, No. 5, pp. 1028–1034, doi: 10.1016/j.automatica.2011.01.070.
- [11] CHEN, Y. F.—LI, Z. W.—AL-AHMARI, A.: Nonpure Petri Net Supervisors for Optimal Deadlock Control of Flexible Manufacturing Systems. IEEE Transactions on Systems, Man, and Cybernetics – Part A: Systems, Vol. 43, 2013, No. 2, pp. 252– 265, doi: 10.1109/TSMCA.2012.2202108.
- [12] PIRODDI, L.—CORDONE, R.—FUMAGALLI, I.: Selective Siphon Control for Deadlock Prevention in Petri Nets. IEEE Transactions on Systems, Man, and Cybernetics – Part A: Systems and Humans, Vol. 38, 2008, No. 6, pp. 1337–1348, doi: 10.1109/TSMCA.2008.2003535.
- [13] MA, Z. Y.—LI, Z. W.—GIUA, A.: Characterization of Admissible Marking Sets in Petri Nets with Conflicts and Synchronizations. IEEE Transactions on Automatic Control, Vol. 62, 2017, No. 3, pp. 1329–1341, doi: 10.1109/TAC.2016.2585647.
- [14] ZHU, G. H.—LI, Z. W.—WU, N. Q.: Model-Based Fault Identification of Discrete Event Systems Using Partially Observed Petri Nets. Automatica, Vol. 96, 2018, pp. 201–212, doi: 10.1016/j.automatica.2018.06.039.
- [15] ZHU, G. H.—LI, Z. W.—WU, N. Q.—AL-AHMARI, A.: Fault Identification of Discrete Event Systems Modeled by Petri Nets with Unobservable Transitions. IEEE Transactions on Systems, Man, and Cybernetics: Systems, Vol. 49, 2019, No. 2, pp. 333–345, doi: 10.1109/TSMC.2017.2762823.
- [16] REVELIOTIS, S. A.: Real-Time Management of Resource Allocation Systems: A Discrete Event Systems Approach. Springer, New York, International Series in Opera-

tions Research and Management Science, Vol. 79, 2005, doi: 10.1007/b104057.

- [17] MURATA, T.: Petri Nets: Properties, Analysis and Applications. Proceedings of the IEEE, Vol. 77, 1989, No. 4, pp. 541–580, doi: 10.1109/5.24143.
- [18] LIU, G. J.: Complexity of the Deadlock Problem for Petri Nets Modeling Resource Allocation Systems. Information Sciences, Vol. 363, 2016, pp. 190–197, doi: 10.1016/j.ins.2015.11.025.
- [19] CHEN, Y. F.—LI, Z. W.—BARKAOUI, K.—WU, N. Q.—ZHOU, M. C.: Compact Supervisory Control of Discrete Event Systems by Petri Nets with Data Inhibitor Arcs. IEEE Transactions on Systems, Man, and Cybernetics: Systems, Vol. 47, 2017, No. 2, pp. 364–379, doi: 10.1109/TSMC.2016.2521833.
- [20] TONG, Y.—LI, Z. W.—GIUA, A.: On the Equivalence of Observation Structures for Petri Net Generators. IEEE Transactions on Automatic Control, Vol. 61, 2016, No. 9, pp. 2448–2462, doi: 10.1109/TAC.2015.2496500.
- [21] LI, Z. W.—WU, N. Q.—ZHOU, M. C.: Deadlock Control of Automated Manufacturing Systems Based on Petri Nets – A Literature Review. IEEE Transactions on Systems, Man, and Cybernetics, Part C (Applications and Reviews), Vol. 42, 2012, No. 4, pp. 437–462, doi: 10.1109/TSMCC.2011.2160626.
- [22] ZHANG, H. M.—FENG, L.—WU, N. Q.—LI, Z. W.: Integration of Learning-Based Testing and Supervisory Control for Requirements Conformance of Black-Box Reactive Systems. IEEE Transactions on Automation Science and Engineering, Vol. 15, 2018, No. 1, pp. 2–15, doi: 10.1109/TASE.2017.2693995.
- [23] EZPELETA, J.—COLOM, J. M.—MARTINEZ, J.: A Petri Net Based Deadlock Prevention Policy for Flexible Manufacturing Systems. IEEE Transactions on Robotics and Automation, Vol. 11, 1995, No. 2, pp. 173–184, doi: 10.1109/70.370500.
- [24] LI, Z. W.—ZHOU, M. C.: Two-Stage Method for Synthesizing Liveness-Enforcing Supervisors for Flexible Manufacturing Systems Using Petri Nets. IEEE Transactions on Industrial Informatics, Vol. 2, 2006, No. 4, pp. 313–325, doi: 10.1109/TII.2006.885185.
- [25] LI, Z. W.—HU, H. S.—WANG, A. R.: Design of Liveness-Enforcing Supervisors for Flexible Manufacturing Systems Using Petri Nets. IEEE Transactions on Systems, Man, and Cybernetics, Part C (Applications and Reviews), Vol. 37, 2007, No. 4, pp. 517–526, doi: 10.1109/TSMCC.2007.897333.
- [26] CHEN, Y. F.—LI, Z. W.—ZHOU, M. C.: Optimal Supervisory Control of Flexible Manufacturing Systems by Petri Nets: A Set Classification Approach. IEEE Transactions on Automation Science and Engineering, Vol. 11, 2014, No. 2, pp. 549–563, doi: 10.1109/TASE.2013.2241762.
- [27] CHEN, Y. F.—LI, Z. W.—AL-AHMARI, A.—WU, N. Q.—QU, T.: Deadlock Recovery for Flexible Manufacturing Systems Modeled with Petri Nets. Information Sciences, Vol. 381, 2017, pp. 290–303, doi: 10.1016/j.ins.2016.11.011.
- [28] HU, H. S.—LIU, Y.—ZHOU, M. C.: Maximally Permissive Distributed Control of Large Scale Automated Manufacturing Systems Modeled with Petri Nets. IEEE Transactions on Control Systems Technology, Vol. 23, 2015, No. 5, pp. 2026–2034, doi: 10.1109/TCST.2015.2391014.
- [29] LI, Z. W.-ZHOU, M. C.: Elementary Siphons of Petri Nets and Their Application

to Deadlock Prevention in Flexible Manufacturing Systems. IEEE Transactions on Systems, Man, and Cybernetics – Part A: Systems and Humans, Vol. 34, 2004, No. 1, pp. 38–51, doi: 10.1109/TSMCA.2003.820576.

- [30] WANG, S. G.—WANG, C. Y.—ZHOU, M. C.: Controllability Conditions of Resultant Siphons in a Class of Petri Nets. IEEE Transactions on Systems, Man, and Cybernetics – Part A: Systems and Humans, Vol. 42, 2012, No. 5, pp. 1206–1215, doi: 10.1109/TSMCA.2011.2170419.
- [31] LIU, G. J.—JIANG, C. J.—ZHOU, M. C.: Two Simple Deadlock Prevention Policies for S³PR Based on Key-Resource/Operation-Place Pairs. IEEE Transactions on Automation Science and Engineering, Vol. 7, 2010, No. 4, pp. 945–957, doi: 10.1109/TASE.2010.2050059.
- [32] HUANG, Y. S.—CHUNG, T. H.—SU, P. J.: Synthesis of Deadlock Prevention Policy Using Petri Nets Reachability Graph Technique. Asian Journal of Control, Vol. 12, 2010, No. 3, pp. 336–346, doi: 10.1002/asjc.188.
- [33] GHAFFARI, A.—REZG, N.—XIE, X.: Design of a Live and Maximally Permissive Petri Net Controller Using the Theory of Regions. IEEE Transactions on Robotics and Automation, Vol. 19, 2003, No. 1, pp. 137–141, doi: 10.1109/TRA.2002.807555.
- [34] UZAM, M.—LI, Z. W.—GELEN, G.—ZAKARIYYA, R. S.: A Divide-and-Conquer-Method for the Synthesis of Liveness Enforcing Supervisors for Flexible Manufacturing Systems. Journal of Intelligent Manufacturing, Vol. 27, 2016, No. 5, pp. 1111–1129, doi: 10.1007/s10845-014-0938-z.
- [35] LI, Z. W.—ZHOU, M. C.—WU, N. Q.: A Survey and Comparison of Petri Net-Based Deadlock Prevention Policies for Flexible Manufacturing Systems. IEEE Transactions on Systems, Man, and Cybernetics, Part C (Applications and Reviews), Vol. 38, 2008, No. 2, pp. 173–188, doi: 10.1109/TSMCC.2007.913920.
- [36] LI, Z. W.—ZHOU, M. C.: Clarifications on the Definitions of Elementary Siphons of Petri Nets. IEEE Transactions on Systems, Man, and Cybernetics – Part A: Systems and Humans, Vol. 36, 2006, No. 6, pp. 1227–1229, doi: 10.1109/TSMCA.2006.878966.
- [37] LI, Z. W.—ZHAO, M.: On Controllability of Dependent Siphons for Deadlock Prevention in Generalized Petri Nets. IEEE Transactions on Systems, Man, and Cybernetics – Part A: Systems and Humans, Vol. 38, 2008, No. 2, pp. 369–384, doi: 10.1109/TSMCA.2007.914741.
- [38] CHEN, Y. F.—LI, Z. W.—KHALGUI, M.—MOSBAHI, O.: Design of a Maximally Permissive Liveness-Enforcing Petri Net Supervisor for Flexible Manufacturing Systems. IEEE Transactions on Automation Science and Engineering, Vol. 8, 2011, No. 2, pp. 374–393, doi: 10.1109/TASE.2010.2060332.
- [39] LI, C. Z.—CHEN, Y. F.—ZHONG, Z. F.—UZAM, M.—LI, Z. W.—WU, N. Q.— ZHANG, M. J.: A Think-Globally-Act-Locally-Based Method of Maximally Permissive Liveness-Enforcing Supervisors for Flexible Manufacturing Systems. Journal of Control Engineering and Applied Informatics, Vol. 23, 2021, No. 4, pp. 46–56.
- [40] UZAM, M.—ZAKARIYYA, R.—LI, Z. W.—GELEN, G.: The Computation of Liveness Enforcing Supervisors from Submodels of a Petri Net Model of FMSs. Proceedings of the 2013 IEEE International Conference of IEEE Region 10 (TENCON 2013), 2013, pp. 1–4, doi: 10.1109/TENCON.2013.6718803.

- [41] UZAM, M.—ZHOU, M. C.: An Improved Iterative Synthesis Method for Liveness Enforcing Supervisors of Flexible Manufacturing Systems. International Journal of Production Research, Vol. 44, 2006, No. 10, pp. 1987–2030, doi: 10.1080/00207540500431321.
- [42] UZAM, M.—ZHOU, M. C.: An Iterative Synthesis Approach to Petri Net Based Deadlock Prevention Policy for Flexible Manufacturing Systems. IEEE Transactions on Systems, Man, and Cybernetics – Part A: Systems and Humans, Vol. 37, 2007, No. 3, pp. 362–371, doi: 10.1109/TSMCA.2007.893484.
- [43] YAMALIDOU, K.—MOODY, J.—LEMMON, M.—ANTSAKIS, P.: Feedback Control of Petri Nets Based on Place Invariants. Automatica, Vol. 32, 1996, No. 1, pp. 15–28, doi: 10.1016/0005-1098(95)00103-4.
- [44] UZAM, M.—LI, Z. W.—ABUBAKAR, U. S.: Think Globally Act Locally Approach for the Synthesis of a Liveness-Enforcing Supervisor of FMSs Based on Petri Nets. International Journal of Production Research, Vol. 54, 2016, No. 15, pp. 4634–4657, doi: 10.1080/00207543.2015.1098785.
- [45] UZAM, M.—GELEN, G.—SALEH, T. L.: Think-Globally-Act-Locally Approach with Weighted Arcs to the Synthesis of a Liveness-Enforcing Supervisor for Generalized Petri Nets Modeling FMSs. Information Sciences, Vol. 363, 2016, pp. 235–260, doi: 10.1016/j.ins.2015.09.010.
- [46] LI, C. Z.—CHEN, Y. F.—LI, Z. W.—BARKAOUI, K.: Synthesis of Liveness-Enforcing Petri Net Supervisors Based on Think-Globally-Act-Locally Approach and Vector Covering for Flexible Manufacturing Systems. IEEE Access, Vol. 5, 2017, pp. 16349–16358, doi: 10.1109/ACCESS.2017.2720630.
- [47] TRICAS, F.—GARCÍA-VALLÉS, F.—COLOM, J. M.—EZPELETA, J.: An Iterative Method for Deadlock Prevention in FMS. In: Boel, R., Stremersch, G. (Eds.): Discrete Event Systems. Springer, Boston, MA, The Springer International Series in Engineering and Computer Science, Vol. 569, 2000, pp. 139–148, doi: 10.1007/978-1-4615-4493-7_14.
- [48] TRICAS, F.—GARCIA-VALLÉS, F.—COLOM, J. M.—EZPELETA, J.: A Petri Net Structure-Based Deadlock Prevention Solution for Sequential Resource Allocation Systems. Proceedings of the 2005 IEEE International Conference on Robotics and Automation, 2005, pp. 271–277, doi: 10.1109/ROBOT.2005.1570131.
- [49] LI, Z. W.—LIU, G. Y.—HANISCH, H. M.—ZHOU, M. C.: Deadlock Prevention Based on Structure Reuse of Petri Net Supervisors for Flexible Manufacturing Systems. IEEE Transactions on Systems, Man, and Cybernetics – Part A: Systems and Humans, Vol. 42, 2012, No. 1, pp. 178–191, doi: 10.1109/TSMCA.2011.2147308.



Chengzong LI received the B.Sc. degree from the North China Electric Power University, Baoding, China, in 2015, the M.Sc. degree from the Xidian University, Xi'an, China, in 2018. He is currently pursuing his Ph.D. degree in the Institute of Systems Engineering at Macau University of Science and Technology. His interests include Petri nets and supervisory control of discrete event systems.



Yongyao LI received his B.Sc. degree from the University of Electronic Science and Technology of China, Chengdu, China, in 2015, his M.Sc. degree from the Shijiazhuang Railway Institute, Shijiazhuang, China, in 2002. He is currently pursuing his Ph.D. degree in the Institute of Systems Engineering at Macau University of Science and Technology. His interests include intelligent system theory and application.



Yufeng CHEN received his B.Sc. and his Ph.D. degrees from Xidian University, Xi'an, China, in 2006 and 2011, respectively. He is the author or coauthor of over 20 publications. He is a coauthor with Zhiwu Li of the book Optimal Supervisory Control of Automated Manufacturing Systems (CRC Press, Taylor & Francis Group, 2013). He is with the Institute of Systems Engineering, Macau University of Science and Technology, Macau, China. His research interests include Petri net theory and applications, supervisory control of discrete event systems, and scheduling flexible manufacturing systems.



Naiqi WU received his B.Sc. degree in electrical engineering from the Anhui University of Technology, Huainan, China, in 1982, his M.Sc. and Ph.D. degrees in systems engineering, both from the Xi'an Jiaotong University, Xi'an, China in 1985 and 1988, respectively. From 1988 to 1995, he was with the Shenyang Institute of Automation, Chinese Academy of Sciences, Shenyang, China, and from 1995 to 1998, with the Shantou University, Shantou, China. He moved to the Guangdong University of Technology, Guangzhou, China in 1998. He joined the Macau University of Science and Technology, Taipa, Macau in 2013. He

is currently Professor at the Institute of Systems Engineering, Macau University of Science and Technology, Taipa, Macau. His research interests include production planning and scheduling, manufacturing system modeling and control, discrete event systems, Petri net theory and applications, intelligent transportation systems, and energy systems. He is the author or coauthor of one book, five book chapters, and 140+ peer-reviewed journal papers. He was Associate Editor of the IEEE Transactions on Systems, Man, and Cybernetics, Part C, IEEE Transactions on Automation Science and Engineering, IEEE Transactions on Systems, Man, and Cybernetics: Systems, and Editor-in-Chief of Industrial Engineering Journal, and is Associate Editor of Information Sciences and IEEE/CAA Journal of Automatica Sinica. He is Fellow of IEEE.



Zhiwu LI received his B.Sc. degree in mechanical engineering, the M.Sc. degree in automatic control, and his Ph.D. degree in manufacturing engineering from the Xidian University, Xi'an, China, in 1989, 1992, and 1995, respectively. He joined the Xidian University in 1992. He is also currently with the Institute of System Engineering, Macau University of Science and Technology, Macau, China. His research interests include discrete event systems, data mining, and Petri nets. He was a recipient of an Alexander von Humboldt Research Grant, Alexander von Humboldt Foundation, Germany. He is listed in Marquis

Who's Who in the World, 27th Edition, 2010. He serves as a Frequent Reviewer for over 90 international journals. He is the Founding Chair of Xi'an Chapter of IEEE Systems, Man and Cybernetics Society. He chairs the Discrete-Event Systems Technical Committee of the IEEE Systems, Man and Cybernetics Society and IFAC Technical Committee on Discrete-Event and Hybrid Systems, from 2011 to 2014.



Pengyu MA received his B.Sc. degree in electrical engineering and automation from the South China University of Technology, Guangzhou, China, in 2007, his M.Sc. degree in electrical engineering from the Harbin Institute of Technology, Harbin, China, in 2009. He is the Chief Engineer of Hitachi Building Technology (Guangzhou) Co., Ltd. His research interests include the design of intelligent building automatic control systems. He is the new star of Guangzhou Pearl River Technology in 2019.



Husam KAID is Researcher in the Industrial Engineering Department, College of Engineering, King Saud University, Saudi Arabia. He received his M.Sc. and Ph.D. in industrial engineering from the King Saud University, Saudi Arabia, in 2015 and 2021, respectively. He received his B.Sc. in industrial engineering from the University of Taiz, Taiz, Yemen, in 2010. His research areas and specialties are design and analysis of manufacturing systems, deadlock control in manufacturing systems, supply chain, simulation, operations research, optimization techniques, and bibliometric network analysis.