

## Characterization of Metal-Insulator-Semiconductor Capacitors with Insulating Nitride Films Grown on 4H-SiC

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Nitrided layers were grown on a 4H-SiC(0001) by plasma nitridation method using  $\text{NH}_3$ . Nitridation was enhanced with increasing RF power and with decreasing growth pressure. However, the exact Capacitance-Voltage properties of the nitride layer/SiC interface could not be determined because of the leakage current. The  $\text{SiO}_2$  film was deposited on the nitrided layer by thermal chemical vapor deposition method using Tetraethoxysilane (TEOS) omit obtain an insulating film with sufficient thickness and an exact interface property. The interface state density  $D_{it}$  was evaluated from C-V characteristics by the Terman method. It was indicated that  $D_{it}$  near the mid gap of the TEOS oxide/nitride layer structure was higher than those of the TEOS- $\text{SiO}_2$  films and thermal oxide film. The  $D_{it}$  of the oxide/nitride layer successfully decreased by post  $\text{NH}_3$  annealing.

KEYWORDS: SiC, MIS, nitridation, SiN, interface state density

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## 1. Introduction

The excellent electrical and physical properties of silicon carbide (SiC) and its ability to form an insulating silicon dioxide (SiO<sub>2</sub>) layer by thermal oxidation make it a very attractive material for high temperature and high power metal-oxide-semiconductor (MOS) devices. In the case of Si, MOS technology is based on the thermally grown SiO<sub>2</sub>, as a gate dielectric material, due to its high reliability, very low interface defect density and high resistivity. However, both 6H-SiC and 4H-SiC MOS field-effect transistors (MOSFETs) suffer from low electron mobility in n-channel inversion layers.<sup>1-4)</sup>

Several research groups pointed out that severe degradation of the surface electron mobility in 6H-SiC and 4H-SiC MOS devices is due to the high density of interface electron traps close to the conduction band edge.<sup>1-4)</sup>

To realize SiC based MOS devices, it is important to fabricate the high quality gate oxide and SiC/SiO<sub>2</sub> interface with minimum defect density. At present, the defect state density ( $D_{it}$ ) at the SiO<sub>2</sub>/4H-SiC interface has been reduced to 10<sup>11</sup>-10<sup>12</sup> eV<sup>-1</sup>cm<sup>-2</sup>, which is not enough for device applications.<sup>5)</sup> As a solution, post-annealing of thermal oxide in N<sub>2</sub>O or NO has been reported to be effective in improving the SiO<sub>2</sub>/4H-SiC interface.<sup>6)</sup> Nitrogen atoms can form strong Si≡N bonds which passivate interface traps originating in dangling and strained bonds.

On the other hand, the silicon nitride (Si<sub>3</sub>N<sub>4</sub>) films have been studied as a potential material in replacing silicon-dioxide (SiO<sub>2</sub>).<sup>7-9)</sup> Their main advantages are low density of surface state and high dielectric permittivity.

Previously, we reported that the nitride layer could be grown on the SiC surface by a plasma nitridation method using mixed gasses of NH<sub>3</sub> and N<sub>2</sub>.<sup>10)</sup> However, the thickness of the sample was about several nm. In this work, a SiO<sub>2</sub> layer on the nitride layer formed by thermal CVD method using tetraethyloxysilane (TEOS) in order to obtain enough thickness for interface characterization by C-V method.

## 2. Experimental

The nitride layer was formed on the silicon-faced n-type 4H-SiC(0001) wafers (Cree Research Inc., Research grade,  $\sim 0.1\Omega\text{cm}$ ). The carrier concentration was estimated to be

$2-5 \times 10^{17} \text{cm}^{-3}$  from  $C^{-2}-V$  plot of Schottky contact. As the pretreatment, the substrates were thermally oxidized for 2h at  $1000^\circ\text{C}$  in  $\text{O}_2+\text{H}_2\text{O}$  atmosphere and then were etching in a 5% hydrofluoric acid to obtain a damage free surface. After the pretreatment, the substrate was immediately set in the nitridation furnace.

The nitridation furnace was consisted of water-cooled double quartz tubes and a graphite substrate holder. The graphite susceptor was heated by an induction coil. An optical thermometer was used to measure the temperature of the substrate. The substrate temperature reported in this article is the surface temperature of the substrate. Before the nitridation, the furnace was evacuated by a rotary pump and was purged several times with  $\text{N}_2$  to reduce the residual oxygen. The plasma nitridation was carried out at substrate temperatures of  $800^\circ\text{C}$  in a  $\text{NH}_3$  atmosphere for 1h. The  $\text{NH}_3$  gas was diluted to 50% with  $\text{N}_2$ . The flow rate was maintained at 50 sccm during the nitridation process. The pressure during the nitridation varied from 0.5 to 5 Torr, and the RF power for the plasma generation varied from 7 to 70 W.

The  $\text{SiO}_2$  films were deposited on the nitride layer by a thermal CVD using TEOS. The thermal CVD system consists of a hot wall quartz furnace, a graphite susceptor and a TEOS cylinder in the thermostatic bath. The TEOS vapor was introduced in the furnace by bubbling with carrier  $\text{N}_2$  gas. The  $\text{N}_2$  flow rate and growth pressure were maintained at 20 sccm and 1 atm, respectively. The temperature of the substrate and TEOS cylinder were  $750^\circ\text{C}$  and  $75^\circ\text{C}$ , respectively.

The Al electrode of 0.5 mm in diameter was deposited on the insulating layer to form the MIS diode. The ohmic contact was formed on the back surface by depositing Ni layer and annealing at  $1000^\circ\text{C}$  for 15 min.

X-ray photoelectron spectroscopy(XPS) and C-V measurement were used to characterize the insulator layer. A capacitance meter ( HP 4280A ) was used to measure the C-V characteristics. The signal level was 30 mV at 1MHz. The interface state density was estimated by Terman method.<sup>11)</sup>

### 3. Results and Discussions

Figure 1 shows the dependence of the film composition on the RF power. The concentration ratio of Si-C configuration decreased and the N content increased with increasing RF power. This means that the nitridation is enhanced with increasing RF power. Thus, it was suggested that the  $\text{NH}_x$  radicals increased with increasing RF power.

The dependence of concentration ratio on the growth pressure is shown in Fig. 2. The N ratio increased with decreasing growth pressure. Thus, the nitridation reaction could be enhanced with decreasing growth pressure. The result implies the enhancement of the N extraction reaction with increasing growth pressure.

Figure 3 shows C-V curve of nitrated layer. Sahin and his co-workers reported anomalous peaks in the C-V curves of p-Si metal-interface layer-semiconductor (MIS) Schottky diode,<sup>12)</sup> which were similar to that on the C-V curve in Fig. 3. They showed that the forward biased C-V plot of Schottky diode exhibited a peak due to the substrate series resistance. The current seemed to flow through the thin nitride layer in the sample shown in Fig. 3, and the diode exhibit the C-V characteristics similar to the MIS Schottky contact. In order to estimate the interface states density, the leakage current must be reduced to the low value enough to C-V measurement.

The thickness of nitride layer estimated from C-V characteristics and TEM images was about 5nm. Thus, it is difficult for exact characterization of interface because of the leakage current originating in extremely thin nitride layer. As a solution, a  $\text{SiO}_2$  film was deposited on the nitride layer by thermal CVD using TEOS in order to obtain an insulator film with enough thickness.

Figure 4 shows C-V characteristics of the  $\text{SiO}_2$  film on the nitride layer. The leakage current was confirmed to be reduced by  $\text{SiO}_2$  film deposition and the C-V characteristics could be estimated. The flat band voltage of the  $\text{SiO}_2$ /nitride layer was larger than the other oxide films. It is suggested that the nitride layer has negative fixed charge. This charge results in increase of threshold voltage of n-channel MIS FET. The electrons trapped at the interface states may one of the origins of the positive flat band voltage. The post annealing was effective to decrease the flat band voltage as shown in Fig. 6.

The distribution of  $D_{it}$  on the surface potential is shown in Fig. 5. The  $D_{it}$  of the nitride layer was higher than that of the other oxide layer. The high value of interface state density suggested that traps were formed at and/or near the interface between SiC and nitride layer. The interfaces traps seemed to be introduced during nitridation, because lower interface state density was detected for the sample using the single layer of TEOS CVD SiO<sub>2</sub> as the insulator. The traps between nitrated layer and SiO<sub>2</sub> might be another reason for the high interface state density, because lower interface state density was detected for the sample with the double layer of thermal SiO<sub>2</sub> and TEOS CVD SiO<sub>2</sub>.

In order to improve the interface, the post NH<sub>3</sub> annealing was tried. It is widely known that the SiO<sub>2</sub>/SiC interface improved by post annealing in N<sub>2</sub>O, NO or H<sub>2</sub> atmosphere. It is considered that an annealing effect from both nitrogen and hydrogen is provided using NH<sub>3</sub>. Figure 6 shows  $D_{it}$  values of the oxide/nitride layer before and after NH<sub>3</sub> annealing. The  $D_{it}$  value decreased after annealing. Thus, it is found the NH<sub>3</sub> annealing can also improve the interface property.

Jamet and his co-workers reported that nitridation of carbon clusters was effective to improve the interface properties of SiC MIS structure.<sup>6)</sup> The same result seemed to be obtained in this work. The flat band voltage was decreased by post annealing in NH<sub>3</sub> atmosphere as shown in Fig. 4. This indicated that the fixed charges also decreased at/or near the interface. The formation of Si-N bond was detected by FTIR measurement after the post annealing. This may be another reason for decrease of flat band voltage. The interface state density was decreased by the NH<sub>3</sub> annealing, but the value was still higher than the values reported for other methods.<sup>6,13)</sup> Further improvements are necessary to use this structure to the SiC MISFET. The details are now under investigations.

#### 4. Conclusion

We tried the nitride layer growth on 4H-SiC by plasma nitridation method. Nitridation was enhanced with increasing RF power and with decreasing growth pressure. The interface characteristics of ultra thin SiC nitride layer could be evaluated by depositing SiO<sub>2</sub> film on the nitride layer. The interface properties improved by the post NH<sub>3</sub> annealing technique.

## References

- 1) P. Friedrichs, E. P. Burte, R. Schorner: J. Appl. Phys. **79** (1996) 7814.
- 2) S. Sridevan, B. J. Baliga: Mater. Sci. Forum. **264-268** (1998) 997.
- 3) R. Schorner, P. Friedrichs, D. Peters, D. Stephani: IEEE. Trans. Electron. Devices. **46** (1999) 533.
- 4) T. E. Rudenko, I. N. Osiyuk, I. P. Tyagulski, H. O. Olafsson, E. O. Sveinbjornsson: Solid-State Electron. **49** (2005) 545.
- 5) M. K. Das: Mater. Sci. Forum. **457-460** (2004) 1275.
- 6) P. Jamet, S. Dimitrijevic, P. Tanner: J. Appl. Phys. **90** (2001) 5058.
- 7) S. Altnidal, S. Karadeniz, N. Tugluoglu, A. Tataroglu: Solid-State Electron. **47** (2003) 1847.
- 8) S. Karatas, S. Altnidal, M. Cakar: Physica. B. **357** (2005) 386.
- 9) M.M. Bulbul, S. Zeyrek, S. Altnidal, H. Yuzer: Micro. Engineering. **83** (2006) 577.
- 10) L. YingShen, S. Hashimoto, K. Abe, R. Hayashibe, T. Yamakami, M. Nakao, K. Kamimura: Jpn. J. Appl. Phys. **44** (2005) 673.
- 11) S. M. Sze: PHYSICS OF SEMICONDUCTOR DEVICES, (Wiley, 1981), 362.
- 12) B. Sahin, H. Cetin, E. Ayyildiz: Solid State Communications. **135** (2005) 490.
- 13) K. Fujihira, Y. Tarui, M. Imaizumi, K. Ohtsuka, T. Takami, T. Shiramizu, K. Kawase, J. Tanimura, T. Ozeki: Solid-State Electronics, **49** (2005) 896.

## Figure captions

Fig. 1. Concentration ratio vs RF power.

Fig. 2. Concentration ratio vs growth pressure.

Fig. 3. C-V curve of nitrified layer.

Fig. 4. C-V curve of SiO<sub>2</sub> layer on nitride layer.

Fig. 5.  $D_{it}$  values of the SiO<sub>2</sub>/nitride layer structure.

Fig. 6.  $D_{it}$  values of the oxide/nitride layer before and after annealing.

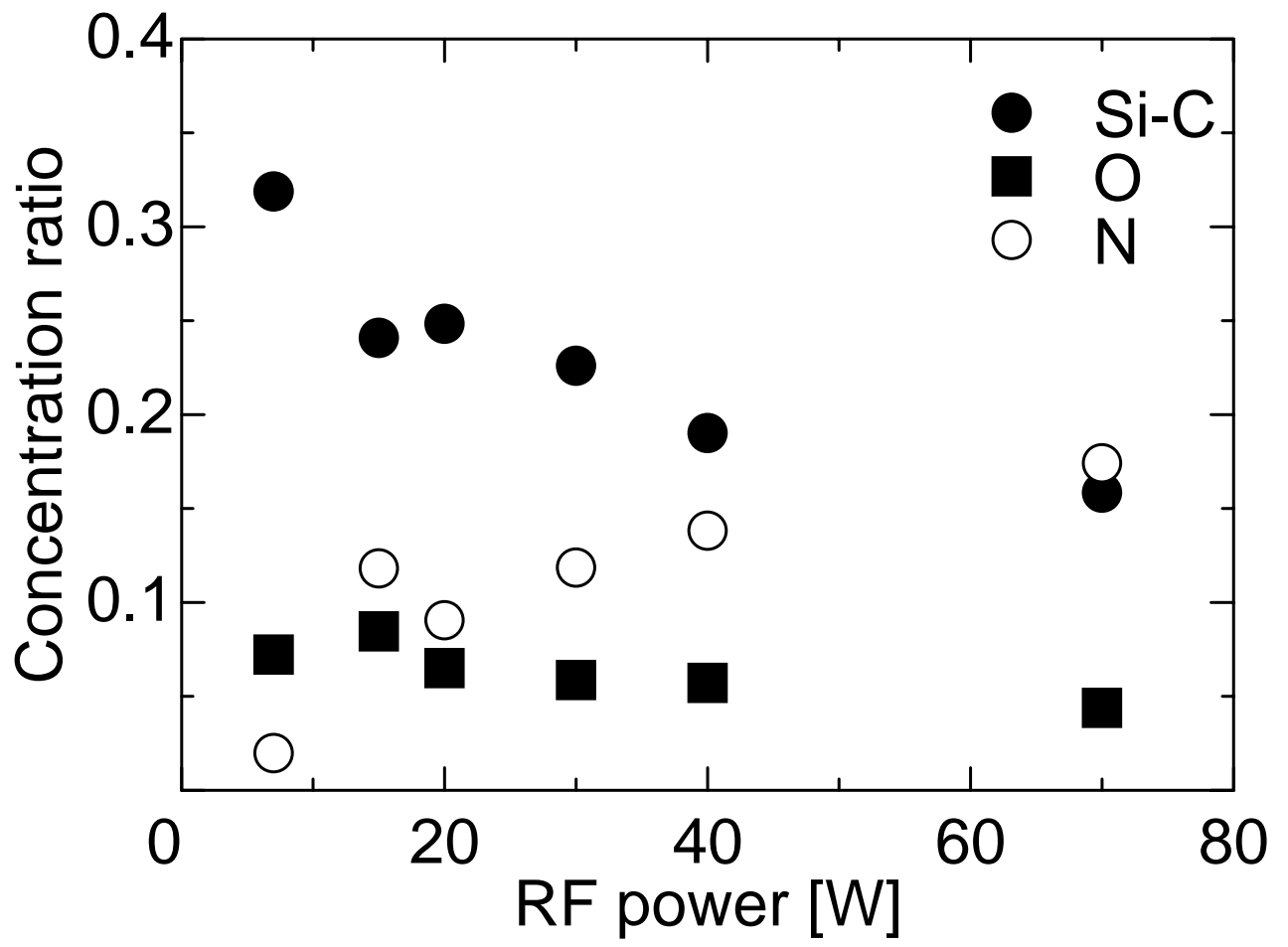


Figure 1



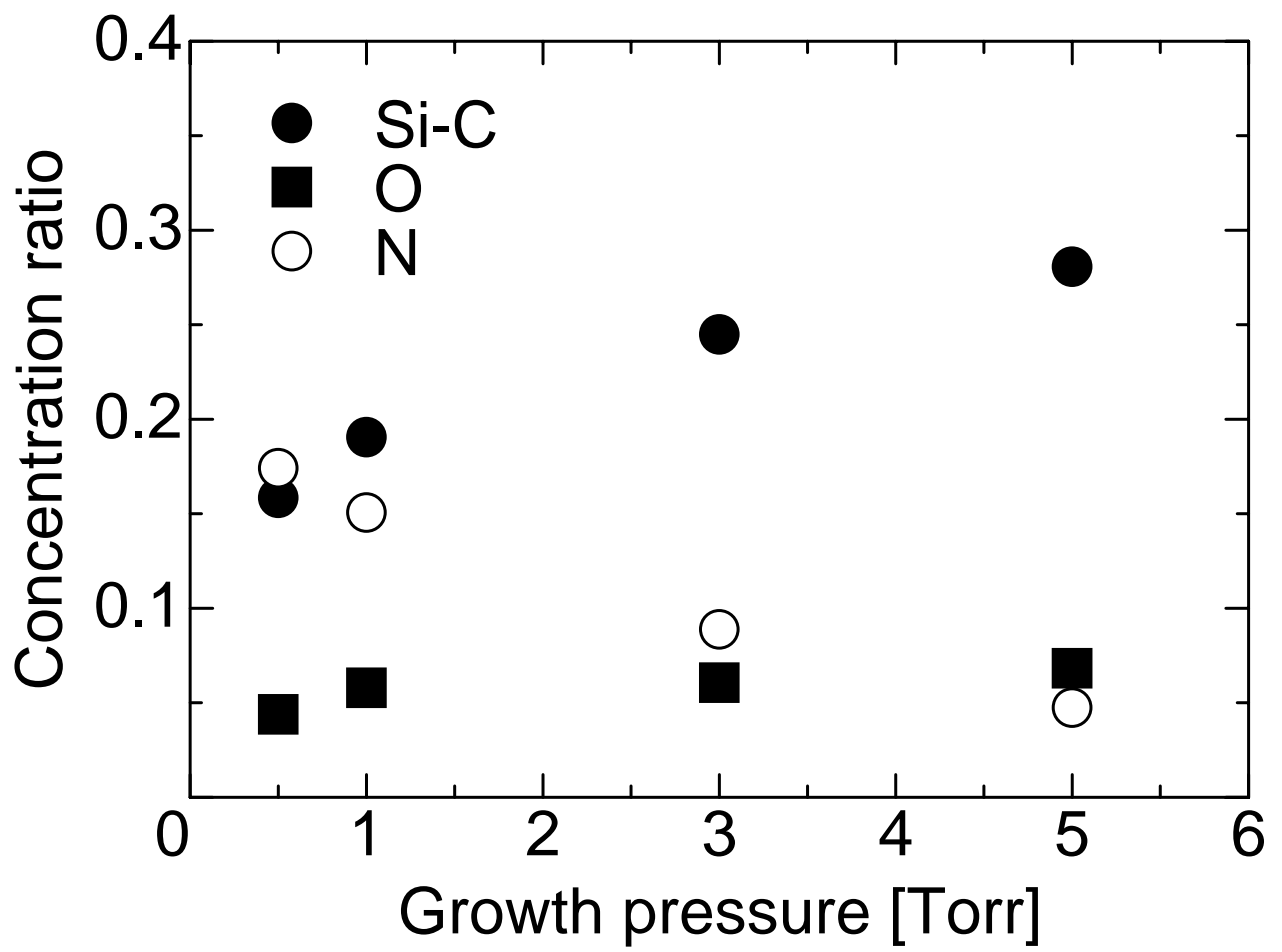


Figure 2

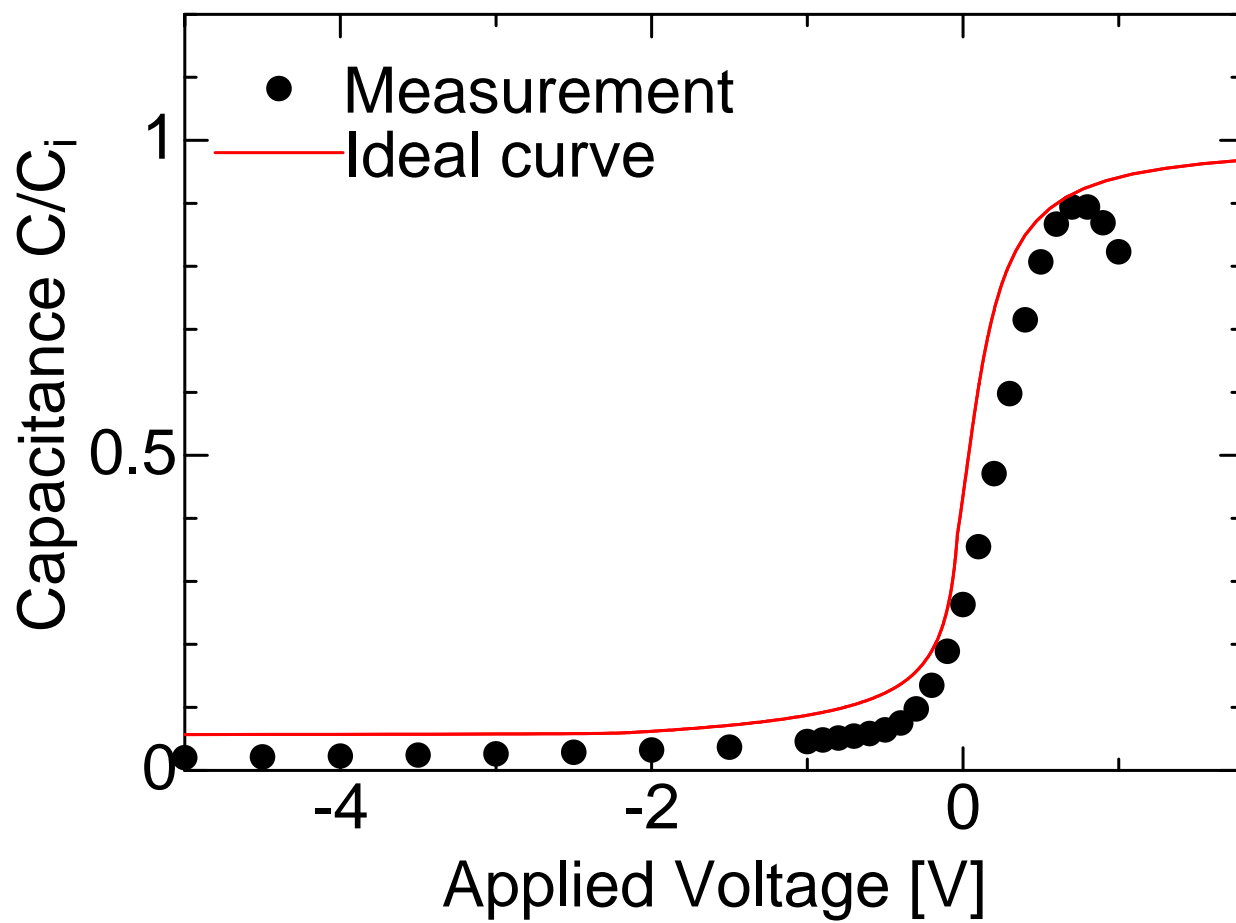


Figure 3

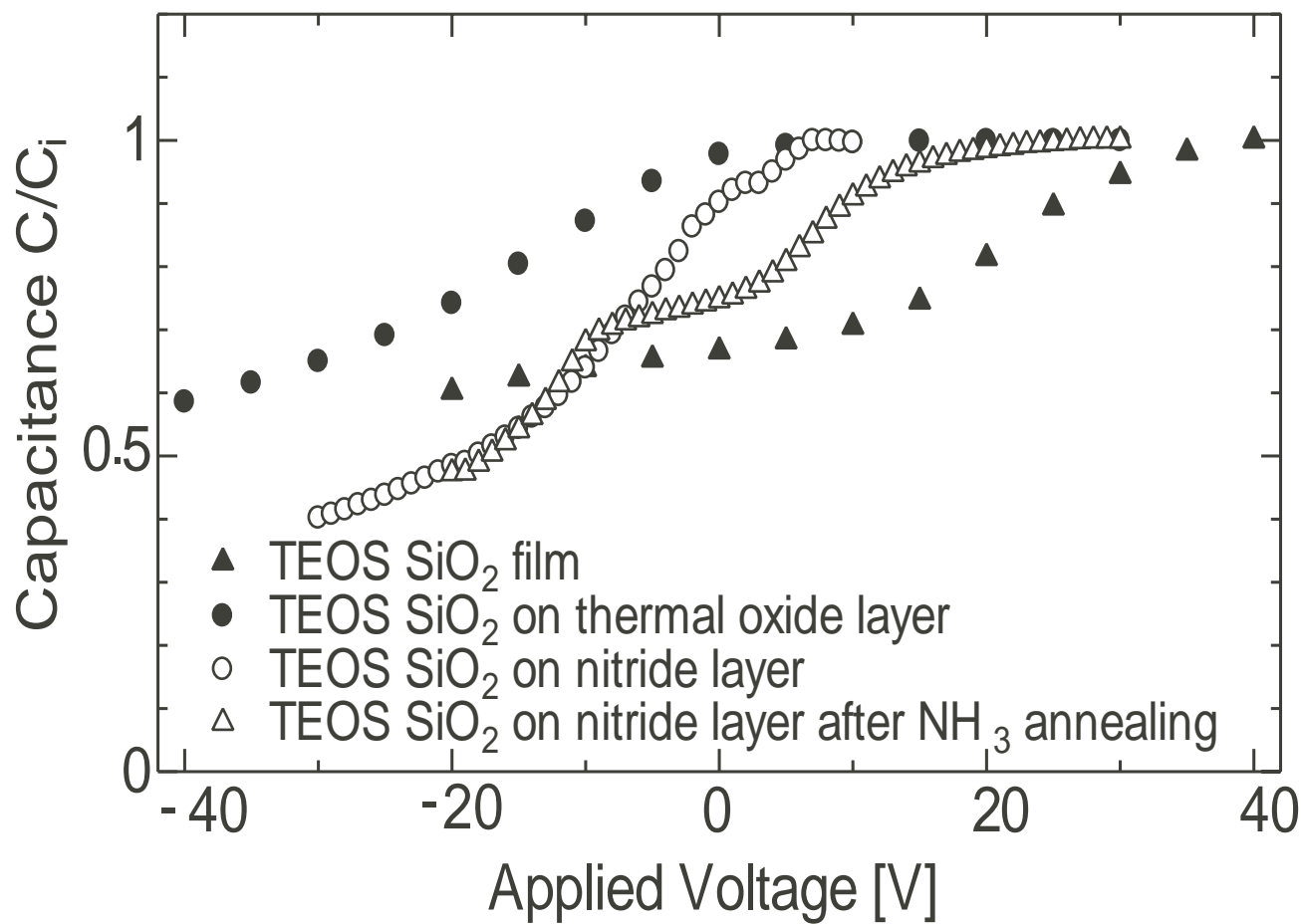


Figure 4

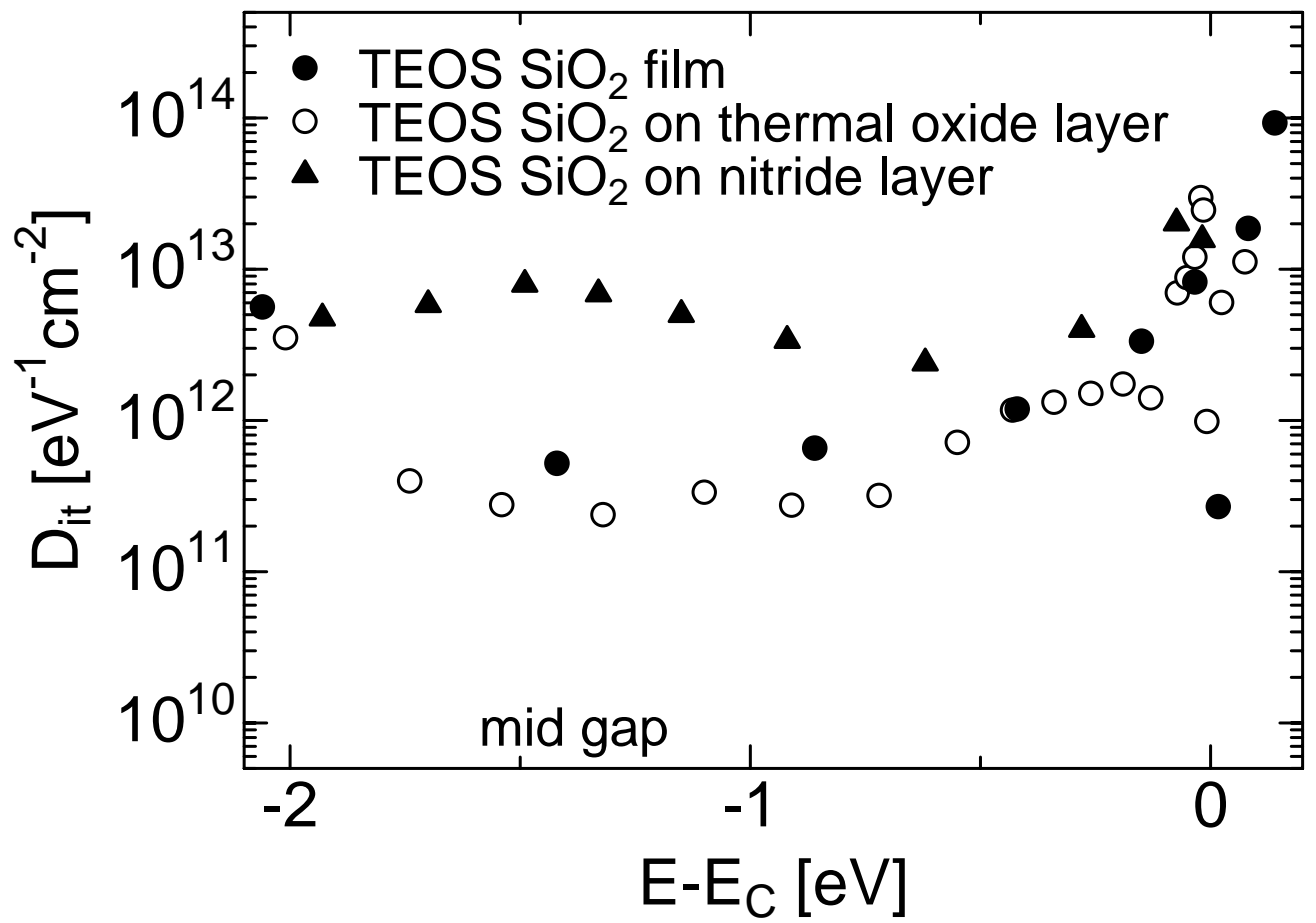


Figure 5

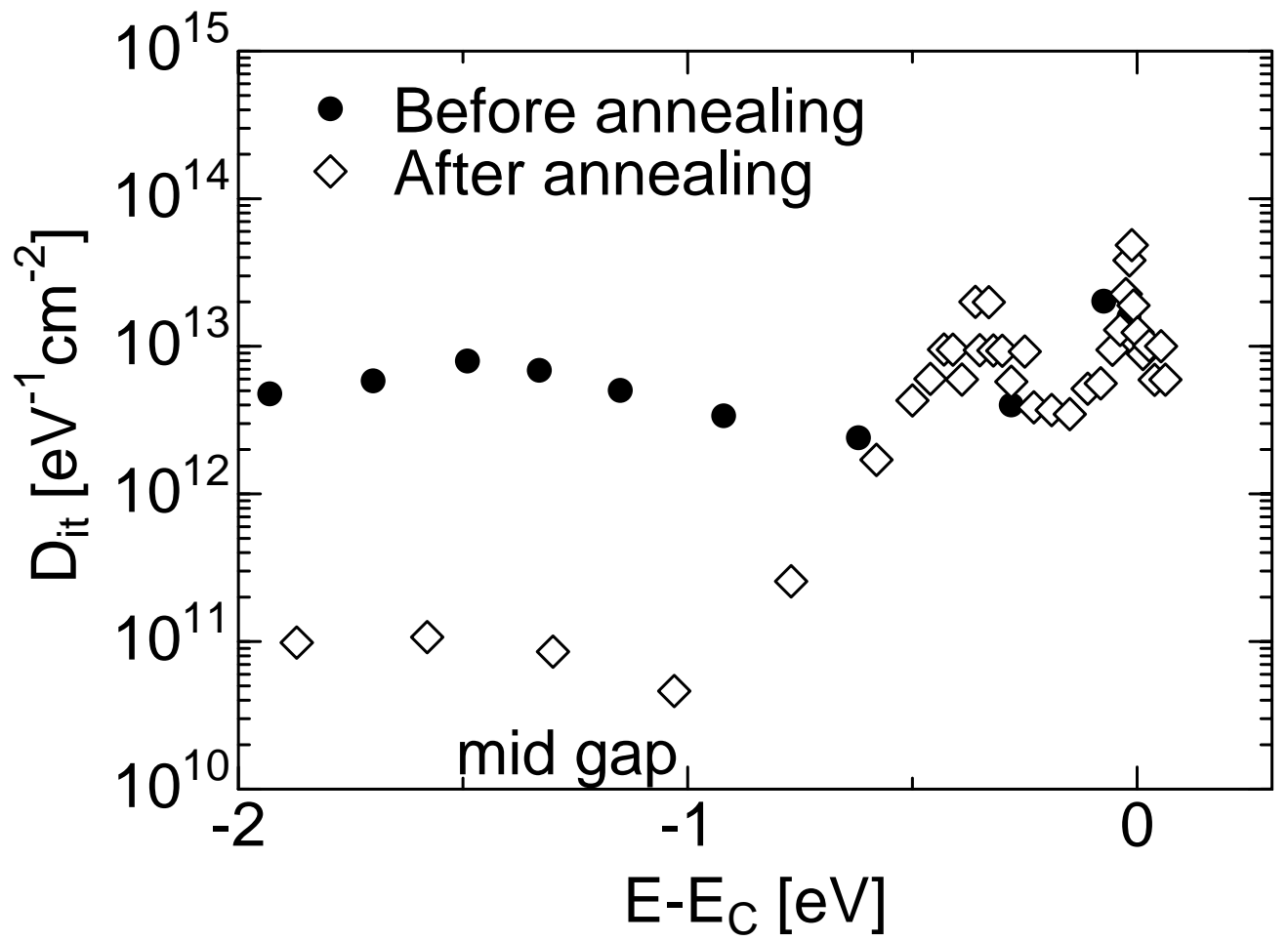


Figure 6