

Relationship among current fluctuations during forming, cell-to-cell variability and reliability in RRAM arrays

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Abstract—In this work, cells behavior during forming is monitored through an incremental pulse and verify algorithm on 4kbit RRAM arrays. This technique allows recognising different cell behaviors in terms of read-verify current oscillation: the impact of these oscillations on reliability and cell-to-cell variability has been investigated during 1k endurance cycles and 100k pulse stress under a variety of cycling conditions. Conductance histograms for the post-forming current reveal the nanosized nature of the filamentary paths across the dielectric film.

I. INTRODUCTION

Resistive Random Access Memories (RRAM) gathered increasing interest in the last few years because of [1]. However, an extensive research activity is still to be performed on this innovative technology in order to increase RRAM reliability and performance: to bring such technology to a maturity level the characterization of array structures is mandatory [2]. RRAM behavior is based on the possibility of electrically modifying the conductance of a Metal-Insulator-Metal (MIM) stack: the Set operation switches the cell into a high conductive state, whereas Reset brings the cell back to a low conductive state. Some technologies require a preliminary forming operation to activate such a switching behavior [3]–[6]. Even if such forming process is performed just once, it plays a fundamental role in determining the system performance [6]. A deep understanding of the forming process allows recognising faulty cells from scratch and to get a first glance insight on the cells reliability and performances during lifetime. In this work, cell behavior during forming is monitored through an incremental pulse forming with verify algorithm. Such technique allows recognising different cells behavior during forming in terms of read-verify current oscillations: the impact of these oscillations, interpreted either as the charging of a trap close to the surface of the conductive filament (CF) or the movement of an atom/defect in the filament [7], has been investigated in terms of reliability and cell-to-cell variability

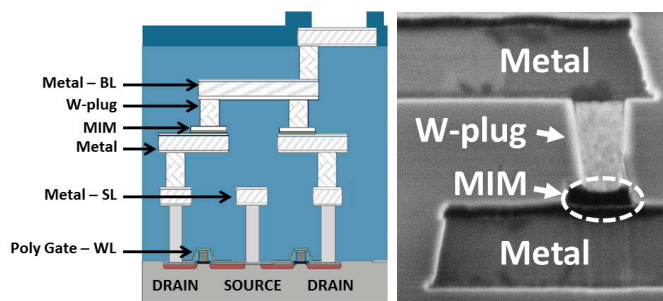


Fig. 1. Schematic and cross-sectional STEM image of the integrated RRAM cell.

during 1k endurance cycles and 100k stress pulses in different cycling conditions.

II. MEMORY ARCHITECTURE AND EXPERIMENTAL SETUP

The variable resistor of the cells in the 4kbits arrays [8] is a MIM stack composed by 150 nm TiN top and bottom electrode layers deposited by magnetron sputtering, a 10 nm Ti layer, a 9 nm HfO₂ AVD-deposited layer [3]. The schematic and cross-sectional SEM image of the integrated RRAM cell including the metal lines and the W based Via-connections is shown in Fig. 1. The resistor area is equal to 1 μm^2 . The memory cells are constituted by a select NMOS transistor, which also sets the current compliance, whose drain is in series to the MIM stack connected to the bitlines (BL). Forming operation has been performed using a pulse-verify scheme: a sequence of increasing voltage pulses from 2V to 3.5V with $V_{step} = 0.01V$, $T_{step} = 10\mu\text{s}$ is applied on the BL with a wordline (WL) voltage $V_{WL} = 1.4V$ to set the forming current compliance and after every pulse a read-verify operation with $V_{read} = 0.2V$, $T_{read} = 10\mu\text{s}$ is performed. When the read current reaches $I_{target} = 20\mu\text{A}$ the forming operation is stopped. Incremental pulse scheme with verify has

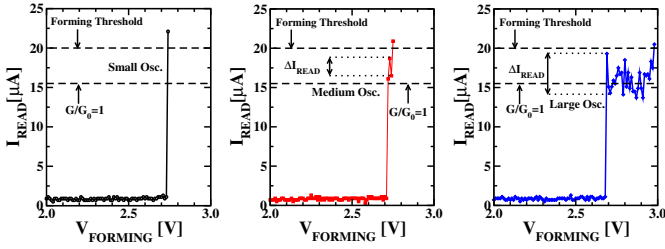


Fig. 2. Three different behaviors observed during forming process: small (left), medium (centre) and large (right) read-verify current oscillations.

been also implemented for set and reset operations, increasing V_{set} pulses on the BL and V_{reset} pulses on the source line (SL) from 1 V to 3.5 V with $V_{step} = 0.05V$, $T_{step} = 10\mu s$, $V_{WL,set} = 1.4V$, $V_{WL,reset} = 2.8V$ and the same read-verify condition used in forming. $T_{rise} = T_{fall} = 1\mu s$ have been used on all pulses in order to avoid overshoot issues. Set operation is stopped on a cell when the read-verify current reaches $I_{target} = 20\mu A$, whereas reset is stopped when $I_{target} = 10\mu A$ ensuring a minimum resistance ratio of two. Set and reset BL/SL voltages necessary to reach the requested read-verify current targets are defined as V_{SET} and V_{RES} . I_{LRS} and I_{HRS} are defined as the read currents measured after set and reset operations, respectively.

III. EXPERIMENTAL RESULTS AND DISCUSSION

Three different behaviors observed during forming process are reported in Fig. 2: while in many cells the read-verify current shows a sudden increase due to the creation of the CF, there are some cells showing read current oscillations with different amplitudes during forming due to the charging of a trap close to the surface of the CF or the movement of an atom/defect in the filament. Oscillations generally appears after reaching the quantum conductance unit $G_0 = 2e^2/h$ corresponding to the creation of a single mode nanowire [9], where e is the electron charge and h is the Planck's constant.

Fig. 3(a) shows the cumulative distribution of the maximum $|\Delta I_{read}|$ measured during forming. ΔI_{read} is the difference between two consecutive read verify steps after $G/G_0 = 1$ has been reached, where $G = I_{read}/V_{read}$. The cells have been arbitrarily gathered in three groups with the same amount of cells defined as follows, as a function of the maximum $|\Delta I_{read}|$ oscillation: small ($|\Delta I_{read}| < 0.5\mu A$), medium ($0.5 \leq |\Delta I_{read}| \leq 2.2\mu A$) and large ($|\Delta I_{read}| > 2.2\mu A$). Fig. 3(b) shows the cumulative distributions of the forming voltages, defined as the voltage required to reach the read-verify target $I_{target} = 20\mu A$ during the incremental pulse and verify forming scheme. It can be seen that cells with lower forming voltages exhibit smaller current fluctuations.

To evaluate the endurance properties of the cells, 1k cycles have been performed through an incremental set/reset procedure: Fig. 4 shows the cumulative distributions of the resistance ratio, set and reset voltages calculated after cycling. Resistance ratio is calculated as I_{LRS}/I_{HRS} at $V_{read} = 0.2V$. The cells formed with smaller oscillations are shown to require

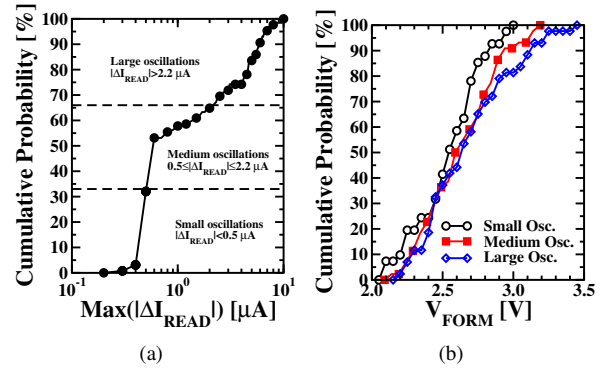


Fig. 3. Cumulative distribution of the maximum read current oscillations measured during forming (a). Cumulative distribution of the forming voltage for the three forming oscillations groups (b).

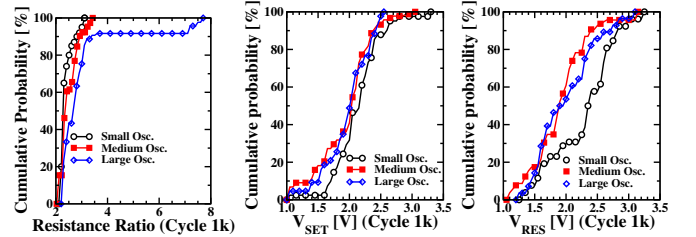


Fig. 4. Resistance ratio, V_{SET} , V_{RES} cumulative distributions for the different forming oscillations groups calculated on cycled devices.

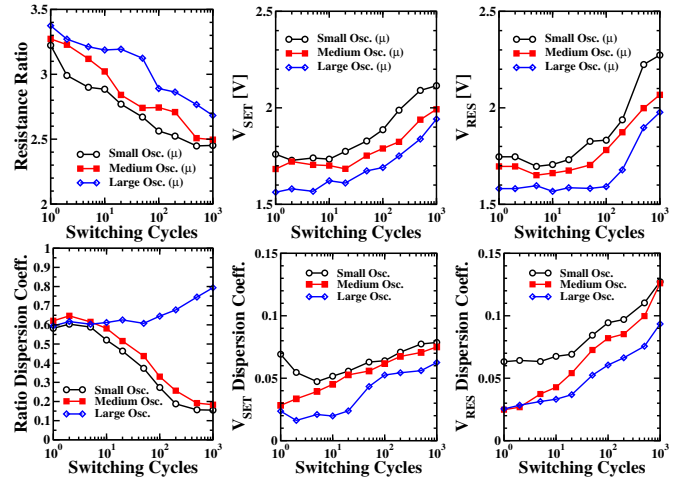


Fig. 5. Resistance Ratio, V_{SET} and V_{RES} average values and dispersion coefficients calculated during cycling.

higher V_{SET} and V_{RES} after 1k cycles: that means small oscillations correspond to wider filaments. The Resistance Ratio, V_{SET} , V_{RES} average values and dispersion coefficients calculated during cycling are reported in Fig. 5. To evaluate the cell-to-cell variability the dispersion coefficient of I_{LRS} and I_{HRS} distributions, defined as (σ^2/μ) , has been used. Resistance ratio of cells with large forming oscillations show both higher average value and dispersion coefficient in all cycling conditions: that means large fluctuations correspond to narrower filaments. V_{SET} , V_{RES} average values and dis-

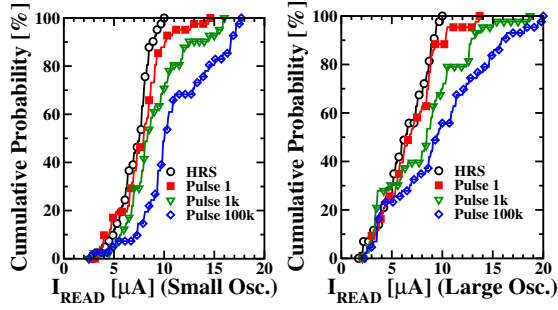


Fig. 6. Cumulative distributions of the read currents (with $V_{read} = 0.2V$) measured during set stress on HRS after different number of disturb pulses, at endurance cycle 1.

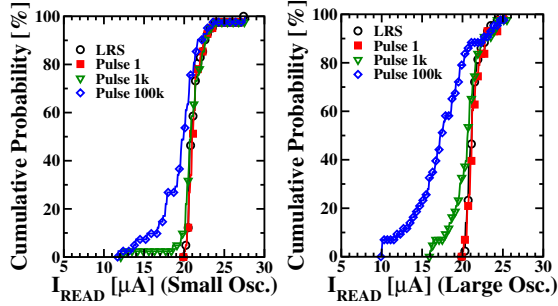


Fig. 7. Cumulative distributions of the read currents (with $V_{read} = 0.2V$) measured during reset stress on LRS after different number of disturb pulses, at endurance cycle 1.

persion coefficients are shown to increase during cycling: switching voltages on cells formed with large oscillations show lower average values and dispersion in all cycling conditions. This indicates cells with lower V_{SET} , V_{RES} have a not fully developed filament: this explains the large fluctuations. One reason of the parameters dispersion could be the root mean square surface roughness of HfO_2 films due to the columnar structure of the TiN bottom metal electrode [10].

To evaluate the disturbs immunity of each cells group, 100k reset stress pulses have been applied after set with $V_{stress,res} = 0.8V$, $T_{stress,res} = 10\mu s$ and 100k set stress pulses after reset with $V_{stress,set} = 0.8V$, $T_{stress,set} = 10\mu s$ at different cycles. Set/reset stress voltage pulses with 0.8V have been used since it's almost half of the average set/reset voltage measured on fresh devices. Cumulative distributions of the read currents measured after reset (HRS), set (LRS) and during set and reset stress on fresh devices are reported in Fig. 6 and Fig. 7, respectively: in both cases cells formed with larger current oscillations show a lower disturb immunity. That reveals larger fluctuations indicate a not so well formed filament thus more prone to exhibit lower immunity.

The average current variation and dispersion coefficient calculated on LRS cells during reset stress are shown in Fig. 8: cells with larger current oscillations during forming show a higher variation of the average read current and dispersion coefficient during reset stress in both cycling conditions. Similar consideration can be derived on Fig. 9, showing the

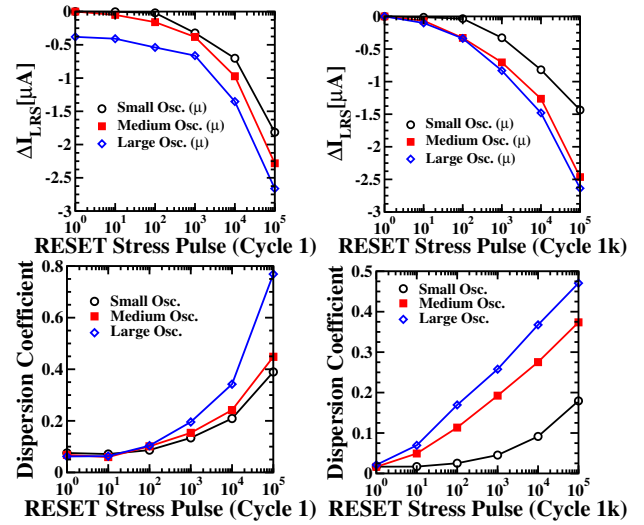


Fig. 8. Average read current variation ($V_{READ} = 0.2V$) of LRS and dispersion coefficient evolution calculated during 100k reset pulse stress, with $V_{pulse} = 0.8V$ after endurance cycle 1 (left) and 1k (right).

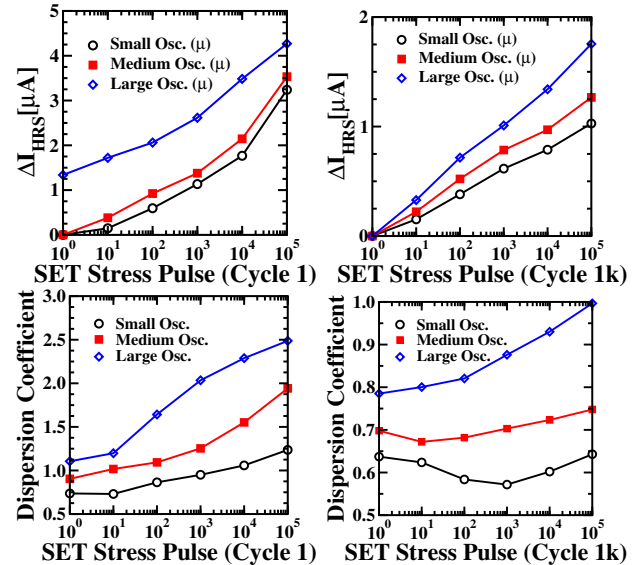


Fig. 9. Average read current variation ($V_{READ} = 0.2V$) of HRS and dispersion coefficient evolution calculated during 100k set pulse stress, with $V_{pulse} = 0.8V$ after endurance cycle 1 (left) and 1k (right).

average current variation and dispersion coefficient calculated on HRS cells during set stress. Even if after 1k cycles cells show lower sensitivity to set stress due to devices degradation, which means smaller current variation compared to fresh devices, cells with larger current oscillations during forming still show slightly lower disturbs immunity.

To provide a possible physical explanation of the measured phenomenon, Fig. 10 shows the distributions of the read-verify currents measured during forming with medium and large oscillations in units of G_0 . In order to evaluate only oscillations observed after the creation of the conductive filament, the analysis has been performed considering only

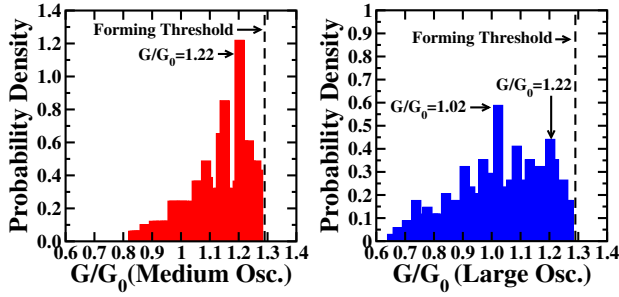


Fig. 10. Distribution of the read-verify current values measured during forming process in units of G_0 .

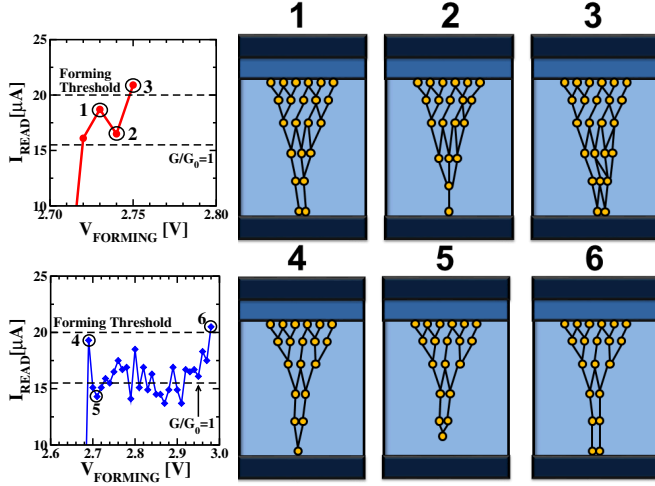


Fig. 11. Schematic representation of the CF evolution during medium (1,2,3) and large (4,5,6) read-verify current oscillations.

the read currents values measured in the three verify steps performed after that $G/G_0 = 1$ has been reached. It can be observed that the distribution of medium read-verify current oscillations shows the highest peak in $G/G_0=1.22$, whereas large oscillation current distribution shows the highest peak in $G/G_0=1.02$ and the second highest peak $G/G_0=1.22$. This means that in this case we have two quasi-stable states: the one with the lower conductance corresponds to the narrowest constriction. This is consistent with the current magnitude observed in Fig. 2: the largest fluctuations corresponds to the lower current level in which the filament is formed by only a few atoms and that is why it is very sensitive. A schematic representation of the CF evolution during medium (1,2,3) and large (4,5,6) current oscillations is shown in Fig. 11: a narrow constriction is more prone to exhibit more fluctuations because it is formed by very few atoms/vacancies. Fig. 12 represents the post-forming conductive filaments with different current oscillation properties.

IV. CONCLUSIONS

In this work the relationship between cells behavior during incremental pulse and verify forming algorithm and their performances in terms of endurance, cell-to-cell variability, and disturbs immunity have been investigated. Cells with

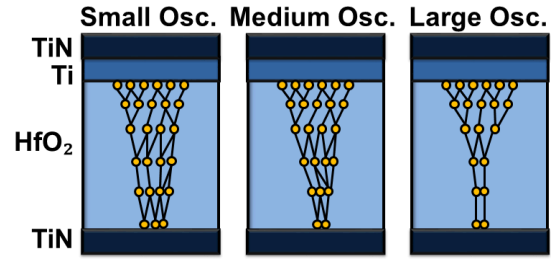


Fig. 12. Schematic representation of the post-forming conductive filaments showing different current oscillation properties.

narrowest CF show higher read-verify current oscillations during forming, lower disturbs immunity and higher variability but also better switching properties in terms of set and reset voltages and higher resistance ratio.

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REFERENCES

- [1] S. F. Karg, G. I. Meijer, J. G. Bednorz, C. Rettner, A. G. Schrott, E. A. Joseph, C. H. Lam, M. Janousch, U. Staub, F. La Mattina, S. F. Alvarado, D. Widmer, R. Stutz, U. Drechsler, and D. Caimi, "Transition-metal-oxide-based resistance-change memories," *IBM Journal of Research and Development*, vol. 52, no. 4.5, pp. 481–492, 2008.
- [2] C. Zambelli, A. Grossi, P. Olivo, D. Walczyk, J. Dabrowski, B. Tillack, T. Schroeder, R. Kraemer, V. Stikanov, and C. Walczyk, "Electrical characterization of read window in ReRAM arrays under different SET/RESET cycling conditions," in *IEEE Int. Memory Workshop (IMW)*, May 2014, pp. 1–4.
- [3] D. Walczyk, T. Bertaud, M. Sowinska, M. Lukosius, M. A. Schubert, A. Fox, D. Wolansky, A. Scheit, M. Fraschke, G. Schoof, C. Wolf, R. Kraemer, B. Tillack, R. Korolevych, V. Stikanov, C. Wenger, T. Schroeder, and C. Walczyk, "Resistive switching behavior in TiN/HfO₂/Ti/TiN devices," in *Int. Semiconductor Conf. Dresden-Grenoble (ISCDG)*, Sept 2012, pp. 143–146.
- [4] N. Raghavan, A. Fantini, R. Degraeve, P. Roussel, L. Goux, B. Govoreanu, D. Wouters, G. Groeseneken, and M. Jurczak, "Statistical insight into controlled forming and forming free stacks for HfOx RRAM," *Microelectronic Engineering*, vol. 109, pp. 177 – 181, 2013.
- [5] C. Chen, H. Shih, C. Wu, C. Lin, P. Chiu, S. Sheu, and F. Chen, "RRAM defect modeling and failure analysis based on march test and a novel squeeze-search scheme," *IEEE Trans. on Computers*, vol. 64, no. 99, pp. 1–1, 2014.
- [6] P. Lorenzi, R. Rao, and F. Irrera, "Forming kinetics in HfO₂-based RRAM cells," *IEEE Trans. on Electron Devices*, vol. 60, no. 1, pp. 438–443, 2013.
- [7] S. Balatti, S. Ambrogio, A. Cubeta, A. Calderoni, N. Ramaswamy, and D. Ielmini, "Voltage-dependent random telegraph noise (RTN) in HfOx resistive RAM," in *IEEE Int. Reliability Physics Symposium (IRPS)*, June 2014, pp. MY.4.1–MY.4.6.
- [8] C. Zambelli, A. Grossi, D. Walczyk, T. Bertaud, B. Tillack, T. Schroeder, V. Stikanov, P. Olivo, and C. Walczyk, "Statistical analysis of resistive switching characteristics in ReRAM test arrays," in *IEEE Int. Conf. on Microelectronics Test Structures (ICMETS)*, Mar 2014, pp. 27–31.
- [9] E. Miranda, C. Walczyk, C. Wenger, and T. Schroeder, "Model for the resistive switching effect in HfO₂ MIM structures based on the transmission properties of narrow constrictions," *IEEE Electron Device Letters*, vol. 31, no. 6, pp. 609–611, 2010.
- [10] P.-S. Chen, Y.-S. Chen, H.-Y. Lee, T.-Y. Wu, P.-Y. Gu, F. Chen, and M.-J. Tsai, "Impact of flattened TiN electrode on the memory performance of HfO₂ based resistive memory," *Electrochem. and Solid-State Letters*, vol. 15, no. 4, pp. H136–H139, 2012.