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# Design, fabrication, characterization and reliability study of CMOS-MEMS Lorentz-force magnetometers

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## Abstract

This article presents several design techniques to fabricate micro-electro-mechanical systems (MEMS) using standard complementary metal-oxide semiconductor (CMOS) processes. They were applied to fabricate high yield CMOS-MEMS shielded Lorentz-force magnetometers (LFM). The multilayered metals and oxides of the back-end-of-line (BEOL), normally used for electronic routing, comprise the structural part of the MEMS. The most important fabrication challenges, modeling approaches and design solutions are discussed. Equations that predict the Q factor, sensitivity, Brownian noise and resonant frequency as a function of temperature, gas pressure and design parameters are presented and validated in characterization tests. A number of the fabricated magnetometers were packaged into Quad Flat No-leads (QFN) packages. We show this process can achieve yields above 95 % when the proper design techniques are adopted. Despite CMOS not being a process for MEMS manufacturing, estimated performance (sensitivity and noise level) is similar or superior to current commercial magnetometers and others built with MEMS processes. Additionally, typical offsets present in Lorentz-force magnetometers were prevented with a shielding electrode, whose efficiency is quantified. Finally, several reliability test results are presented, which demonstrate the robustness against high temperatures, magnetic fields and acceleration shocks.

## Introduction

Today, the most common form of mass-production semiconductor device fabrication is CMOS technology. The dedicated integrated circuit (IC) interfaces of commercial sensors are realized using this technology. However, the sensing elements need to be manufactured using specialized micro-machining processes. For a number of applications, integration of CMOS electronics and MEMS devices on a single chip (CMOS-MEMS) has the potential of reducing fabrication costs, size, parasitics and power consumption, along with higher speed, compared to other integration approaches<sup>1–3</sup>. These advantages have attracted a lot of

research interest during the past decades that led to several developments, generally categorized depending on when the MEMS are built, compared to the CMOS circuitry:

- **Pre-CMOS or MEMS before CMOS<sup>4–6</sup>:** The MEMS are built before CMOS. Once finished, the wafer can undergo standard CMOS processing. The MEMS is free of CMOS thermal budget limitations<sup>7,8</sup>, but it must be compatible with the subsequent standard CMOS processing steps. Unfortunately, CMOS foundries tend to be reluctant to work with already MEMS-processed wafers.
- **Intra-CMOS or MEMS between FEOL and BEOL<sup>9–14</sup>:** The CMOS process is interrupted for MEMS processing. Again, close collaboration with the CMOS foundry is required, but several products are under volume production<sup>3</sup>.
- **Post-CMOS or MEMS after CMOS:** MEMS are created only once the standard CMOS process is finished, so the CMOS foundries do not need to

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modify the standard process. However, the MEMS process cannot surpass the CMOS thermal budget<sup>7,8</sup>, which limits the MEMS processing options.

- **MEMS on top**<sup>15–17</sup>: Several foundries like TSMC, XFAB, UMC and DALSA, and IMEC's SiGe MEMS followed this route<sup>18</sup>. The DMD from Texas Instruments<sup>19</sup> is another well known example.
- **BEOL CMOS-MEMS**<sup>6,20–26</sup>: Remarkably, a CMOS-MEMS device may be built with the back-end-of-line (BEOL) layers of the finished CMOS process. This approach minimizes the number of post-processing steps and is the one discussed in this work.

Despite its advantages, the BEOL CMOS-MEMS approach has proven to be very challenging given the current lack of commercial products in the market. The most important reasons are two: Firstly, achieving a competitive performance with a fixed process, which is not optimized for MEMS, is challenging. Secondly, cost advantages vanish if the MEMS yield is not sufficiently high or its area too large. Both issues are addressed in this work.

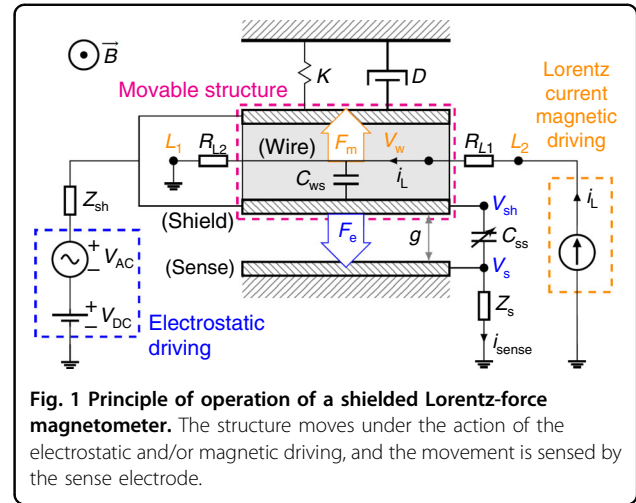
We present and discuss the challenges, modeling and design techniques used to fabricate high-yield BEOL CMOS-MEMS devices. They were applied to fabricate integrated Lorentz-force magnetometers (LFM) which were packaged, characterized and subjected to several reliability tests. These are missing in most technical works in the literature, yielding the practical commercialization of LFM's still unknown. The measured performance is very competitive compared to current products in the market.

All commercial magnetometers are non-Lorentz-force ones. They are typically based on the Hall effect, anisotropic magnetoresistance (AMR), giant magnetoresistance (GMR), or, recently, tunnel magnetoresistance (TMR)<sup>27</sup>, also called magnetic tunnel junction (MTJ). They all have some sort of magnetic material, like flux concentrators<sup>28</sup>. The magnetic material may be damaged by high magnetic fields, imposes temperature limitations and is susceptible to magnetic hysteresis, which in turn, may lead to reduced accuracy and require tedious re-calibration from the user. Although LFM do not require magnetic materials, they suffer from other offsets related to electrical interference<sup>29–32</sup>. This will be analyzed, solutions will be presented and their efficiency quantified.

### Lorentz-force magnetometers: principle of operation and analysis

The operational principle of the shielded LFM discussed in this work is illustrated in Fig. 1. The magnetic field  $\vec{B}$  is sensed with a current  $\vec{i}_L$  (Lorentz current) that flows along a wire (Lorentz wire) of length  $L$  inside a movable structure, which experiences a force  $F_m$  given by:

$$\vec{F}_m = L \cdot \vec{i}_L \times \vec{B} \quad (1)$$



The system behaves as a damped harmonic oscillator. In this work, the movable structure is formed by several ( $n_B$ ) clamped beams, and the Lorentz current passes along each beam  $n_w$  times. This increases the effective Lorentz current  $n_w \times n_B$  times. Only a single wire and beam are depicted in Fig. 1 for clarity reasons. Additionally,  $i_L$  is applied as a square wave whose frequency is coincident with the first resonant frequency of the structure ( $f_r$ ) in order to maximize the output or sensed current ( $i_{sense}$ ), produced by the sense capacitance variation  $C_{ss}$ . The Lorentz wire is shielded from the sense electrode, so  $i_{sense}$  is independent of  $i_L$ , and this provides some important benefits that will be examined later.

In order to sustain the oscillation when  $\vec{B} = 0$ , or simply to characterize the device, an electrostatic driving is applied between the shield and sense electrodes. This creates an electrostatic force labeled as  $F_e$  in Fig. 1, which allows to track the resonance frequency as done in many works<sup>31,33–38</sup>.

Also, the figure shows the most important electrical parameters of the whole system.

### Sensitivity and offsets induced by the Lorentz current

The electrostatic force  $F_e$  is a function of the gap ( $g$ ) between plates, the sensing area ( $A$ ) and the voltage difference ( $\Delta V = V_{sh} - V_s$ ). For the typical impedance values and operating frequencies  $V_{sh}/V_s > 10^4$ , so  $\Delta V \approx V_{sh}$ . Additionally, the Lorentz wire AC voltage ( $V_w$ ) induces an interference voltage ( $V_{sh}^{int}$ ) in the shield electrode, yielding  $V_{sh} \approx V_{DC} + V_{AC} + V_{sh}^{int}$ , assuming small  $Z_{sh}$ . These considerations allow to write  $i_{sense}$  and  $F_e$  as:

$$i_{sense} = \frac{dQ_{ss}}{dt} = \frac{d(C_{ss}\Delta V)}{dt} \approx \underbrace{C_{ss} \frac{dV_{AC}}{dt}}_{\text{Electrical coupling}} + \underbrace{C_{ss} \frac{dV_{sh}^{int}}{dt}}_{\text{Electrical interference}} + \underbrace{V_{sh} \frac{dC_{ss}}{dt}}_{\text{Motional current}(i_m)} \quad (2)$$

where  $Q_{ss}$  is the charge of  $C_{ss}$ . The motional current ( $i_m$ ) is caused by the magnetic field ( $B$ ) and its associated Lorentz force ( $F_m$ ), and/or by the electrostatic force ( $F_e$ )<sup>39</sup>:

$$F_e = \frac{\partial}{\partial g} \left[ \frac{1}{2} C_{ss} (V_{sh} - V_s)^2 \right] \approx \frac{V_{sh}^2}{2} \frac{\partial C_{ss}}{\partial g} \approx \underbrace{-\frac{\epsilon_0 A}{2g^2} V_{DC}^2}_{F_{dc}(\omega=0)} - \underbrace{\frac{\epsilon_0 A}{g^2} \left( \overbrace{V_{DC} V_{AC}}^{\text{Electrostatic driving}} + \overbrace{V_{DC} V_{sh}^{int}}^{\text{Electrostatic interference}} \right)}_{F_{\omega_r}(\omega=\omega_r)} - \underbrace{\frac{\epsilon_0 A}{2g^2} (V_{AC} + V_{sh}^{int})^2}_{F_{2\omega_r}(\omega=2\omega_r)} \quad (3)$$

where a simple parallel plate configuration was assumed for now. Only the  $F_{\omega_r}$  terms excite the device at its resonant frequency and therefore contribute to motional current at  $\omega_r$ . The  $V_{DC}V_{AC}$  term corresponds to the expected electrostatic driving force. The  $V_{DC}V_{sh}^{int}$  term is an electrostatic interference caused by the Lorentz wire: it causes an undesired mechanical resonance (magnetic offset) that is, unfortunately, indistinguishable from the one caused by a constant external magnetic field  $B$  in unshielded LFM. The underlying reason is that  $B$  and  $V_{sh}^{int}$  are both proportional to the Lorentz current. Interestingly, in a shielded LFM this current is in quadrature with the magnetic field sense current. However, its suppression with electronic techniques is challenging.

Two interference mechanisms due to the Lorentz wire have been identified: the electrical interference in Eq. (2) and the electrostatic interference in Eq. (3). Ultimately, both processes appear as an offset in the measured magnetic field. In conventional unshielded magnetometers this offset is generally orders of magnitude larger than the magnetic signal<sup>30,40</sup>. In fact, this is a well-known drawback of resonant Lorentz-force magnetometers<sup>29–32</sup>. The offset can be compensated, for example, using a DC compensation voltage applied to the MEMS structure to null the electrostatic force<sup>29,40</sup>, but it is difficult to eliminate its associated drift reliably and cheaply<sup>30</sup>. There is a patented technique<sup>31,33,41</sup>, based on current chopping, that reduces this effect greatly. It will be used in the Offset and shielding efficiency section. The motional current in Eq. (2) depends on the applied force ( $F_m + F_e$ ). For a damped harmonic oscillator driven at the resonant frequency:

$$i_m = V_{sh} \frac{dC_{ss}}{dt} = V_{sh} \frac{dC_{ss}}{dg} \frac{dg}{dt} = V_{sh} \frac{dC_{ss}}{dg} j\omega_r Q \frac{F_m + F_e}{K} \quad (4)$$

where  $j$  indicates 90° phase and  $Q$  is the quality factor of the resonator, and  $K$  the spring constant.

The sensitivity  $S$  of the device to the external magnetic field  $B$  may now be derived by combining Eqs. (1) to (4):

$$S = \frac{\partial i_m}{\partial B} = V_{sh} \frac{dC_{ss}}{dg} j\omega_r Q \frac{i_L n_w n_B L}{K} \quad (5)$$

In this work, the Lorentz current  $i_L$  must be applied at resonance, generating an alternating force on the beam. This alternating force creates an alternating output

current at the resonant frequency that can be amplified by the electronics. Hence, the sensor information is not at DC, despite the magnetic field is, thus avoiding flicker 1/f noise effects and offsets. No other frequencies or mixing byproducts are present.

### Noise and heading accuracy

Three devices aligned along the three Cartesian axes form a three-axis magnetometer that can work as a magnetic compass. In our case, one device ( $z$ ) detects out-of-plane magnetic field, and two devices ( $xy$ ) detect the in-plane components. They all can be monolithically integrated in a single die, so the alignment is straightforward, as the two  $xy$  devices are orthogonal by mask design.

The best heading accuracy, or angle error, of a magnetic compass sensor is limited by the Brownian noise. It can be calculated as the ratio between the equivalent magnetic field noise ( $B_{noise}$ ) and the Earth’s magnetic field ( $B_{earth}$ ):

$$\theta_{noise}^{RMS} \left( ^\circ 1/\sqrt{\text{Hz}} \right) = \text{atan} \left( \frac{B_{noise}^{RMS}}{B_{earth}} \right) \approx \frac{B_{noise}^{RMS}}{B_{earth}} \cdot \frac{180}{\pi} \quad (6)$$

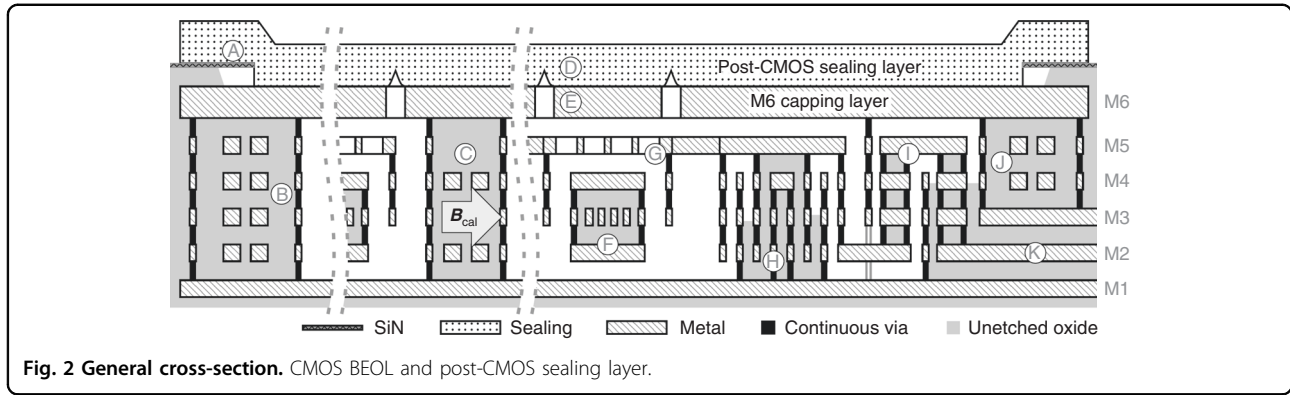
where a linear approximation is valid given that  $B_{noise} < B_{earth}$ . Earth’s magnetic field horizontal intensity ranges from around 40  $\mu\text{T}$  in Southeast Asia to 15 – 20  $\mu\text{T}$  in areas like South America, South Africa, Siberia and northern Canada<sup>42,43</sup>. So, regarding heading accuracy, we think that considering  $B_{earth} = 20 \mu\text{T}$  in Eq. (6) is a reasonable assumption.

To calculate  $B_{noise}^{RMS}$  let us first calculate the total Lorentz force  $F_m$  per unit of magnetic field  $B$ :

$$\frac{F_m^{RMS}}{B} = i_L^{RMS} \cdot L \cdot n_w \cdot n_B = i_L \cdot \frac{4}{\pi} \cdot \frac{1}{\sqrt{2}} \cdot L \cdot n_w \cdot n_B \quad (7)$$

where  $L$  is the length of the beams and the RMS Lorentz current component at the resonant frequency ( $i_L^{RMS}$ ) is calculated with the square wave Lorentz current  $i_L$ :

$$i_L^{RMS} = i_L \cdot \frac{4}{\pi} \cdot \frac{1}{\sqrt{2}} \text{ (Square wave)} \quad (8)$$



**Fig. 2 General cross-section.** CMOS BEOL and post-CMOS sealing layer.

which was used to obtain the last term of Eq. (7). On the other hand, the Brownian noise force ( $F_{noise}$ ) is given by<sup>44</sup>:

$$F_{noise}^{RMS} = \sqrt{4K_B T D} = \sqrt{\frac{4K_B T M \omega_r}{Q}} \quad (9)$$

where  $D = M\omega_r/Q$  is the damping coefficient of the considered 1-D or lumped system of mass  $M$ .

Using the previous equations allows us to write the equivalent magnetic field noise in units of  $T/\sqrt{Hz}$  as:

$$B_{noise}^{RMS} = \frac{F_{noise}^{RMS}}{F_m^{RMS}/B} = \frac{\pi\sqrt{K_B T M \omega_r}}{i_L^{peak} \cdot L \cdot n_w \cdot n_B \cdot \sqrt{2Q}} \quad (10)$$

Equations (6) and (10) will be evaluated for the fabricated devices later in the Results and discussion section.

## Materials and methods

### CMOS-MEMS fabrication process

The CMOS-MEMS process used in this work uses the back-end-of-line (BEOL) of a standard 6-metal 0.18  $\mu\text{m}$  CMOS process to build the MEMS. The unwanted inter-metal-dielectric (IMD) oxide is etched away with a vapor HF (vHF) process already described in previous works<sup>26,45,46</sup>. The vHF enters through small holes in the last metal of the BEOL (detail E of Fig. 2), dissolving the oxide selectively and releasing the MEMS structure (details F and G of Fig. 2). For fabrication reasons, each BEOL layer is composed of several sub-layers<sup>26</sup>, which makes the mechanical modelization of the final multi-layered structure difficult. Finally, the MEMS devices are sealed in vacuum with a post-CMOS layer of Aluminum (detail D of Fig. 2) deposited on top of the last metal, diced and packaged<sup>23</sup>.

We mostly used 200 mm wafers from the 1P6M 0.18  $\mu\text{m}$  CMOS process from Global Foundries (GF), but similar processes from LFoundry (1P6M 0.15  $\mu\text{m}$ ) and TSMC (1P6M 0.18  $\mu\text{m}$ ) worked well, also.

The fabrication process is simple, but using the BEOL as structural layers has important drawbacks such as non

repeatability, excessive curvature and creep<sup>26</sup>. In addition, although the IMD oxide etching rate is uniform, the vHF etch is highly catalyzed along the metals<sup>45</sup> producing a runaway etch that must be stopped. This increases horizontal etching speed and reduces greatly etching isotropy. We refer to this as the capillarity effect.

### Design techniques to overcome the CMOS-MEMS fabrication process challenges

#### Continuous vias to stop vHF

Vias (or plugs) are generally square, tungsten-based and used for connecting two different metal levels in CMOS design. However, vias may be made very long in one direction (continuous vias) and thus fill completely the gap between two metal layers with tungsten, which is a vHF-resistant material. Vertical metal walls that stop the advance of vHF can be created this way. Although via detaching problems have been observed with other release agents<sup>47</sup>, vHF has never caused these issues in our experience. This technique opens a myriad of possible structures for CMOS-MEMS design. One example are anchors, as the one supporting the sensing electrode in detail H of Fig. 2.

#### Anchors to attach mechanically and isolate electrically

The resonating beams and driving/sensing electrodes must be mechanically attached to something and, typically, electrically isolated. Attaching them to unetched oxide proved unreliable and complicated due to the capillarity effect, which leads to very fast etching around the edges of the metals, and quick detachment from any unetched oxide. When electrical isolation is not needed, one option is to attach them to vertical walls created with metals and continuous vias. When it is needed, one compact option is to use anchors as in detail H of Fig. 2, where they provide mechanical support to the sensing electrode. They work by forcing the vHF to travel upwards and downwards, taking advantage of the slower vertical etch rate, and keeping under control the capillarity effect this way<sup>45</sup>.

Anchors leave some oxide exposed to the MEMS cavity which may outgas if the device undergoes sufficiently high and long temperature excursions. We have found that, in devices that require a vacuum level under 1 mbar, the exposed oxide should be minimized. Therefore, using as few anchors as possible may be a good design strategy in terms of outgassing minimization.

**Clamped-clamped beams to overcome curvature issues**

Large curvature and variability are observed in CMOS-MEMS BEOL structures<sup>24–26,46</sup>. Fortunately, clamped-clamped (c-c) beams made of several stacked BEOL layers may be an excellent design option to circumvent these issues.

The reason is that, as long as a given critical temperature/compressive residual stress is not reached, the c-c beams will remain very flat even when there is a very large stress gradient. Simulations predict that c-c beams below the buckling load display deformations in the nanometer range (see Fig. 3) while, if only clamped at one end, they would deform from a few to hundreds of microns (as predicted by equation 7 from Valle et al.<sup>26</sup>). All long structures are doubly clamped in this work.

**Several beams coupled to improve SNR and repeatability**

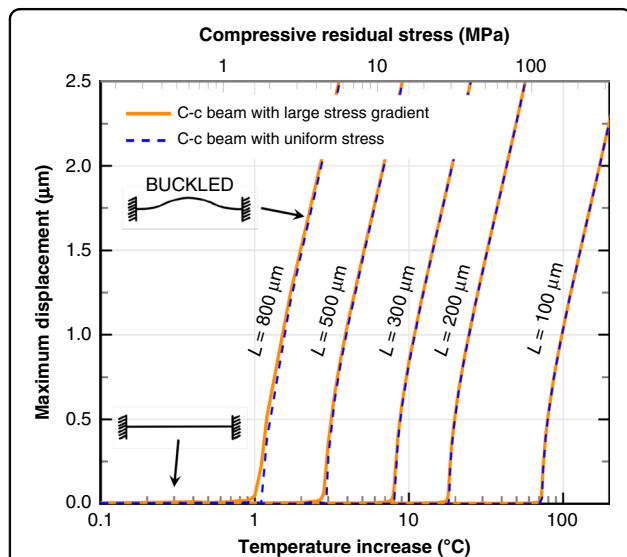
If  $n_B$  beams are mechanically coupled they will behave as a single resonating structure. Given that the coupling is far from any surface and very small compared to the

rest of the vibrating structure, the damping coefficient  $D$  is proportional to the number of coupled beams  $n_B$ . Therefore, the Brownian force noise (Eq. (9)) is only proportional to  $\sqrt{n_B}$ . By contrast, the total Lorentz force  $F_m$  is proportional to  $n_B$  (Eq. (7)). As a consequence, the equivalent magnetic noise (Eq. (10)) is proportional to  $1/\sqrt{n_B}$ , which implies that coupling more beams lowers the noise of the system. In addition, improved repeatability is also expected as variations in geometric or material properties are averaged when several beams are coupled. On the other hand, the device occupies larger space, and power consumed by the Lorentz current is increased.

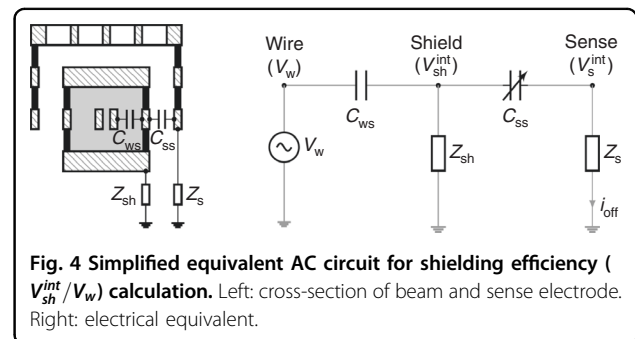
**Beam design: Offset prevention via shielding and Lorentz multiwire**

The two inherent offsets present in LFM which are caused by the Lorentz current were discussed in the introduction. In the present work both offsets and their drifts are prevented by design: firstly, the Lorentz wires are decoupled from the sensing electrodes using a metal shield around the wires as described in Sánchez-Chiva et al.<sup>46</sup>, and as depicted in the cross-section of the beam in detail F of Fig. 2, 4, 6a, b and 7. Secondly, a symmetric Lorentz wire routing design with respect to the shield electrode was adopted ( $R_{L1} = R_{L2}$  in Fig. 1). This way, the central point of the wire may be kept at constant voltage: half the total voltage drop ( $V_w = 0.5 \cdot V_{drop}$ ). Then, the interference due to the AC voltage in the rest of the wire would cancel out due to symmetry, apart from fabrication variability. Hence, little or no compensation techniques are needed. Later on, this will be shown experimentally.

Equation (11) was derived from the model in Fig. 4. It quantifies the shielding efficiency, which depends on the capacitance between the shield and the Lorentz wire ( $C_{ws}$ ), and on the impedance between the shield and its grounding ( $Z_{sh}$ ). As it turns out, the capacitance between the shield and the sense electrodes ( $C_{ss}$ ) is irrelevant in practice as its associated impedance is substantially larger



**Fig. 3 Deformation effect of stress gradient on clamped-clamped (c-c) beams.** FE simulations prove the negligible effect of stress gradient on c-c beams below the critical stress or temperature, above which the beam buckles. The critical temperature depends on the beam length, but for tensile stress (negative temperature increase) all beams remain flat regardless of stress gradient level.



**Fig. 4 Simplified equivalent AC circuit for shielding efficiency ( $V_{sh}^{int}/V_w$ ) calculation.** Left: cross-section of beam and sense electrode. Right: electrical equivalent.

than  $Z_{sh}$ . Typical values yield  $V_{sh}/V_w < 10^{-5}$ .

$$\frac{V_{sh}^{int}}{V_w} \approx j\omega \cdot Z_{sh} \cdot C_{ws} \quad (11)$$

The metal shield provides additional advantages: it encloses unetched oxide along with one or two metal layers that can be used to route several Lorentz wires ( $n_w > 1$ ) along each beam (multiwire beams), as illustrated in the beam cross-sections of Figs. 6a, b and 7. Increasing the Lorentz current per beam delivers several obvious benefits, such as sensor area reduction and improvement of sensitivity (Eq. (5)), signal-to-noise ratio (SNR) (Eq. (10)) or heading accuracy (Eq. (6)). The number of wires per beam has limitations, though, such as electromigration current, maximum resistance allowed for the Lorentz wire due to maximum supply voltage, or filtering limitations due to the impedance of the wire at high frequency. Usually, electromigration current is the limiting factor.

#### Electrostatic sensing/actuation techniques

A 3D magnetometer demands both vertical and horizontal sensing electrodes. Devices that vibrate vertically detect in-plane xy magnetic field. Conversely, devices that vibrate horizontally detect out-of-plane z magnetic field as Eq. (1) dictates. Fortunately, the CMOS BEOL layers allow multiple design options (see yellow electrodes of devices in Figs. 6a, b and 7). The cross-section of a typical lateral sensing design is depicted in the zoomed orange box of Fig. 7. Two different vertical sensing designs are shown in Figs. 6a, b. Note that the sensing electrode in Fig. 6a has a vertical flange at each end: they act as stiffeners that reduce potential curling caused by the stress gradient. One advantage of the vertical sensing design of Fig. 6b is that it allows to use 4 layer beams and, therefore, have 2 layers dedicated for the Lorentz current-carrying wires.

While a large variation of capacitance versus displacement is generally desired to maximize sensitivity (Eq. (5)), other aspects such as damping and Q factor need to be taken into account. In this respect, the aforementioned designs may be improved by adding through-holes that reduce air squeezing and, therefore, damping. The vertical sensing electrode shown in Figs. 2 and 6a has these holes, for instance. Also, two z devices with  $g = 0.35 \mu\text{m}$  with lateral sensing electrodes solid as in Fig. 7 showed Q factors 1.5 smaller than the same device with sensing electrodes formed by layers joined with standard vias which let the air flow through.

The sensing electrodes are supported on anchors at both ends and at intermediate points. They were designed as clamped-clamped structures given that this was found to be the most mechanical reliable design.

In order to maximize the ratio capacitance variation versus static capacitance, the sensing electrodes of some devices are only at the central part of the beams, where the vibration amplitude is maximum, as in the devices of Fig. 6a, b. This is also beneficial in terms of Q and, therefore, sensitivity enhancement.

#### Electrical routings

**Output from vHF area** The sensing electrodes are in the etched area. As a consequence, connecting them to the electronics while avoiding the capillarity effect and thus, containing the vHF, proved challenging. The technique used in this work takes advantage of the vHF etching anisotropy, as the anchors previously described do: The output routing describes a vertical zigzag as detail I of Fig. 2 and bottom-right cross-section in Fig. 7 show.

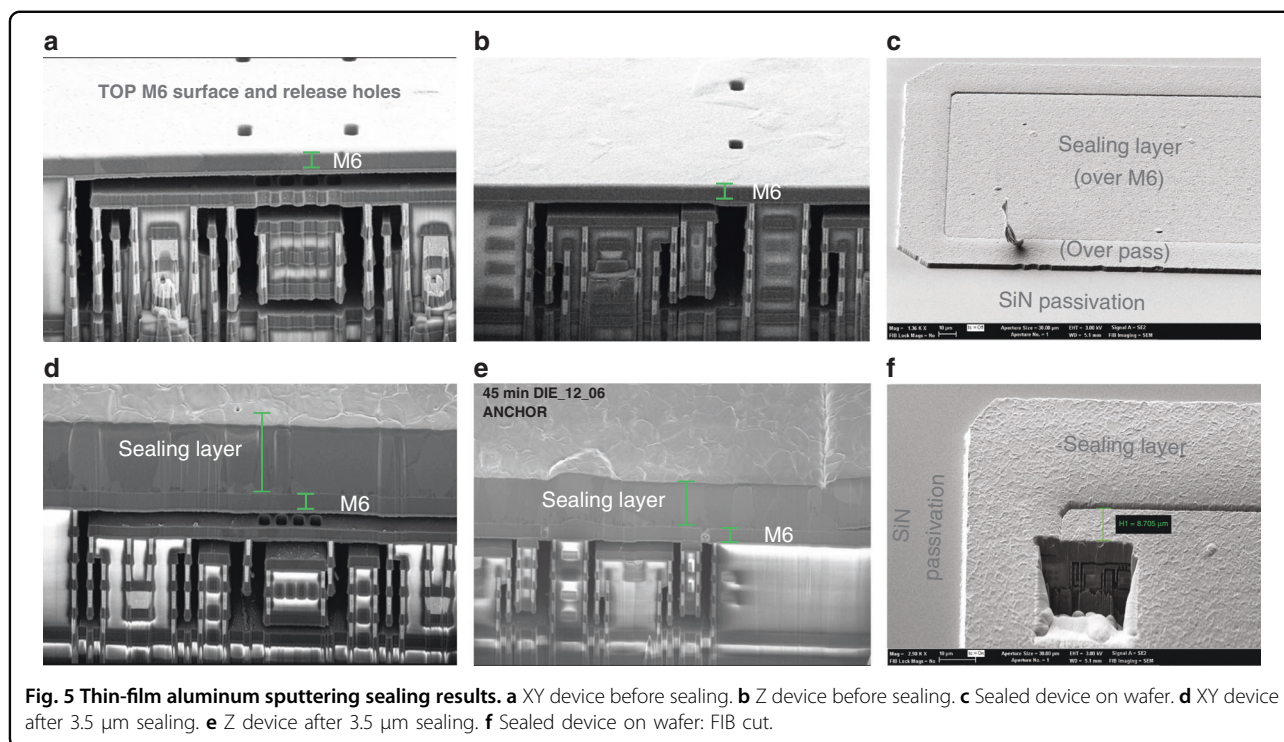
**Lorentz routing** The Lorentz wires run along all beams and each beam multiple times (detail B of Fig. 2), increasing this way, the total Lorentz force/current ratio substantially. For example, for a 6-beam 4-wire per beam device as in Fig. 6a, this ratio is increased 24 times with respect to a conventional LFM. Similar multiwire approaches were followed in some works<sup>32,48,49</sup>. This will achieve a very low noise floor, only surpassed when using piezoelectric amplification to increase SNR<sup>50,51</sup>.

Each Lorentz wire has a return path along the external parts of the device (detail B of Fig. 2). The magnetic field created by the Lorentz wires is at frequency much higher than Earth's magnetic field. Thus, their associated magnetic forces, detected by adjacent beams, should be mechanically filtered. In addition, the return wires are placed symmetrically at both sides of the device in such a way that the total magnetic force on the beams is zero.

Also, as already stated previously, the Lorentz wire was designed symmetric with respect to the shield electrode as a first step to suppress its interference and associated offsets.

Running many more wires along each beam could further increase the force vs current ratio, but ohmic resistance and available voltage impose limitations on the maximum length and minimum width of the Lorentz wires. For the case of devices with only one or few beams, electromigration is typically the bottleneck, and limits the minimum width for a given Lorentz current (around 1 – 2 mA/ $\mu\text{m}$  in the processes we have used).

The Lorentz wire impedance imposes another important limitation: it increases quite abruptly above a given pole frequency ( $\omega_{pole} = 1/(R_L C_{ws})$ ). The pole may fall close to the mechanical resonance frequency if the device has too many coupled beams and/or turns, or its resonance frequency is too high. For the devices considered in this work it is at least one order of magnitude above the resonance frequency, so it is not a problem.



**Fig. 5** Thin-film aluminum sputtering sealing results. **a** XY device before sealing. **b** Z device before sealing. **c** Sealed device on wafer. **d** XY device after 3.5 μm sealing. **e** Z device after 3.5 μm sealing. **f** Sealed device on wafer: FIB cut.

Joule heating due to the Lorentz current is proportional to the intensity squared and, therefore, takes place at twice the resonance frequency and higher, so its effects are filtered out both mechanically and electronically.

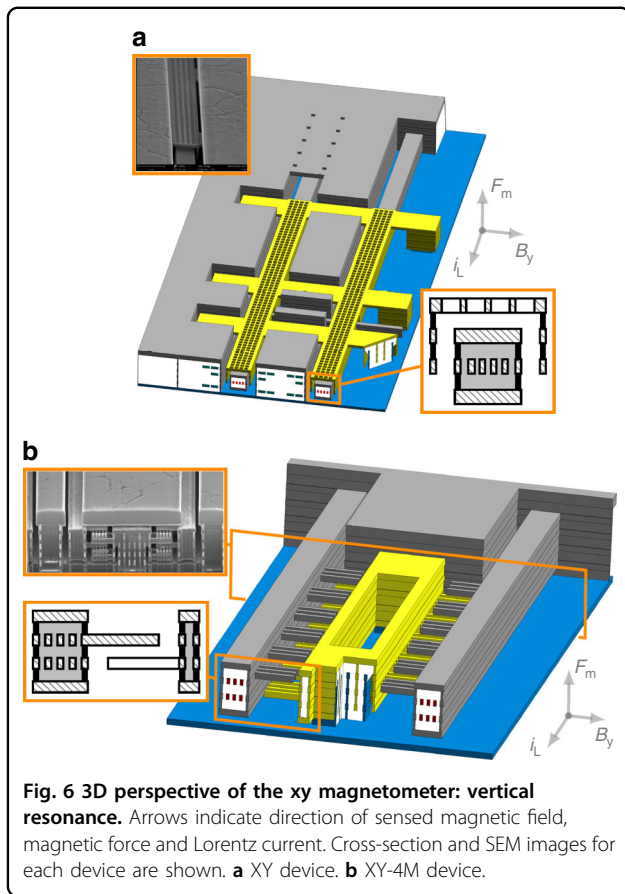
**Autocalibration routing** Some kind of autocalibration is generally very desirable in commercial sensors to compensate inherent sensitivity variations and/or offset drifts. We have implemented it by adding wires that run along the device in specific arrangements that can create a known magnetic field, or autocalibration magnetic field. These can be seen in the white oxide areas cut in Fig. 6a. The horizontal field that is created can be seen in detail C of Fig. 2. When it is activated the sensitivity can be measured and readjusted, which is a unique feature of the presented LFM. The coupled-beam arrangement of our devices allows to add the autocalibration routing very close to the Lorentz wires, reducing this way the power consumed by the autocalibration field.

**M6 capping and sealing layer to protect MEMS before wafer sawing and packaging**

The packaging step in MEMS products will often be the decisive one in terms of yield. The design must be robust enough to withstand wafer sawing, manipulation and final encapsulation conditions<sup>32</sup>. Our magnetometers have a robust design but additional mechanical

protection is given by covering the whole device with the top metal layer (M6 capping), as seen in detail E of Fig. 2. In Fig. 6a most of the top metal covering has been hidden to show the structures underneath. The top metal layer is grounded and it is part of the electrical shield.

Enhanced performance requires vacuum encapsulation. This is achieved with an outgassing step followed in less than 4 hours by a 3 μm aluminum sputtered layer (sealing layer) deposited on top of the device, which takes place at around 6.4 μbar. The outgassing step consists on heating the wafer at the highest temperature possible while the wafer is kept at very low vacuum, so the outgassed and sublimated species are pumped out. The CMOS process and the MEMS yield limit the maximum temperature that can be applied<sup>7,8</sup>. In our case, we chose 350 °C. This step reduces the trapped gasses in the materials exposed to the MEMS cavity and therefore reduces future outgassing processes. The sealing aluminum layer is preceded by a titanium/titanium nitride thin layer. The deposition temperature needs to be high in order to improve step coverage and seal the release holes properly. We found that 0.9 μm holes are sealed well with a 2 μm-thick layer deposited at 350 °C. However, 3 μm-thick layers were used for improved safety margin. This is shown in the focused-ion-beam (FIB) cuts of Fig. 5 and detail D in Fig. 2. The sealing layer provides additional mechanical robustness for further post-processing, along with a very good vacuum level that will be partially lost



after final packaging due to outgassing. It is patterned and etched using the passivation SiN (detail A in Fig. 2) as the etch barrier. Only the pads and the devices remain covered.

At this point the wafer is ready for undergoing standard packaging processes. In our case, the wafers were subjected to a back-grinding process to decrease their thickness down to 280 μm, and divided into dice with standard wafer sawing at an assembly house. Then, each die was attached to a leadframe, wire-bonded and over-molded to obtain the final QFN packaged devices. Characterization and yield measurements of QFN devices are later presented in this work.

**Variants of Lorentz-force CMOS-MEMS magnetometers**

The three variants analyzed in this work are shown in Figs. 6a, b and 7. The first two are xy devices that detect in-plane magnetic field and, therefore, resonate vertically. The third one is a z device that detects out-of-plane magnetic field and vibrates horizontally. For each type, devices with beam lengths from 80 μm to 800 μm were manufactured. This allowed to characterize and obtain reliability data as a function of the beam length.

The z device is comprised of 4-metal stack beams. The two xy devices have different sensing techniques: parallel plate in the xy device and fingers in the xy-4m device. Also, the number of stacked layers differs: 3 layers in the xy device versus 4 layers in the xy-4m device. These differences will be critical in terms of reliability as it will be demonstrated later. Most of the characterization, analysis and tests were focused on the z and xy variant.

**Modeling**

In this section, the resonance frequency of clamped-clamped beams, which comprise the basic elements of the fabricated LFMs, is expressed as a function of design parameters and residual stress/temperature. Additionally, an electrical model of the MEMS, used for measuring the LFMs is developed and the most important equations derived.

**Resonance frequency**

The resonant frequency of beams under no axial load is very well known (Blevins and Plunkett<sup>52</sup>, Table 8-1):

$$f_0 = \frac{\lambda_i^2}{2\pi L^2} \left( \frac{EI}{m} \right)^{\frac{1}{2}} = \frac{4.73004^2}{2\pi L^2} \left( \frac{Et^2}{12\rho} \right)^{\frac{1}{2}} \quad (12)$$

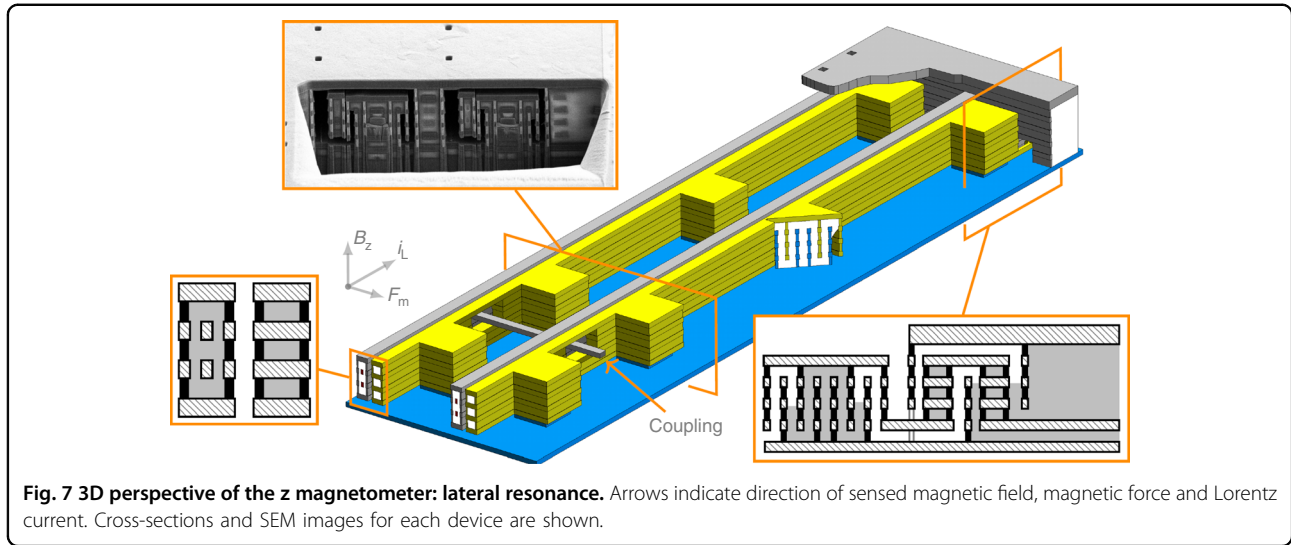
where *i* is the mode number, *E* the Young’s Modulus, *I* the moment of inertia ( $I = wt^3/12$  for a rectangular section of thickness *t* and width *w*), *m* the mass per unit length,  $\rho$  is the density and  $\lambda = 4.73004$  for the fundamental mode (*i* = 0) and clamped-clamped conditions.

However, dealing with axially stressed clamped-clamped structures is very common in MEMS and nano design. Tensile stress ( $\sigma > 0$ ) increases the resonant frequency and compressive stress ( $\sigma < 0$ ) decreases it. This is a very important effect in MEMS structures that often renders Eq. (12) insufficient for correct predictions. For a given compressive load, called the critical load ( $F_{cr}$ ) the resonant frequency is zero and the beam buckles due to elastic instability<sup>53</sup>. The most accurate formula that describes the frequency-residual stress dependency was given by the authors<sup>54</sup>. It works in the full range of axial load ( $F_a$ ), from the buckling point to the high-tension or string-limit regime:

$$f_r(F_a) \approx f_0 \left( 1 + \gamma \frac{F_a}{F_{cr}} + \frac{1}{\frac{1}{\alpha\gamma} \frac{F_{cr}}{F_a} + \frac{1}{\beta}} \right)^{\frac{1}{2}} \quad (13)$$

where  $f_0$  is given by Eq. (12) and  $\alpha = 0.19514$ ,  $\beta = 1.2114$  and  $\gamma = 0.81626$  are given in Table A.3 of Valle et al.<sup>54</sup> for the fundamental mode and clamped-clamped (c-c) conditions. The buckling axial load of a rectangular c-c





**Fig. 7 3D perspective of the z magnetometer: lateral resonance.** Arrows indicate direction of sensed magnetic field, magnetic force and Lorentz current. Cross-sections and SEM images for each device are shown.

beam is  $F_{cr} = 4\pi^2 EI/L^2 = \pi^2 wt^3 E/(3L^2)$ , as can be found in Table A.3 and Appendix C of Valle et al.<sup>54</sup>. Eq. (13) will be used to explain the observed resonant frequency of c-c beams as a function of their length and their temperature. By using Eq. (13), the dependence with beam length can be obtained, yielding:

$$f_r \propto \frac{t}{L^2} \sqrt{\frac{E}{\rho}}, \text{ when } |F_a| \ll F_{cr} \text{ (Small stress)} \quad (14a)$$

$$f_r \propto \frac{1}{L} \sqrt{\frac{\sigma}{\rho}}, \text{ when } |F_a| \gg F_{cr} \text{ (Stress – dominated)} \quad (14b)$$

Note that for the beams where the tension/residual stress is the main contributor to their stiffness, the Young’s Modulus  $E$  or the beam thickness  $t$  are of no importance for  $f_r$  dependence with beam length  $L$ .

By assuming that the thickness  $t$ , the density  $\rho$  and the length  $L$  of the beam are constant, the resonance frequency dependence on temperature can be readily derived from Eq. (14a) and (14b):

$$f_r \propto \sqrt{E(T)}, \text{ when } |\sigma| \ll \sigma_E \text{ (Small stress)} \quad (15a)$$

$$f_r \propto \sqrt{\sigma(T)}, \text{ when } |\sigma| \gg \sigma_E \text{ (Stress – dominated)} \quad (15b)$$

An implicit temperature-dependency on  $E$  and the linear coefficient of thermal expansion (CTE) is contained in Eq. (15b): the axial stress  $\sigma$  of a clamped-clamped beam depends on temperature due to the thermal expansion/

contraction as:

$$\begin{aligned} \sigma(T) &= \sigma(T_0) - \int_{T_0}^T \sum_i E_i (\alpha_i - \alpha_{subs}) \frac{A_i}{A} dT \\ &\approx \sigma(T_0) - \sum_i E_i (\alpha_i - \alpha_{subs}) \frac{A_i}{A} (T - T_0) \end{aligned} \quad (16)$$

where sub-index  $i$  refers to the material number,  $\alpha$  is the beam CTE,  $\alpha_{subs}$  is the substrate CTE,  $A_i/A$  is the fraction of the beam section occupied by material  $i$ , and  $\sigma(T_0)$  is the stress at an arbitrary reference temperature  $T_0$ . Note that the substrate also expands/contracts and needs to be taken into account in the  $f_r(T)$  calculation. Typically, the Young Modulus decreases with temperature and the CTE increases with temperature. Interestingly, their product  $E \cdot \alpha$  for the CMOS BEOL materials remains approximately constant, so the integral in Eq. (16) may be substituted by the temperature increment  $(T - T_0)$ .

**MEMS electrical model**

The equation of motion of a driven damped spring-mass system is:

$$M \frac{\partial^2 x}{\partial t^2} + D \frac{\partial x}{\partial t} + Kx = f(t) \quad (17)$$

where  $M$ ,  $D$  and  $K$  are the mass, damping coefficient and stiffness of the system, respectively, and  $f(t)$  is the applied lumped force.

Let’s now assume we apply a voltage  $V = V_{DC} + V_{AC}$  to a movable capacitor of capacitance  $C$  that is part of a damped spring-mass system, as in Fig. 1. The current flow

through this moving capacitor is:

$$i = \frac{\partial CV}{\partial t} = C \frac{\partial V}{\partial t} + V \frac{\partial C}{\partial t} = C \frac{\partial V_{AC}}{\partial t} + \eta \frac{\partial x}{\partial t} \quad (18)$$

where  $\eta = V_{DC} \frac{\partial C}{\partial x}$  is the electromechanical coupling factor.

The component of the electrostatic force between the plates of the capacitor at the frequency of  $V_{AC}$  is:

$$F_e = \frac{\partial U}{\partial x} = \frac{\partial}{\partial x} \left( \frac{1}{2} CV^2 \right) = V_{AC} V_{DC} \frac{\partial C}{\partial x} = \eta V_{AC} \quad (19)$$

It is important to note that  $x$  refers to the lumped displacement used in Eq. (17), which is 0.542 times the central displacement  $x_c$  of the real clamped-clamped beam. This value was calculated so that  $\frac{\partial C}{\partial x} = \frac{\partial C_c}{\partial x_c}$ , where  $C_r$  and  $C$  are the real and lumped capacitance values, respectively. This way, the lumped force  $f(t)$  is equivalent to the total uniform load acting on the real beam. This is convenient for the considered devices given that the electrostatic and the Lorentz forces are applied uniformly along the beam span.

Now, defining  $i_{mot} = \eta \frac{\partial x}{\partial t}$  and substituting into Eq. (17) yields:

$$\frac{M}{\eta} \frac{\partial i_{mot}}{\partial t} + \frac{D}{\eta} i_{mot} + \frac{K}{\eta} \int i_{mot} dx = f(t) \quad (20)$$

Let us assume that the force  $f(t)$  is the combination of the electrostatic force  $F_e$  produced by the measurement signal of an impedance analyzer and a magnetic force  $F_m$ . If we define the ratio  $\Omega = F_m/F_e$ , by substitution of  $f(t)$  into Eq. (20) it is straightforward to arrive at:

$$\begin{aligned} \frac{M}{\eta^2(1+\Omega)} \frac{\partial i_{mot}}{\partial t} + \frac{D}{\eta^2(1+\Omega)} i_{mot} + \frac{K}{\eta^2(1+\Omega)} \int i_{mot} dx \\ = L_m \frac{\partial i_{mot}}{\partial t} + R_m i_{mot} + \frac{1}{C_m} \int i_{mot} dx = V_{AC} \end{aligned} \quad (21)$$

which corresponds to an inductor  $L_m$ , capacitor  $C_m$  and resistor  $R_m$  in series. They represent the motional inductance, capacitance and resistance values of the MEMS, respectively, and they simulate the mechanical dynamics of the MEMS resonator. It is the dashed region of the MEMS electrical model<sup>55</sup> used to fit the measurements in this work (see Fig. 8a, b).

Components  $R_p$  and  $C_p$  in Fig. 8b generally represent the physical electrical resistance and capacitance between the shield and sense electrodes. However, under some circumstances, the conductance  $G$  and/or susceptance  $B$  seen from

nodes 1-2 may show calibration/interference offsets that are absorbed by  $R_p$  and  $C_p$ , respectively. In this case, they no longer represent the physical resistance and capacitance of the MEMS. In practice these offsets are unavoidable when there is capacitive coupling between the Lorentz current wire and the sensing electrodes, as described in this and many other works<sup>31,33,41,46</sup>. In this work, the Lorentz wire is coupled only to the shield electrode, as shown in the explicit electromechanical model of Fig. 8a. This creates an interference signal created by  $V_w$  which changes the admittance of the system seen from 1-2. Fortunately, the model in Fig. 8a can be simplified to the model in Fig. 8b, which is the one used in this work to fit G-B curves of MEMS magnetometers with and without interferences. When no current flows through the Lorentz wire there is no interference and  $C_{ss} = C_p$  and  $R_p \rightarrow \infty$ . Finally, the impedance components  $Z_{sh}$  and  $Z_s$  represent the output impedance of the measurement instrument connected to 1-2.

The motional parameters are related to the mechanical and electrical properties of the system and also to  $\Omega$ . This is described by the following equations, derived from Eq. (21):

$$R_m = \frac{D}{\eta^2} \frac{1}{1+\Omega} = \frac{\sqrt{KM}}{Q} \frac{1}{\eta^2} \frac{1}{1+\Omega} \quad (22)$$

$$L_m = \frac{M}{\eta^2} \frac{1}{1+\Omega} \quad (23)$$

$$C_m = \frac{\eta^2}{K} (1+\Omega) \quad (24)$$

where  $Q$  is the quality factor of the system:

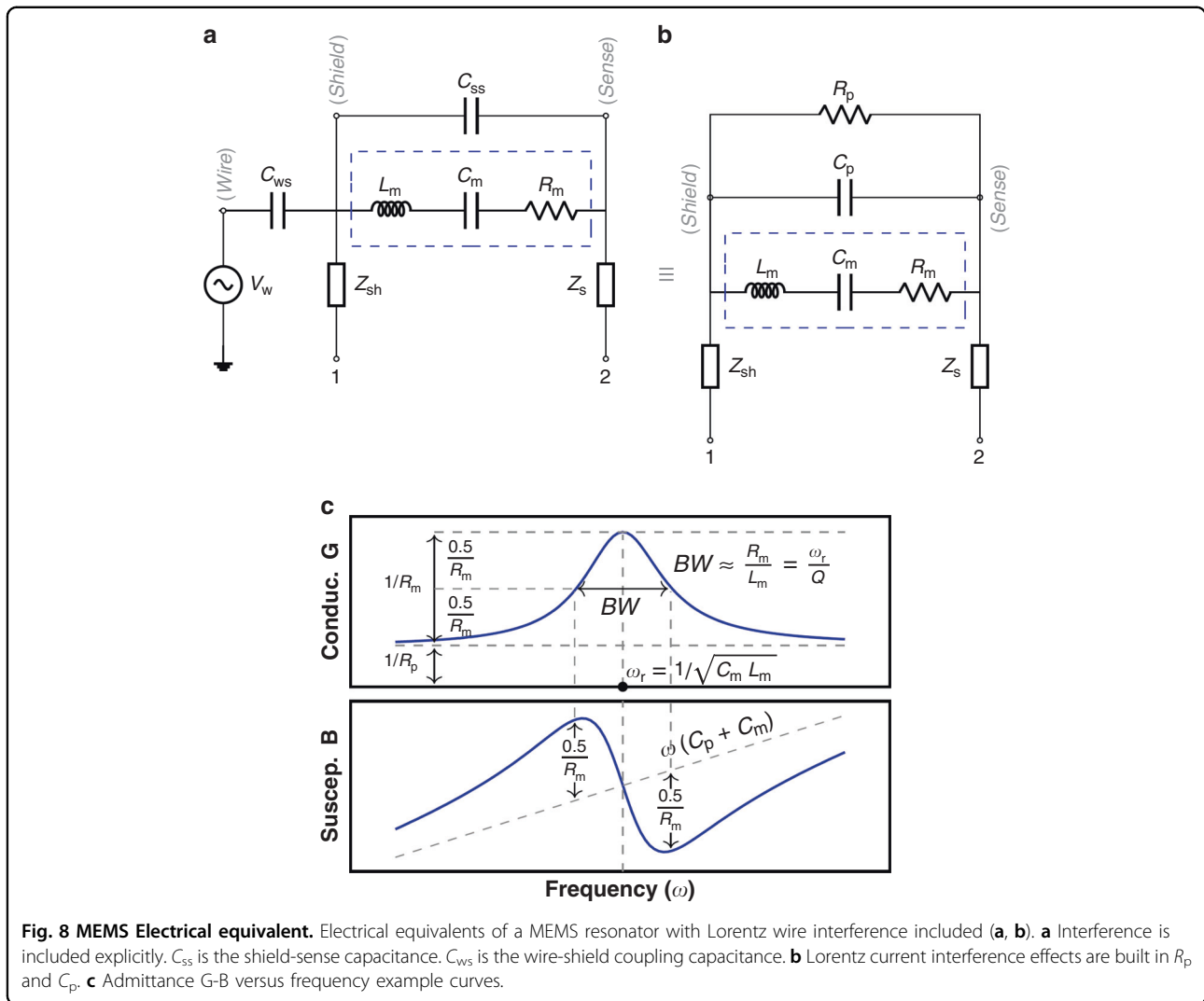
$$Q = \frac{1}{R_m} \sqrt{\frac{L_m}{C_m}} \quad (25)$$

Note that the  $L_m$ ,  $C_m$  and  $R_m$  values seen by an impedance analyzer depend on the ratio  $\Omega = F_m/F_e$ . When the MEMS is just characterized with an impedance analyzer, and no Lorentz current is injected  $F_m = 0$  and so  $\Omega = 0$ . In some experiments in this work  $F_m \neq 0$ , like when extracting the sensitivity of the MEMS to magnetic fields.

In addition, the magnetometer sensitivity (Eq. (5)) may be rewritten in a much simpler form as a function of the parameters measured directly with the IA in units of Amperes per Tesla (A/T):

$$S = \frac{\partial i_m}{\partial B} = \frac{\Omega V_{AC}}{R_m B} \quad (26)$$

which can be further generalized in units of Amperes per Tesla and per DC voltage and Lorentz current used



(A/(TV A)), as Eq. (5) shows:

$$S' = \frac{S}{V_{DC}i_L} = \frac{\Omega V_{AC}}{R_m B V_{DC} i_L} \tag{27}$$

lumped vibration amplitude  $X$  may be written as:

$$X(\omega) = \frac{f(\omega)}{\omega \eta^2} \left( G - \frac{1}{R_p} + j(B - \omega C_p) \right) \tag{30}$$

The admittance ( $Y = G + jB$ ) of the circuit in Fig. 8b is:

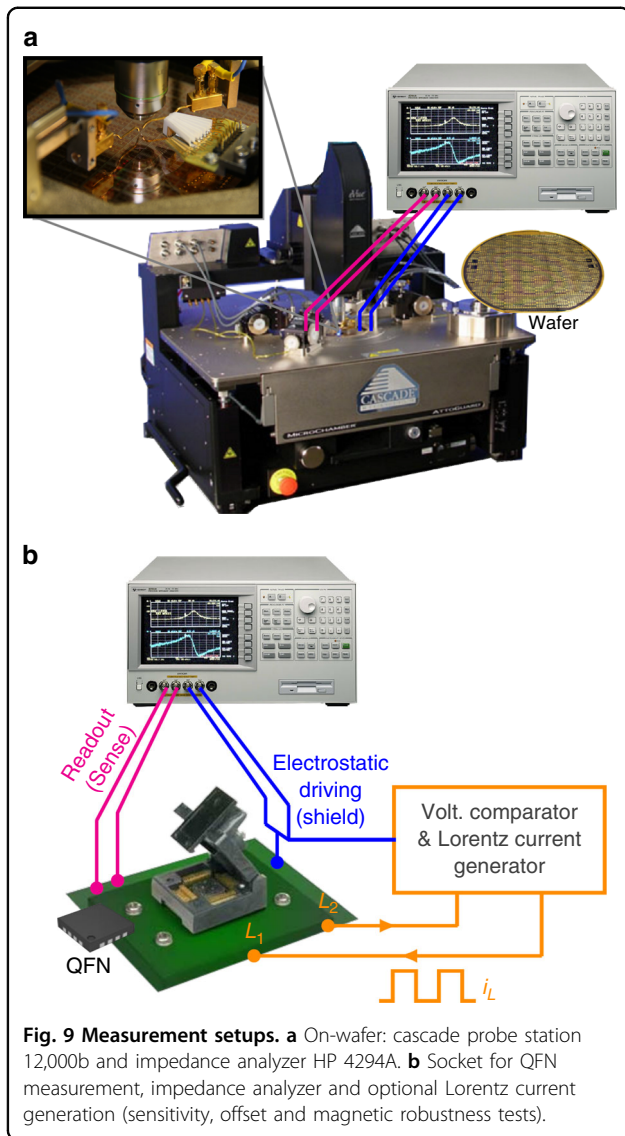
$$G(\omega) = \frac{1}{R_p} + \frac{R_m}{R_m^2 + \left( \omega L_m - \frac{1}{\omega C_m} \right)^2} \tag{28}$$

$$B(\omega) = \omega C_p - \frac{\omega L_m - \frac{1}{\omega C_m}}{R_m^2 + \left( \omega L_m - \frac{1}{\omega C_m} \right)^2} \tag{29}$$

where  $\omega$  is the angular frequency. The G-B curves are plotted in Fig. 8c along with the circuit parameters that determine their shape. Finally, and for completeness, the

**Methodology and measurement setup**

The status of the CMOS-MEMS devices was assessed by measuring their admittance vs frequency curve  $Y(\omega)$ , also called G-B curve, that typically showed a resonance peak. Then, the electrical equivalent of Fig. 8b was fitted to the obtained curve. This allows accurate extraction of important electrical and mechanical parameters of the beams, like their resonant frequency and residual stress, or the capacitance between the beam and the excitation electrode, from where the status of the beam can be inferred. For example, a deformed beam which is touching the adjacent metal electrode would lead to



higher conductance and no resonance peak at the expected frequency.

The G-B curve measurement was carried out with impedance analyzers (HP 4294A or Agilent E4990A) set to measure admittance values. Radio-frequency (RF) probes were used for a reduced measurement noise. A Cascade probe station 12000b was used for wafers or single dice (Fig. 9a). A socket was used for packaged samples (Fig. 9b). The beams were excited with a sinusoidal test voltage ( $V_{AC}$ ) superimposed to a DC bias voltage ( $V_{DC}$ ), which results in an excitation force at the frequency of  $V_{AC}$  (as in Eq. (3)). The test voltages were applied between the beams and an electrode (used for both driving and sensing) which was placed either on top of the beam for exciting vertical out-of-plane vibration, or to one side for horizontal in-plane vibration excitation. In order to achieve the cleanest resonance curve, the test

voltages were adjusted experimentally depending on a number of factors, such as quality factor, resonance frequency, parasitic capacitance, noise level and non-linear behavior of the system. A Lorentz current in phase with the AC voltage of the impedance analyzer was injected into the MEMS in some tests, namely sensitivity, offset and magnetic robustness tests. This was accomplished with the system described in Sánchez-Chiva et al.<sup>56</sup>, for which additional equations were derived in the MEMS electrical model section: the  $\Omega$  parameter plays a key role in all the tests with injected Lorentz current while the IA measures the device.

## Results and discussion

### Characterization

#### *Q* factor versus pressure, resonance frequency/beam length: $Q(P, f_r)$

Accurate on-wafer characterization of the *Q* factor as a function of the pressure ( $P$ ) was carried out. Please see the Supplementary Material for a detailed explanation. This allowed to extract a formula that predicts very well the *Q* factor as a function of the device type, resonance frequency  $f_r$  and pressure that may range from 1  $\mu$ bar to ambient pressure. At pressures over a few millibar, only air damping needs to be considered. At lower pressures, thermoelastic damping becomes dominant and the measured quality factors reach a plateau. The total *Q* factor can be expressed as:

$$\frac{1}{Q_{total}} = \frac{1}{Q_{air}} + \frac{1}{Q_{TED}} \quad (31)$$

where  $Q_{TED}$  is given in Table 1 and  $Q_{air}$  is:

$$Q_{air} = \gamma \frac{f_r}{\mu_0} \left( 1 + \frac{5\lambda_g}{L_c} \right) \left( 1 + \frac{f_r^2}{f_c^2} \right) \quad (32)$$

where the pressure dependence is contained in the mean free path length of the gas molecules  $\lambda_g \propto T/P$ . The dynamic viscosity of the gas ( $\mu_0 = f(T)$ ) may be considered independent of  $P$ . The characteristic length of the structure ( $L_c$ ) is considered to be equal to the sensing gap  $g$ . Table 2 provides experimental data for five different devices. It shows  $\gamma$ , which is a proportionality parameter that depends on the considered geometry. It also shows  $f_c$ , which is the cut-off frequency.

#### Resonance frequency versus length: $f_r(L)$

The resonance frequency of magnetometers built using clamped-clamped beams of different lengths is plotted in Fig. 10. It was done for two types of beam sections and for out-of-plane and in-plane vibrations. The obtained data was fitted using Eq. (13). The fit is very good, which indicates that both the Young's Modulus ( $E$ ) and the

tension ( $P_a$ ) (or residual stress  $\sigma_r = P_a/(wt)$ ) are independent of the beam length.

Optical methods have been used to determine  $E$  and  $\sigma_r$  in several studies<sup>57–59</sup>. Unfortunately, these methods can only extract the effective values of composite beams applicable to the out-of-plane direction. Fortunately, the presented method also allows determination along the in-plane direction.

Three mechanical behaviors or regions may be distinguished in Fig. 10: one, for short beams or low tensile stress ( $|P_a/P_{cr}| < 0.2$ ), where the resonant frequency ( $f_r$ ) mostly depends on the value of  $E$  and it is inversely

proportional to the length squared<sup>52</sup>; another one for long beams or high tensile stress ( $|P_a/P_{cr}| > 50$ ), where it mostly depends on  $P_a$  or  $\sigma_r$  and it is inversely proportional to the length, like for cables and membranes<sup>52</sup>; and a third mixed region ( $0.2 < |P_a/P_{cr}| < 50$ ) in which neither  $E$  nor  $P_a$  can be neglected. The dependence for the 3 regimes may be readily derived from Eq. (13) by taking the appropriate limits. Note that longer beams converge to the same  $f_r$  because their ratio  $\sigma_r/\rho$  is very similar, by coincidence. Longer beams were not fabricated due to die size limitation, so the maximum length achievable remains to be studied.

Remarkably, beams as long as 800  $\mu\text{m}$  remained functional, indicating a planarity better than 0.9  $\mu\text{m}$  (vertical gap between beam and electrode). Tensile residual stress was used in order to achieve long structures that are planar and robust against temperature excursions as was put forward in Fig. 3 and confirmed with these measurements.

**Table 1 Quality factors due to TED ( $Q_{TED}$ ) used in Eq. (31)**

L =	100 $\mu\text{m}$	200 $\mu\text{m}$	400 $\mu\text{m}$	800 $\mu\text{m}$
Z devices $Q_{TED} =$	2850	8200	23,000	50,000
XY devices $Q_{TED} =$	1090	1160	2000	7000

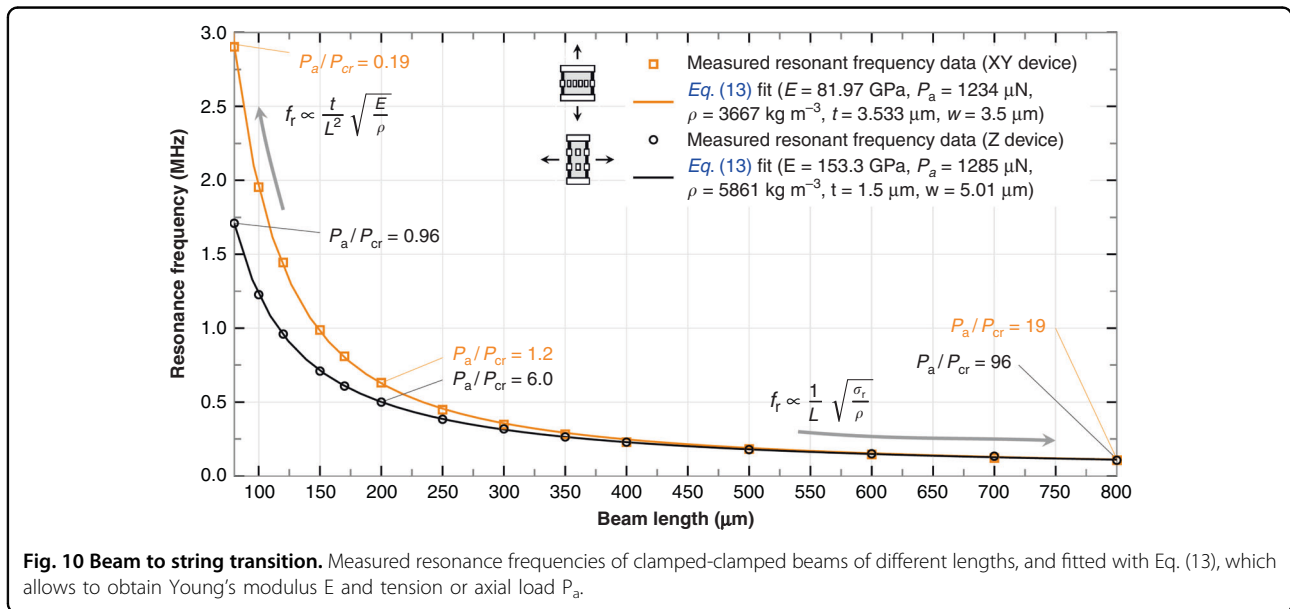
**Table 2 Parameters used in Eq. (32)**

	$L_c(\text{nm})$	$\gamma$	$f_c(\text{MHz})$
Z devices	350	$4.4 \times 10^{-11}$	$\approx 1.6$
Z devices*	350	$6.8 \times 10^{-11}$	$\approx 2.3$
Z devices	500	$8.3 \times 10^{-11}$	$\approx 2.0$
Z devices	1000	$26 \times 10^{-11}$	$\approx 2.0$
XY devices**	900	$35 \times 10^{-11}$	$\approx 3.0$

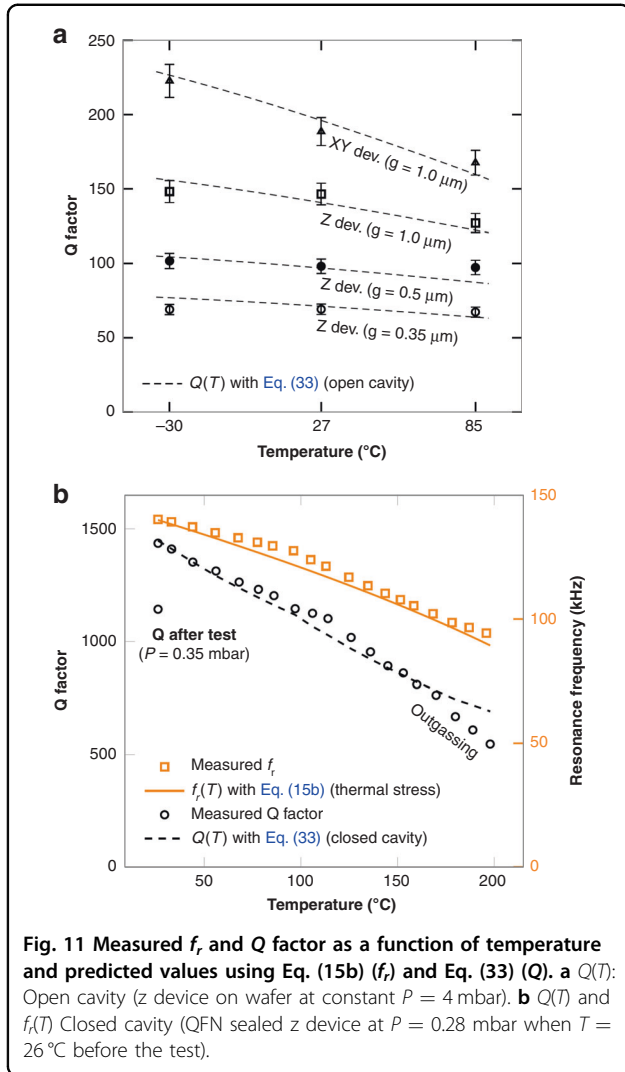
\*Sensing electrode is hollow between layers and air can flow through. \*\*Double vertical gap of 900 nm, one of them with holes and air can flow through

**Temperature experiments:  $Q(T)$  and  $f_r(T)$**

**Maximum working temperature. Closed cavity** A QFN z device (600  $\mu\text{m}$ -long beams with 0.5  $\mu\text{m}$  gap) was put inside an oven and heated up from 26  $^\circ\text{C}$  to 198  $^\circ\text{C}$  in steps of 10  $^\circ\text{C}$ , approximately. Its resonance curve was permanently monitored. After each temperature increment we waited between 20 min and 34 min until a stable temperature was reached and then the measurement was taken. Finally, the device was removed from the oven and measured again. The obtained resonance frequencies and Q factors are plotted in Fig. 11b. The device survived the test.



**Fig. 10 Beam to string transition.** Measured resonance frequencies of clamped-clamped beams of different lengths, and fitted with Eq. (13), which allows to obtain Young’s modulus  $E$  and tension or axial load  $P_a$ .



The resonant frequency decreased as the temperature rose. Eqs. (15b) and (16) predicted well the resonant frequency as it can be appreciated in Fig. 11b. It is important to note that the CTE of the substrate (Silicon) must be included in the equation. Otherwise, the  $f_r$  decrease with temperature will be overestimated. Constant values of  $E_i\alpha_i$  with respect to temperature can be assumed given that this is approximately true for the BEOL materials of the beams. If not, their temperature dependence ( $E_i(T)$  and  $\alpha_i(T)$ ) should be introduced into Eq. (16).

Equation (32), with the values listed in Table 2 for the xy device with  $g = 0.5 \mu\text{m}$  and  $Q = 1436$ , was used to estimate the pressure inside the cavity at the beginning of the experiment: 0.28 mbar. At this pressure, TED ( $Q_{TED} > 20000$ ) is negligible compared to air damping ( $Q_{air} \approx 1400$ ). Therefore, we can rely on Eq. (32), rather than Eq. (31), for Q estimation and its dependence with temperature. In addition, the  $f_c$  effect may be neglected in

Eq. (32) given that  $f_r \ll f_c$  ( $f_r = 140$  kHz and  $f_c$  is in the order of MHz). In a closed cavity, the pressure increases linearly with temperature. So, the mean free path  $\lambda_g \propto T/P$  remains constant when the temperature changes. Contrarily, in an open cavity at constant pressure the mean free path increases linearly with temperature. By taking into account these considerations, Q factor dependence with temperature may be greatly simplified from Eq. (32) to:

$$Q \propto \frac{f}{\mu_0} \cdot \lambda_g \propto \begin{cases} \frac{f}{\mu_0} & \text{Closed Cavity} \\ & (\lambda_g \propto T/P = \text{constant}) \\ \frac{f}{\mu_0} \cdot T & \text{Open Cavity} \\ & (\lambda_g \propto T, P = \text{constant}) \end{cases} \quad (33)$$

Note that this approximation is not valid in other situations such as lower pressures where TED dominates, in the fluidic regime or when the resonant frequency is comparable to the cut-off frequency. Full Eq. (31) shall be used in those cases.

Initially, the gas in the cavity is Argon at  $1.4 \mu\text{bar}$ . But during the packaging steps other species are out-gassed, the pressure rises to generally more than  $100 \mu\text{bar}$  and the final gas composition is unknown. In Eq. (33), the viscosity of the surrounding gas  $\mu_0$  was calculated using the formulas given in Lemmon and Jacobsen<sup>60</sup> for air. For Nitrogen or using the tabulated values for air<sup>61</sup> the results are very similar. The viscosity of gases increases with temperature and it does not change appreciably with pressure.

Using Eq. (33) the predicted Q factor as a function of the temperature is plotted in Fig. 11b. The measured values follow the curve within 7%, except in the higher temperature region, where the measured Q factor drops appreciably. This indicates that, above  $150 - 175^\circ\text{C}$  an outgassing mechanism is exacerbated. As a consequence, after the device is cooled down to room temperature, the Q factor decreased from 1436 to 1146, indicating that the pressure inside the cavity increased from 0.28 mbar to 0.35 mbar, approximately. The datapoint at  $198^\circ\text{C}$  can be used to estimate the Q factor once the device returns to room temperature using the closed cavity case of Eq. (33):

$$\begin{aligned} Q^{26^\circ\text{C}} &= Q^{198^\circ\text{C}} \frac{f^{(26^\circ\text{C})}}{f^{(198^\circ\text{C})}} \frac{\mu_0^{(198^\circ\text{C})}}{\mu_0^{(26^\circ\text{C})}} = 546 \frac{138}{94.3} \frac{25.95}{18.47} \\ &= 1123 \approx Q_{\text{measured}}^{26^\circ\text{C}} = 1143 \end{aligned} \quad (34)$$

which confirms the validity of Eq. (33) for a closed cavity.

**Maximum working temperature. Open cavity** In order to confirm the validity of Eqs. (32) and (33) for the open cavity case, four devices were measured at wafer level before sealing at 3 different temperatures and constant pressure (4 mbar). The results are shown in Fig. 11a. The error bars represent the Q uncertainty caused by the

measurement noise. The dashed lines predict reasonably well the measured data and were produced using Eq. (32) and a  $\gamma$  10% smaller than in Table 2. Note that the on-wafer measured devices are from a wafer and lot different from the QFN devices, and that 10% is accountable for the expected process variability. In the open cavity case the Q factor variation is significantly smaller (around 2-4 times) than for the QFN devices (closed cavity). The reason is that the  $f_r/\mu_0$  variation is partially compensated by the mean free path variation in the open cavity case, as Eq. (33) shows.

**Performance**

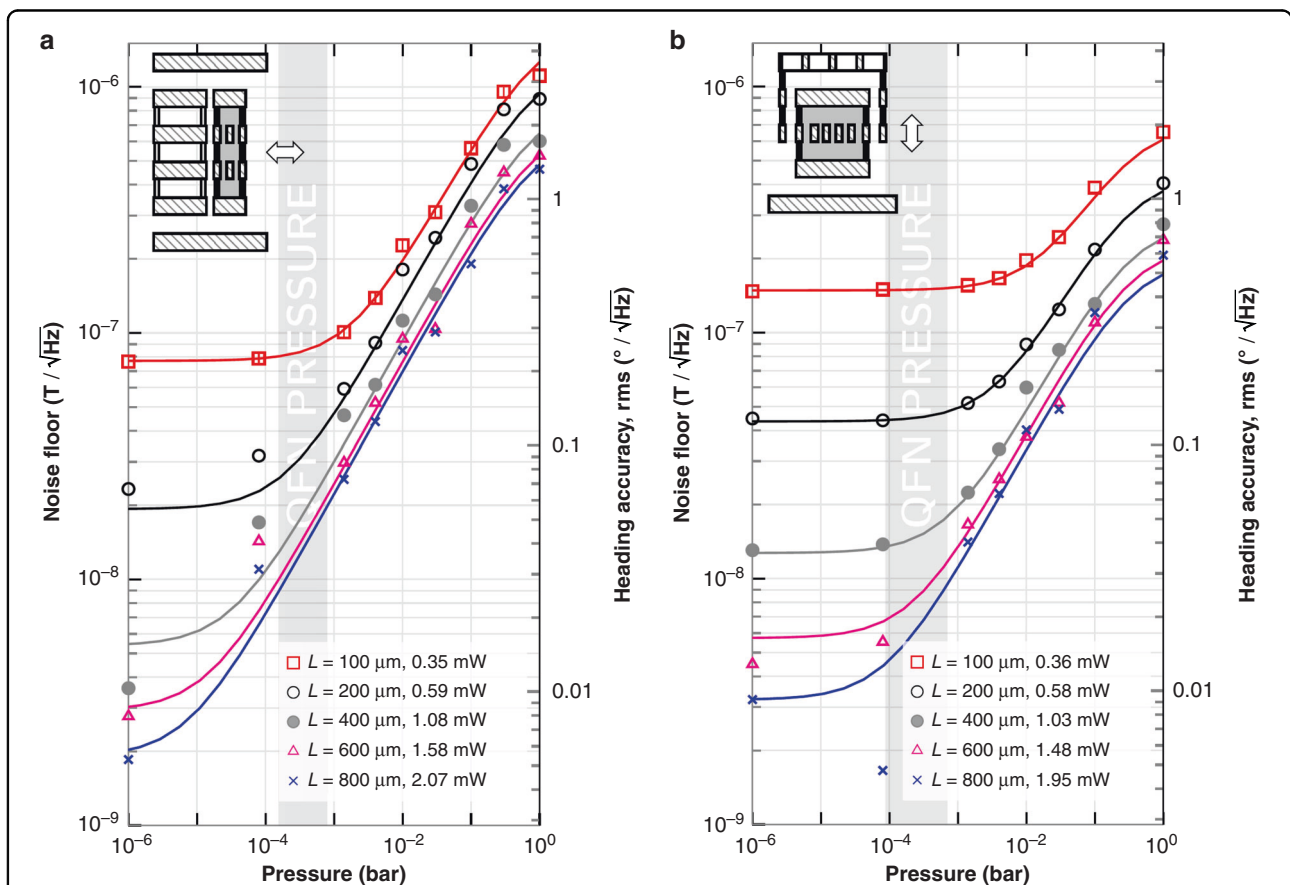
**Noise and heading accuracy**

The heading accuracy (Eq. (6)) and the noise floor (Eq. (10)) were plotted in Fig. 12 using data from the measured devices, which provided  $Q$ ,  $f_r$ ,  $\eta$  and  $L_m$ . The continuous curves were calculated using the Q factor from Eq. (31) and the lumped mass  $M = \eta^2 L_m$  obtained from the G-B

curves of the measured devices. The lowest limiting noise values are around  $2 - 3 \text{ nT}/\sqrt{\text{Hz}}$  and the best case heading accuracy around  $0.006^\circ 1/\sqrt{\text{Hz}}$  were achieved for the z and xy longest devices, and using a Lorentz current of  $600 \mu\text{A}$ . These numbers are state-of-the-art for 3 axis Lorentz-force magnetometers: the lowest noise ( $10 \text{ nT}/\sqrt{\text{Hz}}$ ) had been reported in Kyynarainen et al.<sup>32</sup>. They could be potentially improved with longer beams or higher current which does not necessarily mean higher power consumption if the Lorentz wires are designed wider accordingly. Actually, small improvements ( $< 10$ ) can be achieved by increasing the width of the wires.

The approximate pressure range of a QFN device is depicted in Fig. 12 as a light gray area. For QFN devices, the lowest noise values are around  $7-10 \text{ nT}/\sqrt{\text{Hz}}$  with an associated heading accuracy around  $0.02-0.03^\circ 1/\sqrt{\text{Hz}}$ .

The Lorentz current can be reduced as desired and the noise floor will increase linearly. The dissipated power ( $\propto I^2 R$ ) will decrease quadratically. So the system may be run in a number of different configurations depending on



**Fig. 12** MEMS brownian noise floor and heading accuracy versus pressure for different designs. Lorentz current = 0.6 mA (square wave), which respects maximum electromigration current. Noise is inversely proportional to Lorentz current. The continuous lines were obtained using the lumped mass derived from G-B curves and the Q given by Eq. (31), which models well experimental data. The data points were obtained from G-B curves at different pressures. In dark gray, range of pressures of QFN packaged devices. Heading accuracy or angle error is calculated assuming  $B_{earth} = 20 \mu\text{T}$ . **a** Z device:  $g = 0.35 \mu\text{m}$ ,  $n_B = 10$ ,  $n_w = 2$ . **b** XY device:  $g = 0.9 \mu\text{m}$ ,  $n_B = 6$ ,  $n_w = 4$ .

the requirements. For example, if power consumption specs are stringent,  $i_L^{peak} = 100\mu\text{A}$  may be used, and the voltage drop along the MEMS would be less than 0.5 V. In that case, the current may be reused for the electronics. The equations presented in this work allow to calculate the expected performance for other configurations.

System-level simulations at the resonant frequency show that SNR (Signal-to-Noise Ratio) due to the mechanical Brownian noise decreases with Q with a factor of  $\sqrt{Q}$  as Eq. (10) shows, while the SNR of the electronics increases linearly with Q, as its noise only depends on the parasitic capacitances and the circuit current consumption. With a low-noise amplifier of 50  $\mu\text{A}$  biasing ( $i_{bias}$ ), the Brownian noise dominates at the pressure level inside the QFN packaged devices. For a given device size and power constraint, system-level power-noise optimization shall redistribute the  $i_{bias}/i_L$  current ratio for optimal overall SNR for minimum power consumption.

### Sensitivity

The sensitivity to magnetic fields was measured using a Helmholtz coil. The applied magnetic field ranged from  $-473\mu\text{T}$  to  $+473\mu\text{T}$ . A square Lorentz current of  $50.14\mu\text{A}$  ( $45.14\mu\text{A}^{RMS}$  at  $f_r$ ) was injected in phase with the Vac voltage of the impedance analyzer ( $10\text{mV}^{RMS}$ ). Phase alignment was achieved with the system described in Sánchez-Chiva et al.<sup>56</sup>. The Lorentz force is added to the electrostatic force and modifies the measured G-B curve (and  $R_m$ ,  $L_m$  and  $C_m$  values) as described in the MEMS electrical model section. Eight magnetic sweeps were performed so the G-B curve was measured eight times at each of the seven B values to reduce measurement noise and/or temperature drifts. The variance of each group of 8 points was used to perform a weighted linear regression that yielded the  $1/R_m$ ,  $1/L_m$  and  $C_m$  values ( $R^2 > 0.999$ ) as a function of the magnetic field (see Table 3). It was done for one z and one xy device. The  $\Omega$  value obtained from the fit is displayed in the table, along with the sensitivity calculated using Eqs. (26) and (27). It is important to note that the Q factor of the chosen z and xy devices is low: the worst case scenario after QFN packaging. The expected sensitivity of a nominal QFN device is between 1.25 and 2.5 times higher than the measured ones for the xy and z devices, respectively.

The measured  $\Omega$  is similar to the theoretical value (within 15%). This similarity is an indication that our theoretical estimation of the electrostatic and magnetic forces (and associated Brownian noise) are correct. This 15% discrepancy is perfectly accountable by the inherent process variability, which affects lateral gaps and layer thicknesses. In general,  $\Omega$  must be calculated using simulations to estimate the electrostatic force correctly. Fortunately, the electrostatic force of the z device may be

calculated analytically quite accurately, with parallel plate assumption. For illustration purposes, its  $\Omega$  value may be calculated as follows:

$$\Omega_z = \frac{F_m}{F_e} = \frac{i_L n_w L B}{\epsilon_0 \frac{V_{DC} V_{AC} L w}{g^2}} = 101,5 \cdot B(T) \quad (35)$$

where  $w = 5\mu\text{m}$  is the width of the electrostatic area of one beam. The parallel plate assumption yields  $\Omega_z = 0.048$  for  $B = 473\mu\text{T}$ , similar to the measured and simulated values shown in Table 3.

The maximum conductance value  $G_{max}$  may be used to obtain the sensitivity as in Sánchez-Chiva et al.<sup>56</sup>. It is not used given that variability in the  $R_p$  values, for example due to electrostatic coupling between sense and Lorentz wire, introduces errors in the measurement that are overcome using the  $R_m$ ,  $L_m$  and  $C_m$  values.

Finally, the very high coefficient of determination obtained in non-weighted fits ( $R^2 > 0.999$ ) confirms the high linearity of Lorentz-force magnetometers<sup>62,63</sup>, mainly limited by the non-linearity of the motion detection capacitors ( $C(x)$ ). It also proves that this new method for measuring the sensitivity of Lorentz-force magnetometers works well. In fact, the  $\Omega$  ratio may be found with very good accuracy (we estimate better than 2% in our case).

### Offset and shielding efficiency

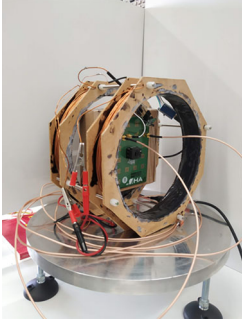
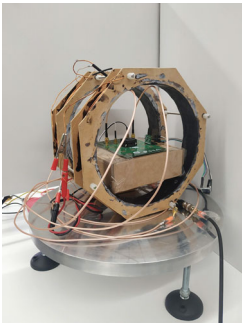
One the best features of the presented magnetometers is their Lorentz current shielding from the sense electrode. The theoretical models have been already presented. Now, in order to check the shielding efficiency experimentally, the G-B curve was measured while a Lorentz current of  $52\mu\text{A}$  (peak value of square wave) in phase with impedance analyzer alternating voltage ( $V_{1-2}$  in Fig. 8) was injected into the MEMS. The external magnetic field was compensated with Helmholtz coils. The experiment was repeated but this time using a Lorentz current in anti-phase. The resultant admittance curves are shown in Figs. 13c, g for the z device, and in Fig. 13a, e for the xy device.

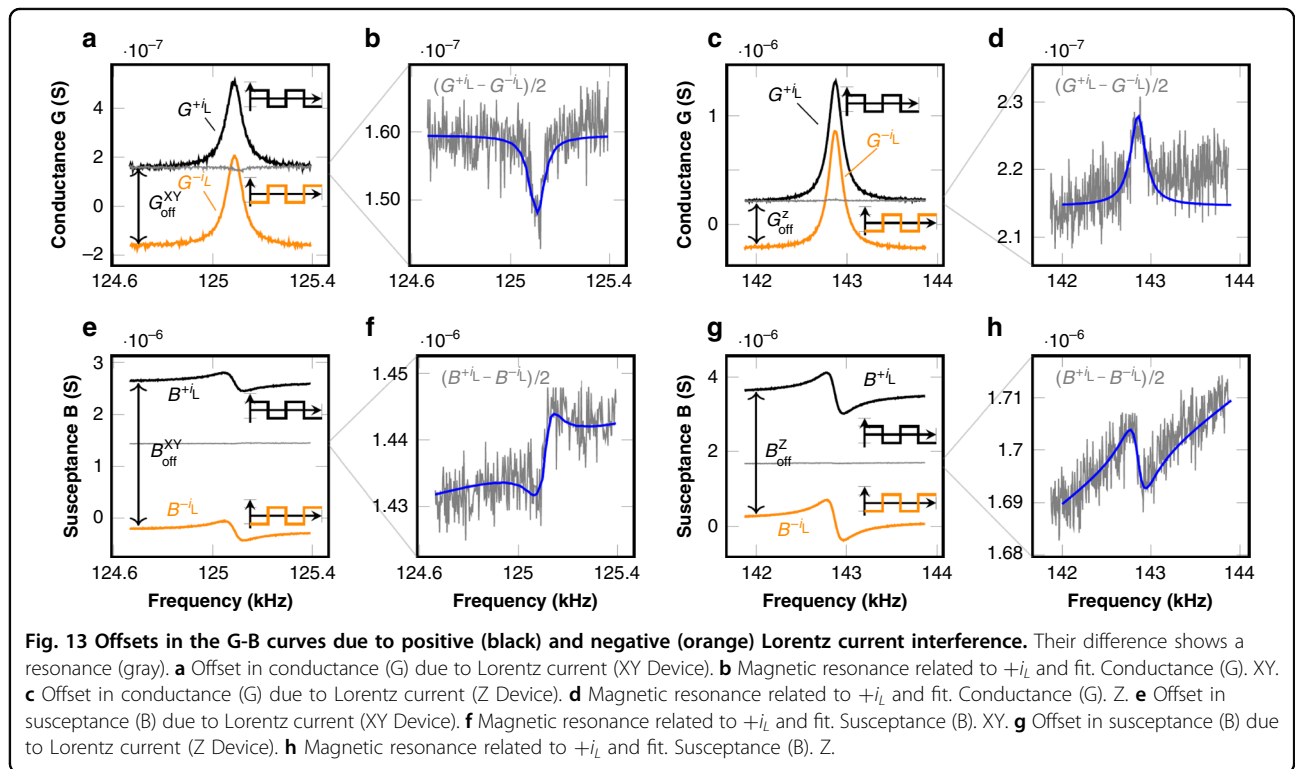
An appreciable constant offset due to the Lorentz current interference effect was measured, especially in the susceptance (B) curves. This corresponds to the electrical interference term in Eq. (3). The interference magnitude ( $V_{sh}^{int}$ ) and the shielding efficiency ( $V_{sh}^{int}/V_w$ ) can be deduced from the G-B curves as follows:

The impedance analyzer extracts the admittance from the nodes 1-2 of Fig. 8 by applying an AC voltage ( $V_{1-2}$ ) and measuring the current flow. The interference caused by a Lorentz current  $+i_L$  introduces an additional unwanted current  $i^{int}$  flowing through 1-2 and this, in turn, is seen as an admittance change. The admittance offsets ( $Y_{off} = G_{off} + jB_{off}$ ) shown in the figures are produced by a  $2 \cdot i_L$  current, so the offset must be divided by a



**Table 3 Measured  $\Omega$  and sensitivity  $S$  (Eqs. (26) and  $S'$  (Eq. (27)) of two QFN-packaged devices**

Device type	Setup Helmholtz coil	Measurement conditions (RMS)	Fitted linear eq. (Eqs. (22) to (24)) $Y_m = Y_m^{B=0} \cdot (1 + \Omega)$	Magnetic sensitivity (RMS)
Z $L = 600 \mu\text{m}$ $g = 0.5 \mu\text{m}$ $Q = 770$ $n_w = 2, n_B = 10$		$V_{AC} = 10 \text{ mV}$ $V_{DC} = 0.5 \text{ V}$ $i_L = 45.14 \mu\text{A}$	$1/R_m = 1.075 \times 10^{-6} \cdot (1 + 96.00 \cdot B(T))$ $1/L_m = 1.255 \times 10^{-3} \cdot (1 + 97.77 \cdot B(T))$ $C_m = 1.543 \times 10^{-15} \cdot (1 + 97.80 \cdot B(T))$	$\Omega_z = 96.89 \cdot B(T)$ $S_z = 1.05 \mu\text{A/T}$ $S'_z = 46.3 \mu\text{A/(T V mA)}$
XY $L = 600 \mu\text{m}$ $g = 0.9 \mu\text{m}$ $Q = 1700$ $n_w = 4, n_B = 6$		$V_{AC} = 10 \text{ mV}$ $V_{DC} = 1 \text{ V}$ $i_L = 45.14 \mu\text{A}$	$1/R_m = 0.3958 \times 10^{-6} \cdot (1 + 311.0 \cdot B(T))$ $1/L_m = 1.9287 \times 10^{-4} \cdot (1 + 306.8 \cdot B(T))$ $C_m = 2.851 \times 10^{-16} \cdot (1 + 306.8 \cdot B(T))$	$\Omega_{xy} = 308.9 \cdot B(T)$ $S_{xy} = 1.22 \mu\text{A/T}$ $S'_{xy} = 27.1 \mu\text{A/(T V mA)}$



**Table 4 Interference/offset reduction achieved with Lorentz routing symmetric design (\*) and Lorentz current shielding (\*\*)**

Device type	Measurement conditions	Device impedances	Admittance offset	$V_{drop}$ ( $i_L R_{wire}$ )	$V_w$ (RMS) Eq. (38)	$V_{sh}^{int}$ (RMS) Eq. (37)	Magnetic offset
Z	$i_L = 51.1 \mu A^{peak}$ = squarewave	$C_{ws} = 6.20$ pF $C_{ss} = 1.36$ pF	$G_{off}^Z = 0.459$ $\mu S$	295 mV $\rightarrow$	3.08 mV $\rightarrow$	0.428 $\mu V$	0.43 $\mu T$
$L = 600$ $\mu m$ $g = 0.5$ $\mu m$ $Q = 770$ $n_w = 2, n_B = 10$	$V_{1-2}^{dc} = 0.5V$ $V_{1-2}^{ac} = 10mV^{RMS}$	$Z_{sh} = 25\Omega$ $R_{wire} = 4.50$ k $\Omega$	$B_{off}^Z = 3.39$ $\mu S$	$(\frac{1}{45})^*$	$(\frac{1}{7184})^{**}$		
XY	$i_L = 52.5 \mu A^{peak}$ = square wave	$C_{ws} = 3.56$ pF $C_{ss} = 2.9$ pF	$G_{off}^Z = 0.321$ $\mu S$	282 mV $\rightarrow$	5.17 mV $\rightarrow$	0.362 $\mu V$	0.13 $\mu T$
$L = 600$ $\mu m$ $g = 0.9$ $\mu m$ $Q = 1575$ $n_w = 4, n_B = 6$	$V_{1-2}^{dc} = 1V$ $V_{1-2}^{ac} = 10mV^{RMS}$	$Z_{sh} = 25\Omega$ $R_{wire} = 4.22$ k $\Omega$	$B_{off}^Z = 2.88$ $\mu S$	$(\frac{1}{26})^*$	$(\frac{1}{14301})^{**}$		

factor of two in order to calculate  $i^{int}$ :

$$i^{int} = \frac{Y_{off}}{2} \cdot V_{1-2} = \frac{G_{off} + jB_{off}}{2} \cdot V_{1-2} \quad (36)$$

Consequently, an interference voltage  $V_{sh}^{int}$  is also added on top of the shield voltage  $V_{sh}$ . We can calculate this voltage simply as:

$$V_{sh}^{int} = Z_{sh} \cdot i^{int} \quad (37)$$

where  $Z_{sh}$  is the output impedance of the impedance analyzer. The coupling capacitance between wire and shield  $C_{ws}$  was measured. Then,  $V_w$  can also be readily deduced after substituting equations Eqs. (36) and (37) into Eq. (11), which yields:

$$V_w \approx (B_{off} - jG_{off}) \cdot \frac{V_{1-2}}{2\omega C_{ws}} \quad (38)$$

Note that  $V_w$  is an equivalent AC voltage that creates the observed interference. It is just a fraction of the real voltage drop  $V^{drop}$  along the Lorentz wire, caused by the fabrication inherent variability resulting in a small lack of symmetry, non-compensated AC voltage and its associated interference, already mentioned in the Materials and methods section. The real voltage drop is readily calculable with the resistance value of the Lorentz wire. Therefore, the interference reduction achieved with symmetric Lorentz routing design can also be estimated as  $V_w/V^{drop}$ .

By looking at Eq. (38), if  $V_w$  is perfectly in phase or in anti-phase with  $V_{1-2}$ , then  $G_{off}$  should be zero. It turned out to be small, but not zero. From the ratio  $B_{off}/G_{off}$  we can deduce that  $V_w$  was in  $83^\circ$  phase with  $V_{1-2}$  during the measurements, and not perfectly in phase.

The measurement conditions and the amount of electrical interference obtained from Eqs. (11) and (36) to (38)

are shown in Table 4. We can see that the shielding electrode reduced the interference voltage by a 4 order magnitude factor, approximately. This value is highly dependent on the impedance connected to the shield electrode. In an ASIC we estimate similar shielding attenuation factors. Additionally, the Lorentz routing design reduced the interference between 25 and 50 times. In total, a 5-6 order of magnitude reduction was achieved.

The interference adds an offset to the admittance curves (electrical interference term in Eq. (2)). This offset may be almost eliminated by using the current chopping technique<sup>33,41</sup> mentioned in the introduction. Unfortunately, this technique cannot eliminate the magnetic offset (electrostatic excitation term in Eq. (2)). Let us apply this technique to the measured data by subtracting the  $+i_L$  and  $-i_L$  curves, and dividing by two, so the result corresponds to  $+i_L$ : the resultant curves were plotted in Figs. 13b, d, f and h. A small resonance peak was found. The peak was fitted to the MEMS electrical model and, by using the sensitivity equations from Table 3, we found that it corresponds to a magnetic field of approximately 100  $\mu T$ . We think this is caused by the permanent magnetization of the prototyping package and/or measuring setup. One reason is that the magnetic field has the same magnitude but opposite direction for the z and xy device: note that the interference AC voltage would excite electrostatically the device and create peaks in the same direction, so is an indication that they correspond to a physical magnetic field. Additionally, the observed peaks (100  $\mu T$ ) are between 2 and 3 orders of magnitude larger than the one created by the interference voltage  $V_{sh}^{int}$  (0.1 – 0.4  $\mu T$ ), not observed in these measurements. The magnetic offsets were calculated by setting  $\Omega = 1$  (in Eq. (35) for the z device) and obtaining the correspondence between magnetic field and  $V_{AC}$ .

In conclusion, the current chopping technique in conjunction with the beam shielding successfully eliminated the electrical interference. In addition, the magnetic offset was reduced almost 6 orders of magnitude (4 orders due to shielding and 2 orders due to symmetric Lorentz routing), down to, at most, 0.4  $\mu\text{T}$ , approximately. We believe this is an important achievement given that this magnetic offset cannot be compensated with the current chopping technique.

**Yield and reliability tests**

**Yield after QFN packaging**

The two types of z and xy devices shown in Fig. 6a and 7 and in Table 3 were packaged into QFN packages. The chosen variants were formed by 600  $\mu\text{m}$ -long c-c beams. They were measured and the results and expected cavity pressure, noise floor and associated best heading accuracy are summarized in Table 5. The cavity pressure was deduced from the Q factor and resonant frequency using Eq. (31) and values from Table 2. Noise floor and heading accuracy can be found in Fig. 12. Results showed that the z-yield is high but the xy-yield was significantly lower. We will show in the following section how the xy device was redesigned to improve this yield. The main failure mode was a too high G value, indicating a short-circuit between sense and shield electrode. Statistical data will show that this may be caused by a non-optimum sense electrode or coupling link design.

**Temperature robustness and yield improvement**

On-wafer devices were subjected to one of the two stringent thermal profiles shown in Fig. 14, which are close to altering the CMOS electronics performance<sup>7,15,64</sup>.

Also, Aluminum suffers a significant Young’s modulus softening at those temperatures<sup>65</sup>. Withstanding high temperatures for several minutes is very important so the device may be subjected to outgassing thermal treatments (annealings)<sup>66,67</sup> and other post processes. In our experience, a MEMS device that shows high yield after these tests also shows high yield after packaging into QFN, and viceversa. Therefore, it can be used as a quick and cheap method to foresee yield issues before QFN packaging.

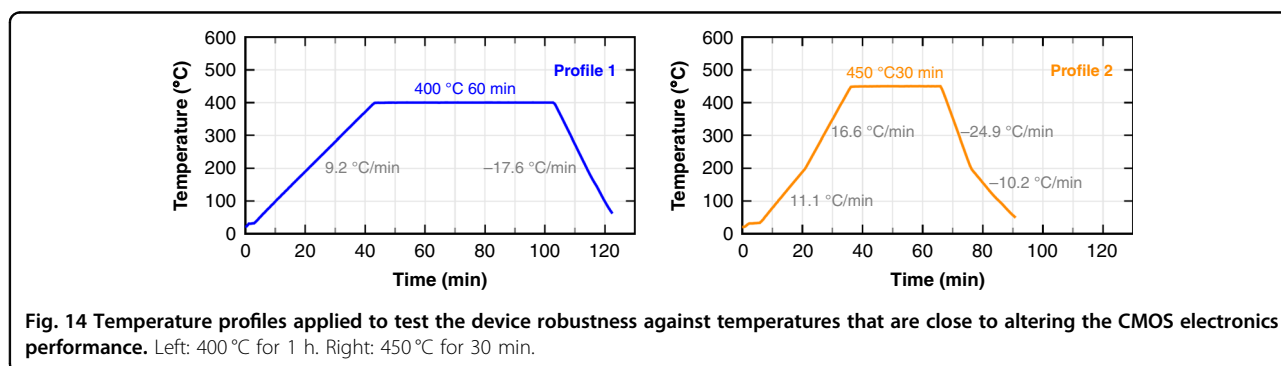
The devices were measured before and after the experiment and the yield results are summarized in Tables 6, 7, 8. The number of mechanical couplings and length of the sensing structures (sensing plate length x number of repetitions) is shown for each device type and length.

Results show that, with proper design, very robust CMOS-MEMS structures may be manufactured. For example, the length of the sensing electrodes of the xy devices had an important effect on the robustness against high temperature. In fact, older devices (not shown here) with longer sensing plates showed much lower yield. Shortening the sense electrode length allowed to improve the yield of the 3-metal xy device from 56% to 96% in the 400°C – 1 h test. Also, it was found that the number of couplings should be minimized: it increased the yield of the xy-4m device with sensing fingers from 60% to 96% in the 450°C – 0.5 h test, achieving similar yields to the z device. This is strong evidence that the yield after QFN packaging of the xy-4m device would be similar to that of the z device.

Implementing a large number of design variants proved a decisive factor in the yield optimization process, which also allowed to reduce the number of iterations and therefore development time.

**Table 5 Variability and yield of QFN devices**

Dev.	$f_r$ <sup>+sample max</sup> / <sub>-sample min</sub>	Q <sup>+sample max</sup> / <sub>-sample min</sub>	P <sup>+sample max</sup> / <sub>-sample min</sub>	Noise floor (RMS)	Heading accuracy (RMS)	Yield
Z	144 <sup>+15</sup> / <sub>-6</sub> kHz	1687 <sup>+1122</sup> / <sub>-1093</sub>	270 <sup>+520</sup> / <sub>-114</sub> $\mu\text{bar}$	15 <sup>+5</sup> / <sub>-3</sub> nT/ $\sqrt{\text{Hz}}$	0.045 <sup>+0.015</sup> / <sub>-0.015</sub> / $\sqrt{\text{Hz}}$	118/124 (95%)
XY	133 <sup>+9</sup> / <sub>-10</sub> kHz	2049 <sup>+935</sup> / <sub>-966</sub>	245 <sup>+245</sup> / <sub>-157</sub> $\mu\text{bar}$	9.5 <sup>+2.5</sup> / <sub>-2.5</sub> nT/ $\sqrt{\text{Hz}}$	0.027 <sup>+0.009</sup> / <sub>-0.009</sub> / $\sqrt{\text{Hz}}$	68/114 (53%)



**Table 6 Yield results of the two high temperature tests on xy devices**

<b>XY device with long sensing plates</b>					
<b>Dev. L (<math>\mu\text{m}</math>)</b>	<b>300</b>	<b>400</b>	<b>500</b>	<b>600</b>	
<b>Sense L (<math>\mu\text{m}</math>)</b>		<b>100 x 4</b>	<b>82 x 6</b>	<b>74 x 8</b>	
<b>Couplings</b>		<b>4</b>	<b>6</b>	<b>6</b>	<b>TOTAL</b>
Yield before 400 °C 1 h		13/13 (100%)	11/13 (85%)	13/13 (100%)	37/39 (95%)
Yield after 400 °C 1 h		7/13 (54%)	5/13 (38%)	10/13 (77%)	22/39 (56%)
Yield before 450 °C 0.5 h		13/13 (100%)	13/13 (100%)	12/13 (92%)	38/39 (97%)
Yield after 450 °C 0.5 h		5/13 (38%)	2/13 (15%)	0/13 (0%)	7/39 (18%)
<b>XY device improved with shorter sensing plates (Fig. 6a)</b>					
<b>Dev. L (<math>\mu\text{m}</math>)</b>	<b>300</b>	<b>400</b>	<b>500</b>	<b>600</b>	
<b>Sense L (<math>\mu\text{m}</math>)</b>		<b>33 x 6</b>		<b>36 x 7</b>	
<b>Couplings</b>		<b>2</b>		<b>6</b>	<b>TOTAL</b>
Yield before 400 °C 1 h		12/13 (92%)		13/13 (100%)	25/26 (96%)
Yield after 400 °C 1 h		12/13 (92%)		13/13 (100%)	25/26 (96%)
Yield before 450 °C 0.5 h		12/13 (92%)		13/13 (100%)	25/26 (96%)
Yield after 450 °C 0.5 h		5/13 (38%)		3/13 (23%)	8/26 (31%)

**Table 7 Yield results of the two high temperature tests on xy-4m devices**

<b>XY-4M device with sensing fingers (Fig. 6b), several couplings</b>					
<b>Dev. L (<math>\mu\text{m}</math>)</b>	<b>300</b>	<b>400</b>	<b>500</b>	<b>600</b>	
<b>Sense L (<math>\mu\text{m}</math>)</b>	<b>28 x 4</b>	<b>28 x 6</b>	<b>28 x 8</b>	<b>28 x 10</b>	
<b>Couplings</b>	<b>2</b>	<b>4</b>	<b>4</b>	<b>6</b>	<b>TOTAL</b>
Yield before 400 °C 1 h	13/13 (100%)	13/13 (100%)	13/13 (100%)	13/13 (100%)	52/52 (100%)
Yield after 400 °C 1 h	13/13 (100%)	10/13 (77%)	5/13 (38%)	3/13 (23%)	52/52 (100%)
Yield before 450 °C 0.5 h	13/13 (100%)	13/13 (100%)	13/13 (100%)	13/13 (100%)	52/52 (100%)
Yield after 450 °C 0.5 h	13/13 (100%)	10/13 (77%)	5/13 (38%)	3/13 (23%)	31/52 (60%)
<b>XY-4M device with sensing fingers (Fig. 6b), 1 coupling</b>					
<b>Dev. L (<math>\mu\text{m}</math>)</b>	<b>300</b>	<b>400</b>	<b>500</b>	<b>600</b>	
<b>Sense L (<math>\mu\text{m}</math>)</b>	<b>28 x 6</b>	<b>28 x 6</b>	<b>28 x 6</b>	<b>28 x 8</b>	
<b>Couplings</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>TOTAL</b>
Yield before 400 °C 1 h	13/13 (100%)	13/13 (100%)	13/13 (100%)	13/13 (100%)	52/52 (100%)
Yield after 400 °C 1 h	13/13 (100%)	13/13 (100%)	13/13 (100%)	13/13 (100%)	52/52 (100%)
Yield before 450 °C 0.5 h	13/13 (100%)	13/13 (100%)	13/13 (100%)	13/13 (100%)	52/52 (100%)
Yield after 450 °C 0.5 h	13/13 (100%)	13/13 (100%)	13/13 (100%)	11/13 (85%)	50/52 (96%)

**Magnetic robustness**

In order to test the robustness against large magnetic fields the z device G-B curve was measured while using

a square wave Lorentz current of 50  $\mu\text{A}$  in the presence of a magnetic field of variable intensity. The maximum applied magnetic field was estimated in around 32 mT.

**Table 8** Yield results of the two high temperature tests on z devices

Z device with gap = 0.5 $\mu\text{m}$ (Fig. 7), 1 coupling					
Dev. L ( $\mu\text{m}$ )	300	400	500	600	
Sense L ( $\mu\text{m}$ )	37 x 8	50 x 8	40 x 12	37 x 16	
Couplings	1	1	1	1	TOTAL
Yield before 400 °C 1 h	13/13 (100%)	13/13 (100%)	13/13 (100%)	13/13 (100%)	52/52 (100%)
Yield after 400 °C 1 h	13/13 (100%)	13/13 (100%)	12/13 (92%)	12/13 (92%)	50/52 (96%)
Yield before 450 °C 0.5 h	13/13 (100%)	13/13 (100%)	13/13 (100%)	13/13 (100%)	52/52 (100%)
Yield after 450 °C 0.5 h	13/13 (100%)	13/13 (100%)	13/13 (100%)	13/13 (100%)	52/52 (100%)

It was created with a magnet placed on top of the QFN and a Helmholtz coil, generating 24 mT and 8 mT each, respectively. No malfunction was found during or after the test. Due to the magnetization of external components, offsets of around a few hundreds of  $\mu\text{T}$  remained when the magnetic field was set to zero. In addition, non-linear behavior became slightly apparent when vibration amplitudes reached around 20 – 30% of the gap which took place when the magnetic field was around 14 – 20 mT.

### Shock tests

The QFN z-device underwent several shock tests along the 3 directions. The impacts were performed manually. A commercial triaxial piezoelectric accelerometer (834M1-6000)<sup>68</sup> was used as the reference accelerometer. Its maximum acceleration range is  $\pm 6000$  g, so that was the maximum shock that could be recorded. An Arduino microcontroller board was also attached to the test assembly, which recorded the 3 axis readings when a given threshold was surpassed and later sent to a PC via serial communication. The accelerometer bandwidth is above 6 kHz with analog output and its sensitivity 5 g/LSB. Its offset was compensated in the Arduino code. Three readings, one for each axis, were taken every 4.5  $\mu\text{s}$  and the total recording time was 9 ms. The QFN, accelerometer and Arduino were assembled together as shown in Fig. 15d.

The devices survived all the tests performed. The recorded accelerations of the 3 strongest shocks are shown in the graphs in Fig. 15. Along the y and z axes they reached 6000 g. The correct functioning after the test was confirmed by measuring the G-B resonance with an impedance analyzer.

In addition, 5 QFN z devices were subjected to two standard mechanical shock tests:

- Method 2002.5, Condition B: MIL-STD-883 1500 g 0.5 ms Half Sine, 5 shocks in each direction of 3 mutually perpendicular axes. (30 total).
- Method 2007.3, Condition A: MIL-STD-883 1.5 mm

pk-pk / 20 g pk min, 20-2000 Hz, 4 sweeps in each of 3 mutually perpendicular axes at 3 octaves/min. The 5 devices survived the tests.

Finally, one QFN z device underwent a 3 s free fall from a 5.60 m height and landed on a concrete floor at an estimated velocity of 7  $\text{km s}^{-1}$ . The acceleration during the impact was not recorded. No appreciable damages were observed in the QFN package and the device continued functioning correctly.

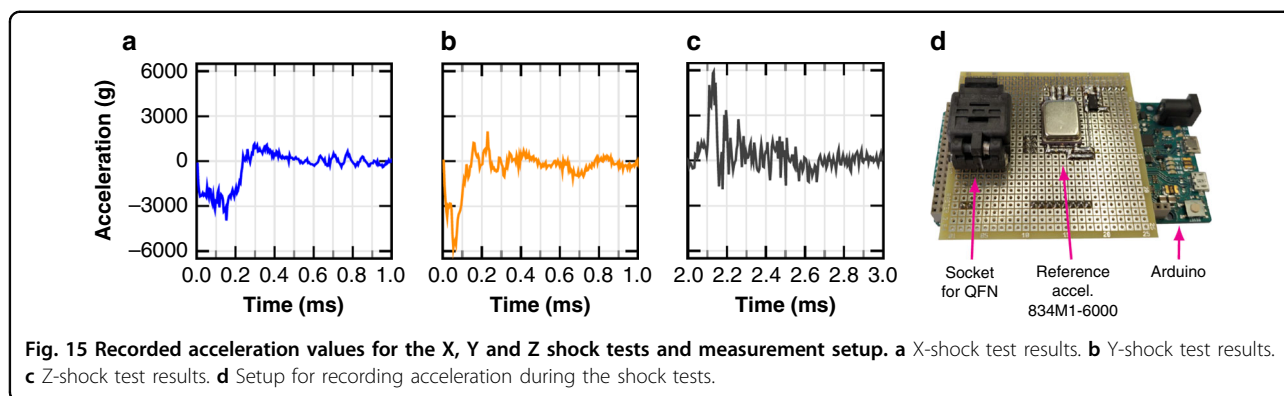
### Performance comparison with other magnetometers

A list of the commercial magnetometers used in smartphones may be found in Matyunin et al.<sup>69</sup>. Some of them are compared with 3 axis MEMS magnetometers in Table 9. The recently commercialized TMR technology displays the best noise figure-of-merit (FOM) of commercial magnetometers. However, the Lorentz-force magnetometers built with the presented CMOS-MEMS process show very competitive results in QFN packages, compared to both MEMS and commercial devices. For example, the sensor area is the smallest found in 3 axis MEMS magnetometers.

The xy and z devices packaged into QFN occupy an area of  $680 \times 185 \mu\text{m}^2$  and  $690 \times 210 \mu\text{m}^2$ , respectively, which corresponds to a total area of 0.4  $\text{mm}^2$ . If the MEMS yield is kept high, the area taken by the MEMS has a CMOS fabrication cost smaller than \$0.01 for 200 mm wafers from 0.18  $\mu\text{m}$  CMOS processes. In addition, in multi-die approaches, a relatively high area is wasted in pads, especially in a small die. Also MEMS die area is limited by the fact that cutting very small dice is not straightforward. All this is avoided with CMOS-MEMS and highlights the area-cost advantage of BEOL CMOS-MEMS integration.

### Conclusions

Integration of MEMS on CMOS using the BEOL layers is a long-sought objective that would provide significant size, cost, speed and power advantages in some applications. However, successful commercialization has proven to be difficult due to two main reasons:



**Table 9 Comparison of commercial and in research-state 3 axis magnetometers**

3D Magnetometer	Technology	Full scale range ( $\pm$ mT)	Current per axis ( $\mu$ A <sub>rms</sub> )	Figure of merit* ( $\mu$ T $\mu$ A <sub>rms</sub> /√Hz)	Footprint 3 axes (mm <sup>2</sup> )	Offset ( $\mu$ T)
<b>COMMERCIAL</b>						
STMicroelectronics LIS3MDL <sup>70</sup>	AMR	1.2	90	~10	4	100
STMicroelectronics LSM303AGR <sup>71</sup>	AMR	4.9	33–316 <sup>a</sup>	6–20 <sup>a</sup>	4 <sup>b</sup>	6 <sup>c</sup>
Freescale MAG3110 <sup>72</sup>	TMR	1.0	2.9–300 <sup>a</sup>	3.6–19 <sup>a</sup>	4	100
AKM AK8975 <sup>73</sup>	Hall	1.2	117	~100	4	300
AKM AK09940 <sup>74</sup>	TMR	1.2	40–267	0.20–0.76	2.56	No data
Honeywell HMC5883 <sup>75</sup>	AMR	0.8	33/640	3.4	9	No data
Bosch BMM150 <sup>76</sup>	AMR+Hall	1.3	57–1630 <sup>a</sup>	35.6–155 <sup>a</sup>	2.43	40 <sup>b</sup>
<b>R&amp;D</b>						
Kyyräräinen et al. <sup>32</sup>	MEMS LFM	0.2	100	X/Y: 1.0, Z: 7.0	>11.5	25
Laghi et al. <sup>40</sup>	MEMS LFM	5.5	33 <sup>d</sup>	X/Y: 6.1, Z: 6.7 <sup>f</sup>	4 <sup>d</sup>	5000 <sup>b</sup>
Marra et al. <sup>49,77</sup>	MEMS LFM	X: 6.0, Y: 5.5, Z: 7.0	70 <sup>d</sup>	X: 8.4, Y: 5.2, Z: 7.7 <sup>f</sup>	0.53 <sup>d</sup>	No data <sup>e</sup>
This work (QFN)	CMOS-MEMS LFM	Z: 32 <sup>g</sup>	0–600 <sup>d</sup>	X/Y: 1.8, Z: 3.0 <sup>f</sup>	0.4 <sup>d</sup>	X/Y: 0.13, Z: 0.43

\*Normalized for a single axis. For R&D, X, Y and Z axis values are given

<sup>a</sup>Value varies depending on the selected current/bandwidth

<sup>b</sup>3D magnetometer and 3D accelerometer

<sup>c</sup>Can be reduced to a few  $\mu$ T with manual DC compensation or calibration

<sup>d</sup>ASIC current consumption/area not included

<sup>e</sup>Expected to be in the same order of magnitude as Laghi et al.<sup>40</sup> given the design and manufacturing process similarities

<sup>f</sup>Assuming current is reused for the 3 axes. Otherwise the triple value must be taken

<sup>g</sup>Tested with a current of 50  $\mu$ A

MEMS yield and MEMS performance. In this work, the fabrication process and the design techniques to overcome the main challenges to build reliable and competitive CMOS-MEMS Lorentz-force magnetometers (LFM) have been presented.

Three-axis Lorentz-force magnetometers were designed, fabricated and extensively characterized: equations that accurately predict the Q factor and resonant frequency of multilayered clamped-clamped

beams as a function of temperature, design parameters, and gas pressure from 1 bar to 1  $\mu$ bar were derived and verified experimentally. TED was the main damping mechanism at low pressures as finite element simulations confirmed. Gas viscosity explained Q factor temperature variations in the air damping regime. Thermal stress accounted for the variation of resonance frequency with temperature. The beam-to-string transition of clamped-clamped beams with the same axial

stress but different length was measured and fitted accurately the expected behavior. This demonstrates that accurate modelization of complex multilayered structures built with the BEOL of CMOS is feasible.

Lorentz-force magnetometers do not have magnetic materials, which provides several advantages over other magnetometer technologies. Unfortunately, offsets in LFM are, probably, their main drawback. In this work, the current chopping technique in conjunction with the beam shielding successfully eliminated the electrical interference. In addition, the electrostatic interference/offset, which cannot be compensated with the current chopping technique, was reduced almost 6 orders of magnitude (4 orders due to shielding and 2 orders due to symmetric Lorentz routing) down to 0.13  $\mu\text{T}$  and 0.43  $\mu\text{T}$  for the xy and z axes, respectively.

Despite CMOS technology not being a MEMS process, Brownian noise in the final CMOS-MEMS QFN-packaged devices was between 9.5 – 15 nT/ $\sqrt{\text{Hz}}$  when using a current of 600  $\mu\text{A}$ . A heading accuracy as low as 0.045°/ $\sqrt{\text{Hz}}$ , approximately, may be achieved by a compass formed by the packaged magnetometers. This is similar or better than what commercial magnetometers and state-of-the-art three-axis LFMs built with MEMS-dedicated processes can provide. Apart from the offset and noise benefits, the sensor area is the smallest found in 3 axis MEMS magnetometers. The area taken by the MEMS has a CMOS fabrication cost smaller than 0.01\$. One of the tested devices on wafer reached a Q factor of around 40000 at 107 kHz. This is equivalent to a Brownian noise level of 2 nT/ $\sqrt{\text{Hz}}$  with a Lorentz current of 600  $\mu\text{A}$ . This is lower than the three-axis LFMs found in the literature. A lower noise level could be achieved with longer beams not fabricated in this work, or higher Lorentz current.

Yield is usually one the major concern in MEMS products. Conveniently, we showed that the final yield of a QFN packaged CMOS-MEMS device can be around 95%. In addition, some device variants withstood very high temperatures with none or little yield loss: 450 °C for 30 min and 400° for 1 h. As summary, robust CMOS-MEMS devices with potential to equal or out-best commercial products is possible using the appropriate design techniques.

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#### Author contributions

J.V. conceived the design techniques, designed the sensors, performed simulations, proposed and analyzed the measurements, established the new theoretical models and wrote the manuscript. J.M.S.-C. and J.V. performed measurements. J.M.S.-C. and D.F. conceptualized test setups. All authors participated in the testchip designs and in the sensor high-level model development. D.F. and J.M. conceived the system design and supervised the research. J.M. obtained public funding for some prototypes and test setup. All the authors discussed system design and results. All the authors revised the manuscript.

#### Competing interests

The authors declare no competing interests.

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#### References

- Baltes, H. et al. *CMOS-MEMS: Advanced Micro and Nanosystems*. (John Wiley & Sons, 2008) <https://onlinelibrary.wiley.com/doi/book/10.1002/9783527616718>.
- Fedder, G. K., Howe, R. T., Liu, Tsu-JaeKing & Quevy, E. P. Technologies for cofabricating MEMS and electronics. *Proc. IEEE* **96**, 306–322 (2008).
- Qu, H. CMOS MEMS fabrication technologies and devices. *Micromachines* **7**, 14 (2016).
- Yasaitis, J. A. et al. A modular process for integrating thick polysilicon MEMS devices with sub-micron CMOS. *SPIE* **4979**, 145–154. <https://doi.org/10.1117/12.478294> (2003).
- Smith, J. H., Montague, S., Sniegowski, J. J., Murray, J. R. & McWhorter, P. J. Embedded micromechanical devices for the monolithic integration of MEMS with CMOS. In *Electron Devices Meeting, 1995, International*, 609–612. <http://ieeexplore.ieee.org/iel3/3584/10696/00499295.pdf?tp=&isnumber=&number=499295> (1995).
- Baltes, H., Brand, O., Hierlemann, A., Lange, D. & Hagleitner, C. CMOS MEMS - present and future. In *The Fifteenth IEEE International Conference on Micro Electro Mechanical Systems*, **2002**, 459–466 <https://doi.org/10.1109/MEMSYS.2002.984302>. <https://ieeexplore.ieee.org/document/984302> (2002).
- Sedky, S., Witvrouw, A., Bender, H. & Baert, K. Experimental determination of the maximum post-process annealing temperature for standard CMOS wafers. *IEEE Trans Electron Devices* **48**, 377–385 (2001).
- Takeuchi, H., Wung, A., Sun, X., Howe, R. T. & King, Tsu-Jae. Thermal budget limits of quarter-micrometer foundry CMOS for post-processing MEMS devices. *IEEE Trans Electron Devices* **52**, 2081–2086 (2005).
- Core, T. A., Tsang, W. K. & Sherman, S. J. Fabrication technology for an integrated surface-micromachined sensor. *Solid State Technol.* **36**, 39–39 (1993).
- Heuvelman, W. Method for containing a device and a corresponding device. <https://patents.google.com/patent/US20070004096>.
- Beunder, M. A., van Kampen, R., Lacey, D., Renault, M. & Smith, C. G. A new embedded nvm technology for low-power, high temperature, rad-hard applications. In *Symposium Non-Volatile Memory Technology*, 4–68 (2005). <https://doi.org/10.1109/NVMT.2005.1541401>. <https://ieeexplore.ieee.org/document/1541401>.
- Joshi, V. A non volatile MEMS switch for harsh environment memory applications. (Technical report, Cavendish Kinetics, October 2009). [http://nccavs-usergroups.avs.org/wp-content/uploads/TFUG2009/2009\\_10joshi.pdf](http://nccavs-usergroups.avs.org/wp-content/uploads/TFUG2009/2009_10joshi.pdf).
- Gaddi, R. et al. MEMS technology integrated in the CMOS back end. *Microelectron. Reliability* **50**, 1593–1598 (2010).
- Parpia, J. M. et al. CMOS integrated micromechanical resonators and methods for fabricating the same. <https://www.google.com/patents/US8704315>.

15. Franke, A. E. et al. Post-CMOS integration of germanium microstructures. In *Technical Digest. IEEE International MEMS 99 Conference. Twelfth IEEE International Conference on Micro Electro Mechanical Systems (Cat. No. 99CH36291)*, 630–637 <https://doi.org/10.1109/MEMSYS.1999.746901>. <https://ieeexplore.ieee.org/document/746901> (1999).
16. Mehta, A. et al. Novel high growth rate processes for depositing poly-sige structural layers at CMOS compatible temperatures. In *Micro Electro Mechanical Systems, 2004. 17th IEEE International Conference on. (MEMS)*, 721–724 <http://ieeexplore.ieee.org/iel5/9060/28749/01290686.pdf?tp=&isnumber=&anumber=1290686> (2004).
17. Mehta, A., Gromova, M., Czarnacki, P., Baert, K. & Witvrouw, A. Optimisation of PECVD poly-SiGe layers for MEMS post-processing on top of CMOS. In *The 13th International Conference on Solid-State Sensors, Actuators and Microsystems, 2005. Digest of Technical Papers. TRANSDUCERS '05*, 1326–1329 (2005). <https://doi.org/10.1109/SENSOR.2005.1497325>. <https://ieeexplore.ieee.org/document/1497325>.
18. Rusu, C. et al. New low-stress PECVD poly-SiGe layers for MEMS. *J. Microelectromech. Systems* **12**, 816–825 (2003).
19. Van Kessel, P. F., Hornbeck, L. J., Meier, R. E. & Douglass, M. R. A MEMS-based projection display. *Proc. IEEE* **86**, 1687–1704 (1998).
20. Bugnacki, M. A micromachined thermal accelerometer for motion, inclination, and vibration measurement. *Sensors* **18**, 98–104 (2001).
21. Kress, H.-J., Bantien, F., Marek, J. & Willmann, M. Silicon pressure sensor with integrated CMOS signal-conditioning circuit and compensation of temperature coefficient. *Sens. Actuators A Phys.* **25**, 21–26 (1990).
22. Abadal, G. et al. Monolithic integration of MEMS resonators in a 0.35  $\mu\text{m}$  CMOS technology for gravimetric sensor and radiofrequency applications. In *Integration Issues of Miniaturized Systems-MOMS, MOEMS, ICS and Electronic Components (SSI)*, 1–8. <https://ieeexplore.ieee.org/document/5760511> (2008).
23. Montanyà, J., Valle, J., Barrachina, L. and Fernández, D. MEMS devices and sensors in standard CMOS processing. In *Solid-State Sensors, Actuators and Microsystems, Transducers Eurosensors XXVII*, 713–717. <https://doi.org/10.1109/Transducers.2013.6626866>. <https://ieeexplore.ieee.org/document/6626866> (2013).
24. Fernández, D., Ricart, J. and Madrenas, J. Experiments on the release of CMOS-micromachined metal layers. *J. Sensors* (2010). <https://doi.org/10.1155/2010/937301>. <https://www.hindawi.com/journals/js/2010/937301/>
25. Michalik, P., Fernández, D., Wietstruck, M., Kaynak, M. & Madrenas, J. Experiments on MEMS integration in 0.25  $\mu\text{m}$  CMOS process. *Sensors* **18**, 2111 (2018).
26. Valle, J., Fernández, D., Madrenas, J. & Barrachina, L. Curvature of BEOL cantilevers in CMOS-MEMS processes. *J. Microelectromech. Systems* **26**, 895–909 (2017).
27. Lahrach, A. eCompass Comparative Report. Technical report, System Plus Consulting (Nantes, France, 2015).
28. Nhalil, H. et al. Planar Hall effect magnetometer with 5 pT resolution. *IEEE Sens. Lett.* **3**, 1–4 (2019).
29. Li, M., Rouf, V. T., Thompson, M. J. & Horsley, D. A. Three-axis Lorentz-force magnetic sensor for electronic compass applications. *J. Microelectromech. Systems* **21**, 1002–1010 (2012).
30. Laghi, G. et al. Torsional MEMS magnetometer operated off-resonance for in-plane magnetic field detection. *Sens. Actuators A Phys.* **229**, 218–226 (2015).
31. Horsley, D. and Li, M. Offset suppression in micromachined Lorentz force magnetic sensor by current chopping, US Patent 9,891,290. <https://patents.google.com/patent/US20170059666> (2018).
32. Kyyräinen, J. et al. A 3d micromechanical compass. *Sens. Actuators A Phys.* **142**, 561–568 (2008).
33. Li, M. and Horsley, D. A. Offset suppression in a micromachined Lorentz force magnetic sensor by current chopping. *J. Microelectromech. Systems*. <https://doi.org/10.1109/JMEMS.2014.2316452>. <https://ieeexplore.ieee.org/document/6803909> (2014).
34. Li, M., Sonmezoglu, S. & Horsley, D. A. Extended bandwidth Lorentz force magnetometer based on quadrature frequency modulation. *J. Microelectromech. Systems* **24**, 333–342 (2015).
35. Sunier, R. et al. Resonant magnetic field sensor with frequency output. *J. Microelectromech. Systems* **15**, 1098–1107 (2006).
36. Bahreyni, B. & Shafai, C. A resonant micromachined magnetic field sensor. *J. IEEE Sensors* **7**, 1326–1334 (2007).
37. Sánchez-Chiva, JosepMaria, Valle, J., Fernández, D. & Madrenas, J. A mixed-signal control system for Lorentz-force resonant MEMS magnetometers. *J. IEEE Sensors* **19**, 7479–7488 (2019).
38. Zhang, W. & Lee, J. E.-Y. A horseshoe micromachined resonant magnetic field sensor with high quality factor. *Electron Device Lett.* **34**, 1310–1312 (2013).
39. Reitz, J. R., Milford, F. J. & Christy, R. W. *Foundations of Electromagnetic Theory* (Pearson/Addison-Wesley, **2009**). <https://books.google.es/books?id=vNVDpGAACAj>.
40. Laghi, G., Marra, C. R., Minotti, P., Tocchio, A. & Langfelder, G. A 3-D micro-mechanical multi-loop magnetometer driven off-resonance by an on-chip resonator. *J. Microelectromech. Systems* **25**, 637–651 (2016).
41. Sonmezoglu, S. & Horsley, D. A. Reducing offset and bias instability in Lorentz force magnetic sensors through bias chopping. *J. Microelectromech. Systems*, 1–10. <https://doi.org/10.1109/JMEMS.2016.2626259>. <https://ieeexplore.ieee.org/abstract/document/7747521> (2016).
42. Chulliat, A. et al. The US/UK world magnetic model for 2015–2020: Technical report. <https://doi.org/10.7289/V5TB14V7>. [http://www.geomag.bgs.ac.uk/documents/WMM2015\\_Report.pdf](http://www.geomag.bgs.ac.uk/documents/WMM2015_Report.pdf) (2015).
43. Chulliat, A. et al. The US/UK world magnetic model for 2020–2025: Technical report. <https://doi.org/10.25923/ytk1-yx35>. <https://repository.library.noaa.gov/view/noaa/24390> (2020).
44. Gabrielson, T. B. Mechanical-thermal noise in micromachined acoustic and vibration sensors. *IEEE Trans. Electron Devices* **40**, 903–909 (1993).
45. Valle, J., Fernández, D. & Madrenas, J. Experimental analysis of vapor HF etch rate and its wafer level uniformity on a CMOS-MEMS process. *J. Microelectromech. Systems* **25**, 401–412 (2016).
46. Sánchez-Chiva, J. M., Valle, J., Fernández, D. & Madrenas, J. A CMOS-MEMS BEOL 2-axis Lorentz-force magnetometer with device-level offset cancellation. <https://doi.org/10.3390/s20205899> (2020).
47. Mata-Hernandez, D., Fernández, D., Banerji, S. & Madrenas, J. Resonant MEMS pressure sensor in 180 nm CMOS technology obtained by BEOL isotropic etching. *Sensors* **20**, 6037 (2020).
48. Laghi, G., Longoni, A. F., Minotti, P., Tocchio, A. & Langfelder, G. 100  $\mu\text{A}$ , 320 nT/ $\sqrt{\text{Hz}}$ , 3-axis Lorentz force MEMS magnetometer. In *2015 Transducers-2015 18th International Conference on Solid-State Sensors, Actuators and Microsystems (TRANSDUCERS)*, 803–806 (IEEE, 2015). <https://doi.org/10.1109/TRANSDUCERS.2015.7181045>. <https://ieeexplore.ieee.org/document/7181045>.
49. Marra, C. R. et al. 100 nT/ $\sqrt{\text{Hz}}$ , 0.5 mm<sup>2</sup> monolithic, multi-loop low-power 3-axis MEMS magnetometer. In *2018 IEEE Micro Electro Mechanical Systems (MEMS)*, 101–104, (2018). <https://doi.org/10.1109/MEMSYS.2018.8346493>. <https://ieeexplore.ieee.org/document/8346493>.
50. Mahdavi, M., Ramezany, A., Kumar, V. & Pourkamali, S. SNR improvement in amplitude modulated resonant MEMS sensors via thermal-piezoresistive internal amplification. In *2015 28th IEEE International Conference on Micro Mechanical Systems (MEMS)*, 913–916. <https://doi.org/10.1109/MEMSYS.2015.7051108>. <https://ieeexplore.ieee.org/document/7051108> (2015).
51. Kumar, V., Ramezany, A., Mahdavi, M. & Pourkamali, S. Amplitude modulated Lorentz force MEMS magnetometer with picotesla sensitivity. *J. Micromech. Microeng.* **26**, 105021 (2016).
52. Blevins, R. D. and Plunkett, R. *Formulas for natural frequency and mode shape*, **47**, 461–462 (1980).
53. Warren Clarence, Young and Richard Gordon, Budynas. *Roark's formulas for stress and strain*, volume 7. (McGraw-Hill New York, 2002). ISBN 978-0070725423.
54. Valle, J., Fernández, D. & Madrenas, J. Closed-form equation for natural frequencies of beams under full range of axial loads modeled with a spring-mass system. *Int. J. Mech. Sci.* (2019). <https://doi.org/10.1016/j.ijmecsci.2019.02.014>. <http://www.sciencedirect.com/science/article/pii/S0020740318322896>.
55. Tilmans, Harrie A. C. Equivalent circuit representation of electromechanical transducers: I. Lumped-parameter systems. *J. Micromech. Microeng.* **6**, 157 (1996).
56. Sánchez-Chiva, J. M., Fernández, D. & Madrenas, J. A test setup for the characterization of Lorentz-force MEMS magnetometers. In *2020 27th IEEE International Conference on Electronics, Circuits and Systems (ICECS)*, 1–4. <https://doi.org/10.1109/ICECS49266.2020.9294898>. <https://ieeexplore.ieee.org/document/9294898> (2020).
57. Marshall, J. C., Herman, D. L., Vernier, P. T., DeVoe, D. L. & Gaitan, M. Young's modulus measurements in standard IC CMOS processes using MEMS test structures. *IEEE Electron Device Lett.* **28**, 960–963 (2007).
58. Tung, R. C., Garg, A., Kovacs, A., Peroulis, D. & Raman, A. Estimating residual stress, curvature and boundary compliance of doubly clamped MEMS from their vibration response. *J. Micromech. Microeng.* **23**, 045009 (2013).



59. Sun, C., Zhou, Zai-Fa, Li, Wei-Hua & Huang, Qing-An A simple method for extracting material parameters of multilayered MEMS structures using resonance frequency measurements. *J. Micromech. Microeng.* **24**, 075014 (2014).
60. Lemmon, E. W. & Jacobsen, R. T. Viscosity and thermal conductivity equations for nitrogen, oxygen, argon, and air. *Int. J. Thermophysics* **25**, 21–69 (2004).
61. Engineering ToolBox. Air - Dynamic and Kinematic Viscosity, (2003) (accessed 17 November 2020); [https://www.engineeringtoolbox.com/air-absolute-kinematic-viscosity-d\\_601.html](https://www.engineeringtoolbox.com/air-absolute-kinematic-viscosity-d_601.html).
62. Park, B., Li, M., Liyanage, S. & Shafai, C. Lorentz force based resonant MEMS magnetic-field sensor with optical readout. *Sens. Actuators A Phys.* **241**, 12–18 (2016).
63. Thompson, M. J. & Horsley, D. A. Parametrically amplified z -axis Lorentz force magnetometer. *J. Microelectromech. Systems* **20**, 702–710 (2011).
64. Madou, M. J. *Fundamentals of microfabrication: the science of miniaturization*. (CRC press, 2018). <https://doi.org/10.1109/MEMB.2003.1195708>.
65. CEN. EN 1999-1-2:2007 Eurocode 9: Design of aluminium structures. Part 1-2: General rules - Structural fire design. <https://www.phd.eng.br/wp-content/uploads/2014/11/en.1999.1.2.2007.pdf> (1999).
66. Li, Q., Goosen, J. F. L., van Beek, J. T. M., van Keulen, F. & Zhang, G. Q. Outgassing of materials used for thin film vacuum packages. In *2009 International Conference on Electronic Packaging Technology High Density Packaging*, 802–806. <https://doi.org/10.1109/ICEPT.2009.5270637>. <https://ieeexplore.ieee.org/document/5270637> (2009).
67. Savomin, B. et al. Outgassing characterization of MEMS thin film packaging materials. In *2013 IEEE 63rd Electronic Components and Technology Conference*, 1514–1518 (IEEE, 2013). <https://doi.org/10.1109/ECTC.2013.6575772>. <https://ieeexplore.ieee.org/abstract/document/6575772>.
68. TE. *Model 834M1-6000 IEPE SMD Triaxial Piezoelectric Accelerometer*. Agastat Relays/TE Connectivity (2015) (accessed 7 December 2020); [https://eu.mouser.com/datasheet/2/418/5/NG\\_DS\\_834M1\\_Accelerometer\\_A2-1130248.pdf](https://eu.mouser.com/datasheet/2/418/5/NG_DS_834M1_Accelerometer_A2-1130248.pdf).
69. Matyunin, N. et al. Magneticspy: exploiting magnetometer in mobile devices for website and application fingerprinting. In *Proceedings of the 18th ACM Workshop on Privacy in the Electronic Society*, 135–149. <https://arxiv.org/abs/1906.11117> (2019).
70. STMicroelectronics LIS3MDL. Product specification (accessed 13 January 2021); <https://www.st.com/resource/en/datasheet/lis3mdl.pdf>.
71. STMicroelectronics LSM303AGR. Product specification (accessed 13 January 2021); <https://www.st.com/resource/en/datasheet/lsm303agr.pdf>.
72. Freescale MAG3110. Product specification (accessed 13 January 2021); <https://www.nxp.com/docs/en/data-sheet/MAG3110.pdf>.
73. AKM AK8975. 3-axis electronic compass IC, product specification (accessed 27 May 2015); <http://www.akm.com/akm/en/product/datasheet1/?partno=AK8975>.
74. AKM AK09940. Product specification (accessed 13 January 2021); <https://www.akm.com/eu/en/products/tri-axis-magnetic-sensor/ak09940/>.
75. Honeywell HMC5883. Product specification (accessed 13 January 2021); <https://datasheetspdf.com/pdf-file/795536/Honeywell/HMC5883/1>.
76. Bosch BMM150. Product specification (accessed 13 January 2021); <https://www.bosch-sensortec.com/products/motion-sensors/magnetometers-bmm150/#technical>.
77. Marra, C. R., Gadola, M., Laghi, G., Gattere, G. & Langfelder, G. Monolithic 3-Axis MEMS multi-loop magnetometer: a performance analysis. *J. Microelectromech. Systems* **27**, 748–758 (2018).