

An Image Based PCB Fault Detection And Its Classification

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Abstract- The field of electronics is skyrocketing like never before. The habitat for the electronic components is a printed circuit board (PCB). With the advent of newer and finer technologies it has almost become impossible to detect the faults in a printed circuit board manually which consumes lot of manpower and time. This paper proposes a simple and cost effective method of fault diagnosis in a PCB using image processing techniques. In addition to fault detection and its classification this paper addresses various problems faced during the pre-processing phase. This paper overcomes the drawbacks of the previous works such as improper orientations of the image and size variations of the image. Basically image subtraction algorithm is used for fault detection. The most commonly occurring faults are concentrated in this work and the same are implemented using MATLAB tool.

Keywords- PCB, fault, orientation, size correction, image subtraction

I. INTRODUCTION

Industrial automation is one of the booming fields today. Automation helps in reducing a lot of manpower and time. The same applies to printed circuit board (PCB) manufacturing industries. One of the highest costs in manufacturing PCBs is visual inspection which includes various manual methods. So there is a tradeoff between cost and quality. The quality assurance is always important in an industry thus it is required to achieve maximum quality of a product with minimal cost.

Printed circuit defects are those defects which brings a deviation from the normal characteristics and functionality of a PCB. Any missing element or any extra elements on the board which is not intended is referred to as a defect. PCB defects can be categorized into two types namely functional defects and structural defects. Functional defects are those defects which are pertaining to the functionality of the circuit or the overall system. These are troublesome in nature. The other kind of the defect is called structural defect. It is also often referred to as cosmetic defects. Cosmetic defects refer to the changes in the appearance of the circuit board. The PCB manufacturing process is based on chemical and mechanical actions that may damage the intended design. Computer generated printed circuit board are those images which are defect free. These are often known as Base images/Template Images. These are designed as control images to compare with the image that contains defects.

This paper proposes a method which overcomes the drawbacks of the existing works such as improper orientations of the image and size variations of the image. Basically image subtraction algorithm is used for fault detection. With few

modifications to image subtraction algorithm the faults can be classified into separate types. The majorly occurring faults in a PCB are missing conductors, etching, wrong size hole, missing hole and pinhole. Along with these some problems with the image orientation and size variation corrections which takes place during preprocessing phase are addressed in this work.

II. LITERATURE SURVEY

[1] This paper has discussed about detection of faults using image subtraction technique and the classification of the defects into various groups. They discuss the various possible ways of fault detection in a PCB alongside defects are categories into seven groups with a minimum of one defect and up to a maximum of 4 defects in each group using MATLAB image processing tools this research separates two of the existing groups containing two defects each into four new groups containing one defect each by processing synthetic images of bare through-hole single layer PCBs. [2] This paper has proposed a method for defect detection and classification for the faults in a PCB. Image subtraction algorithm has been used for finding the defects and separate algorithms with further modifications have been implemented. In this work a base image is read and stored. An inspection image is compared with the stored image and is subtracted to give out the results. The main algorithm remains the same i.e, image subtraction. The other subsidiary algorithms are used for classifying the faults individually rather than in groups. [3] This paper deals with the fault detection of assembled PCBs where the inspection is done even after assembling the components with the PCB. [4] This paper enhances the work by inspecting the solder pasted PCBs. [5] This work brings up

the concept of neural networks in order to address the situation of solder joints inspection combined with sophisticated genetic algorithm. [6] This paper presents the techniques used to inspect the defects on Surface Mount PCBs. The technique of windowing is employed to reduce the amount of redundant data to be processed and computation time.

Even though there are plenty of algorithms to find the faults in a PCB using image processing techniques, there are only few methods to classify those defects and group them. Individual defects cannot be found out accurately. Thus they are formed in groups. Thus one can improve the method by increasing the classification of the faults and enhance the existing group numbers to a higher number of groups so that one can easily find which fault has occurred in particular. Also some pre processing techniques such as image orientation and size correction can be implemented.

III. METHODOLOGY

A. Problem Statement

The current techniques give focus on defect detection and defect classification. But our method proposes a system which addresses few problems such as irregular orientations and irregular sizes of test images when compared with the master image. Thus the problem is to correct these preliminary requisites.

B. Problem Formulation

Various pre processing techniques like orientation correction and size correction are done in the first phase. The later phase includes defect detection and followed by defect classification. Thus it is a three tier procedure.



Figure 1. Pre processing Techniques.

C. System Implementation

The test image is first read in MATLAB environment. For image orientation correction first we check various angles such as 90, 180 and 270 degrees. Thus there are 3 test cases for image orientation correction. Once we are done with image orientation correction we proceed further for image size correction. Although it is not a sequential procedure the figure1 just shows the flow. Thus image size correction is done to the test image if any.

The previous methods have few drawbacks one of them being the image orientation. Image orientation is regarded as very important thing because if the orientation of the images are not matched with each other it fails to give out the correct results. The image can be tilted in any direction. As a test case 3 important orientations are addressed in our work. They are

namely 90, 180 and 270 degrees. The algorithm proposes recursive steps of tilt angle correction. Once it is matched with the original image, faults can be found out and can be classified individually.

The other drawback of the existing work is that the arbitrary sizes of the images which doesn't give proper results. If the sizes aren't properly matched it is impossible to carry out image subtraction. When the size of the master image doesn't match with that of test image this problem occurs. If the image is of improper size or not cropped properly the pixel values doesn't match each other. In such case size correction is necessarily required for computation failing to which we will not be able to proceed further.

D. Proposed Algorithm

The proposed methodology for image orientation correction is carried out by following steps

- Import a master image
- Read the column values of the master image
- Import a test image
- Read the column values of the test image
- Compare both the values
- If the compared values are same then subtract the images else correct the orientation by some angle of tilt recursively until both of them match with each other

The proposed methodology for image size correction is carried out by following steps

- Import a master image
- Read the row values of the master image
- Import a test image
- Read the row values of the test image
- Compare both the values
- If the compared values are same then subtract the images else correct the size by comparing each pixel value with the master image pixel value similar to windowing technique.

Having done with the preprocessing techniques we enter the mainstream of PCB faults detection. A PCB can have as many as 14 different types of faults. 1.Missing conductor 2.Pin hole 3.Wrong size hole 4.Etching Defect 5.Missing hole 6.Breakout 7.Spur 8.Short 9.Open circuit 10.Conductors too close 11.Spurious copper 12.Excessive short 13.Mouse bite 14.Overetch. Out of these some of the most commonly occurring faults are numbered from 1 to 5 namely Missing conductor, Pin hole, Wrong size hole, Etching Defect and Missing hole. So this work mainly concentrates on diagnosing these faults and classifying them individually rather than in groups.

Once the pre processing steps are taken care the next step is to find the defects in the PCB. To find the overall defects in the

PCB, image subtraction algorithm is employed and thus it is considered as master algorithm. For further individual classification of faults separate algorithms are implemented. These algorithms are referred to as subsidiary algorithms since each of them are derived from the master algorithm. Considering two images A and B as master image and test images respectively, we can perform two kinds of subtraction operations on them i.e., (A-B) and (B-A). For both the operations we get two different resultant images. They are called as positive image and negative image respectively. A positive image gives the extra unwanted material which gives one set of faults. A negative image gives the missing material which gives another set of faults. On combing both of these faults together we get the overall set of faults.

In the next step we classify the defects into different types individually. Any missing path of copper in the board is considered as missing conductor. To classify this fault the faulty image is imported and is complemented. The complemented image is subjected to flood fill operation. The same is subtracted with the negative image. This results in an image with missing conductors.

Any unnecessary extra material of copper on the master image is considered as etching defect. Here the master image is complemented. The complemented image is subjected to flood fill operation. The same is subtracted with the positive image. This results in an image with etching defect.

Any undesirable hole like projection that is either partially filled or completely filled with the copper is referred to as the hole defect. The completely filled space is referred to as missing hole. The partially filled space is referred to as wrong size hole. The etching defect image is retained from the previous result and is subtracted with positive image. This results in hole defect classification.

Any tiny dot or a pin point like projection which is regarded as an undesirable entity on the copper material is referred to as a pinhole. Any such hole can be considered as connected component label. Image labeling operation is carried out here. Each of it can have 4 neighbors or eight neighbors. If such a kind exists then it is classified as pinhole defect.

Hence the most commonly occurring faults can be diagnosed and can be classified separately by following the above algorithms.

IV. RESULTS

Our proposed work takes two input images. One is a master image and other is a test image or a faulty image. The test image is subjected to preprocessing corrections thereby correcting the preliminary requisites for image subtraction process. Once the images are fit to be subtracted they undergo image subtraction which gives only one output image. The output image is further processed to carry out different types of classification and each of them appears in a separate image

in the MATLAB window. A graphical user interface has been created for convenience.

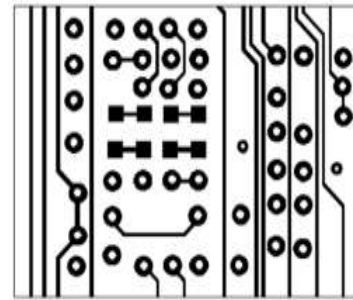


Fig 2. Master image

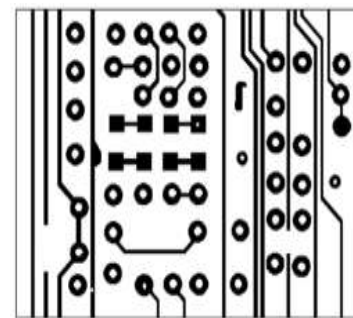


Fig 3. Faulty image

Figure 2 and 3 shows master image and faulty image respectively. Here both the images are having same orientations and gives out good results. But if there is mismatch between the two it fails to give out faithful results.

A. Preprocessing corrections

The different orientations are shown below

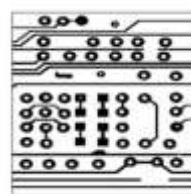


Fig 4 (a)

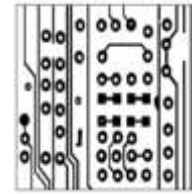


Fig 4 (b)

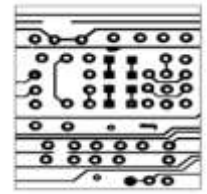


Fig 4(c)

Figures 4(a), 4(b) and 4(c) shows 90, 180 and 270 degrees of rotation respectively.

The angle of rotation of the various test images are with respect to the master image. These images are subjected to image orientation correction process to recover the proper angle.

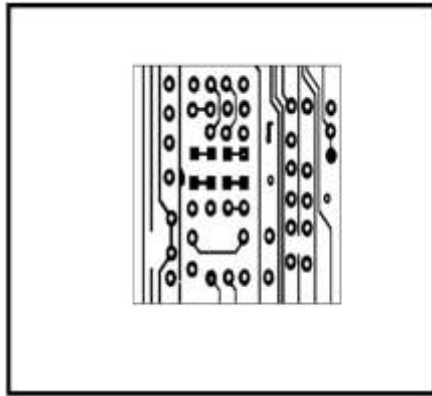


Fig 5. Varied size faulty image

The figure5 shows the test image which has a size that is different from the master image. To address this problem the test image is subjected to size correction which recovers the original size. Once both the images match with each other further procedure is carried out.



Fig 6(b). Corrected images during preprocessing phase

The operations were done on different types of test images for all the three test cases and were found to be correct. Various images of different sizes were considered. The best case and the worst case of the faults were also considered and were successfully tested.

B. Overall defect detection

Once the preprocessing correction steps are taken care the next step is to find the overall faults in the defective PCB image. This can be done with the help of image subtraction principle. Defect detection pushbutton helps to obtain the overall defect image. Thus an image with total defects can be found out.



Fig 6(a) MATLAB based GUI

The above figure shows a GUI which allows the user to import the images of his interest. Various push buttons are assigned for particular operations to be done. A master image can be imported by clicking the green button titled by its name signifying that as original image. On pushing the red button we get a chance to select the faulty image of our interest. It is not necessary to select the wrong size image simultaneously but we can select it by clicking wrong size image button. In order to correct the orientation of the image we need to press orientation correction button. In order to correct the size of the image we need to press size correction button. Thus this completes the preprocessing corrections. The corrected images are shown as follows.

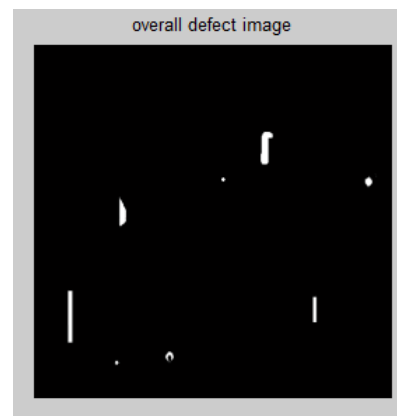


Fig 7. Overall defects

On successfully finding the overall faults the next step is to classify them into which types they belong to. In order to do defect classification a sequence of operations has to be done on the overall defect image.

C. Defect classification

The overall defect image just gives the information whether there exists any fault in the PCB or not. If any fault is present it is shown in the window otherwise it displays nothing. In the former case even though it shows the overall defects, it fails to classify them individually into particular type of fault. So this is done by defect classification button. On executing that button the various faults are classified in each window. Each fault is handled using simple algorithm which follows image

subtraction algorithm followed by image complementation, image flood fill operation and image indexing operations as common procedures with slight difference to each of it. By implementing each algorithm faults can be classified and distinguished clearly without any ambiguity.

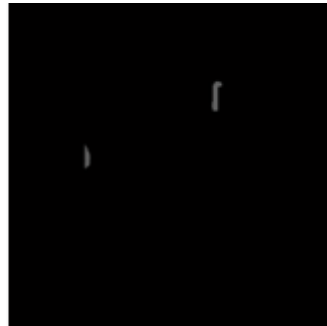
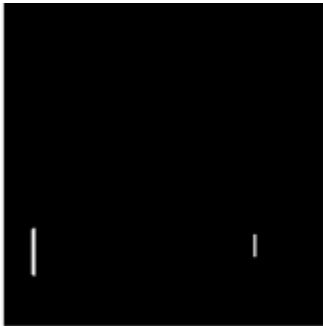


Fig 8(a) Missing conductor

Fig 8(b) Etching defect

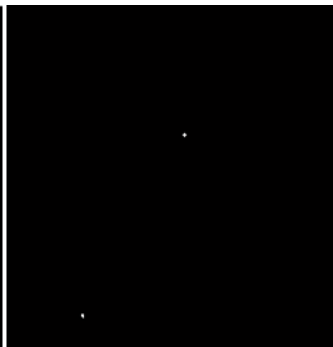
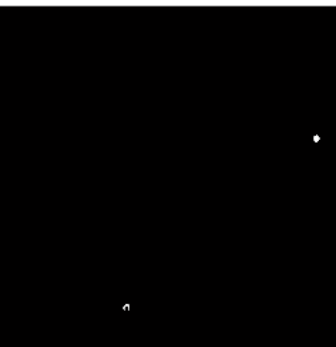


Fig 8(c) Hole defects

Fig 8(d) Pinhole defect

V. CONCLUSION AND FUTURE SCOPE

Our work overcomes the existing drawbacks such as improper orientations of test image with respect to master image and improper sizes of test image compared to master image. In addition to that our work successfully detects the defects in a PCB image and classifies the same accurately. This method provides less time complexity when compared with other segmentation procedures. As a future scope one can develop an IC which can test the PCB and classify the defects in one single go.

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