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Towards Privacy Aware Deep Learning for Embedded Systems

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ABSTRACT

Memorization of training data by deep neural networks enables an adversary to mount successful membership inference attacks. Here, an adversary with *blackbox* query access to the model can infer whether an individual’s data record was part of the model’s sensitive training data using only the output predictions. This violates the data confidentiality, by inferring samples from proprietary training data, and privacy of the individual whose sensitive record was used to train the model. This privacy threat is profound in commercial *embedded systems* with on-device processing. Addressing this problem requires neural networks to be *inherently private by design* while conforming to the memory, power and computation constraints of embedded systems. This is lacking in literature.

We present the first work towards membership privacy by design in neural networks while reconciling *privacy-accuracy-efficiency* trade-offs for embedded systems. We conduct an extensive *privacy-centred* neural network design space exploration to understand the membership privacy risks of well adopted state-of-the-art techniques: model compression via pruning, quantization, and off-the-shelf efficient architectures. We study the impact of model capacity on memorization of training data and show that compressed models (after retraining) leak more membership information compared to baseline uncompressed models while off-the-shelf architectures do not satisfy all efficiency requirements. Based on these observations, we identify quantization as a potential design choice to address the three dimensional trade-off.

We propose **ГЕССО** training methodology where we explicitly add resistance to membership inference attacks as a design objective along with memory, computation, and power constraints of the embedded devices. We show that models trained using **ГЕССО** are comparable to prior defences against blackbox membership attacks in terms of accuracy and privacy while additionally providing efficiency. This enables **ГЕССО** models to be deployed on embedded systems while providing membership privacy.

KEYWORDS

Membership Privacy, Inference Attacks, Efficient Deep Learning, Embedded Computing.

1 INTRODUCTION

The tremendous performance of machine learning (ML), especially deep neural networks (NNs), has resulted in their deployment to low-powered wearable devices and embedded systems for various applications such as video processing and analytics [4]. Specifically, Internet of Things (IoT) devices prefer on-device processing to reduce communication latency and overhead, while also preserving the confidentiality of data from an untrusted data curator [34]. Such NN architecture designs should conform to efficiency constraints on

memory, energy, and computation for embedded devices, and also maintain high prediction accuracy. Additionally, privacy laws, such as HIPAA and GDPR, require on-device processing to maintain the privacy of an individual’s sensitive data (e.g., medical records, location traces, purchase preferences) against privacy attacks such as membership inference attacks [27, 30, 32, 39]. Hence, NNs deployed for low powered embedded systems are required to incorporate privacy, accuracy and efficiency.

Membership Inference Attack. NNs, deployed for wearable devices and embedded systems (e.g., for remote patient monitoring), are trained using private and potentially sensitive data collected from multiple individuals. Such systems, executing the fully trained model, are then used by customers, for instance, to monitor health indicators from physiological signals. An adversary, given access to the model via an API, leverages membership inference attack to identify whether a particular target data record was used in the proprietary and sensitive training data of the model. This reveals the health status of the individual corresponding to the target data record. This is a privacy risk in cases where the knowledge of a data record’s membership in the model’s training dataset is sensitive which has been extensively studied in prior literature [27, 30, 32, 39]. For instance, an adversary can exploit a model trained on data records containing diagnostic information of cancer patients to identify whether an individual has cancer based on if the data record was used to train the model. This violates (a) the individual’s privacy and (b) confidentiality of the sensitive data. In such cases, it is crucial to design NNs resistant to membership inference attacks, where the adversary infers unobservable, sensitive information (e.g., individual’s health status) from the observable information (e.g., model predictions).

Motivation: Privacy by Design. Embedded systems demand on-device processing of data using NNs while conforming to the memory, power and computation constraints, leading to an efficiency and accuracy trade-off [24]. To bring NNs to edge devices, several optimizations such as model compression through pruning, quantization, and efficient off-the-shelf architectures are adopted. Currently, the designs of NNs for embedded systems do not consider privacy and such optimization schemes are not resistant to membership inference attacks, leaking private information of individuals’ training data. Further, it has been shown that membership inference attacks are due to memorization of training data by large capacity NN models and current privacy defences result in *privacy-accuracy* trade-off [1, 22]. However, *the impact of model capacity on membership privacy is unexplored and understanding this is crucial to design NNs while accounting for privacy-efficiency-accuracy trade-off*. This problem of designing NNs for embedded systems which are inherently private by design along with good efficiency and accuracy, motivates our work. We address this by answering two research questions in sequence:

*Work partly done at author’s current affiliation with University of Waterloo, Canada

- RQ1** How does varying model capacity, which influences the memorization of training data, impact membership privacy risks?
- RQ2** How can we design NNs with membership privacy along with accuracy and efficiency constraints for embedded systems?

Overview of Proposed Approach. First, we address **RQ1** by studying the membership privacy risks of three state-of-the-art hardware software co-design techniques, i.e., namely, model compression, quantization and efficient off-the-shelf architectures. We show that model compression (i.e., pruning the network followed by retraining) leaks more information compared to baseline uncompressed models indicating a higher membership privacy risk to the individual’s data. Off-the-shelf architectures (i.e., MobileNet and SqueezeNet) do not meet all the efficiency requirements but limits the membership privacy risk. Quantization satisfies privacy and efficiency constraints at the cost of accuracy.

This analysis forms the basis of identifying the best design choice for neural network architecture for embedded devices to address **RQ2**. We identify *quantization* as our design choice. We propose **GECKO** – a two phase training methodology for designing NNs optimized specifically for privacy, accuracy and efficiency. Phase I of **GECKO** quantizes the model parameters and intermediate layerwise outputs by constraining their values to $\{-1,+1\}$ to reduce the memory, energy consumption, and computation overhead. This optimizes the model for efficiency *and* privacy but at the cost of accuracy.

Main Contributions. We claim the following main contributions:

- We present the **first work on membership privacy by design in NNs** and conduct a privacy-centred design space exploration of NNs to understand the trade-offs between privacy, efficiency and accuracy for embedded devices. We find that quantization provides significant resistance to membership inference attacks while satisfying efficiency requirements compared to model compression and off-the-shelf efficient architectures. (Section 4 and 5)
- We propose **GECKO** training for NNs by choosing **quantization as the key design choice combined with knowledge distillation** to improve the privacy, accuracy and efficiency trade-off¹. (Section 6 and 7)
- We show **GECKO** models are **comparable in accuracy and privacy risks to prior state-of-the-art defences against black-box membership inference attacks**, adversarial regularization and differential privacy, while additionally providing efficiency for deployment to embedded systems. (Section 8)

2 BACKGROUND

ML algorithms learn a function $f : X \rightarrow Y$ mapping from the input space X to the space of corresponding class labels Y . This is modeled as an optimization where the objective is to find the parameters θ by minimizing the model’s loss, $\min_{\theta} L(f(x), y; \theta)$. We discuss the constraints of executing NNs on embedded devices (Section 2.1) and the current state-of-the-art NN designs tailored for embedded devices (Section 2.2). Then, we describe the privacy threat of membership inference attacks considered in this work (Section 2.3).

¹Code: <https://github.com/vasishtduddu/EmbeddedMIA>

2.1 Requirements for Embedded Devices

The list below presents the most important requirements accounted by designers of NNs based embedded systems [35], and unfortunately, privacy is not a part of it:

- **Energy Efficiency.** Energy consumption is a vital constraint to maximize the battery lifetime of low powered embedded devices which operate for long duration. In NN execution, multiply and accumulate (MAC) operations computes the product between model parameter matrix and layerwise output vector (called activations) and stores the intermediate results. Specifically, MAC operations access memory to read weights, inputs, and intermediate outputs from previous layer, and write the computed output to the memory. These read-write operations consume energy which is higher than the actual MAC computations [15]. Hence, energy efficiency is achieved by reducing the memory accesses by (a) optimizing hardware to exploit sparsity in MACs [37] and (b) reducing the precision to increase the throughput of data [16, 20, 24].
- **Computation Efficiency.** The total MAC operations quantifies the requirement of computation efficiency [35]. The computation speed of CPUs on embedded devices restricts the processing rate of MACs, which reduces with the number of parameters (of NNs) [16, 20, 24]. Additionally, replacing MACs with cheaper binary arithmetic significantly lowers the computational overhead [16, 20, 24].
- **Memory Efficiency.** The memory footprint of NN parameters and additional runtime storage for intermediate outputs, should be within the memory constraints of the embedded devices. There are two ways to achieve this requirement: (a) reducing the precision of the parameters and intermediate outputs [16, 20, 24] and (b) pruning the parameters by increasing sparsity [10–12, 38].

2.2 Neural Network Designs for Efficiency

To address the computational, memory and energy requirements of embedded devices, model designers use three state-of-the-art approaches for designing efficient NN models for embedded systems: (a) model compression via pruning, (b) quantization of model parameters and activations and (c) designing standard architectures (off-the-shelf efficient architectures).

Model Compression (Pruning + Retraining).² NNs are overparameterized, i.e., have a large number of redundant weights. Pruning the network removes these redundant weights (setting them as “0”) without degradation of model accuracy. The pruning operation results in a model with sparse parameters for which the hardware designers skip the multiplication and memory storage to improve efficiency. Sparse weights can be stored in a compressed format in the hardware using the compressed sparse row or column format which reduces the overall memory bandwidth [9, 10, 12]. Furthermore, aggressive pruning to compress the model significantly, requires the model to be re-trained to restore the original accuracy. For a sensitivity threshold τ and NN model f_{θ} , the parameters θ close to “0” are replaced by “0”: $f'(\theta) = \begin{cases} 0, & \text{if } -\tau \leq \theta \leq \tau \\ \theta, & \text{otherwise} \end{cases}$

²We use the terms pruning and compression interchangeably

Off-the-Shelf Efficient Architectures. NNs can be redesigned by changing the hyperparameters (i.e., filter size in convolution, number of layers and their types) to reduce the number of parameters and hence, the memory footprint. One approach is to replace larger convolution filters with multiple smaller filters with less number of parameters but covering the same receptive fields. For instance, one 5x5 filter can be replaced by two 3x3 filters. Alternatively, 1x1 convolutional layers reduce the number of channels in output feature map, lowering the computation and number of parameters. For instance, for an input activation of dimension 1x1x64, 32 1x1 convolutional filter downsamples the activation maps to get an output of 32 channels. Such optimizations enable to design compact network architecture with layers having lower parameters compared to the original model, extensively adopted in MobileNet [28] and SqueezeNet [18].

Quantization. Quantization reduces the precision of the model’s parameters and the intermediate activations during execution. Quantization maps parameters and activations to a fixed set of discrete levels [17]. The number of quantized levels determines the precision of the operands ($\log_2(\#levels)$). This reduction in the precision of the parameters lowers the storage cost of the model in memory. In addition, reducing the precision of activations lowers the computation overhead by replacing MACs with binary arithmetic. This further reduces the energy consumption by lowering the memory accesses and increasing throughput. Aggressively quantizing the parameters and activations to binary and ternary precision significantly improves the overall efficiency, however, at the cost of accuracy [24]. For instance, Binarized NNs quantize the operands to $\{-1,+1\}$ values [16] while ternary NNs have values $\{-w, 0, w\}$ where w can be fixed or learnt during training [20]. These are examples of uniform quantization.

Alternatively, weight sharing maps several parameters to a single value reducing the number of unique parameters [10]. This mapping is done using K-Means clustering or a hashing function where a *codebook* maps different parameters to the corresponding shared values.

2.3 Privacy Threat: Membership Inference

NNs for embedded systems do not account for privacy threats, however these threats still exist if personal and potentially sensitive data feed the system. For instance, if the adversary learns something specific about a user’s data record used in the training dataset, we refer to such information as membership privacy leakage. This privacy leakage about a user’s record can be, for instance, the membership details of the record in the training set of the model, referred to as membership inference attack.

In this work, we specifically use membership inference attacks to quantify information leakage in ML models following several prior work [27, 30, 32, 39]. ML models are more confident while predicting the class of already seen train data records compared to unseen test data records. Membership inference attacks exploit this difference in the model’s confidence to classify a new data record as being a “Member” or “Non-Member” of the model’s training data. This is a binary decision problem where the adversary classifies the membership of a given input x using the model’s output prediction

$f(x; \theta)$ to infer whether a given data record was used in the model’s training data or not.

Attack Details. Given a user’s data record $x \sim P(X, Y)$, where $P(X, Y)$ is the data distribution from which the training data D_{train} was sampled, the adversary estimates $P(x \in D_{train})$ using the model’s prediction $f(x; \theta)$. Empirically, the adversary identifies a threshold to estimate whether $x \in D_{train}$ which can also be learnt using a binary classifier. In this work, we use the confidence score attack where the adversary obtains $f(x; \theta)$ and finds the maximum posterior value and infers $x \in D_{train}$ if the value is greater than a threshold [27, 39]. The attack is based on the observation that the maximal posterior of a member data record is higher (more confident) than a non-member data record of the training dataset. This particular attack has been used in several prior work for empirically estimating membership privacy risks [7, 27, 32, 33, 39].

Threat Model. We consider a blackbox setting where the adversary is assumed to have no knowledge about the target model. Formally, given a target model $f()$, the adversary only sees the final model prediction $f(x; \theta)$. The adversary does not know the architecture of $f()$ nor the model parameters θ . We do not consider whitebox setting where the adversary has the access to both the model output predictions $f(x; \theta)$ as well as the architecture of $f()$ and the model parameters θ . Indeed, this whitebox setting does not necessarily result in any benefit to the adversary in terms of attack accuracy (shown theoretically [26] and empirically [29, 32]). Consequently, blackbox setting is a more practical setting seen typically in ML as a Service (MLaaS) and commercial embedded devices such as wearable and IoT devices, where the adversary submits an input query to the trained model via an API and obtains the corresponding output.

3 EXPERIMENT SETUP

We describe our experimental setup: the datasets and architectures used in our analysis (Section 3.1), and the considered metrics (Section 3.2).

3.1 Datasets and Architectures

For evaluating and comparing different efficiency algorithms, we mainly use four standard benchmarking datasets: FashionMNIST, CIFAR10, PURCHASE, and LOCATION. We train the model for 75 epochs for FashionMNIST and 100-150 epochs for CIFAR10, PURCHASE, and LOCATION.

FashionMNIST consists of 60,000 training examples and a test set of 10,000 examples. Each data record is a 28x28 grayscale image which is mapped to one of 10 classes consisting of fashion products such as coat, sneaker, shirt, shoes. For this dataset, we use a modified LeNet architecture with two convolution layers followed by maxpool and dense layers: [Conv 32 (3,3), Conv 64 (3,3), Maxpool (2,2), Dense 128, Dense 10] (Architecture 1). Additionally, we use a fully connected model [512, 512, 512] (Architecture 2).

CIFAR10 is a major image classification benchmarking dataset where the data records are composed of 32x32 RGB images where each record is mapped to one of 10 classes of common objects such as airplane, bird, cat, dog. For this dataset, we use standard state-of-the-art architectures: Network in Network (NiN), AlexNet and VGGNet.

PURCHASE is a dataset capturing the purchase preferences of online customers taken from the authors of [30]. The data records have 600 binary features and each record is classified into one of 100 classes identifying each user’s purchase. For this dataset, we use a fully connected architecture with the nodes in each layers as [1024, 512, 256, 128, 100].

LOCATION is a dataset capturing user’s location check-ins taken from the authors of [30] where each record has 446 binary features which is mapped to one of 30 classes each representing a location. For this dataset we use a fully connected architecture with hyperparameters as [512, 256, 128, 30].

3.2 Metrics

We consider two types of metrics in our evaluation capturing the efficiency and the privacy.

Efficiency. We evaluate efficiency in terms of memory, computation and energy. Memory efficiency is compared based on the reduction in the memory footprint of the model computed from the parameters stored in the memory. Computation efficiency is compared based on the reduction in the MAC operations which influences the execution time. Finally, the energy consumption is compared based on memory accesses from reading inputs and writing results to the memory. Since, significant literature has compared the efficiency empirically, we provide a qualitative comparison for the baseline algorithms from prior work [35]. We instead focus on privacy-centred analysis as our main contribution building on top of the efficiency analysis from prior work.

Privacy. We use the membership inference attack accuracy to estimate the membership privacy risks. An accuracy above random guess 50% indicates a training data membership leakage. This indicates that the adversary is able to identify the membership status of a data record with an accuracy higher than random guess. The success of inference attack accuracy is strongly correlated with the model’s extent of overfitting empirically measured as the difference between the train and test accuracy (i.e., generalization error). Higher generalization error (i.e., overfitting) results in higher distinguishability between the test and train resulting in higher membership inference accuracy [30]. Additionally, the accuracy of the model is computed on an unseen test data.

4 EFFICIENCY ANALYSIS

In view of the memory, computation and energy efficiency requirements, we qualitatively compare the three baseline algorithms (i.e., model compression, off-the-shelf architecture, and quantization).

Memory Efficiency. We specifically focus on the impact of the three optimization techniques on the model’s static memory required to store the model parameters. This static memory requirement is the major bottleneck compared to the runtime memory requirement (storing the intermediate activation outputs with lower dimension than model parameters).

- **Model Compression:** Here, the model parameters which are pruned are simply replaced by a value of “0”. Hence, storing even the “0” parameter takes up memory and does not necessarily decrease the overall memory footprint unless the hardware is optimized to skip the storage of all the zero values in the memory.

This requires additional hardware logic to exploit the sparsity which may not be readily available.

- **Off-the-shelf Architectures:** The models are designed to specifically reduce the memory footprint. For instance, the memory footprint of Squeezenet and MobileNet is 5MB and 14MB compared to 250MB of Alexnet and more than 500MB of VGG architectures [18, 28].
- **Quantization:** Lowering the model precision from 64 or 32 bit floating point to binary precision results in a direct reduction of 64x or 32x in the overall memory footprint of the model.

Computation Efficiency. A high computational efficiency, i.e., lower number of floating point MAC operations between parameters and intermediate outputs from each layer, typically results in faster inference.

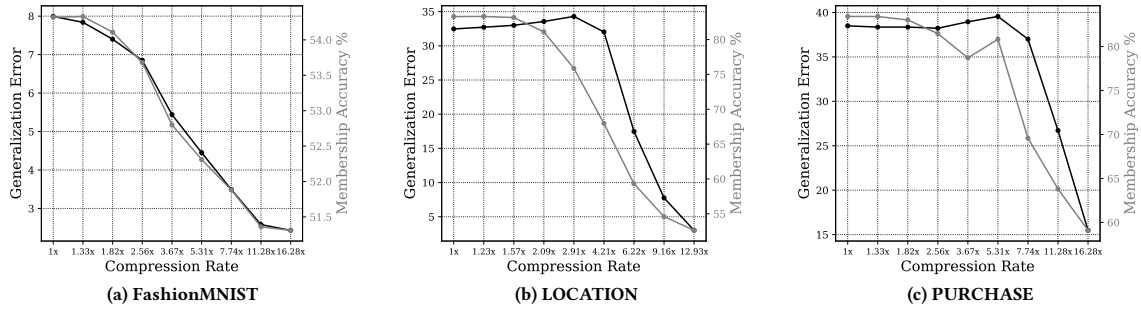
- **Model Compression:** Here, achieving efficiency requires additional hardware optimization. Particularly, instead of actually computing the multiplications with “0” pruned values, the hardware optimization enables the user to skip the computation and replace the output by a “0” directly.
- **Off-the-shelf Architectures:** These models replace the complex matrix-vector multiplications to smaller dimensions. This reduces the overall number of parameters but it has been shown empirically³ that this does not necessarily reduce the number of multiply accumulate operations [2].
- **Quantization:** For models with binarized parameters and activations, the MACs can be replaced by binary XNOR operations, maxpool can be replaced by OR operation, while the activations can be replaced by checking the sign bit. This reduces the floating point operations per second drastically [25].

Energy Efficiency. Energy efficiency does not vary much with reduction of number of parameters and data type, but the number of memory accesses plays a vital role [15]. The benchmarking of energy consumption for different optimization and architectures is well explored and out of scope of this work. We refer the readers to [35] for more details. We qualitatively summarize the impact on energy consumption for the three specific algorithms considered.

- **Model Compression:** energy efficiency can be marginally improved by additionally providing hardware optimization [10, 38].
- **Off-the-shelf Architectures:** while computation efficiency improves, the energy efficiency is close to large scale state-of-the-art models like AlexNet [18, 35].
- **Quantization:** the energy efficiency is high as the memory access can be drastically reduced by increasing the throughput of data fetched from the memory. Specifically, lowering the precision from 32 bit floating point to binary results in lowering the memory accesses and 32x improvement in energy consumption [16, 24]. While some improvements are seen natively for quantized models (from replacing MACs with XNOR), higher benefits can be achieved via additional hardware optimization [37].

Summary: A qualitative comparison of efficiency based on empirical results from prior work between different optimization techniques indicates that the quantized architectures show significant benefits over the other alternatives.

³<https://github.com/albanie/convnet-burden>



dataset. This evaluation is done only on CIFAR10 dataset (i.e., gathering a collection of images) as these state-of-the-art architectures are specialized for bigger datasets and not adapted for the smaller datasets. As seen in Table 1, the SqueezeNet and MobileNet models show lower membership inference attack accuracy of 53.07% and 55.57% compared to larger models which have higher membership privacy risks.

Table 1: Model capacity influences membership privacy risks.

CIFAR10				
Architecture	Memory Footprint	Train Accuracy	Test Accuracy	Inference Accuracy
SqueezeNet	5 MB	88.21%	81.92%	53.07%
MobileNetV2	14 MB	97.50%	87.24%	55.57%
AlexNet	240 MB	97.86%	80.34%	60.40%
VGG11	507 MB	99.13%	86.43%	58.04%
VGG16	528 MB	99.58%	88.95%	58.70%
VGG19	549 MB	99.09%	88.18%	57.85%

Summary: Lowering the model capacity by reducing the memory footprint indeed lowers privacy risk as privacy risks for off-the-shelf architectures compared to large capacity models.

5.3 Quantization

We now evaluate the technique of reducing the precision of both model’s parameters and intermediate activations. We consider the extreme and the most optimized case of binarizing the parameters and activations. As a motivating example, we evaluate on Fashion-MNIST dataset for two model architectures with convolutional and fully connected layers as seen in Table 2.

Table 2: Reducing the model precision decreases the inference attack but at the cost of test accuracy.

FashionMNIST				
Architecture	Memory Accuracy	Train Footprint	Test Accuracy	Inference Accuracy
Architecture 1				
Full Precision	38.39 MB	100%	92.35%	57.46%
BinaryNet	1.62 MB	88.68%	86.90%	55.45%
XNOR-Net	1.62 MB	87.19%	85.68%	51.05%
Architecture 2				
Full Precision	29.83 MB	99.34%	89.88%	54.86%
BinaryNet	0.93 MB	97.61%	89.60%	54.30%
XNOR-Net	0.93 MB	92.67%	86.68%	51.74%

In both architectures, the computation on binarized values reduces the membership privacy risk. However, on replacing the MAC operations with XNOR operations the membership privacy risk decreases close to random guess, but at the cost of prediction accuracy. The corresponding results for CIFAR10 are shown in Table 4.

Summary: Lowering the model capacity using aggressive quantization (binarization of parameters and activation) along with XNOR computation provides strong resistance against inference attacks at the cost of accuracy.

5.4 Summary of Comparison

We summarize the properties satisfied by each of the approaches in terms of privacy, computation, memory and energy efficiency (Table 3) based on the observations in Section 4 and 5. Note that the privacy analysis and corresponding conclusions of each of the three embedded deep learning techniques is independent of the dataset allowing us to understand their trade-offs and compare them based on the trends observed. Here, we mark the attributes which are satisfied with ●, require additional hardware optimization as ◐ and do not satisfy the property with ○.

Table 3: Only quantization satisfies all the efficiency and privacy requirements but suffers from accuracy degradation.

Requirements	Compression	Off-the-shelf	Quantization
Computation Efficiency	◐	○	●
Memory Efficiency	◐	●	●
Energy Efficiency	◐	○	●
Privacy	○	●	●
Accuracy	●	●	◐

In order to design NNs for embedded devices, quantization (binarization with XNOR computation) is an attractive design choice which not only satisfies the computation, memory and energy efficiency but also provides high resistance against membership inference attacks. On the other hand, model compression leaks more training data membership details making it more vulnerable to membership inference attacks. Additionally, it requires hardware support and optimization to achieve better efficiency. Off-the-shelf architectures, while provide decent privacy, do not satisfy all aspects of efficiency. Hence, we choose quantization as a NN design choice for GECKO to provide a good three dimensional trade-off between privacy-accuracy-efficiency. We observe an accuracy degradation on using quantization and propose an approach as part of GECKO to address it.

6 GECKO: DESIGN OVERVIEW

Based on the comparative analysis described in the previous section, we propose GECKO which answers the research question RQ2. GECKO is a training methodology to construct NNs dedicated to embedded systems (e.g., IoT, wearable) with efficiency, accuracy, and privacy as main requirements:

- *Privacy.* The model should preserve the membership privacy of an individual’s data record in the training set of the model.
- *Efficiency.* The model should consume low energy, memory, and computation capacity for deployment to embedded devices.
- *Accuracy.* The model should have high test accuracy.

We address the above three requirements and reconcile the trade-offs in GECKO in two phases: In Phase I (Section 6.1), the quantized NN is generated by binarizing the parameters and activations to ensure efficiency and privacy and replacing MAC operations with

XNOR computations. However, we obtain efficiency and privacy at the cost of accuracy. In Phase II (Section 6.2), we account for the accuracy and improve it by optimizing the model obtained from Phase I. We use knowledge distillation by using a state-of-the-art full precision model’s predictions as target labels for training the quantized model.

6.1 Phase I

Based on our privacy-centric design space exploration in Section 5, we first quantize the NN’s parameters and intermediate activations. Specifically, we binarize the (parameter) values and intermediate activations (outputs) from each layer, i.e., map them to $\{+1,-1\}$. This operation (as seen in Section 5.3) results in high resistance to membership inference attacks as well as satisfies the different efficiency requirements (Section 4). The NN achieves computation efficiency by replacing the expensive matrix-vector multiplications with simple Boolean arithmetic operations, i.e., XNOR computations. Alternatively, instead of using multiplication and addition circuits in the hardware, we leverage XNOR logic on the inputs followed by a bitcount operation (counting the number of high bits “1” in a binary output sequence). The equation can be represented as follows: $\mathbf{x} \cdot \theta = N - 2 \times \text{bitcount}(\text{xnor}(\mathbf{x}, \theta))$

In terms of memory efficiency, binarization results in a direct reduction of the model size as well as intermediate output memory requirements by 32x to 64x. Lowering the precision also reduces the number of memory access by 32x to 64x resulting in a significant decrease in the energy consumption. This is due to the packing of 32x or 64x more number of single bit values for a single iteration of reading or writing in the memory compared to one value of 32 bit or 64 bit floating point values.

Algorithm 1 Inference stage of binary Neural Network with XNOR operations where W_k^b are the binarized weights (W_k) and a_k is the activation of the k^{th} layer

```

for  $k = 1$  to  $L$  do
   $W_k^b \leftarrow \text{Binarize}(W_k)$ 
   $a_k \leftarrow N - 2 \times \text{bitcount}(\text{xnor}(a_{k-1}^b, W_k^b))$ 
  if  $k < L$  then
     $a_k^b \leftarrow \text{Binarize}(a_k)$ 
  end if
end for

```

The complete inference stage of the binarized NN with XNOR computation is given in Algorithm 1. The matrix multiplication is done between the previous layer activation a_{k-1} and the current layer’s weights with the bitcount of XNOR operation’s output. Binarize() uses a threshold to map the input values to $\{-1,+1\}$. In addition to the above design, we use optimizations for XNOR-Net to avoid a significant loss in accuracy. It is well documented that it is difficult to converge a binarized model during training in case of incompatible hyperparameter settings and to this extent, we use the first and last layer of the model as full precision [36]. These additional optimizations have been used previously for XNOR based networks and provide higher accuracy and model convergence at a smaller memory and energy consumption overhead [24, 35].

6.2 Phase II

While we optimize for both privacy and efficiency in Phase I (at the cost of significantly reduced accuracy shown in Section 5.3), we restore the accuracy in Phase II such that it is close to the original full precision accuracy by using knowledge distillation [14]. Here, we consider a pre-trained teacher model $f_{teacher}$ with state-of-the-art accuracy on the classification task and use it to guide the training of the quantized classifier from Phase-I. During training of the quantized model (student), we do not compute the loss between the true label y and prediction vector $f_{student}(x)$. We instead estimate the cross entropy loss between the predicted label $f_{student}(x)$ and the prediction vector for the full precision teacher model $f_{teacher}(x)$.

This ensures that the student model learns to map the decision boundary of the teacher model and mimics the prediction behaviour for different inputs. Therefore, the accuracy of the student model increases compared to the original baseline of standalone training without the teacher model. We use the same training data for the quantized model and the full precision model.

7 EVALUATION OF GECKO

In this section, we present an extensive evaluation of GECKO on CIFAR10 dataset for Phase I (Section 7.1) and Phase II (Section 7.2). We focus our evaluation of GECKO on CIFAR10 to enable comparison with prior defences against membership inference attacks which are mainly evaluated on CIFAR10, e.g., differential privacy, which currently does not scale to larger datasets. Note that the proposed approach of using quantization followed by knowledge distillation is independent of the dataset and can be extended to larger datasets [14].

7.1 Evaluating Phase I

On quantization and replacing the MACs with cheap XNOR operations, we observe that the membership inference attack accuracy decreases significantly for all the three architecture close to random guess (~50%) (Table 4 for CIFAR10 and Table 2 for FashionMNIST). Specifically for CIFAR10, the membership inference accuracy decreases from 56.69% to 51.76% for NiN, 60.40% to 51.40% for AlexNet and 58.70% to 52.65% for VGGNet. However, since Phase I only optimizes the network for privacy and efficiency, the resultant model shows poor accuracy. We observe a significant loss in test accuracy for all the three models: around 8% accuracy drop from 86.16% to 78.74% for NiN; 14% accuracy drop from 80.34% to 66.8% for AlexNet; 14% for VGG model from 88.95% to 74.64%. In order to restore the accuracy, we use knowledge distillation as described in Phase II of the GECKO training methodology.

The privacy provided by quantized NN is due to the decrease in overfitting, empirically measured as the difference between the train and test accuracy. Furthermore, GECKO models have a lower model capacity on quantizing the parameters which lowers the memorization of training data by the parameters. Further, the quantization acts as a noise to strongly regularize the model [16]. At the same time, this optimization provides high degree of efficiency to be executed on low powered embedded devices.

Table 4: Reducing precision lowers membership privacy risk at the cost of accuracy.

CIFAR10				
Architecture		Train Accuracy	Test Accuracy	Inference Accuracy
NiN	Full Precision	98.16%	86.16%	56.69%
	Binary Precision	81.93%	78.74%	51.76%
AlexNet	Full Precision	97.86%	80.34%	60.40%
	Binary Precision	68.62%	66.8%	51.40%
VGG13	Full Precision	99.58%	88.95%	58.70%
	Binary Precision	79.67%	74.64%	52.65%

7.2 Evaluating Phase II

The objective of Phase II of G_ECKO is to enhance the accuracy of the quantized model with XNOR computations which depicts high membership inference attack resistance and efficiency. In Phase II, we use the teacher-student model (described in Section 6) to train the quantized student model. The quantized model training is supervised using the output predictions of the full precision teacher model. Here, Phase II is heterogeneous, i.e., we are flexible to choose any full precision teacher model which can provide high accuracy on the considered CIFAR10 dataset (Table 5). For the teacher models, we consider pre-trained state-of-the-art architectures⁴: DenseNet169 and ResNet50, along with the full precision versions of NiN, Alexnet and VGGNet. The standalone test accuracy of the DenseNet169 and ResNet50 architectures are 92.84% and 92.12% respectively with membership inference attack accuracy of 55%. The full precision test and attack accuracy for NiN, AlexNet and VGGNet are given in Table 4.

Table 5: Phase II of G_ECKO improves the accuracy of the private-efficient model from Phase I (CIFAR10).

Teacher	Student	Train Accuracy	Test Accuracy	Inference Accuracy
Standalone Models				
Binary NiN	-	81.93%	78.74%	51.76%
Binary AlexNet	-	68.62%	66.8%	51.40%
Binary VGG13	-	79.67%	74.64%	52.65%
Homogeneous Architecture Distillation				
NiN	Binary NiN	90.49%	83.52%	53.90%
AlexNet	Binary AlexNet	76.79%	73.5%	51.85%
VGG13	Binary VGG13	89.45%	81.58%	54.98%
Heterogeneous Architecture Distillation				
DenseNet169	NiN	92.84%	83.71%	54.95%
DenseNet169	AlexNet	81.87%	76.23%	53.51%
DenseNet169	VGG13	93.45%	85.8%	54.17%
ResNet50	NiN	91.74%	83.77%	54.53%
ResNet50	AlexNet	80.12%	74.92%	53.12%
ResNet50	VGG13	94.23%	86.52%	54.46%

Homogeneous distillation transfers knowledge from the same full precision model architectures to their quantized versions, e.g., full precision NiN with Binarized NiN. Here, we see that there is 5% increase in test accuracy (from 78.74% reported Table 4 to 83.52%)

⁴https://github.com/huyvnphan/PyTorch_CIFAR10

for NiN with an increase of 2% in membership inference attack. Similarly, there is an increase of 7% test accuracy for AlexNet with a very small membership privacy leakage increase of 0.45%; and increase of 7% test accuracy at the cost of 2% membership inference attack accuracy for VGGNet.

Heterogeneous distillation combines different architectures, e.g., DenseNet169 and ResNet50, with the quantized models from Phase I. We see that the increase in test accuracy is only minimally higher than the homogeneous models for NiN and AlexNet but shows a higher increase in the membership inference attack accuracy. However, in case of VGGNet, we observe an increase of 4% additional test accuracy compared to homogeneous knowledge distillation with a small decrease in the membership inference test accuracy. In Phase II, increase in test accuracy is accompanied with a small but acceptable increase in the inference attack accuracy indicating a privacy-utility trade-off. Compared to the full precision counterparts, we observe that the distilled models show an accuracy degradation of only 3% for NiN (86.66% to 83.77%), 4% for AlexNet (80.34% to 76.23%) and 2% for VGGNet (88.95% to 86.52%).

Summary: Compared to heterogeneous distillation, homogeneous distillation results in higher improvement in test accuracy but small gain in privacy. In view of this privacy-accuracy trade-off, the choice of using homogeneous or heterogeneous knowledge distillation is specific to the architecture and the acceptable privacy-accuracy requirements of the application.

Explaining the privacy gain. The G_ECKO framework results in models which make the output confidence of the train and test data records similar reducing the inference attack accuracy (Figure 3).

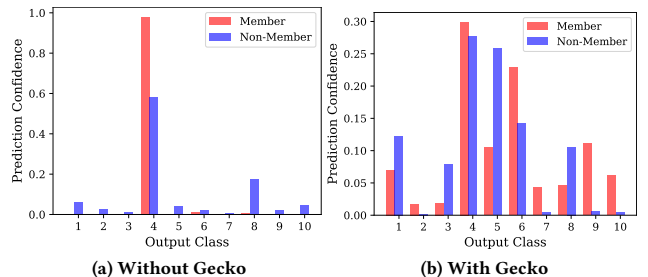


Figure 3: (a) Distinguishable predictions between train and test data records in undefended models makes them vulnerable to membership inference attacks, (b) G_ECKO models have indistinguishable confidence scores (CIFAR10).

Further, knowledge distillation in Phase II enables to lower the model’s loss compared to Phase I resulting in higher test accuracy as shown in Figure 4. However, this loss function is still higher than the full precision version indicating the small test accuracy degradation for some privacy gain.

8 COMPARISON WITH PRIOR DEFENCES

The privacy defences proposed in literature can be categorized into (a) modification of training algorithm (e.g., adversarial regularization (AR) and differential privacy (DP)) and (b) post-training

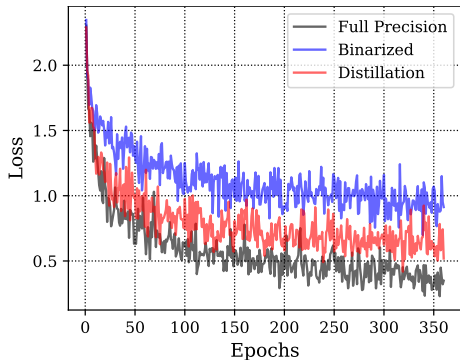


Figure 4: Loss functions in Phase I (Binarized) are higher than Phase II (Distilled Binarized) indicating the improvement in accuracy (CIFAR10).

techniques (e.g., MemGuard). GECKO training methodology is part of category (a) where we modify the training of the ML model in order to provide acceptable levels of privacy and accuracy.

MemGuard [19] adds carefully crafted noise to the target model’s output observations to ensure the misclassification of the adversary’s attack model). MemGuard has been shown to be ineffective against the membership inference attack considered in this work, i.e., prediction confidence attack [32]. Hence, we consider AR and DP as baselines for comparison.

Adversarial Regularization (AR) [22]. The problem of defending against membership inference attack is modelled as a mini-max game between two NNs: classifier model and attacker model. The two models are trained alternatively with conflicting objectives: first, the attacker model is trained to distinguish between the training data members and non-members followed by training the classifier model to minimize the loss as well as fool the attacker model.

Differential privacy (DP) [1]. We consider DP-SGD which adds carefully crafted noise to the gradients during backpropagation in stochastic gradient descent algorithm. The noise is sampled from a Laplacian or Gaussian distribution proportional to the model’s sensitivity which is then added to the gradients during backpropagation. This provides provable bound on the information leaked about an individual data record in the dataset and ensures that the presence or absence of a data record does not change the model’s output, hence defending against membership inference attacks.

Results. The comparison of models trained using GECKO is shown in Figure 5 (FP stands for Full Privacy and reports results without defence). Models trained using GECKO are comparable in test accuracy and resisting membership inference leakage to AR and DP. The inference accuracy for NiN is 52.90% (GECKO) compared to 54.09% (DP) and 51.92% (AR) and test accuracy of 83.52% (GECKO) compared to 85.11% (DP) and 83.66% (AR). For AlexNet, the inference accuracy is 51.85% (GECKO) compared to 52.81% (DP) and 51.83% (AR) and test accuracy of 73.5% (GECKO) compared to 79.27% (DP) and 71.02% (AR). For VGGNet, the inference accuracy is 53.17% (GECKO) compared to 52.90% (DP) and 53.33% (AR) and test accuracy of 85.8% (GECKO) compared to 84.91% (DP) and 85.19% (AR).

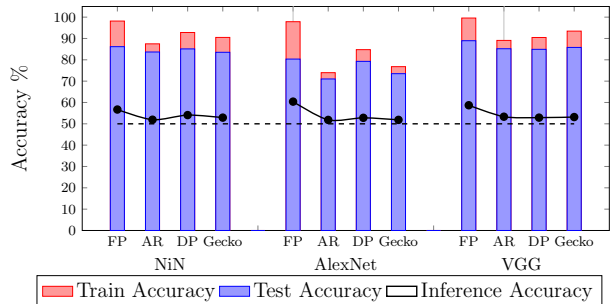


Figure 5: GECKO models are comparable to prior state-of-the-art privacy defences in terms of test accuracy and inference accuracy while additionally ensuring efficiency (CIFAR10).

Summary: GECKO has comparable accuracy and privacy to prior defences against prediction confidence based membership inference attack. Unlike other defences, GECKO is efficient for deployment to embedded systems.

9 RELATED WORK

Embedded Deep Learning. Several works have explored optimization of NNs via quantization to different precision [16, 17, 20, 24] and the challenges in training them [36]. Further, specially designed NNs with low memory footprint (e.g., SqueezeNet and MobileNet) have been deployed for low powered devices such as mobile phones and micro-controllers [18, 28]. In addition to traditional model compression techniques considered in this work [10–12], recent work prune the model with specific objective functions (e.g., efficiency) [38]. Alternatively, hardware accelerators are designed to reuse weights and intermediate computation which enable significant performance improvement [9] and optimized specifically for low precision NNs [37] while skipping computations to improve performance for compressed models [6]. However, none of these work consider privacy as a design choice and address the efficiency, accuracy, and privacy trade-off.

Privacy in Deep Learning. Privacy risks in ML models have been extensively studied via inference attacks such as membership inference in a blackbox setting [27, 30] or in the context of white-box setting [23]. Further, membership privacy risks have been shown for generative models [13], graph models [7] and federated learning [21, 23]. Other than membership inference attacks, attribute/property inference attacks enable an adversary to infer sensitive attributes from the training dataset [8, 21]. However, none of the prior work consider the privacy risks in the context of embedded deep learning.

10 CONCLUSIONS

We perform a privacy-centred design space exploration of state-of-the-art algorithms for improving efficiency in NNs: model compression, quantization and efficient off-the-shelf architectures. We identify quantization as a design choice which shows high resistance against membership inference attacks while satisfying all the efficiency requirements. Model compression, after retraining to

restore accuracy, leaks more membership information compared to the original uncompressed model while off-the-shelf architectures do not provide the best efficiency guarantees. Based on our observation, we propose a two phase GECO training framework to design private, efficient and accurate NNs for deployment to low powered embedded devices.

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