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NUMERICAL SIMULATIONS OF CONDUCTION AND LOW-FREQUENCY NOISE IN POLYSILICON THIN FILM TRANSISTORS

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Abstract

Numerical simulations of static conduction and low-frequency noise are carried out in N-channel polysilicon thin film transistors. The Meyer-Neldel effect associated with the drain current is related to trapping/detrapping processes of carriers from dangling bonds located at the interface. Low-frequency noise is simulated by generation-recombination processes. The sources responsible of noise in the thin film transistors are mainly located close to the interface. The microscopic parameter deduced from numerical simulation is lower than the macroscopic one deduced from noise measurements. The ratio of these two parameters is considered as a factor of merit to qualify thin film transistor technology.

1. Introduction

Polysilicon thin-film transistors (TFTs) are key elements for large area electronics because of their high potential usefulness in driving circuits and in addressing pixels. Some improvements remain because electrical properties are strongly affected by defects in polysilicon. A well understood of transport characteristic in TFTs is necessary to improve device modelling and TFT technologies. In this paper we study, the impact of the defects, in terms of nature (strained or dangling bonds) and spatial localization within the active layer, on the electrical behaviour of TFTs biased from weak to strong inversion, and operating in the linear mode.

2. Simulated devices

The simulated device cross section of the TFTs is shown in the insert of the figure 1. The doped and undoped polycrystalline regions are 150nm thick. The oxide thickness t_{ox} is 60nm, the length and width of the channel are 20 μ m and 50 μ m respectively. The polycrystalline silicon in the channel is described by equally spaced grain boundaries (GBs) perpendicular to the direction of carrier propagation from drain to source. The grain boundary is depicted as thin amorphous silicon with a width of 2 nm. The grain size is 300nm. Geometrical dimensions of each part of the structure correspond to those of processed TFTs (see ref [1]).

3. Physical models for simulations

Simulations of carrier transport, distribution of traps, and low-frequency noise were made by using the DESSIS-ISE multi-dimensional simulator of ISE-TCAD software [2].

A- Carrier transport

In simulations we used a Drift-Diffusion (DD) model. The governing equations for charge transport are the Poisson equation and the carrier continuity equations.

B- Trap state distributions

Band tail state distributions associated with strained bond-type defects are described by:

$$N_{sb}(E) = N_{max}^a \exp \left\{ - \left| (E_c - E) / E_{sa} \right| \right\} + N_{max}^d \exp \left\{ - \left| (E - E_v) / E_{sd} \right| \right\} \quad (1)$$

and trap state distributions associated with dangling bonds are given by:

$$N_{db} = N_{db}^a \exp \left\{ - (E_D - E)^2 / 2\sigma_{db}^2 \right\} + N_{db}^d \exp \left\{ - (E - E_D)^2 / 2\sigma_{db}^2 \right\} \quad (2)$$

Indexes a and d refer respectively to the acceptor-like and the donor-like states. $N_{max}^{a,d}$ and $E_{sa,d}$ are the characteristic parameters of the band tail states distribution. $N_{db}^{a,d}$ stands for the

maximum density of deep trap states at the energy bandgap level E_D and σ_{db} is the characteristic energy.

C- Low frequency noise

The noise simulation in ISE-TCAD simulator is based on the impedance field method (IFM) of Shockley. A numerically efficient Green function approach to the Langevin equation-based simulation of the IFM is the basis for the implementation in ISE simulator. The complex Green functions G describe the propagation of perturbations inside the device to the contacts. The noise voltage spectral density was calculated from integration over all noise sources:

$$S(\omega, r) = \sum_{\alpha=n,p} \int_{\Omega} G_{\alpha} K_{\alpha,\alpha}^{GR} G_{\alpha}^* dr + \sum_{\alpha=n,p} \int_{\Omega} G_{\alpha} K_{\alpha,\alpha}^{fGR} G_{\alpha}^* dr \quad (3)$$

where K^{GR} , K^{fGR} and Ω represent respectively, the Generation-Recombination (GR) and the flicker GR local noise sources and the device volume. The GR noise source model is expressed as a tensor:

$$K_{n,n}^{GR} = \frac{J_n \otimes J_n}{n} \frac{4\alpha_{gr} \tau_{gr}}{1 + \omega^2 \tau_{gr}^2} \quad (4)$$

J_n is the local current density, n the local carrier density, τ_{gr} the equivalent GR lifetime, α_{gr} a constant which represents the quasi Poissonian character of the carrier number (N) fluctuations ($\text{var } \Delta N = \alpha_{gr} \Delta N$), and ω the angular frequency. The $1/f$ noise source is represented as the sum of GR noises produced by sub bands of impurities or defects, with a continuous distribution of relaxation times between τ_1 and τ_2 : $g(\tau) = (\ln(\tau_2 / \tau_1) \tau)^{-1}$. The flicker GR local noise source is given by a tensor:

$$K_{n,n}^{fGR} = \frac{J_n \otimes J_n}{n} \frac{2\alpha_H}{\pi \log(\frac{\tau_2}{\tau_1})} [\arctan(\omega \tau_2) - \arctan(\omega \tau_1)] \quad (5)$$

α_H is the microscopic Hooge noise parameter associated with the local source of noise.

4. Results and discussion

4.1 Electrical conduction

Simulated transfer characteristics were fitted with experimental data issued from Si-poly TFTs (see figure 1) considering strained bonds at the interface with defect distribution parameters given as reference values: $N_{max}^a = 2.2 \times 10^{14} \text{ cm}^{-2} \text{ eV}^{-1}$, $E_{sa} = 0.085 \text{ eV}$, $N_{max}^d = 5 \times 10^{17} \text{ cm}^{-2} \text{ eV}^{-1}$.

Moreover, the experimental drain current follows the Meyer-Neldel (MN) rule [3,4]:

$$\begin{cases} I_D = \beta(V_{GS} - V_0) \exp\left(-\frac{E_A}{kT}\right) \\ \beta = \beta_0(V_{DS}) \exp\left(\frac{E_A}{E_{MN}}\right) \end{cases} \quad (6)$$

with E_A the activation energy decreasing with the gate voltage, E_{MN} the MN characteristic energy, and $\beta_0(V_{DS})$ the transconductance. MN effect is related to multiple-trapping transport [5], and E_{MN} ($\approx 0.045 \text{ eV}$) is related to process fabrication parameters [6]. The analysis of the MN effect on the drain current is associated with the study of the E_A/E_{MN} ratio with trap state distribution parameters. Subsequently we analyze the MN effect by numerical simulation and results are presented at $V_{GS} = 1 \text{ V}$.

The influence of strained bond-type defects located at the interface was first analyzed. As I_{DS} is strongly controlled by E_{sa} parameter whereas other parameters have a rather slight effect [6], then $N_{max}^{a,d}$ and E_{sd} were fixed (reference values) and we studied the effect of E_{sa} on E_A/E_{MN} ratio (see figure 2 (a)). The significant increase of this ratio with E_{sa} shows that the MN effect is related to strained bond-type defects located at the interface. The influence of E_{sa} is predominant for deep trap states in the bandgap (see insert fig. 2 (a)) and thus it is more convenient to consider dangling bonds at the interface. Then, band tails states parameters were fixed according to reference values, and we deduced the variations of E_A/E_{MN} versus $N_{db0} = N_{db0}^a = N_{db0}^d$ (figure 2 (b)). The high rate of increase of E_{MN}/E_A with N_{db0} shows the

strong influence of transitions of carriers from deep states induced by dangling bonds located at the interface on the MN effect.

Next, we analyzed the influence of the traps within the grain boundaries. Band tail trap distribution parameters at the interface are fixed (reference values), and first we defined band tail traps at the grain boundaries according to typical experimental values [3,7] with a symmetric distribution: $N_{\max a} = N_{\max d} = N_{\max}$ and $E_{sa} = E_{sd} = E_s$. The corresponding variations of E_A/E_{MN} versus E_s are plotted in the figure 3 (a). No increase is observed suggesting no influence of tails states on MN effect. Next, band tail state distribution parameters at grain boundaries were fixed ($N_{\max} = 10^{20} \text{ cm}^{-3} \text{ eV}^{-1}$ $E_s = 0.11 \text{ eV}$) and we analyzed the effect of the dangling bonds. Variations of E_A/E_{MN} versus N_{db0} plotted in the figure 3 (b) do not display significant increase. Thus, MN effect is not related to dangling bonds at the grain boundaries.

4. 2 Low frequency noise

In noise simulation, we kept the distribution of tail state at the interface with reference values. The calculation of the noise spectral density requires knowledge of the local noise source K . We defined the flicker GR local noise sources K^{fGR} within the active layer with α_H assumed identical for all sources. A $1/f$ noise spectrum is obtained in the studied frequency band (1Hz- 10^3 Hz) with the relaxation times τ_1 and τ_2 fixed at 10^{-7} s and 10^4 s, respectively. For spectrums which contain a Lorentzian we introduced a GR local noise source K^{GR} . A good fit of experimental with numerical noise spectra was obtained (see figure 4), and the microscopic noise parameter α_H associated with K^{fGR} was deduced. Values of α_H versus effective voltage ($V_{GS} - V_0$), with V_0 the flatband voltage, are reported in figure 5 (a). Plots of macroscopic apparent noise parameter α_{app} , deduced from measurements according to the Hooge formula $S_{\text{IDS}}/I_{\text{DS}}^2 = \alpha_{\text{app}}/(fN)$, is also reported. α_H and α_{app} exhibit the same variations: it increases from weak to moderate inversion and then it decreases from moderate to strong inversion. This singular behaviour, previously reported for α_{app} [4], suggests that α_H and α_{app} are related.

Higher values of α_{app} are explained by the current crowding in the device due to defects within the active layer inducing non conducting spots [8]. For homogeneous materials $\gamma=\alpha_H/\alpha_{app}\sim 1$ whereas for disordered materials $\gamma=\alpha_H/\alpha_{app}\ll 1$. For polycrystalline resistors it can be considered as a factor of quality [8]. In our case $\gamma\sim 0.17$.

We give in the figure 5 (b) the 2D spatial distribution in the structure of the simulated local noise voltage spectral density (LNVSD). Noise level is higher close to the interface suggesting a high contribution of the local sources of noise in this region. Although sources were defined within the active layer, but owing to the previous results and to those obtained in §4.1, we can suspect the preponderant role of dangling bonds at the interface on the $1/f$ noise level in the TFTs.

5. Conclusions

Numerical simulations of carrier transport in poly-Si TFT reveal that the MN effect is strongly controlled by trap state distribution rather associated with dangling bonds located at the interface. It suggests that drain current can be described by multiple trapping processes of carriers along the interface. Low frequency noise in TFTs can be simulated by GR processes of carriers, and results show that the noise level is higher close to the interface. Therefore, because of the high density of defects located in this region, $1/f$ noise can be explained by carrier fluctuation due to trapping/detrapping of carriers from these defects, ruling out the theory of the mobility fluctuations proposed in homogeneous sample of high crystal quality. In addition, defects in the device are responsible of overestimation of the measured $1/f$ noise parameter.

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Figure captions

Figure 1: Simulated and experimental transfer characteristics ($V_{DS}=0.3V$) versus temperature. Insert: schematic cross-section of devices for 2D simulation of TFT.

Figure 2: Variations of E_A/E_{MN} versus trap state distribution parameters for defects located at the interface. Effect of:

- (a) strained bonds (insert: corresponding interface state distributions),
- (b) dangling bonds (insert: corresponding interface state distributions).

Figure 3: Variations of E_A/E_{MN} versus grain boundary trap state distribution parameters. Effect of:

- (a) strained bonds at the grain boundaries (insert: corresponding trap state distributions with $N_{max}=10^{20} \text{ cm}^{-2} \text{ eV}^{-1}$, $E_s=0.11\text{eV}$),
- (b) dangling bonds at the grain boundaries (insert: corresponding trap state distributions with $E_D=0.7\text{eV}$ and $\sigma_{db}=0.05\text{eV}$).

Figure 4: Experimental and simulated noise current spectra in TFT at different gate voltages.

Figure 5: (a) Noise parameters versus effective gate voltage, (b) two dimensional spatial distribution of local noise voltage spectral density (LNVSD) in the structure.

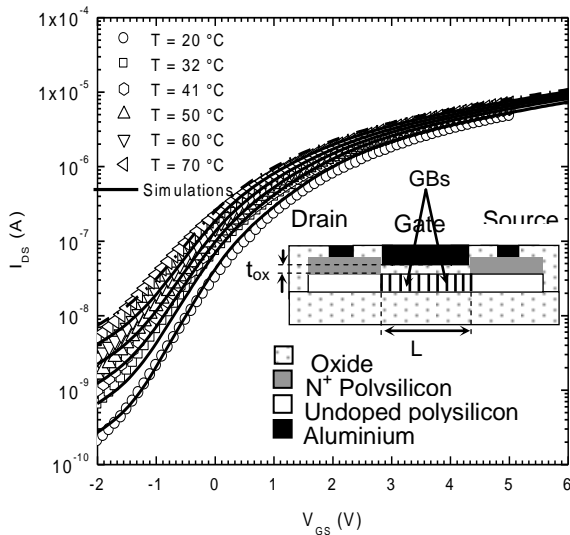


Figure 1

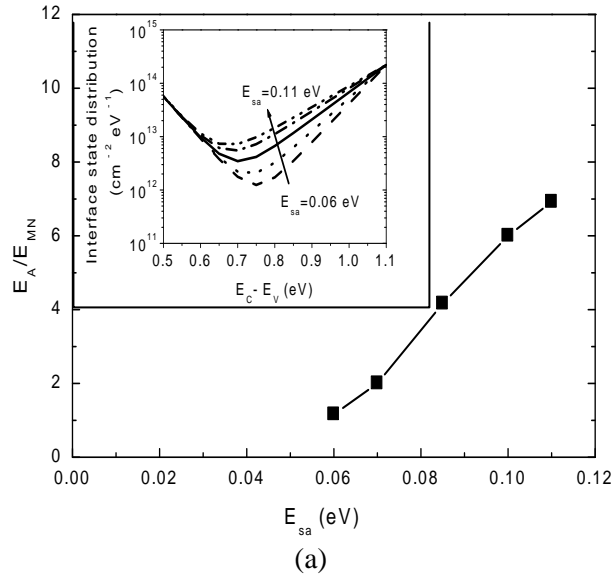


Figure 2

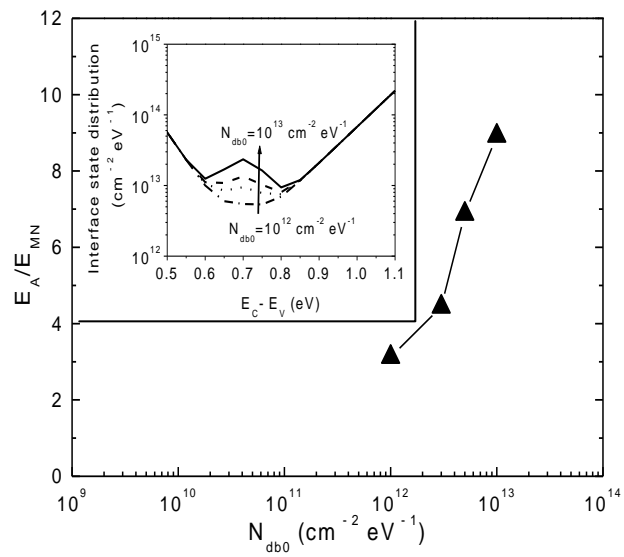
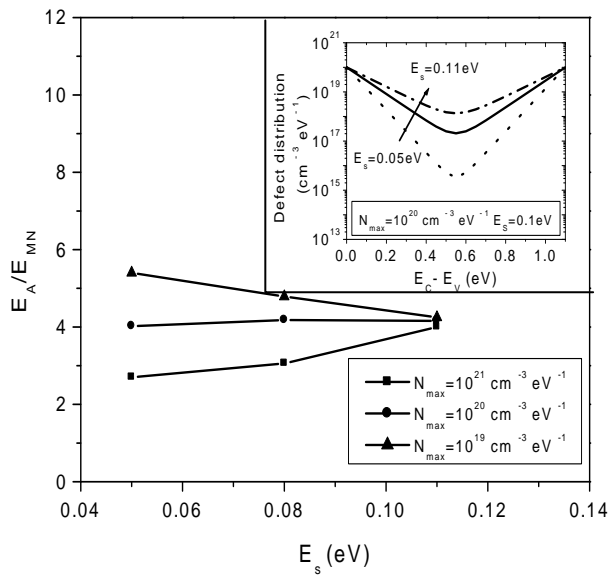
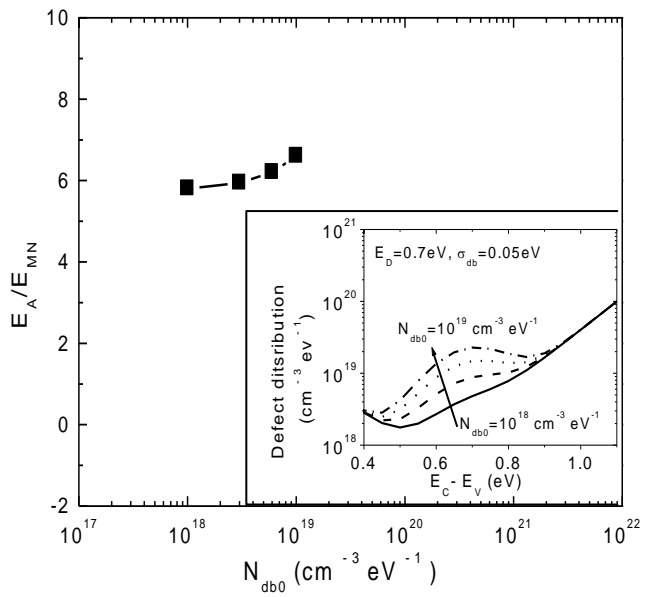


Figure 2



(a)

Figure 3



(b)

Figure 3

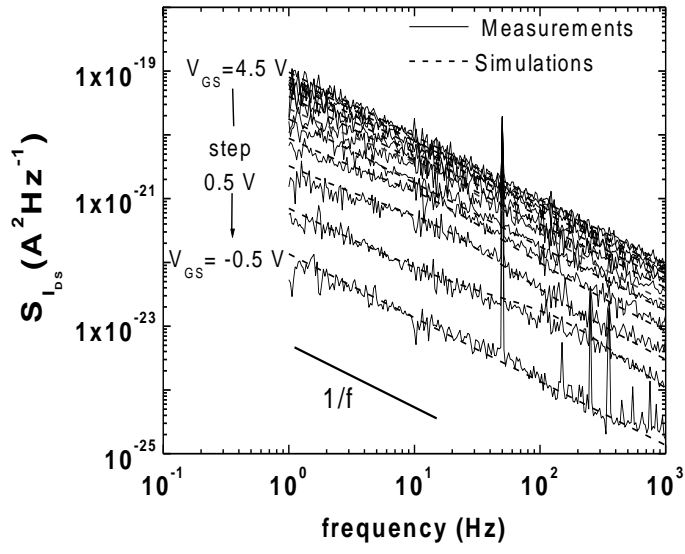
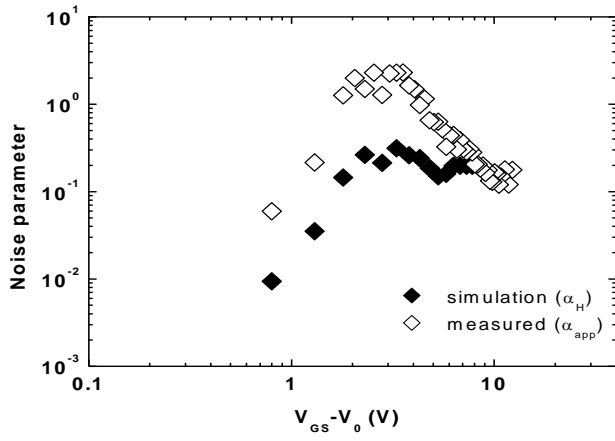
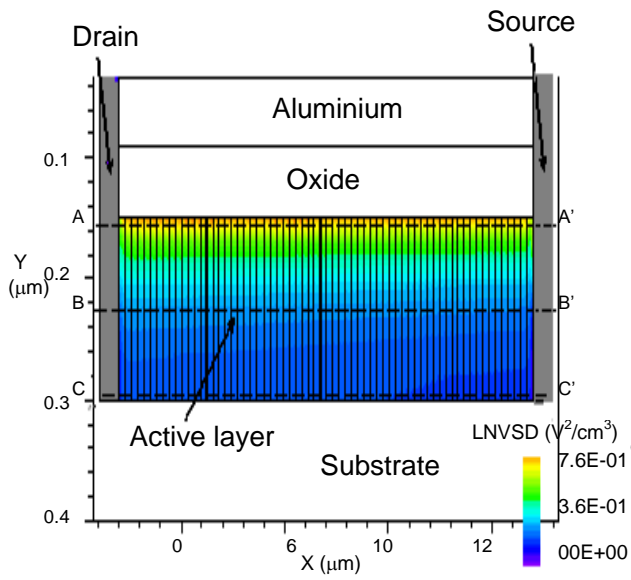


Figure 4



(a)

Figure 5



(b)

Figure 5