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# Towards Quantum Simulations in Particle Physics and Beyond on Noisy Intermediate-Scale Quantum Devices

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We review two algorithmic advances that bring us closer to reliable quantum simulations of model systems in high energy physics and beyond on noisy intermediate-scale quantum (NISQ) devices. The first method is the dimensional expressivity analysis of quantum circuits, which allows for constructing minimal but maximally expressive quantum circuits. The second method is an efficient mitigation of readout errors on quantum devices. Both methods can lead to significant improvements in quantum simulations, e.g., when variational quantum eigensolvers are used.

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## 1. Introduction

The Standard Model (SM) of particle physics classifies all known elementary particles and fully describes the electromagnetic, weak, and strong interactions. All the particles of the SM have by now been identified experimentally, including the infamous Higgs boson, the W- and Z-bosons, the photons, gluons, quarks, neutrinos, and the charged leptons with the electron being the most prominent example. The SM is extremely successful and describes physical phenomena over a large distance range from  $10^{-18}$  m (the distances probed at particle colliders) to  $10^{26}$  m (the size of the observable universe) without any contradiction to experiment. The predictions of the SM have been experimentally confirmed up to 11 digits of precision, such that it can be considered the most precisely tested theory in the history of science.

These are amazing results, in particular because there are extensive world-wide efforts to detect physics beyond the SM, e.g. with experiments such as the LHC at CERN [1], Belle II at KEK [2], CEBAF at JLab [3], XENON1T at LNGS [4], or DUNE at Fermilab [5]. These experimental searches are strongly motivated by the fact that, despite its success, the SM cannot explain several experimentally observed physical phenomena [6]. The probably most striking example is the asymmetry between matter and antimatter in our universe, which leads to our sheer existence. This asymmetry requires a substantial amount of CP-violation, which is orders of magnitude larger than predicted by the SM. At the same time, the SM predicts CP-violation for the weak and strong interactions, but no such violation has been observed for the latter. The SM also cannot explain why the masses of the elementary particles are strongly hierarchical and why the strengths of the fundamental interactions differ by many orders of magnitude. Finally, the theoretical origins of the mysterious dark matter, dark energy, and neutrino masses remain fundamentally open questions of the SM. In addition to these observational puzzles of the SM, there are also several theoretical issues, most importantly the missing UV-completion of quantum gravity [6]. Moreover, the scalar field theory that describes the Higgs boson might become a trivial, non-interacting theory at some high-energy scale. It is currently an open question whether the Higgs self-coupling might vanish well below the Planck scale [7, 8], which is the fundamental cut-off scale of the SM. If so, this could give rise to quantum triviality or vacuum stability issues, which might require extra couplings of the Higgs boson to new physics above LHC energies.

In the light of these open questions of the SM, it is of utmost importance to understand phenomena such as CP-violation or the matter-antimatter asymmetry from a fundamental point of view. To this end, we require analytical tools like perturbation theory and, crucially, numerical tools that enable us to go beyond and explore inherently non-perturbative phenomena.

The standard path to study such non-perturbative phenomena is to put the theory on a Euclidean space-time lattice and employ Markov Chain Monte Carlo (MCMC) methods. The lattice theory provides a non-perturbative regularization<sup>1</sup>, both in the ultraviolet regime through a non-zero value of the lattice spacing and in the infrared regime through a finite volume [14, 15]. Such numerical techniques have been very successful for computing various predictions of the theory behind quarks and gluons, called quantum chromodynamics (QCD). Examples are the computation of the hadron spectrum and their structure, fundamental parameters of QCD, and the order parameter of spontaneous chiral symmetry breaking [16].

However, the MCMC-based methods fail when addressing the questions of CP-violation and the matter-antimatter asymmetry. The reason is the infamous sign problem [17], which leads to complex phases that prevent the application of MCMC. In addition, MCMC calculations are usually performed in Euclidean space-time and therefore fail to study real-time evolution, e.g., the out-of-equilibrium-dynamics following heavy-ion collisions or quench dynamics. Such phenomena are important to understand, e.g., the physics of heavy-ion collisions at the LHC or the Schwinger effect leading to electron-positron production in strong electric fields. This is exactly the point where quantum computers enter the game, which are able to efficiently

<sup>1</sup>For a more general non-perturbative regularization formalism of quantum field theories using the  $\zeta$ -regularization see [9–13]. In particular, in Refs. [9, 13] very first quantum simulations within this framework were performed.

simulate strongly-correlated many-body systems. In particular, quantum simulations rely on the Hamiltonian formulation in Minkowskian space-time and do not rely on MC methods, therefore circumventing the sign problem and enabling the study of real-time dynamics. Thus, quantum computing might eventually open the door to perform simulations of the SM in three spatial dimensions, in particular in regimes that are inaccessible with other approaches.

Of course, the ambitious goal of quantum simulating the SM is presently rather far away, given that only noisy intermediate-scale quantum (NISQ) computers are currently available. Still, first quantum simulations of gauge theories in lower dimensions showing some of the relevant features of the SM have already been successfully performed [18–26]. In addition, resource efficient formulations of gauge theories for quantum computations have been developed [27–32], and the Hamiltonian formulation of the CP-violating topological  $\theta$ -terms in three spatial dimensions has been derived [33]. In general, the path towards quantum simulations of 3+1D particle physics requires many incremental steps, including algorithmic development, hardware improvement, methods for circuit design, as well as error mitigation and correction techniques. In addition, validating quantum simulation experiments is essential for obtaining reliable results, in particular in regimes which are inaccessible with MC methods. Classical Hamiltonian simulations, in particular tensor network methods, have been proven to be suitable for this task [34–45]. For detailed reviews on quantum simulations for high energy physics, see Refs. [46–48].

One promising way to utilize NISQ devices to efficiently perform quantum simulations are hybrid quantum-classical algorithms, most crucially the variational quantum eigensolver (VQE) [49, 50]. The VQE algorithm has already found numerous applications for studying benchmark models of particle physics in lower dimensions [46–48]. Using VQE, the computing-intensive cost function is evaluated on the quantum computer, while the optimization of the circuit parameters is performed on a classical computer via a feedback loop. When performing such simulations, one faces two fundamental challenges, which we will address in this article.

First, the construction of the quantum circuit is key to compute the desired cost function. However, until recently, there had been no general scheme to guide this construction. We filled this gap by developing a method to custom build quantum circuits, incorporate or remove symmetries, and determine the expressivity and minimality of given circuits [51, 52].

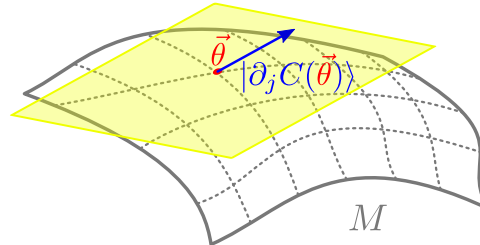
Second, current quantum computers are still very noisy, which means there are large errors in the state preparation, the gate operations, and the readout process. To use quantum computers for quantum simulations, it is extremely important to mitigate, if not correct for these errors. We recently developed an efficient error mitigation method that enables us to mitigate one of the most dominant errors on current superconducting quantum hardware, which is the readout error [53].

Both aspects, the dimensional expressivity analysis and the readout error mitigation, at first sight seem like technical algorithmic points, which defer from our goal to perform quantum simulations of high-energy physics. However, they are essential tools to optimize the performance of present-day noisy quantum devices and to efficiently implement quantum simulations with current and future devices. Thus, these tools are an important step to improve the simulations of low-dimensional benchmark models in high-energy physics and to prepare for quantum simulations in three spatial dimensions in the future. The following two sections will provide a detailed review of the circuit expressivity analysis and the error mitigation scheme developed by us to reach these goals.

## 2. Circuit expressivity

Parametric quantum circuits (see Fig. 2 for an example) are at the heart of VQE. It is therefore essential to find suitable ansatz circuits for obtaining the desired solution, which is usually the low-lying energy spectrum of the given problem Hamiltonian. This requires that the circuit should be equipped with sufficiently many parametric gates to express the solution. At the same time, the number of gates should be chosen to be minimal in order to reduce effects of noise in a VQE simulation. There is no general principle for designing such minimal but maximally

expressive circuits. Therefore, in Refs. [51, 52] we recently developed a dimensional expressivity analysis, which provides a systematic and practical way to determine whether a given quantum circuit is sufficiently expressive and whether there are redundant parameters. In particular, it can be used to optimize a given quantum circuit through the removal of redundant parameters and unwanted symmetries.

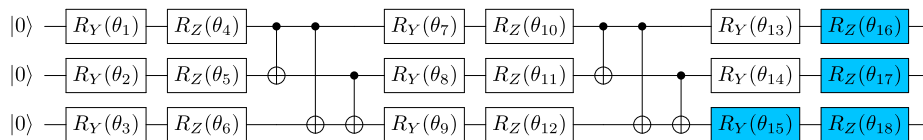


**Figure 1.** Representation of the tangent space (illustrated in yellow) in the point  $\vec{\theta}$  (red dot) of the manifold  $M$  spanned by the tangent vector  $|\partial_j C(\vec{\theta})\rangle$  (blue arrow).

In a quantum circuit  $C(\vec{\theta})$ , a number of unitary gates that depend on parameters  $\theta$  are employed. As such, the quantum circuit  $C(\vec{\theta})$  can be understood as a map from a parameter space into the quantum device state space. This leads to a manifold  $M$  of states  $|C(\vec{\theta})\rangle$  that can be reached. In addition, we have a manifold  $S$  of physical states of the quantum device. A quantum circuit  $C$  that generates only physical states therefore has a manifold  $M$  that is contained in  $S$ . In order to have a minimal but maximally expressive circuit, the co-dimension of  $M$ ,  $\text{codim}(M) = \text{dim}(S) - \text{dim}(M)$ , has to vanish and the number of parameters has to be equal to  $\text{dim}(S)$ .

The co-dimension can be determined by computing the tangent vectors  $|\partial_j C(\vec{\theta})\rangle$  for a given parameter  $\theta_j$ , see Fig. 1, and by testing their linear independence. In particular, a parameter  $\theta_k$  is redundant iff  $|\partial_k C(\vec{\theta})\rangle$  is a linear combination of  $|\partial_j C(\vec{\theta})\rangle, j \neq k$ . Once the parameter  $\theta_k$  is analyzed, the procedure can be iterated to the parameter  $\theta_{k+1}$  until all parameters have been visited or sufficiently many independent parameters have been found to ensure the wanted expressivity. Having identified the set of dependent (and unnecessary) parameters, they can be removed by setting them to a suitable constant value, and a minimal circuit is constructed. This circuit is furthermore maximally expressive if  $\text{dim}(S)$  many independent parameters remain. Hence, the approach becomes efficiently scalable as soon as  $\text{dim}(S)$  no longer scales exponentially in the number of qubits. This is commonly seen in many physical Hamiltonians for which symmetries and entanglement restrictions imply that  $\text{dim}(S)$  only grows polynomially with the number of qubits.

For example, for QISKIT's `EfficientSU2` 2-local circuit for 3 qubits [54] shown in Fig. 2, such an analysis shows that the coloured unitary gates are redundant and can be removed.



**Figure 2.** QISKIT's `EfficientSU2` 2-local circuit with 2 layers. The coloured gates represent the gates that can be removed after the expressivity analysis developed here.

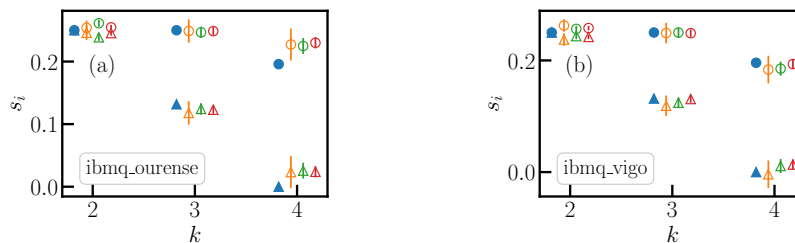
In practice, starting from the (real partial) Jacobian

$$J_k = \begin{pmatrix} \text{Re} |\partial_1 C\rangle & \cdots & \text{Re} |\partial_k C\rangle \\ \text{Im} |\partial_1 C\rangle & \cdots & \text{Im} |\partial_k C\rangle \end{pmatrix}, \quad (2.1)$$

we construct the matrix  $S_k = J_k^* J_k$ . The quantum circuit contains dependent parameters if  $\det S_k = 0$ . This in turn means that the matrix  $S_k$  contains vanishing eigenvalues. The dependence of the parameters can then be determined iteratively by computing the eigenvalues of  $S_k$ . Starting with  $S_1$  (which is trivially independent), we move to  $S_2$  and check whether it has a vanishing eigenvalue. If this is the case, then the second parameter is removed. We continue by adding one parameter at a time and checking for the smallest eigenvalue to be zero, until all parameters have been checked. A nice feature of our approach is that  $S_k$  itself can be computed efficiently on the quantum computer employing one ancilla qubit, while the invertibility of  $S_k$ , i.e. the computation of the eigenvalues, can be performed efficiently on a classical device (see Ref. [51] for details).

### (a) Experimental results

In Fig. 3, we show the smallest and second smallest eigenvalues of  $S_k$  for a 1-qubit test case. Our experiments have been carried out on the `ibmq_ourense` and `ibmq_vigo` quantum hardware devices. As explained in the caption of Fig. 3, we have performed different numbers of shots, meaning different numbers of repetitions of the circuit to collect statistics of the measurement outcomes. The experimental results are shown with open symbols in Fig. 3, while the exact solutions are shown with filled symbols. We observe that we can indeed identify eigenvalues that are compatible with zero within a given precision  $\epsilon$  (see Ref. [51] for details). Thus, our expressivity analysis allows us to identify dependent parameters of quantum circuits on actual quantum hardware.



**Figure 3.** Single-qubit experiments on (a) `ibmq_ourense` and (b) `ibmq_vigo` quantum hardware. We show the smallest (triangles) and second smallest (dots) eigenvalues of the matrix  $S_k$ . The filled symbols are the exact solutions. The open symbols represent different statistics: orange, green and red markers stand for 1000, 4000 and 8000 shots, respectively. The error bars for the experimental data represent the uncertainty from the finite number of shots.

### (b) Discussion

Through our hybrid quantum-classical approach of computing  $S_k$  on the quantum device and testing the invertibility on a classical computer, we are able to escape an exponential scaling of computational resources, rendering our method efficient. As already mentioned above, in all numerical experiments we can identify a zero eigenvalue within a prescribed precision  $\epsilon$ . We can estimate the required computational resources of our method for  $N_p$  parameters in the quantum circuit: we need  $\mathcal{O}(N_p^2)$  memory,  $\mathcal{O}(N_p^4)$  calls on the classical machine, and  $\mathcal{O}(N_p^2/\epsilon^2)$  calls on the quantum device. Note that the numerical cost of analyzing a given quantum circuit is actually independent from the number of qubits employed.

Our fundamental step of identifying redundant parameters can also be employed to remove unwanted symmetries. For example, a quantum circuit does not need to be able to generate the state  $e^{i\alpha} |\psi\rangle$  if it can already generate  $|\psi\rangle$ . To find the parameters that only contribute such an unwanted symmetry to the circuit, we first artificially add this symmetry to the circuit. In the case of the global phase symmetry, we initialize the quantum device in  $|0\rangle$  and insert a single-qubit  $R_Z(\phi)$  gate into the circuit. By checking this parameter  $\phi$  first, it will be considered independent. Thus, any further parameter  $\theta_k$ , whose independent contribution is the phase generation, will now be identified as redundant. Finally, we remove the artificially inserted  $R_Z(\phi)$  gate and obtain a circuit that no longer generates arbitrary phases.

This idea can be generalized to more complicated symmetries. In general, if a continuous symmetry has dimension  $d$ , e.g.  $d = 3$  for  $SU(2)$ , then  $d$  parameters  $\phi_k$  are necessary to artificially enforce the symmetry. The symmetry can then be removed from the circuit  $C(\theta)$  by extending  $C$  to the artificially symmetric circuit  $\tilde{C}(\phi, \theta)$  and testing the parameters  $\phi$  before continuing to test the parameters  $\theta$ .

This circuit optimization becomes even more efficient if we start with an already partially optimized circuit. For example, if the physically relevant state space is the entire quantum device state space but with certain symmetries removed, then we would like to start with a minimal but maximally expressive quantum circuit and optimize it using the removal of symmetries technique. This can be achieved using an inductive procedure. It is easy to find a minimal, maximally expressive circuit on a single qubit, e.g.  $R_Y(\theta_3)R_Z(\theta_2)R_X(\theta_1)|0\rangle$ . Once we have found a minimal, maximally expressive circuit for  $Q$  qubits, we can inductively construct a circuit for  $Q + 1$  qubits. This can be achieved by controlling an already available circuit  $C_Q$  with the additionally added qubit [52]. In this way, it becomes possible to construct candidates for minimal, maximally expressive circuits from their already existing counterparts with one fewer qubit.

In practical applications, it might turn out that a maximally expressive circuit, although being minimal, is still too large in order to implement it on a given hardware device. In such cases, it is important to estimate the best-approximation error of the circuit that should be used. In particular, it is important to understand the worst-case scenario for such a situation. To find the best-approximation error, we have used a technique based on Voronoi diagrams [55, 56]. In particular, we have determined the number of Voronoi points necessary to obtain a good estimate of the best-approximation error [52]. This best-approximation error estimate provides an upper bound that converges to the best-approximation error for infinitely many Voronoi points. Additionally, we have provided a lower bound on the best-approximation error. This provides a practical way to estimate the worst-case best-approximation error in cases where a minimal, maximally expressive circuit cannot be used due to the lack of a sufficient number of gates with high fidelity.

### 3. Error mitigation

Errors on quantum computers are caused by various sources of noise, including limited coherence times, imperfections in the implementations of the gate operations, as well as the measurement process. While current and future NISQ devices do not allow for quantum error correction due to the small number of qubits and their large noise levels, errors can be partly corrected through error mitigation schemes (see, e.g., Refs. [53, 57–67]). The general idea of these schemes is to use a low-overhead procedure, e.g., to alter the circuit executed on the quantum device, to post-process the data collected from the device, to measure modified operators, or combinations thereof. In this way, the effects of quantum noise can be alleviated and more reliable estimates for expectation values of observables can be obtained.

The final measurement can be among the most dominant sources of error, with error rates of up to  $\mathcal{O}(10)$  percent [63]. These errors arise from bit flips, i.e. from erroneously reading out an outcome as 0 given it was actually 1, and vice versa. In the following, we will focus on a specific method developed by us [53], which is tailored for readout errors and can be practically

Outcome	Measured Energy	Operator	Probability
No bit flips	$E_Z = + c_1 ^2 -  c_2 ^2$	$Z$	$(1-p)^2$
$0 \rightarrow 1, 1 \rightarrow 1$	$E_1 = - c_1 ^2 -  c_2 ^2$	$-\mathbb{1}$	$p(1-p)$
$0 \rightarrow 0, 1 \rightarrow 0$	$E_2 = + c_1 ^2 +  c_2 ^2 = -E_1$	$\mathbb{1}$	$(1-p)p$
$0 \rightarrow 1, 1 \rightarrow 0$	$E_3 = - c_1 ^2 +  c_2 ^2 = -E_Z$	$-Z$	$p^2$

**Table 1.** List of possible outcomes of measuring the energy of the operator  $Z$  in a state  $|\psi\rangle = c_1|0\rangle + c_2|1\rangle$  and their probabilities of occurrence for different cases of bit-flips.

implemented on existing quantum hardware. Our method scales only polynomially with the system size and is hence efficient (for a detailed comparison to previous works, see Ref. [53]).

Throughout this section, we focus on readout errors only and neglect all other sources of error. Thus, we assume that the quantum device prepares a pure state  $|\psi\rangle$ , which we measure in the computational basis. In order to be able to obtain the expectation value of an observable, we have to run the circuit a number of times and collect statistics of the measurement outcomes. Just as in the previous section, we refer to this number of repetitions as the number of shots  $s$ . Moreover, we assume that readout errors caused by bit flips between different qubits are uncorrelated.

For an illustration of the basic idea, let us consider a very simple Hamiltonian that consists only of the operator  $Z$ , i.e. the third Pauli matrix. We want to measure the expectation value of this Hamiltonian in an arbitrary pure single-qubit state,  $|\psi\rangle = c_1|0\rangle + c_2|1\rangle$ , where the complex coefficients fulfill the normalization condition  $|c_1|^2 + |c_2|^2 = 1$ . The exact energy then evaluates to

$$E_Z = \langle \psi | Z | \psi \rangle = \langle 0 | c_1^* Z c_1 | 0 \rangle + \langle 1 | c_2^* Z c_2 | 1 \rangle = |c_1|^2 - |c_2|^2. \quad (3.1)$$

This noise-free result changes in the presence of bit-flip errors in the readout process, i.e., due to the misidentification of the outcome 0 as 1 and vice versa. For illustrative purposes, let us first assume that the probabilities for misidentifying the outcome 0 as 1 and the outcome 1 as 0 are the same. For this case, we give in Tab. 1 the possibilities for correct and *wrong* measurements. From Tab. 1 we see that instead of the exact result  $E_Z$ , we instead obtain an *expected value of the observable energy to bit flips*  $\tilde{E}_Z$ , which evaluates to

$$\tilde{E}_Z = (1-p)^2 E_Z + p(1-p)(E_1 + E_2) + p^2 E_3 = (1-2p)E_Z. \quad (3.2)$$

From Eq. (3.2) it becomes apparent that one can reconstruct the exact energy from the noisy result, as long as one knows the bit-flip probability and  $p \neq 1/2$ .

Alternatively, instead of treating the bit-flips as part of the measurement process, we can change the point of view and consider them as part of the operator to be measured. Thus, we consider random operators to be measured. In Tab. 1, we also list the corresponding operators for our single-qubit example. The expected value of the noisy operator subject to bit flips is then given by

$$\mathbb{E}\tilde{Z} = (1-p)^2 Z + p(1-p)(-\mathbb{1} + \mathbb{1}) - p^2 Z = (1-2p)Z. \quad (3.3)$$

This change in point of view also allows to generalize the method to arbitrary operators acting on more than a single qubit. In the following, we illustrate this with a two-qubit example and now consider the general case of *arbitrary* bit-flip probabilities for the different qubits. Performing a similar analysis as for the single-qubit case above, and inverting the corresponding equation, we obtain

$$\begin{aligned} Z_2 \otimes Z_1 = & \frac{1}{\gamma(Z_2)\gamma(Z_1)} \mathbb{E}(\tilde{Z}_2 \otimes \tilde{Z}_1) - \frac{\gamma(\mathbb{1}_1)}{\gamma(Z_2)\gamma(Z_1)} \mathbb{E}(\tilde{Z}_2) \otimes \mathbb{1}_1 \\ & - \frac{\gamma(\mathbb{1}_2)}{\gamma(Z_2)\gamma(Z_1)} \mathbb{1}_2 \otimes \mathbb{E}(\tilde{Z}_1) + \frac{\gamma(\mathbb{1}_2)\gamma(\mathbb{1}_1)}{\gamma(Z_2)\gamma(Z_1)} \mathbb{1}_2 \otimes \mathbb{1}_1. \end{aligned} \quad (3.4)$$



In the expression above the factors  $\gamma(O_q)$  are given by

$$\gamma(O_q) := \begin{cases} 1 - p_{q,0} - p_{q,1} & \text{for } O_q = Z_q \\ p_{q,1} - p_{q,0} & \text{for } O_q = \mathbb{1}_q. \end{cases}, \quad (3.5)$$

where  $p_{q,0}$  denotes the probability that a bit-flip from 0 to 1 occurs on qubit  $q$  and  $p_{q,1}$  denotes the probability for a bit-flip from 1 to 0 on qubit  $q$ . Just as before,  $\mathbb{E}$  denotes the expectation value of the noisy operator subject to bit flips.

From the considerations above, we see that Eq. (3.4) allows us to obtain the correct expectation value of a two-qubit operator by measuring noise-afflicted expectation values of  $Z_1, Z_2$  and  $Z_1 \otimes Z_2$  on the quantum device and combining them with factors that only depend on the known bit-flip probabilities. A key observation is that the statistical expectation value  $\mathbb{E}(\tilde{Z}_Q \dots \tilde{Z}_1)$  of  $Q$  qubits can be factorized in single-qubit expectation values

$$\mathbb{E}(\tilde{Z}_Q \dots \tilde{Z}_1) = \mathbb{E}\tilde{Z}_Q \cdots \mathbb{E}\tilde{Z}_1. \quad (3.6)$$

Note that  $\mathbb{E}$  denotes the expectation value of the noisy operator subject to bit flips, which should not be confused with the quantum mechanical expectation value of the operator. An inductive proof of Eq. (3.6) can be found in Ref. [53]. This equation is the reason why the method of readout error mitigation developed in Ref. [53] only scales polynomially and hence is efficient for  $k$ -local Hamiltonians, i.e., for most model systems in high energy and condensed matter physics.

### (a) Experimental results

As a first non-trivial example to test our readout error mitigation scheme, we have considered the transverse field Ising (TI) model

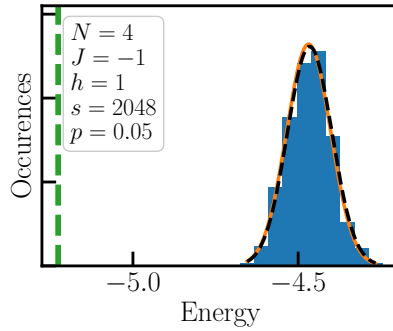
$$\mathcal{H}_{\text{TI}} = J \sum_{i=1}^L Z_i Z_{i+1} + h \sum_{i=1}^L X_i. \quad (3.7)$$

Here,  $J$  and  $h$  are the nearest neighbor and magnetic field coupling, respectively. Note that the model shows a quantum phase transition when  $|J/h| = 1$ , and can hence be considered as a good benchmark model for studying critical phenomena. We have performed numerical computations simulating a noise readout process with bit flips, and measured the resulting ground-state expectation value of the TI Hamiltonian for various parameters and different numbers of shots  $s$ . The obtained histogram for one example with couplings  $J = -1$ ,  $h = 1$  and four qubits is shown in Fig. 4. There, the vertical dashed line is the true ground-state energy, and it can be seen that the mean of the measured histogram is clearly shifted away from the exact result due to the bit flips. However, our method allows to predict the form of the histogram, and the orange line in the figure represents the prediction, which basically coincides with a fit to the histogram indicated by the black solid line. This demonstrates that our method is able to reproduce the measured energy histogram, which in turn allows to reconstruct the correct ground-state energy.

An important question is the scaling of the error of the measured ground-state energy as a function of shots  $s$ . To this end, we have performed simulations for two qubits using Qiskit [54], and compared the mean value and the standard deviation of the error

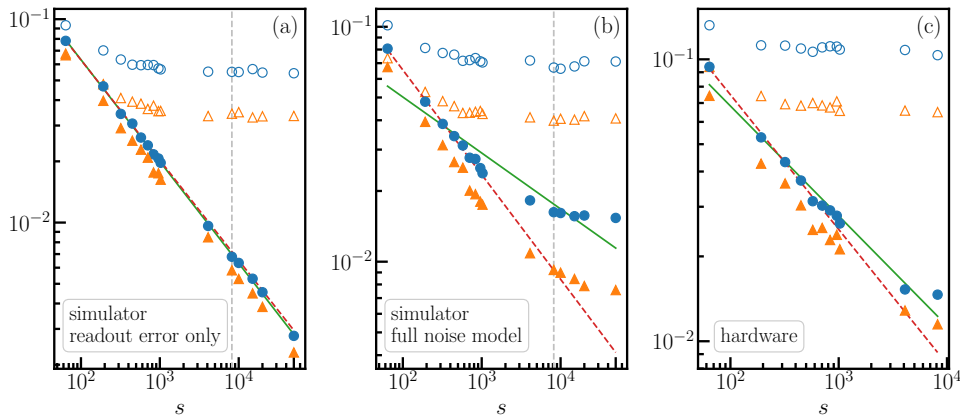
$$\left| \langle \psi | \tilde{Z}_2 \otimes \tilde{Z}_1 | \psi \rangle_{\text{measured}} - \langle \psi | Z_2 \otimes Z_1 | \psi \rangle_{\text{exact}} \right| \quad (3.8)$$

obtained for 1024 randomly drawn wave functions  $|\psi\rangle$ . In Fig. 5, we show the scaling of the mean value of the error as well as the standard deviation as a function of the number of shots, both with and without mitigation. Panel (a) contains the data from a simulation with readout error only, and panel (b) the data obtained using a noise model that mimics the full noise of the quantum hardware. The data are fitted to a power law  $as^{-\beta}$  with the green line taking all data into account, whereas the red line only fits the lowest four data points.



**Figure 4.** Energy histogram for the transverse field Ising model for 2048 experiments,  $N = 4$  qubits, and couplings  $J = -1$ ,  $h = 1$ . For each of the experiments, we used  $s = 2048$  shots and a homogeneous bit-flip probability  $p_{q,b} \equiv p = 0.05$  for all qubits. The vertical dashed green line indicates the true ground-state energy, the solid orange line the theoretical prediction for the noisy measurement outcomes, and the dashed black line a fit to the data.

As can be seen, for the case of readout noise only (cf. Fig. 5(a)) and our error mitigation method applied, the expected error scaling of  $\propto s^{-\beta}$  is observed with  $\beta$  compatible with 0.5, see Tab. 2. When the full noise model is switched on (see Fig. 5(b)), deviations from this scaling at a number of shots  $\mathcal{O}(10^3)$  are found. At this level, the readout error is almost fully corrected and other sources of errors become dominant, which require different noise mitigation techniques. Thus, we find a large reduction of the readout error compared to the results without any mitigation.



**Figure 5.** Mean value (blue dots) and standard deviation (orange triangles) for the absolute error of  $\langle \Psi | Z_2 \otimes Z_1 | \Psi \rangle$  after applying the correction procedure (filled symbols) and without it (open symbols) as a function of the number of shots  $s$ . Each data point is obtained by carrying out 1024 experiments. The different panels correspond to (a) a classical simulation of two qubits of the `ibmq_london` quantum device taking into account readout noise only, (b) the same simulation with a noise model emulating the full hardware noise, and (c) data obtained from the actual quantum hardware. The lines represent a power-law fit to the mean error, where the green line corresponds to a fit to all of our data points, and the red dashed lines corresponds to a fit including only the four smallest values of  $s$ . The vertical gray dashed lines in panels (a) and (b) indicate the maximum number of shots that can be executed on the actual quantum hardware.

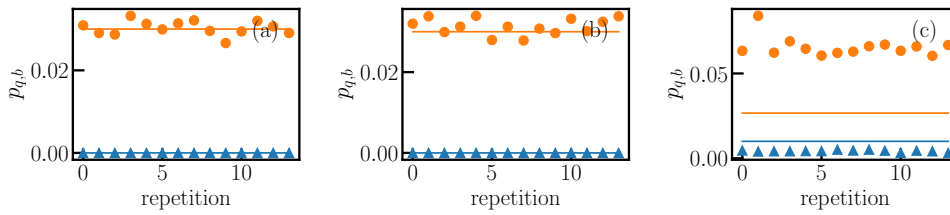
In Fig. 5(c) we show the results of the mean error and the standard deviation for running our experiments on the actual `ibmq_london` quantum hardware. Also here we observe an order of magnitude improvement in the errors and a scaling in the number of shots compatible with

	Simulation (readout only)	Simulation (full noise)	Hardware
full dataset	0.501	0.238	0.390
4 lowest data points	0.492	0.446	0.478

**Table 2.** Exponents  $\beta$  of power-law fits  $as^{-\beta}$  to our simulated and hardware data on the ibmq\_london quantum device.

$\propto s^{-\beta}$ , see Tab. 2 for the values of  $\beta$ . Figure 5(c) demonstrates that our error mitigation approach is working in practice and can hence be very useful in future applications for performing quantum simulations.

A key ingredient of our approach is the calibration of the readout errors of the qubits used in our experiments. Such calibrations have to be carried out before each run since there is a strong drift in the bit-flip probabilities. This is shown in Fig. 6. While the bit-flip probability is consistent with the values of the noise model in the simulator runs (see Figs. 6(a) and 6(b)), the values observed on real hardware differ significantly from those provided in the noise model (cf. Fig. 6(c)). This requires a calibration of the bit-flip probabilities before a quantum simulation is carried out in order to apply our error mitigation scheme.



**Figure 6.** Bit-flip probabilities  $p_{0,0}$  (blue triangles) and  $p_{0,1}$  (orange dots) for the single qubit case as a function of repetitions of the experiment using (a) a noise model mimicking the readout errors of of ibmq\_london only, (b) a noise model mimicking the full the full noise of ibmq\_london, and (c) the actual quantum hardware. The solid lines correspond to the data provided by the noise model.

## (b) Discussion

On the superconducting devices used in our experiments, we generally observe that readout errors of different qubits are uncorrelated to a good approximation. Thus, we have neglected multi-qubit correlations in this work. However, such correlations can be taken into account as discussed in Ref. [53] through multi-qubit calibrations.

Our method can also be applied, at least in principle, to other sources of noise. If, for example, we consider the qubit relaxation error with characteristic time scale  $T_1$  (decay of  $|1\rangle$  to  $|0\rangle$ ) [54], the measurement outcome for the  $Z$  operator corresponds to  $\tilde{Z} = p(t)Z + [1 - p(t)]\mathbb{1}$ , where  $p(t) = \exp[-(t/T_1)]$  is the probability that  $|1\rangle$  has not yet decayed. Then the exact operator  $Z$  is obtained from the noisy one by  $Z = [1/p(t)]\tilde{Z} - \{[1 - p(t)]/p(t)\}\mathbb{1}$ , meaning that we can again reconstruct the operator  $Z$  through noisy measurements.

Another advantage of our method is that it can be pre-processed, meaning the operator to be measured can be directly replaced with an appropriate linear combination of operators whose quantum mechanical expectation value subject to bit flips corresponds to the true expectation value of the original operator. This allows for easy use of our method with existing software frameworks. For example, many libraries provide functions implementing the VQE, which

typically ask for a Hamiltonian as an input parameter. Passing the appropriate bit-flip corrected Hamiltonian as an argument, the readout errors can be mitigated without additional effort.

As already mentioned above, our error mitigation method scales polynomially for  $k$ -nearest neighbor Hamiltonians and adds a moderate overhead for non-local Hamiltonians. Since most models in high energy or condensed matter physics have Hamiltonians with nearest or next-to-nearest interactions, our error mitigation scheme is therefore efficient.

## 4. Conclusion

In this article, we have reviewed two algorithmic advances towards eventually simulating models in high-energy physics and beyond with quantum devices. The motivation for such quantum simulations is twofold: first, there are several experimentally observed phenomena, such as the large amount of CP-violation in the universe, which cannot be explained by the SM; and second, several of these phenomena cannot be simulated with classical MCMC methods.

When realizing simple benchmark models on a quantum computer, for example the 1+1 dimensional Schwinger model, the results can be erroneous even for a small number of qubits. The reasons are various sources of noise on present quantum devices. It is therefore highly desirable to develop methods to reduce the number of noisy quantum gates and to mitigate the noise before embarking on simulations of realistic models.

To this end, we first developed the dimensional expressivity analysis, which allows us to identify redundant gates in a given quantum circuit. This in turn leads to the construction of minimal, but maximally expressive quantum circuits. In this way, we reduced the number of noisy gates and ensured that the constructed quantum circuit can reach the desired manifold of quantum states. If a maximally expressive quantum circuit cannot be realized, we developed an additional technique based on Voronoi diagrams to bound the best-approximation error, which helps us to estimate how far away the quantum computation can be from the desired state space.

As a second step, we developed a very general and efficient scheme for mitigating the readout error, which is presently among the dominating error on superconducting quantum computers. Our scheme relies on the readout error calibration of the used qubits and allows to evaluate the exact Hamiltonian from noisy measurements. We demonstrated the performance of our method by quantum computations both on the IBM-Q simulator and the IBM-Q hardware. Our algorithm shows a polynomial scaling in the number of qubits and is therefore efficient, at least when  $k$ -nearest neighbour interactions are considered. We are presently working on extending this error mitigation scheme to other sources of noise on quantum computers.

These advances in the circuit expressivity analysis and error mitigation tremendously help to render quantum computer simulations more reliable. Thus, one can eventually come back to the targeted simulations of high-energy physics models, with the goal of addressing at least some of the remaining open questions of the SM of particle physics.

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