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Modeling and control of series resonant converter for high voltage applications

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Abstract-Most industrial processes that use electricity as a source of voltage or current, require power circuit for the control of its physical quantities. Power converters are a multi-site privileged in the control of high power systems. In this article we show that the series resonant DC/DC converter, which is a hybrid system. The SRC achieves an output DC voltage equal to n times the input voltage of the converter, the major disadvantage of this type of converter is the the stresses on the power components and Capacitor Charge Time. Furthermore, a control strategy for minimizing the no-load conduction losses is proposed and the transient behavior in case of load steps including output shortcircuit is discussed based on digital simulations.

Index Terms-Power converter, Petri nets, hybrid dynamic systems, resonant converter.

I. INTRODUCTION

High voltage dc power supplies have wide application range. There are several challenging and unique issues to be addressed while designing these power supplies. The Series Resonant Converter (SRC) is a preferred converter topology for high power, high voltage power supplies. The advantages commanded by the SRC are a good compromise over its disadvantages. The classical SRC is a frequency controlled converter. Control can also be achieved by keeping the operating frequency fixed but varying the duty cycle. This article studies the SRC for use in high voltage applications. Complete steady state analysis of the converter for operation above the resonant frequency is presented. Dynamic analysis of the converter is done through numerical simulation. Interest in reprate operation of pulsed power systems has led to an increase in power requirements of high voltage (HV) capacitor charging power supplies (CCPS). The requirements for a power supply charging the capacitor are severe The main requirements for these applications should be as follow:

- Charging voltage regulated up to few kV.

- Charging time as low as few ms.

- The capability of withstanding short circuit conditions. High

Voltage power of the required level is generally not available in the raw form. A suitable HV generating equipment has to be used to convert the low voltage available from the utility supply, on board generators or battery pack to the required voltage level. The HV dc was obtained by rectifying the stepped up voltage using either vacuum tubes or other electromechanical devices [4]. These were not only large but also difficult to control, inefficient and unreliable. Modern HV power generators are solid state having high frequency designs and provide superior performance and control. The first section of this paper is devoted to the modeling of the converter with a study of cell switching. A reconfiguration of the control switch is proposed in response to a variation between the current branches, close control by Petri nets is one of the solutions best suited to this type of system [6]. Finally the results simulation are presented to validate the theoretical results.

II. THE RESONANT DC/DC CONVERTER MODEL

In Figure 1 we show a simplified nonlinear circuit representing the series resonant DC/DC power converter.

The SRC (Fig. 1) is a constant frequency, duty ratio controlled converter. The fixed switching frequency of the SRC is an advantage over the variable switching frequency operation of the classical SRC. The duty ratio is varied through phase modulation [5]. Conduction of switches on the leading leg of the inverter (T_{11}, T_{12}) is phase shifted with respect to the conduction of switches on the lagging leg (T_{21}, T_{22}) resulting in a quasi-square excitation

A direct computation shows that the controlled nonlinear differential equations modelling the circuit are given by[4],[5],[1].

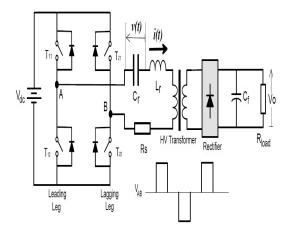


Fig. 1. Principle of series resonance converter (SRC).

$$\begin{cases} L_r \frac{di}{dt} = -R_s \ i - v - mv_o sign(i) + E(t) \\\\ C_r \frac{dv}{dt} = i \\\\ C_o \frac{dv_o}{dt} = mabs(i) - \frac{v_o}{R} - I_o \end{cases}$$
(1)

where v and i are, respectively, the series capacitor voltage and the inductor current in the resonant series tank, while v_o is the output capacitor voltage feeding both the load R and the sink current I_o which, for simplicity, we assume to be of value zero m is transformation ratio. E(t) is a control input signal (usually taken as a periodic square wave of variable amplitude and frequency), even though for design purposes the amplitude is considered fixed leaving the frequency as the only control variable. Which is usually restricted to take values in the discrete set $-V_{dc}$, V_{dc} where V_{dc} is a fixed given constant[4],[5].

The SRC is, therefore, equivalent to an RsLrCr circuit supplied by two alternating square voltages v_{AB} (generated by the action of the switches Q1,Q2, Q3 and Q4) with an amplitude equal to the input voltage, and voltage V_{dc} , as presented in Fig. 2.

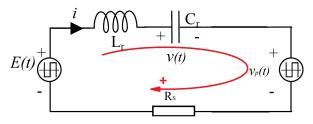


Fig. 2. Series Resonant DC-DC Converter equivalent circuit.

The resonant inductor (L_r) and resonant capacitor (C_r) are in series. The resonant tank and the load act as a voltage divider DC gain is always lower than 1 (maximum gain happens at the resonant frequency). The impedance of resonant tank can be changed by varying the frequency of driving voltage (E(t)).

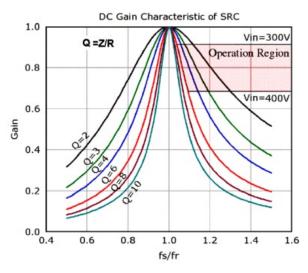


Fig. 3. DC Gain Caracteristic of Series Resonant DC-DC Converter.

Let $X \in \mathbb{R}^n$ be the continuous space of the inverter states and let $Q \in \{q_1, q_2, q_m\}$ be a finite set of discrete states. In case of the inverter from Fig. 1 and the control strategy presented below, n = 3 and m = 5. The continuous state space specifies the possible values of the continuous states for all switching configurations $q \in Q$ of the converter. For each $q \in Q$ the continuous dynamics can be modeled by differential equations of the form[8],[10]:

$$\dot{x} = f(x, q, t)$$

$$y = h(x, q, t)$$
(2)

The states of the circuit from Fig.1 are: $x(t) = [i, v, v_o]^T$. The discrete states of the system are defined by the states of the two-quadrant switches T_{11}, T_{12}, T_{21} and T_{22} . There are several possible strategies for the control of the inverter from Fig.1.

The converter has four modes of operation defined by the four combinations of the values of the two power supplies Fig.4.

Table I identifies the operation modes and the correspondent conducting switches and generated voltages.

| Config | 1 | 2 |
|----------|-------------------------|-------------------------|
| v_{AB} | $+V_{dc}$ | $-V_{dc}$ |
| u_{LC} | $+V_{dc} - v_p - R_s i$ | $-V_{dc} - v_p - R_s i$ |
| Switch | Q_1, Q_2 | D_{3}, D_{4} |
| Config | 3 | 4 |
| v_{AB} | $-V_{dc}$ | $+V_{dc}$ |
| u_{LC} | $-V_{dc} + v_p - R_s i$ | $+V_{dc} + v_p - R_s i$ |
| Switch | Q_3, Q_4 | D_1, D_2 |

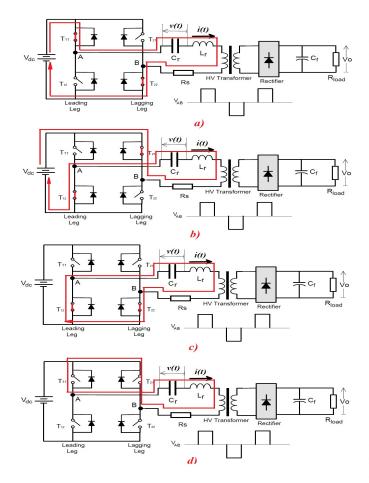


Fig. 4. The five configurations status of the inverter.

III. HYBRID CONTROL OF SERIES RESONANT CONVERTER BASED ON PETRI NETS

A. First case

Dynamical systems are usually continuous or discrete or both. Dynamical Systems Continuous (SDC) have variables whose behavior continuous in time (voltage, current, speed, torque). They are often modeled by differential equations or equations of state and transfer functions. For discrete dynamical systems (SDS), the space of output variables is a discrete set of Boolean value (state opening closing of a switch, number of switches open closed in a simultaneous static converter, number of pulses for controlling switches).

Systems including both continuous characteristics and discrete are called hybrid dynamic systems. In a very simplified one SDH has two sub sets, a continuous block, a block discrete:

- The continuous block symbolizes the dynamic evolution of the state continue in our case the RLC resonant circuit and HV transformer.

- The block has the discrete system is discrete event receives internal events, external conditions, the converter is the state of the switches of switching cells. In this work, we were interested in modeling method and control systems hybrid event-based dominant the use of Petri nets. The method is illustrated in Fig.5.

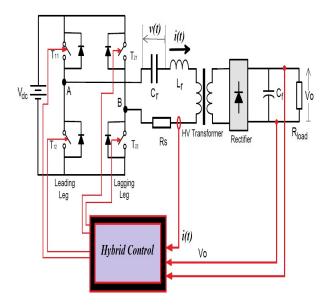


Fig. 5. The schematic of close-loop control of DC-DC resonant converter.

The command consists of two parts, a continuous and a discrete part. The first is based on a control loop PI controller for controlling the output voltage. This loop has as input the error $e_v = v_{o-ref} - v_o$ and I_{ref} as output current. The second control loop is modeled by a Petri net whose mission is the current regulation *i* to value i_{sref} calculated by the PI.

Figure 6 represents the Petri net control of the switches, the places P0, P1 and P2 respectively are modeling the initial state of converter and state of the switches of cells CELL1 and CELL2.

This algorithm is developed in order to control the output voltage of system. The transition from one place to another is dependent on the resonant current state of converter. The closure of the switch of the cell $(Cell_i)$ depends on the validation of the transition t_{i0} . The Petri net contol is composed of tow arcs inhibitors, their role is to prevent the presence of more than one token in places P1 and P2, [1],[7],[9].

Depending on the current i(t) and error $e_i = i_{ref} - i$ the converter is allowed to set in 4 configurations.

The significance of all places and transition is shown in Table 1 and 2

TABLE I SIGNIFICATION OF PLACES

| Places P_i | Designations |
|--------------|-------------------------------|
| P_0 | Initial state |
| P_1 | The switch of the first cell |
| P_2 | The switch of the second cell |

with :

 $-\delta_v =$ Undulation of voltage output

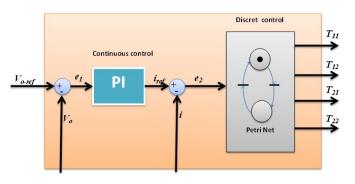


Fig. 6. Hybrid control of DC-DC resonant converter.

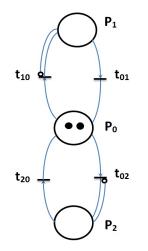


Fig. 7. The Petri net control of system.

TABLE II The transitions

| Transitions | Designations |
|-------------|--|
| t_{01} | $i_{ref} > 0$ and $e_i > \delta_i$ |
| t_{02} | $i_{ref} < 0$ and $e_i < -\delta_i$ |
| t_{10} | $i_{ref} < 0$ or $(i_{ref} > 0 \text{ and } e_i < \delta_i)$ |
| t_{20} | $i_{ref} > 0 \text{ or}(i_{ref} < 0 \text{ and } e_i > -\delta_i)$ |

$-\delta_i =$ Undulation of current

The time response of system is one of the major problems of this type of converter . Pollution of the power system harmonics reactions is one of the other consequences of this problem, we will show in the simulation result the contribution of our approach on the input current and clean up the power grid.

B. Second case

In this part of work we studied two resonant converters in parallel. The regulation consists of two parts:

-Two PI regulators for regulating of output voltage.

-A controller based on Petri nets for the regulation of currents i_1 and i_2 . the outputs of the networks Petri nets are the states of the switches cells.

The same as the first case the command consists of two parts, a continuous and a discrete part. The first is based on a control loop PI controller for controlling the both outputs voltages v_{s1} and v_{s2} (Fig.8). This loop has as inputs the error $e_{s1} = v_{sref1} - v_{s1}$, $e_{s2} = v_{sref2} - v_{s2}$ and I_{ref1} , I_{ref2} as outputs current references. The second control loop is modeled by a Petri net whose mission is the currents regulation i_1 and i_2 respectively to value i_{ref1} and i_{ref2} calculated by the PI.

Figure 9 represents the Petri net control of the switches, the places P0, P1, P2, P3 and P4 respectively are modeling the initial state of converter and state of the switches of cells CELL1, CELL2, CELL3 and CELL4.

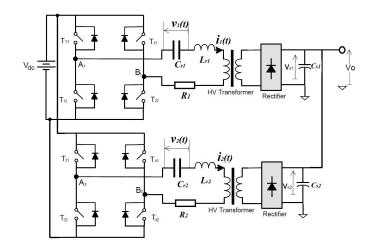


Fig. 8. DC-DC resonant converter.

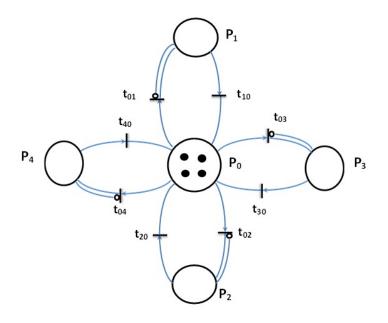


Fig. 9. The Petri net control of system.

This algorithm is developed in order to control the output

voltage of system. The transition from one place to another is dependent on the resonant current state of converter. The closure of the switch of the cell $(Cell_i)$ depends on the validation of the transition t_{i0} . The Petri net control is composed of arcs inhibitors, their role is to prevent the presence of more than one token in places Pi [1],[12],[3]. The significance of all places and transition is shown in Table 3 and 4

TABLE III SIGNIFICATION OF PLACES

| Places P_i | Designations |
|--------------|----------------------|
| P_0 | Initial state |
| P_1 | The switch of cell1 |
| P_2 | The switch of cell2 |
| P_3 | The switch of cell3 |
| P_4 | The switch of cell 4 |

TABLE IV The transitions

| Transitions | Designations | |
|-------------|---|--|
| t_{01} | $i_{ref1} > 0$ and $e_{i1} > \delta_{i1}$ | |
| t_{02} | $i_{ref1} < 0$ and $e_{i1} < -\delta_{i1}$ | |
| t_{03} | $i_{ref2} > 0$ and $e_{i2} > \delta_{i2}$ | |
| t_{04} | $i_{ref2} < 0$ and $e_{i2} < -\delta_{i2}$ | |
| t_{10} | $i_{ref1} < 0 \text{ or } (i_{ref1} > 0 \text{ and } e_{i1} < \delta_{i1})$ | |
| t_{20} | $i_{ref1} > 0 \text{ or}(i_{ref1} < 0 \text{ and } e_{i1} > -\delta_{i1})$ | |
| t_{30} | $i_{ref2} < 0 \text{ or } (i_{ref2} > 0 \text{ and} e_{i2} < \delta_{i2})$ | |
| t_{40} | $i_{ref2} > 0 \text{ or}(i_{ref2} < 0 \text{ and } e_{i2} > -\delta_{i2})$ | |

with :

 $-\delta_{i1}$ = Undulation current through the first resonant circuit $-\delta_{i2}$ = Undulation current through the second resonant circuit

IV. SIMULATION RESULTS

The simulation results are obtained using the parameters of converter as follows::

 $R_r = 2 \ m\Omega, \ L_s = 24 \ 10^{-6}H, \ L_p = 150 \ 10^{-6}H, \ C_s = 0.75 \ 10^{-6}F, \ V_{sref} = 50 \ 10^{+3}V, \ R_{load} = 10 \ \Omega, \ C_o = 100 \ 10^{-9}F, \ F_o = 15 \ kHz \ \text{and} \ V_{dc} = 560 \ \text{V}.$

After application of the close control of the converter, simulation results are shown in the following figures:

The Fig.10 and Fig.11 show respectively a simulation of the evolution of the output V_s voltage and output current i_s in both control.

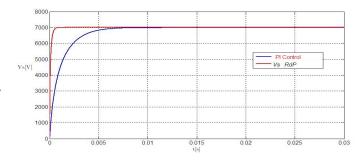


Fig. 10. Output voltage V_s variations in both control.

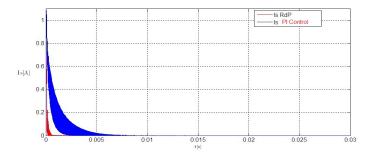


Fig. 11. Output current I_s variations in both control.

The figure Fig.12 represents the results obtained by using th proposed control law on the converter. The result shows the hybrid control using Petri net control regulate the output voltage V_{s1} , V_{s2} and V_s in a finite time.

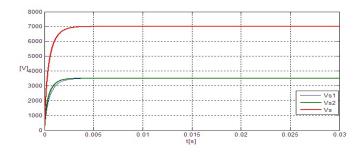


Fig. 12. Output voltages V_{s1} , V_{s2} and V_s

The figure Fig.13 shows the evolution in Output current . Note that if the system has an imbalance current phase, the control by a Petri net consumes less current wavy and this is an important factor for the electrical systems.

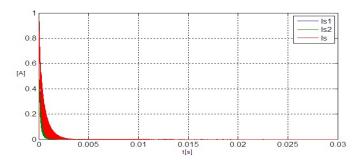


Fig. 13. Output currents I_{s1} , I_{s2} and I_s .

The dynamics of the output voltage is the image of the total current branch. The response time of the system is in the range of 3.10^{-3} s. Simulation results have shown that the hybrid controller is efficient with respect to PI controller. The difference is in the instantaneous reaction to changes in physical parameters of the system.

V. CONCLUSION

This work consists of a modeling and control of Series Resonant Converter, an algorithm for controlling the switches of the converter using Petri nets has been proposed to solve the problem related to the time response and robustness of control. Petri nets are among the powerful tools for modeling and control of such systems which have discontinuities in their mathematical models. The algorithm is based on the statements of resonant current , the resonant current reference calculated by the PI and the configurations of switch cells. Finally simulation results show the convergence of output voltage V_o to a neighborhood of the value of the reference output voltage V_o in finite time. The evolution of the resonant current and primary voltage of transformer shows a remarkable performance of our approach.

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