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Patterning

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Synthesis and Patterning of Gold Nanostructures on InP and GaAs via Galvanic Displacement**

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The increasingly challenging technological demands of metallic interconnects on functional semiconductor devices are driving a new area of nanoscience dedicated to building nanoscale metallic features interfaced directly to materials such as silicon, germanium, and compound semiconductors such as gallium arsenide and indium phosphide.^[1] The need for control over metal morphology (both shape and size), and efficient, reliable, and controllable reaction conditions are a few of the requirements that must be addressed. An approach under investigation for its synthetic potential is galavanic displacement, a type of electroless deposition in which metal ions in an aqueous solution are reduced by electrons arising from the semiconductor itself as shown in Scheme 1.^[2]

The room-temperature reactions require only water, semiconductor, metal salt, and possibly a fluoride or acid source to concomitantly dissolve the insulating oxide that forms on the surface of the semiconductor. Galvanic displacement is particularly advantageous because of its amenity to high-throughput processing, the fact that it requires only simple apparatus, generates little waste in contrast to

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Scheme 1. Outline of galvanic displacement. A semiconductor substrate in contact with a solution of sufficiently oxidizing metal ions (M^+) will reduce the M^+ to M^0 on the surface of the semiconductor. The semiconductor, in turn, is oxidized.

standard evaporation techniques, and efficiency. Films deposited by galvanic displacement are generally composed of irregular, spherical, or oblate metal structures or particles, some of which are crystalline as determined by TEM and XRD, and others yet undetermined.^[2] Recently, however, hexagonal stacks of crystalline silver nanoplates, termed "nano-inukshuks", have been prepared on germanium surfaces via galvanic displacement, and these results suggest that size and shape selectivity may be possible by controlling various reaction parameters.^[3] Galvanic displacement to prepare metal-semiconductor structures is still, however, sorely lacking the control and versatility of wet-chemical solutionphase synthesis to prepare isolated anisotropic metal nanoparticles of various shapes and sizes.^[4] There still remain, however, many combinations of technologically important semiconductors and transition-metal ions that should lead to spontaneous reduction of metal films that have not been studied in detail.

In this Communication, we describe the employment of galvanic displacement to prepare nanostructured gold films on single-crystal InP and GaAs. Metal contacts to these materials are important for applications such as next-generation computing platforms, optoelectronics, and LEDs, among others.^[5] While only a smattering of papers have ex-



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Jillian Buriak is a Professor of Chemistry at the University of Alberta, holder of the Canada Research Chair of Inorganic and Nanoscale Materials, and Group Leader of the Materials and Interfacial Chemistry Group at the National Institute for Nanotechnology of the National Research Council. She was born in Toronto, Canada in 1967, and completed her AB at Harvard University and her PhD at the Université Louis Pasteur (Strasbourg, France). She followed up with a post-doc

at The Scripps Research Institute and took up her first independent position of assistant professor in the Department of Chemistry at Purdue University (USA) in 1997. She was promoted to associate professor in 2001, and moved to her present position in 2003. Recent awards include the Rutherford Medal, the ACS Pure Chemistry Award, and the Dreyfus Teacher-Scholar Award. amined galvanic displacement of noble metals (Au and Pd) on InP and GaAs, alloying or intermetallic formation between the noble metals and these compound semiconductors has been reported.^[6] Galvanic displacement on III–V semiconductors may therefore form the basis for development of a mechanically strong, direct electronic contact between the metal nanoparticle–semiconductor interfaces, important for novel nanoscale metallic interconnects and direct semiconductor–nanoparticle wiring.

Immersion of degreased, native-oxide-capped InP(100) wafer shards in an aqueous HAuCl₄ (1 mM) solution in the presence of dilute acid at room temperature results in rapid deposition of gold nanoparticles on the surface. In the presence of 2% H₂SO₄ as the acid, the surface is decorated with strongly adhering, faceted sub-100 nm-diameter gold nanoparticles within 30 min, as shown in Figure 1. In the center of the wafer, the particles are discrete and separate, but closer to the wafer edges, more metal deposition is noted



Figure 1. SEM images of gold nanoparticles interfaced with InP(100), produced in the presence of 1 mM HAuCl₄ and 2% H₂SO₄. a) Center of InP(100) wafer; b) towards the edge of the InP(100) wafer where increasing deposition is observed; c) centre of an InP(100) wafer produced in the presence of 1 mM HAuCl₄ and 0.1% H₂SO₄.

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and the nanoparticles are in contact (Figure 1 b).^[3] Fivefold twinning appears prevalent based on the observation of many nanoparticles with pseudo-pentagonal shapes and ico-sahedra (Figure 2).^[7] Other forms noted include truncated tetrahedra, hexagonal and triangular plates, rods, and other polyhedra, shapes routinely observed via solution-phase syntheses.^[4a,b] As shown in Figure 2 g, particles with spherical or twinned (fivefold axes visible, including icosahedra) structures predominate, and hexagonal and triangular plates, rods, and other semi-regular shapes such as truncated



Figure 2. Different nanoparticle shapes observed on InP(100): a) icosahedra; b) hexagonal plates; c) rods; d) triangle plates; e, f) truncated tetrahedra. g) Distribution of different shapes observed at 2 % H_2SO_4 and 1 mM HAuCl₄. The distribution was determined by SEM image analysis, based on an average of three different samples, with 160–400 particles per sample.

tetrahedra (Figure 2e and f) make up a small fraction of the gold nanoparticles, on the order of 10-15%. XRD clearly indicates that the structures are crystalline gold (see Supporting Information), as would be expected based on the obvious faceting. The gold nanoparticles resist sonication in methanol, although thicker layers of gold, visible to the naked eye, can be removed with scotch tape. Reducing the sulfuric acid concentration to 0.1% appears to slow down the rate of deposition, and renders it more uniform across the face of the InP(100) wafer. Statistical analysis of the breakdown of nanoparticle shapes reveals, that within experimental error, the distribution at 0.1 % H₂SO₄ is similar to that of 2 % H_2SO_4 (Figure 2 g). Leaving the InP(100) immersed for longer than 2 h in 0.1% or higher of sulfuric acid/1 mM HAuCl₄ results in roughening of the indium phosphide surface between the nanoparticles, followed by continued growth and coalescence of the nanoparticles to form an almost continuous film across the face of the wafer that is visible to the naked eve. Deposition of HAuCl₄ (in aqueous solution) in the absence of an external acid source produces scattered sub-100 nm gold nanoparticles on the surface, but nucleation and growth cease after 30 min, as determined by AFM.

Shape-selective syntheses of gold nanoparticles in solution generally require a structure-directing agent such as cetyltrimethylammonium bromide (CTAB) or poly(vinyl pyrrolidone) (PVP).^[4a-c] Since these additives are absent in this system, the effects of the acid counteranion were investigated for their possible role in gold structure development and selection. HF, HCl, HBr, and H₃PO₄ were examined, the last being of particular interest to mimic possible soluble phosphorous oxidation products of InP(100) corrosion. Surprisingly, only subtle counteranion effects were noted in the gold galvanic displacement products. HF results in a more rapid deposition and thicker nanoparticulate films, and dilute (2%) HCl and HBr reactions were similar to 0.1% sulfuric acid, although HBr deposition was somewhat slower. Nanoparticles formed in the presence of H₃PO₄ appeared less well defined, although similar hexagonal and triangular plates, and fivefold twin structures were noted. The rates of initial nucleation and reduction of gold in the presence of HF, H₂SO₄, and H₃PO₄ on InP(100) were determined by open-cell potential experiments, and are identical, taking place within the first 100 s of immersion (see Supporting Information), as has been noted previously for Ni²⁺ and Ag⁺ deposition on silicon and germanium, respectively.^[2a,3] Following the initial stage of nucleation, only particle growth occurs, explaining the relative similarities of the average particle size, both intra-and inter-sample.

The mixture of shapes observed on the InP(100) surface is very similar to that observed in seeded systems with, for instance, CTAB surfactant mixtures on surfaces.^[8] The reasons for this are not obvious. While superficially these galvanic displacement reactions appear very simple, requiring only water, salt, wafer, and usually an acid source, the systems become complex due to the formation of soluble and insoluble species via galvanic displacement, including oxides of phosphorous and indium, and chloride ions (from the HAuCl₄ precursor), as well as a concentration of metal salt

small

that is continually decreasing. It is possible that one or more of the solution-phase components acts as a directing agent. In addition, since the underlying material on which this deposition is occurring is a semiconductor, electrochemical Ostwald ripening may be playing a role in the growth of larger particles, at the expense of smaller ones, although this remains to be determined.^[9] In situ AFM studies are presently underway to better understand nucleation and growth.

Galvanic displacement of HAuCl₄ on GaAs(100) is much less structured than on InP(100), and does not require an external acid source. Dendritic growth occurs rapidly on the edges of the wafer, and is accompanied by irregular gold deposits on the front face of the wafer with scattered 1-µmdiameter flat hexagonal and triangular plates (see Supporting Information). Open-cell potential experiments also indicate rapid nucleation of gold on the surface, followed by an equilibrium metaldeposition regime. Deposition in dilute H_2SO_4 (0.1%) with 1 mM HAuCl₄ results in almost complete coverage of the GaAs(100) surface with gold within 1 h of deposition (see Supporting Information).

The galvanic displacement reaction is not particularly site-selective, and to demonstrate that this chemistry is amenable to surface patterning, we continued with an approach developed in our group based on static plowing microscopy using an atomic force microscope (AFM).^[10] Using a diamond-coated AFM tip, the oxide coating layer of InP or GaAs could be selectively removed through mechanical abrasion, thus exposing the underlying substrate. Scribing through the native oxide of InP(100) with the diamond AFM tip with a force of



Figure 3. Scanning plowing lithography on InP(100) and GaAs(100). a) Outline of the procedure: plowing with a diamond-coated AFM tip through the oxide exposes the underlying semiconductor. b–g) SEM images of gold deposited on InP(100) formed by scribing through the native oxide, followed by galvanic displacement. h–m) SEM images of gold deposited on GaAs(100) formed by scribing through the thermal oxide, followed by galvanic displacement.

 6μ N, followed (within seconds) by dipping in an aqueous 0.1 mM HAuCl₄ solution for 10 min results in rapid deposition of gold exclusively in the scribed areas, as shown in Figure 3, with a small number of defects visible. Because galvanic displacement of gold occurs rapidly on GaAs(100) without acid, a 5-nm-thick thermal oxide was prepared, and the sample scribed to yield a 4-nm-deep trench on the surface. Rapid immersion in a 0.1 mM HAuCl₄ solution again

results in exclusive metal deposition in the scribed areas, with no pinhole defect deposition; the thermal oxide is sufficiently thick and insulating to prevent large-scale gold deposition in the unscribed areas within this time frame. Detailed studies of feature width and control over the rate of deposition are presently underway.

To conclude, galvanic displacement of gold on InP results in crystalline nanoparticles of various shapes such as

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icosahedra, triangular and hexagonal plates, and other structures, in the absence of any obvious structure-directing agent. Galvanic displacement of gold on GaAs also proceeds, but up to this point, has led to unstructured gold films. This technique is very promising due to its simplicity and high efficiency. The influence of additives is presently under investigation in order to generate greater shape selectivity, as well as detailed electrochemical studies of the nanoparticle–semiconductor interfaces themselves. Through nanolithography based on scribing through surface oxide, the galvanic displacement reaction can be patterned on the surface of both InP and GaAs, and detailed studies of control over the feature sizes, shapes and other parameters are underway.

Experimental Section

InP(100) n-type wafers (resistivity=0.001 $\Omega\,cm$) were purchased from CRYSTACOMM. An n-type InP(100) wafer shard was sonicated in acetone and methanol for 10 min each and then dried with a N₂ flux. The InP wafer shard was placed in 100 mL of 1:1 49% HF (aq) and 18 M Ω (Barnstead) water for 10 min.^[11] The wafer was then dipped for 2 min each into five beakers containing 18 M Ω water and then dried with a N₂ stream.

GaAs(100) n-type wafers (resistivity = 0.001 Ω cm) were purchased from ITME. An n-type GaAs(100) wafer shard was sonicated in acetone and methanol for 10 min each and then dried with a N₂ flux. The GaAs wafer shard was placed in H₂SO₄:H₂O₂:H₂O (4:1:100) solution for 2 min then HCl:H₂O (1:3) for 2 min and 18 M Ω (Barnstead) water for 10 min. The wafer was then dipped for 2 min each into five beakers containing 18 M Ω water and then dried with an N₂ stream.

To carry out galvanic displacement, the InP or GaAs sample was placed in a 10 mL mixture of $HAuCl_4$ (Aldrich) and acid solution in a glass vial and kept in the dark for the required length of time. The InP wafer was then removed from the solution and dipped in water and then methanol and dried under a nitrogen stream.

Scanning probe nanolithography was carried out in the following manner. A diamond-coated silicon tip (Veeco Instruments, ULNC-DCBO, 17 $N\,m^{-1}$ spring constant) was employed to plow through the thermal oxide layers, thereby exposing the underlying bulk semiconductor. While these specialty tips were more cost-prohibitive, the economical oxide-sharpened silicon tips (MikroMasch, NSC-15) were found to lack the strength required for plowing a continuous furrow through the inorganic oxide layers. Static plowing was accomplished in contact-mode operation, where the tip was scanned across the sample surface with a constant force ($\approx 6 \mu N$). This set-point proved sufficient to plow through the thermal oxide, thereby exposing the GaAs(100) or InP(100) surface as scanning progressed. Continuous furrows were reproducibly obtained at scanning speeds of 10-100 $\mu m\, \text{s},^{-1}$ with lengths in hundreds of micrometers, restricted only by the range of the available AFM scanner. Vector scan operation allows for the AFM tip to be driven along paths of arbitrary length scales at predefined angles, while also providing for control over z-axis displacement and applied force. Utilizing the NanoScriptä software (Veeco Instruments), a general programming script provided access to a diverse set of customized scan profiles. As a consequence of an open-loop instrument, such as the Nanoscope III, some drift was observed in the resist furrows. Furrows were imaged employing oxide-sharpened silicon tips (MikroMasch, NSC-15), operating in intermittent (tapping) mode.

Keywords:

galvanic displacement • gold • nanolithography • nanoparticles • semiconductors

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