

## CMRR Boosted Instrumentation Amplifier for Biomedical Application

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### I. Introduction

The Analog Front End is the most important part of a Signal Processing System. For a signal to be processed in any form say digital or analog, first it needs to be acquired properly in desired form. An Analog Front End makes way for this. The design of an AFE depends on the nature of signal of interest. Based on the nature the type of amplifier and filter that needs to be employed can be decided. The design also depends on various factors like controllability that needs to be offered, power, etc. By controllability, it means that the system can be programmable or specific to a particular biomedical signal say ECG, EEG or EMG.

As Shown in figure.1, an AFE basically consists of a pre amplifier, filter and a post amplifier. This is a conventional way of designing an AFE. Many contemporary methods are also available, few of which will be discussed in the forthcoming chapters. The objective of this system is to boost the signal at desired frequency and extracting it for further processing. The nature of the signal must remain the same at the same time the system must be able to sense even a signal with very small amplitude and also the selectivity of the filters must be good. There are many factors that need to be considered before getting into the design.

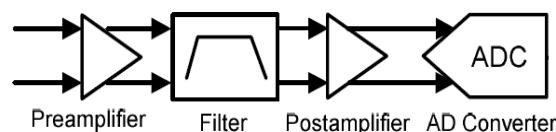


Fig. 1: Conventional Analog Front End

### II. Proposed Design of Folded Cascoded OPAMP

The schematic of the folded cascode OPAMP designed is as shown in Figure 2. The load is comprised of a series of current mirrors that provides high output impedance. PMOS transistors are used at the input to avoid flicker noise. The folded cascode structure is usually implemented in a single stage. As in case of a basic two stage OPAMP there is no capacitance that is explicitly included to have a control over the frequency response. It is the parasitic junction capacitance that influences the response.

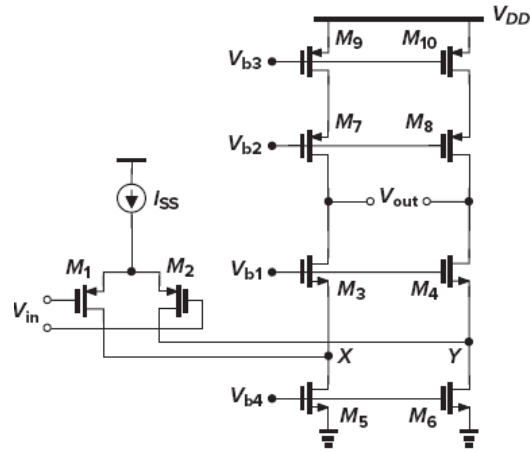


Fig. 2: Proposed folded cascode op-amp design

The main advantage of this architecture is that the output swing is increased. The schematic representation of the folded cascode operational amplifier is shown in figure 3.

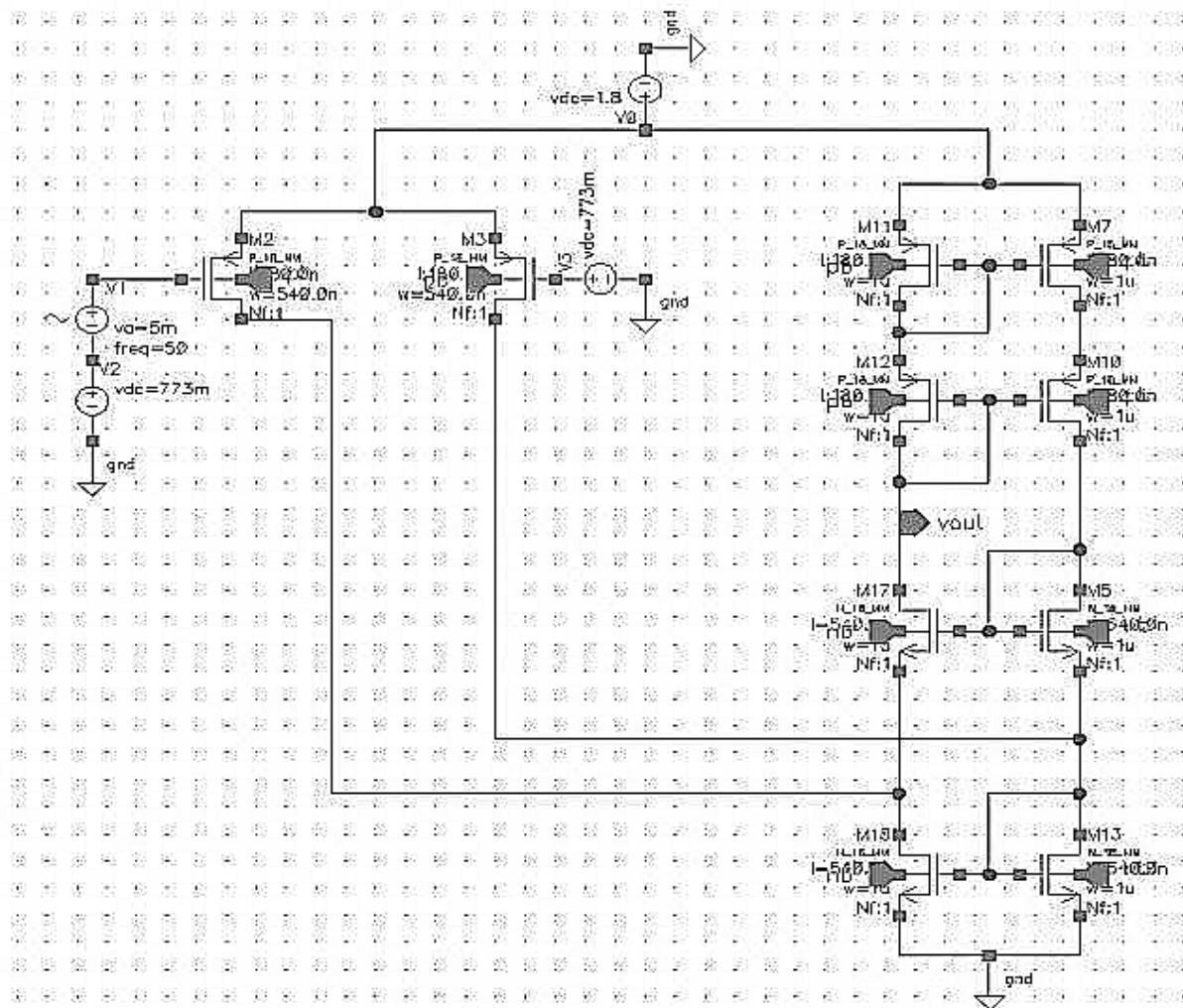


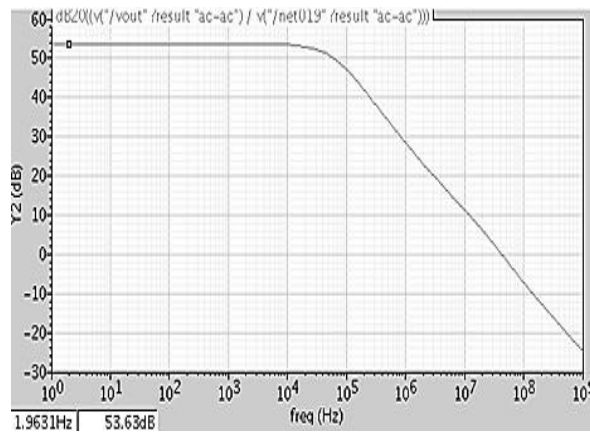
Fig. 3. Schematic of the Folded Cascode OPAMP

The sizes of the transistors play a vital role in this design. Various things like bias voltages, VTC of the amplifier, etc. depend directly or indirectly on the size of these transistors.

**Table I. Transistor Sizing parameters**

Table Column Head		
<i>Transistor</i>	<i>Length</i>	<i>Width</i>
M2,M3	180nm	540nm
M5,M7,M10,M11	540nm	1um
M13,M18,M12,M17	540nm	1um

The AC Response of the designed Folded Cascode OPAMP is shown in Fig. 4. As it can be seen the gain of the OPAMP is 53.34dB.



*Fig. 4. AC Response of the Folded Cascode OPAMP*

### III. Implementation of Proposed Design of Folded Cascoded OPAMP in Instrumentation Amplifier

The low Ri hindrance of the instrumentation amplifier can be sorted out by using buffers made up of voltage followers in the input terminals. A voltage follower is connected in the input terminals of the differential amplifier.

The output is given by,

$$V_o = (R_4 / R_3)(1 + R_2 / R_1)$$

It can be seen that if the open loop gain of the OPAMP is high then the gain of the Instrumentation amplifier depends only on the passive components i.e. the resistors. The structure of the instrumentation amplifier is shown in figure 5.

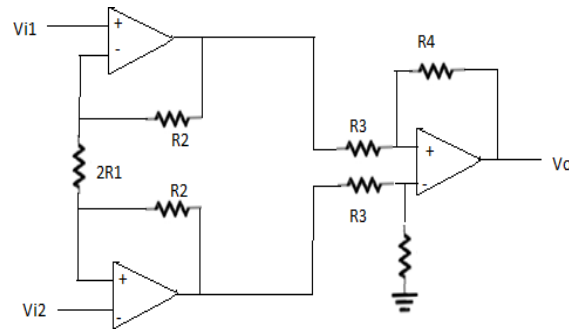


Fig. 5. Instrumentation Amplifier

The Figure 6 shows instrumentation amplifier that utilizes the Two Stage OPAMP previously designed. As mentioned earlier the input impedance of this amplifier is quite high compared to a single OPAMP. If the open gain of the OPAMP is made high then the Instrumentation Amplifier will have a response close to predicted. The OPAMPS in the first stage are use in non-inverting configuration while the one that is used in second stage is a difference amplifier.

In practical the resistors will be replaced by transistors and will be forced to operate in a specific region so that the resistance offered will remain the same throughout the range of the input.

One of the main disadvantages of this architecture is it is hard to maintain the resistances used for a wide range of output even if the resistors are replaced with transistors.

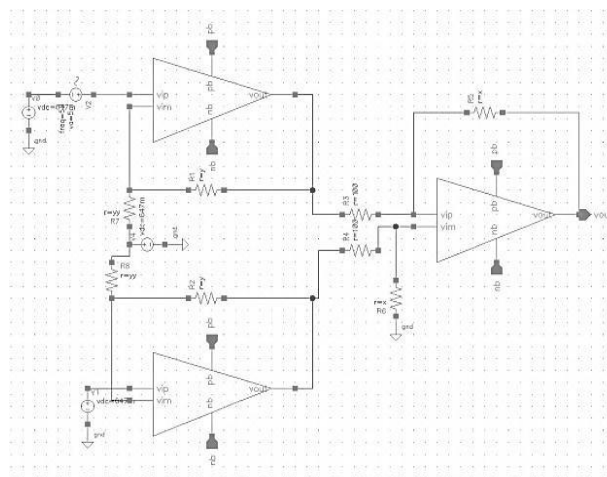


Fig. 6. Simulation of Instrumentation Amplifier with folded cascade op-amp

The main parameters of an OPAMP i.e. the common mode gain and differential mode gain are shown in this section. It can be seen that the same is around -26 dB for a wide range of frequency. The differential mode gain should be as high as possible so that the OPAMP is close to an ideal one The common mode gain and differential mode gain analysis is shown in figure 7 and 8 respectively.

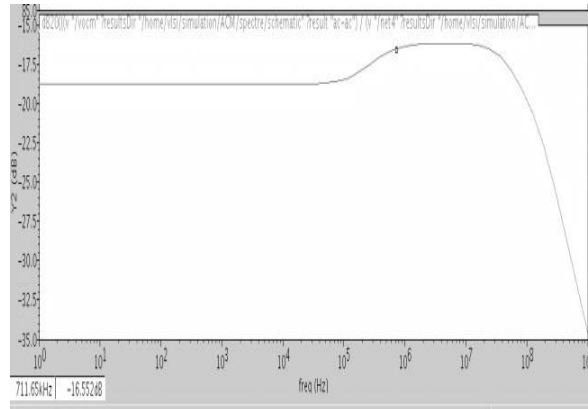


Fig. 6. Common Mode Gain of Instrumentation Amplifier with folded cascade op-amp

The differential mode gain of the OPAMP designed is 59 dB for a wide range of frequencies (till 1Mhz). Thus making it suitable for bio medical applications. From these two results the CMRR of the OPAMP can be found to be 79dB.

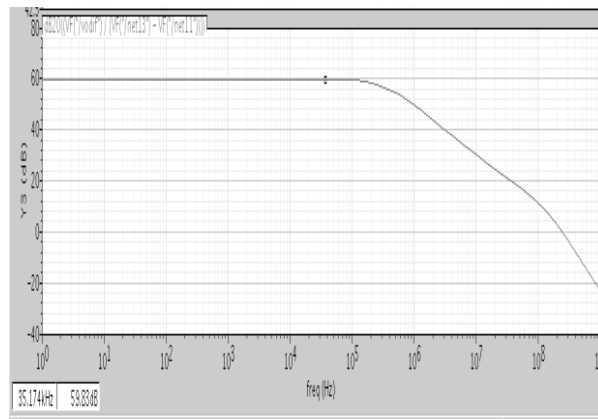


Fig. 7. Differential Mode Gain of Instrumentation Amplifier with folded cascade op-amp

The designed OPAMP has a high CMRR that makes it suitable for bio medical applications. As the gain and CMRR are high the gain of instrumentation amplifier designed from this OPAMP will be free from internal parameters and depend only on passive elements like resistors and capacitors. The simulation analysis of the proposed instrumentation amplifier is shown in figure 8.

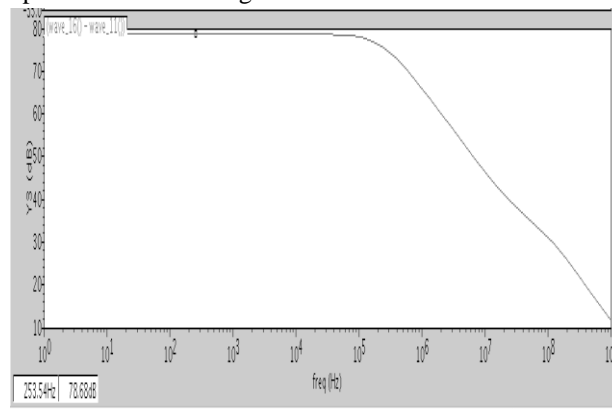


Fig. 8. CMRR of Instrumentation Amplifier with folded cascade op-amp

#### IV. Conclusion

The designed OPAMP has a high CMRR that makes it suitable for bio medical applications. As the gain and CMRR are high the gain of instrumentation amplifier designed from this OPAMP will be free from internal parameters and depend only on passive elements like the resistors and capacitors involved. The simulations are performed using TANNER EDA tool. Future works will be narrowed down toward implementation in Analog Front End.

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