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# Periodic Single-Pass Instruction Sequences

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#### Abstract

A program is a finite piece of data that produces a (possibly infinite) sequence of primitive instructions. From scratch we develop a linear notation for sequential, imperative programs, using a familiar class of primitive instructions and so-called repeat instructions, a particular type of control instructions. The resulting mathematical structure is a semigroup. We relate this set of programs to program algebra (PGA) and show that a particular subsemigroup is a carrier for PGA by providing axioms for single-pass congruence, structural congruence, and thread extraction. This subsemigroup characterizes periodic single-pass instruction sequences and provides a direct basis for PGA's toolset.

Keywords: Program algebra, Repeat instruction, Equational specification.

## Contents

1	Inti	roduction	<b>2</b>
<b>2</b>	K-basics		
	2.1	Primitive instructions, $K$ and $K_r$	3
	2.2	Single-pass congruence and first canonical forms	4
	2.3	$K_r$ -programs and their first canonical forms $\ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots$	6
3	Execution of $K_r$ -programs		
	3.1	Thread algebra	7
	3.2	Behavioral semantics for $K_r$ -programs: threads	9
	3.3	$K_r$ -programs, second canonical forms and thread extraction $\ldots \ldots \ldots \ldots \ldots$	11
4	Discussion and conclusions		13
	4.1	Program algebra	13
	4.2	PGLA	15
R	References		

## 1 Introduction

Our starting point of view is that a "program" is a finite piece of data for which the preferred or natural interpretation (or meaning) is a sequence of primitive instructions (SPI), and we say that a program *produces* a SPI. Primitive instructions comprise jump instructions, test instructions and basic instructions that upon execution may alter some state; in the next section we introduce a set  $\mathcal{U}$  of primitive instructions that we used in previous research.

The execution of a SPI is *single-pass*: it starts with executing the first primitive instruction, and each primitive instruction is dropped after it has been executed or jumped over. This point of departure represents a most basic view of what a program constitutes: the syntactic denotation of a SPI to be executed in single-pass mode.<sup>1</sup> A SPI can either be finite or infinite.

A very basic question is how to define programs that produce the classes of SPIs we are interested in, adopting the point of view that a program itself is a finite sequence of instructions. A first, very straightforward and simple approach to this question is to start from constants for primitive instructions and to adopt *concatenation* as an operation for composing programs: each primitive instruction is a program, and if P and Q are programs, then so is their concatenation P; Q. Furthermore, it is useful to postulate that concatenation is associative and (thus) to leave out brackets in repeated concatenations. This implies that programs built in this way represent the most simple set of programs that produce finite SPIs, and that in mathematical terms, this set constitutes a *semigroup*. We shall use the notation

K

for this very basic semigroup, where K abbreviates Kernel instruction sequence notation.

However, in order to give an account of sequential, imperative programming one needs programs that can produce certain *infinite* SPIs (cf. programs that define the finite-state control of a Turing machine). An infinite SPI is *periodic* if it can be produced by a program of the form

 $u_1;\ldots;u_{n+k};\mathbb{W}\#n$ 

with  $u_i \in \mathcal{U}$ , the set of primitive instructions, n > 0 and  $k \ge 0$ , and with the *repeat instruction*  $\backslash \not \# n$ , which is defined as follows: for n a natural number larger than 0,

 $\parallel \# n$ 

In order to provide a setting for defining programs as finite sequences of instructions, we define below two semigroups, where we write  $S^+$  for the set of finite sequences with elements from alphabet S and for which we (also) use ";" as a separator:

<sup>&</sup>lt;sup>1</sup>In [4], SPIs are referred to as *program objects*.

- The semigroup with domain  $\mathcal{U}^+$  and concatenation as its operation, representing the finite SPIs and the finite programs over  $\mathcal{U}$ . As stated above, we use the name K for this semigroup.
- The semigroup with domain  $(\mathcal{U} \cup \{ \setminus \#n \mid n \in \mathbb{N}_{>0} \})^+$  and concatenation as its operation; this semigroup will be used to represent periodic SPIs. We use the name  $K_r$  for this semigroup.

In Section 2 we introduce the primitive instructions we work with and for programs in Kand  $K_r$ , we provide an axiomatization of *single-pass congruence*, the congruence that identifies programs that produce identical SPIs. We discuss the fact that not each sequence of instructions in  $K_r$  can be called a program. For example, the question whether the  $K_r$ -expression

 $u; \setminus \#2$ 

produces a SPI — and if so, which one — has no obvious answer. We distinguish a subset of the domain of  $K_r$  that rules out this question and contains the  $K_r$ -programs that produce all finite and periodic SPIs. In Section 3 we discuss the behavior of  $K_r$ -programs using thread algebra and we define a thread extraction operator that can be applied to  $K_r$ -programs. For K-programs and  $K_r$ -programs we provide axioms for structural congruence, a congruence that admits the unchaining of jump counters and preserves the behavioral semantics of programs (thread extraction applied to structural congruent programs yields equal threads). In Section 4, we relate our approach to PGA (program algebra, [4]), which represents the analysis of SPIs starting from a more mathematically oriented design of a program notation for periodic SPIs (comprising a repetition operator instead of repeaters), and to our program notation C [7], a program notation design based on primitive instructions that explicitly prescribe whether the orientation of the execution order is left-to-right or vice versa.

## 2 K-basics

In this section we formally define our set of primitive instructions (taken from [4]) and the semigroups K and  $K_r$ . Then we discuss canonical forms as a preferred form of representation of  $K_r$ -expressions and define  $K_r$ -programs.

### **2.1** Primitive instructions, K and $K_r$

Let A be a set of constants and write  $\mathbb{N}_{>0}$  for  $\mathbb{N}\setminus\{0\}$ , where  $\mathbb{N}$  represents the natural numbers.

**Definition 1.** *K*-expressions, also called *K*-programs, are defined by the following grammar, where  $a \in A$  and  $k \in \mathbb{N}$ :

 $P ::= \mathbf{a} \mid +\mathbf{a} \mid -\mathbf{a} \mid \#k \mid ! \mid P; P$ 

and where the operation; is called concatenation.

 $K_r$ -expressions are defined by the following grammar, where  $\mathbf{a} \in A$  and  $k \in \mathbb{N}$ ,  $n \in \mathbb{N}_{>0}$ :

 $P ::= \mathbf{a} \mid +\mathbf{a} \mid -\mathbf{a} \mid \#k \mid ! \mid \backslash \backslash \#n \mid P; P$ 

 $(K_r$ -programs are defined in Definition 4).

Let  $\mathbf{a} \in A$  and  $k \in \mathbb{N}$ . Then each of  $\mathbf{a}, +\mathbf{a}, -\mathbf{a}, \#k, !$  is called a *primitive instruction* and primitive instructions occurring in K-expressions can be explained as follows:

- A basic instruction  $\mathbf{a} \in A$  prescribes an atomic piece of behavior that is considered indivisible and executable in finite time. After completion of its execution, it prescribes to execute the next instruction (if available). One can consider various specific instances of A and we mention here the set of molecular programming primitives, see, e.g., [1].
- A basic instruction can be turned into a *test instruction* by prefixing it with either the symbol + (positive test instruction) or with the symbol (negative test instruction), thus typically +a, -b etc. Test instructions control subsequent execution via the result of their execution, which is a Boolean reply that may depend on the execution state:<sup>2</sup> we explain this in detail in Section 3.1.
- A next kind of primitive instruction is the *jump instruction* #k where  $k \in \mathbb{N}$ : this instruction prescribes to jump k primitive instructions ahead (if possible; otherwise deadlock occurs) and generates no observable behavior. The special case #0 prescribes deadlock.
- The *termination instruction* ! prescribes successful termination, an event that is taken to be observable.

We write  $\mathcal{U}$  for the set of primitive instructions and we shall use  $u, u_1, u_2, ..., v, v_1, v_2, ...$  as typical variables for elements in  $\mathcal{U}$ . We define each element of  $\mathcal{U}$  to be a SPI (Sequence of Primitive Instructions).

Finite SPIs are produced by K-expressions (see Definition 1). We take concatenation to be an *associative* operator and leave out brackets in repeated concatenations, so we simply write

 $u_1; u_2; \ldots; u_n$ 

for the K-expression built up from the primitive instructions  $u_1, ..., u_n$  by n-1 repeated concatenations. Thus K is the free semigroup with generators from  $\mathcal{U}$ .

Finally, the non-primitive repeat instruction  $\backslash \not \# n$ , where  $n \in \mathbb{N}_{>0}$ , prescribes to repeat the last preceding n instructions. Repeat instructions are also called *repeaters*. So-called *periodic* SPIs are produced by  $K_r$ -expressions (see Definition 1). Again, we take concatenation to be an *associative* operator and leave out brackets in repeated concatenations, thus  $K_r$  is the free semigroup generated by  $\mathcal{U} \cup \{ \backslash \# n \mid n \in \mathbb{N} \}$ . By definition, K is a subsemigroup of  $K_r$ .

## 2.2 Single-pass congruence and first canonical forms

In this section we define *single-pass congruence*, the congruence that characterizes extensional equality of SPIs, i.e., the equality defined by having the same primitive instruction at each position in the SPI that is produced.<sup>3</sup>

For K-expressions, thus  $K_r$ -expressions not containing repeaters, single-pass congruence boils down to the associativity of concatenation.

 $<sup>^2</sup>$  Upon reply true, a positive test instruction prescribes to execute the next instruction (if available) and

#### 

$$\mathbb{N}\#n; X = \mathbb{N}\#n \tag{2}$$

$$u_1;\ldots;u_m;v_1;\ldots;v_n;u_1;\ldots;u_m;\backslash\!\!\backslash \#m+n = u_1;\ldots;u_m;v_1;\ldots;v_n;\backslash\!\backslash \#m+n \tag{3}$$

Table 1: The axiom set  $E_{spc}$  for single-pass congruence on  $K_r$ -expressions, where  $m, n \in \mathbb{N}_{>0}$ and  $u_i, v_j \in \mathcal{U}$ 

Define for n > 0 and X an  $K_r$ -expression,  $X^{n+1} = X; X^n$  and  $X^1 = X$ . Single-pass congruence for  $K_r$ -expressions is axiomatized by the axiom schemes (1)–(3) in Table 1 and equational logic, and we write

 $E_{spc}$ 

for this proof system. Although equations (1)–(3) are in fact schemes in  $m, n \in \mathbb{N}_{>0}$ , we further refer to these as "axioms". Whenever two  $K_r$ -expressions X and Y are single-pass congruent, this is written

 $X =_{spc} Y,$ 

and the subscript *spc* will be dropped if no confusion can arise.

**Proposition 1.** The unfolding property

 $u_1;\ldots;u_n; \mathbb{N} \# n = (u_1;\ldots;u_n)^2; \mathbb{N} \# n$ 

follows from  $E_{spc}$ .

Proof.

$$E_{spc} \vdash u_1; \dots; u_n; \backslash \backslash \# n = (u_1; \dots; u_n)^2; \backslash \backslash \# 2n \qquad by (1)$$
$$= u_1; \dots; u_n; (u_1; \dots; u_n)^2; \backslash \backslash \# 2n \qquad by (3)$$

$$= u_1; \ldots; u_n; u_1; \ldots; u_n; \ \ \forall \neq n$$
 by (1)

$$= (u_1; \ldots; u_n)^2; \backslash \! \backslash \# n.$$

In  $E_{spc}$ , axiom (2) implies that each expression in  $K_r$  can be equated to one that contains at most one repeat instruction. This leads to the following preferred representation of  $K_r$ -expressions.

upon reply **false** it prescribes to skip the next instruction and to proceed execution with the instruction thereafter. A negative test instruction has the same effect, but with the role of the replies reversed.

<sup>&</sup>lt;sup>3</sup>Although a bit long, *primitive instruction sequence congruence* would also be an adequate name.

**Definition 2.** A  $K_r$ -expression is a first canonical form if it is of the form

 $u_1;\ldots;u_n$  or  $u_1;\ldots;u_k;\backslash\backslash\#n$ 

with  $u_i \in \mathcal{U}, k \in \mathbb{N}$  and  $n \in \mathbb{N}_{>0}$ , where  $u_1; \ldots; u_0$ ; represents the empty sequence.

For each  $K_r$ -expression, its first canonical form is obtained by applying axiom (2) to the leftmost occurring repeater if present, and otherwise it is that expression itself.

Not all  $K_r$ -expressions have an intuitive meaning. For example,

 $a; \parallel \#2$  and  $\#7; +a; \parallel \#5$ 

illustrate this situation. Note that such first canonical forms can not be rewritten using any of the axioms (1)–(3) in Table 1. As a consequence, single-pass congruence is not a meaningful notion for such first canonical forms and in the next section we will exclude such  $K_r$ -expressions.

### **2.3** $K_r$ -programs and their first canonical forms

Let  $K_r^-$  stand for the subset of  $K_r$ -expressions whose first canonical form has the property that the repeat instruction  $\ \# n$  (if present) is preceded by at least n primitive instructions. In fact,  $K_r^-$  is a subsemigroup of  $K_r$ : if  $X, Y \in K_r^-$ , then  $X; Y \in K_r^-$ . In the following definition, we refine the notion of a first canonical form.

**Definition 3.** Let  $u_1; \ldots; u_k$  with  $k \ge 1$  be a SPI (thus all  $u_i$  are primitive instructions). Then  $u_1; \ldots; u_k$  is a first canonical  $K_r$ -form. This first canonical  $K_r$ -form is minimal by definition.

The  $K_r$ -expression  $u_1; \ldots; u_k; \ \#n$  is a first canonical  $K_r$ -form if  $0 < n \leq k$ . This first canonical  $K_r$ -form is minimal if its repeating part  $u_{k-n+1}; \ldots; u_k$  can not be made smaller with axiom (1), and its non-repeating part  $u_1; \ldots; u_{k-n}$  can not be made smaller with axiom (3).

Two examples, where the right-hand sides are minimal first canonical  $K_r$ -forms:

+**a**; -**b**; #4; -**b**; #4; \\#4 =<sub>spc</sub> +**a**; -**b**; #4; \\#2, -**a**; +**c**; #4; +**c**; \\#2; +**b** =<sub>spc</sub> -**a**; +**c**; #4; \\#2.

**Definition 4.** Elements in  $K_r^-$  are referred to as  $K_r$ -programs.

Recall that two  $K_r$ -programs P and Q are single-pass congruent if, and only if,

 $E_{spc} \vdash P = Q.$ 

Single-pass congruence for  $K_r$ -programs is captured by the next result.

**Theorem 1.** Single-pass congruence of  $K_r$ -programs is decidable.

*Proof.* Assume P and Q are two  $K_r$ -programs that denote identical SPIs. If both programs do not contain repeat instructions, they are syntactically identical, apart from the possible use of brackets, which is then captured by the associativity of concatenation, which we adopted throughout this paper. In the other case, application of axiom (2) yields first canonical  $K_r$ -forms and these expressions still denote the same SPI. With axiom (3) the non-repeating parts of the two  $K_r^-$ -expressions (if present) can be made as short as possible, so these should be identical for both expressions. Removal of these non-repeating parts yields two expressions of the form  $u_1; \ldots; u_n; \backslash\!\!/\#n$  and  $v_1; \ldots; v_m; \backslash\!\!/\#m$  that denote identical SPIs. With axiom (1) one then derives

$$E_{spc} \vdash u_1; \dots; u_n; \ \| \#n = (u_1; \dots; u_n)^m; \ \| \#nm$$
$$= (v_1; \dots; v_m)^n; \ \| \#nm$$
$$= v_1; \dots; v_m; \ \| \#m.$$

Of course, the values n and m in these repeating parts can be effectively minimized with axiom (1). Then single-pass congruence coincides with the syntactic equality of both minimal first canonical  $K_r$ -forms, which immediately implies the mentioned decidability.

Without loss of generality, we further only consider  $K_r$ -programs that contain at most one repeat instruction.

## **3** Execution of $K_r$ -programs

We briefly discuss Thread Algebra (cf. [9]), earlier described in e.g. [2, 4]. For basic information on thread algebra we refer to [3, 9]; more advanced matters, such as an operational semantics for thread algebra, are discussed in [5].

### 3.1 Thread algebra

Threads model the execution of SPIs. In order to define threads, we consider the set A of basic instructions also as a set of so-called *actions* that model the execution of basic and test instructions, where it is assumed that execution of the action **a** yields a Boolean reply **true** or **false**. Finite threads are defined inductively in the following way:

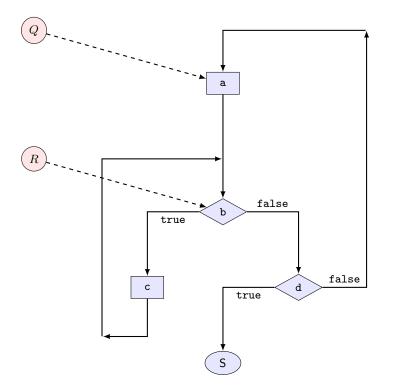
The behavior of the thread  $P \leq a \geq Q$  starts with the *action* **a** and continues as P upon reply **true** to **a**, and as Q upon reply **false**. Note that finite threads always end in **S** or **D**. We use *action prefix*  $\mathbf{a} \circ P$  as an abbreviation for  $P \leq a \geq P$  and take  $\circ$  to bind strongest.

A so-called *regular* thread over A is a finite-state thread in which infinite paths can occur (so, finite threads form a special subset of regular threads). Each regular thread can be defined

by a finite number of recursive equations. As a first example, consider the regular thread Q defined by

$$\begin{split} &Q = \mathtt{a} \circ R, \\ &R = \mathtt{c} \circ R \trianglelefteq \mathtt{b} \trianglerighteq \left( \mathtt{S} \trianglelefteq \mathtt{d} \trianglerighteq Q \right) \end{split}$$

This regular thread Q can be depicted in the following way:



Each regular thread can be specified using a so-called linear recursive specification.

Definition 5. A linear recursive specification is a set of equations

 $\{P_i = t_i(\overline{P}) \mid i = 0, ..., n\}$ 

with  $t_i(\overline{P})$  of the form S, D, or  $P_{i_1} \leq a_i \geq P_{i_2}$ , where  $a_i \in A$  and  $i_1, i_2 \leq n$ .

For the example above, we find  $Q = P_0$  for  $P_0$  defined by the following linear equations:

$$P_0 = P_1 \trianglelefteq \mathbf{a} \trianglerighteq P_1,$$
  

$$P_1 = P_2 \trianglelefteq \mathbf{b} \trianglerighteq P_3,$$
  

$$P_2 = P_1 \trianglelefteq \mathbf{c} \trianglerighteq P_1,$$
  

$$P_3 = P_4 \trianglelefteq \mathbf{d} \trianglerighteq P_0,$$
  

$$P_4 = \mathsf{S}.$$

In the next section we explain in what way  $K_r$ -programs define regular threads.

Let  $X = u_1; \ldots; u_{n+k}; \ \ \#n$ , then  $[\![X]\!]_{K_r} = |1, X|$ , where |j, X| = |j-n, X| if j > n+k, |j, X| = S if  $u_j = !$ ,  $|j, X| = a \circ |j+1, X|$  if  $u_j = a$ ,  $|j, X| = |j+1, X| \le a \ge |j+2, X|$  if  $u_j = +a$ ,  $|j, X| = |j+2, X| \le a \ge |j+1, X|$  if  $u_j = -a$ , |j, X| = D if  $u_j = \#0$ , |j, X| = |j+k+1, X| if  $u_j = \#k+1$ .

Table 2: Equations for thread extraction on  $K_r^-$ , where  $u_i \in \mathcal{U}, k \in \mathbb{N}$  and  $j, n \in \mathbb{N}_{>0}$ 

### **3.2** Behavioral semantics for $K_r$ -programs: threads

As mentioned before, the execution of a SPI is *single-pass*: it starts with the first instruction, and each instruction is dropped after it has been executed or jumped over. In this section we explain the precise *meaning* of primitive instructions and  $K_r$ -programs in terms of their execution.

Let X be a  $K_r$ -program of the form

$$X = u_1; \ldots; u_{n+k}; \mathbb{N} \# n,$$

thus X is a first canonical  $K_r$ -form. In Table 2 we define the *thread extraction* of X, notation

 $\llbracket X \rrbracket_{K_r},$ 

where the auxiliary function |j, X| models the thread extraction of program X when started at its j'th instruction. In the general case of a  $K_r$ -program X, its thread extraction is defined by

 $[\![X]\!]_{K_r} = [\![X; \#0; \backslash \!\backslash \#1]\!]_{K_r},$ 

thus the SPI produced by X that — if it is finite — is extended with an infinite number of #0-instructions. Because each  $K_r$ -program of the form  $X; \#0; \mathbb{W}$  can be converted to a first canonical form  $u_1; \ldots; u_{n+k}; \mathbb{W}$ , the equations in Table 2 match all possible cases:

- Repeaters and the termination instruction ! are dealt with in the first two equations for |j, X|. Observe that termination must always be explicitly defined using !.
- A basic or test instruction yields the equally named action in a post conditional composition. In the case of a positive test instruction +a, the reply true to the associated action a prescribes to continue with the next instruction and the reply false prescribes to skip the next instruction and to continue with the instruction at the position thereafter; for

the execution of a negative test instruction -a, subsequent execution is prescribed by the complementary replies. If there is no next instruction to be executed, deadlock follows.

• A #0-instruction yields deadlock upon execution, and a jump instruction #k+1 shifts |j, X| to |j+k+1, X|.

A first, very simple example is the regular thread obtained by thread extraction of the  $K_r$ -program +a; \\#1. We find that this program prescribes the execution of an infinite sequence of a-actions:

$$\begin{split} \llbracket +\mathbf{a}; \backslash \backslash \#1 \rrbracket_{K_r} &= |1, +\mathbf{a}; \backslash \backslash \#1| = |2, +\mathbf{a}; \backslash \backslash \#1| \trianglelefteq \mathbf{a} \trianglerighteq |3, +\mathbf{a}; \backslash \backslash \#1| \\ &= |1, +\mathbf{a}; \backslash \backslash \#1| \trianglelefteq \mathbf{a} \trianglerighteq |2, +\mathbf{a}; \backslash \backslash \#1| \\ &= \mathbf{a} \circ |1, a; \backslash \backslash \#1|, \end{split}$$

and thus the regular thread captured by the single recursive equation

$$P = \mathbf{a} \circ P \tag{(e_1)}$$

and we may write  $[\![+\mathbf{a}; \backslash \! / \# 1]\!]_{K_r} = P$  for P defined as in equation  $(e_1)$ .

The equations in Table 2 need not immediately yield a regular thread for each  $K_r$ -program: it can be the case that these equations can be consecutively applied without yielding any action, as for example for the program  $X = \#4; \mathbf{a}; \backslash\!\backslash \#2$ , for which we derive

$$\llbracket X \rrbracket_{K_r} = |1, X| = |5, X|$$
  
= |3, X|  
= |1, X|.

In such cases we define the extracted behavior to be D, and with this *default-rule* for thread extraction, each  $K_r$ -program defines a regular thread.

**Example 1.** Let  $X = +\mathbf{a}$ ; #0; +b; #4; -c; #0; \\#4. We show that  $[X]_{K_r} = \mathbf{D} \leq \mathbf{a} \geq P$  with P defined by the recursive equation  $P = \mathbf{D} \leq \mathbf{b} \geq (P \leq \mathbf{c} \geq \mathbf{D})$ .

Let  $Y = +\mathbf{a}$ ; #0; +b; #4; -c; \\#4, then  $X =_{spc} Y$  by axiom (3) and hence  $[X]_{K_r} = [Y]_{K_r}$ . We first derive an intermediate result:

|4, Y| = |8, Y|= |4, Y|,

so by the default-rule, |4, Y| = D. Finally, we derive

$$\begin{split} \llbracket Y \rrbracket_{K_r} &= |1, Y| \\ &= |2, Y| \trianglelefteq a \trianglerighteq |3, Y| \\ &= \mathsf{D} \trianglelefteq a \trianglerighteq P, \\ \\ where \ P &= |3, Y| \\ &= |4, Y| \trianglelefteq b \trianglerighteq |5, Y| \\ &= \mathsf{D} \trianglelefteq b \trianglerighteq (|7, Y| \trianglelefteq c \trianglerighteq |6, Y|) \\ &= \mathsf{D} \trianglelefteq b \trianglerighteq (|3, Y| \trianglelefteq c \trianglerighteq |2, Y|) \end{split}$$

$$= \mathsf{D} \trianglelefteq b \trianglerighteq (P \trianglelefteq c \trianglerighteq \mathsf{D}).$$

Conversely, each regular thread over A can be specified (programmed) by a  $K_r$ -program, as we will discuss in Section 4.1. For example, the regular thread Q that was discussed above and that was specified by the equations

$$\begin{split} Q &= \mathtt{a} \circ R, \\ R &= \mathtt{c} \circ R \trianglelefteq \mathtt{b} \trianglerighteq (\mathtt{S} \trianglelefteq \mathtt{d} \trianglerighteq Q) \end{split}$$

satisfies  $Q = [[a; +b; #2; #3; c; #4; +d; !; ]]_{K_r}$ .

To conclude this section, we mention the fact that in terms of execution behavior, certain different regular threads should be considered equal, e.g.,

 $[\![\mathbf{a}; \backslash ] \#1]\!]_{K_r}$  and  $[\![+\mathbf{a}; \mathbf{a}; \backslash ] \#2]\!]_{K_r}$ 

because both perform repeatedly the action **a** and are thus behaviorally equivalent. A formal way to prove this behavioral equivalence is discussed in [4] (and summarized in [9]) and is considered outside the scope of this paper. Finally, observe that behavioral equivalence of  $K_r$ -programs, say  $\equiv_{be}$ , is not a congruence: although  $\mathbf{a} \equiv_{be} + \mathbf{a}$  because both define the thread  $\mathbf{a} \circ \mathbf{D}$ , we find  $\mathbf{a}$ ;!  $\neq_{be} + \mathbf{a}$ ;! because  $\mathbf{a} \circ \mathbf{S} \neq \mathbf{S} \leq \mathbf{a} \geq \mathbf{D}$ .

## 3.3 $K_r$ -programs, second canonical forms and thread extraction

One can change the jump counters in  $K_r$ -programs while preserving execution behavior, for example

+a; #2; +b; #2; c; d; e and +a; #5; +b; #2; c; d; e

execute apart from their jump counters the same instructions and their thread extraction yields identical threads. The crucial difference between these programs is that the rightmost program contains no *chained jumps*. In Table 3 we introduce the axiom schemes (4)–(7) for the unchaining of jump instructions and we write

 $E_{sc}$ 

for the extension of  $E_{spc}$  with these axiom schemes. Although (1)–(7) are axiom schemes in  $m, n \in \mathbb{N}_{>0}$  and  $k, \ell \in \mathbb{N}$ , we shall refer to all of these as "axioms". The congruence defined by  $E_{sc}$  is called *structural congruence*, and whenever two  $K_r$ -programs X and Y are structurally congruent, this is written

 $X =_{sc} Y.$ 

Note that first canonical forms not in  $K_r^-$  (thus, with a repeat counter that is too large) can not be rewritten using any of the axioms (1)–(7) in Table 3 that contain repeaters. As a consequence, structural congruence is not a meaningful notion for such first canonical forms.

**Definition 6.** A second canonical  $K_r$ -form is a first canonical  $K_r$ -form in which no chained jumps occur, and in the case of  $u_1; \ldots; u_m; \backslash \! \Downarrow \! \#n$ , in which all jumps to  $u_{m-n+1}, \ldots, u_m$  are minimized using axioms (6) and (7).

 $(u_1;\ldots;u_n)^m; \backslash\!\backslash \#mn = u_1;\ldots;u_n; \backslash\!\backslash \#n$ (1)

$$\|\#n; X = \|\#n$$
 (2)

$$u_1; \dots; u_m; v_1; \dots; v_n; u_1; \dots; u_m; \backslash \!\! / \#m + n = u_1; \dots; u_m; v_1; \dots; v_n; \backslash \!\! / \#m + n \tag{3}$$

$$#k+1; u_1; \dots; u_k; #0 = #0; u_1; \dots; u_k; #0$$
(4)

$$#k+1+n; u_1; \dots; u_k; #n = #k+1; u_1; \dots; u_k; #n$$
(5)

$$#k+1+\ell; u_1; \dots; u_k; \backslash \! \backslash \#k+1 = \#\ell; u_1; \dots; u_k; \backslash \! \backslash \#k+1$$
(6)

$$#k+1+\ell+n; u_1; \dots; u_k; v_1; \dots; v_n; \ \|\ \#n = \#k+1+\ell; u_1; \dots; u_k; v_1; \dots; v_n; \ \|\ \#n$$
(7)

Table 3: The axiom set  $E_{sc}$  for structural congruence on SPIs, where  $k, \ell \in \mathbb{N}, m, n \in \mathbb{N}_{>0}$ ,  $u_i, v_j \in \mathcal{U}$ , and  $u_1; \ldots; u_0$ ; represents the empty sequence

Two typical examples, where the  $K_r$ -programs in the right-hand column are second canonical  $K_r$ -forms (and those in the left-hand column are not):

$$\begin{split} \#1; \ & \| \#1 =_{sc} \#0; \ & \| \#1, \\ +\mathbf{a}; \#2; +\mathbf{b}; \#2; -\mathbf{c}; \#4; \ & \| \#4 =_{sc} +\mathbf{a}; \#0; +\mathbf{b}; \#0; -\mathbf{c}; \#0; \ & \| \#4 =_{sc} +\mathbf{a}; \#0; +\mathbf{b}; \#0; -\mathbf{c}; \ & \| \#4. \end{split}$$

The first example is an instance of axiom (6)  $(k = \ell = 0)$ . The last example also provides second canonical  $K_r$ -forms for the  $K_r$ -program considered in Example 1.

It is easily seen that in  $E_{sc}$ , second canonical  $K_r$ -forms have a unique minimal representation in terms of their number of instructions and we have the following result.

**Theorem 2.** Structural congruence of  $K_r$ -programs is decidable, and two  $K_r$ -programs P and Q are structurally congruent if, and only if,

$$E_{sc} \vdash P = Q.$$

*Proof.* By the proof of Theorem 1 it suffices to consider minimal first canonical  $K_r$ -forms, and it is trivial to convert such a form to a second canonical  $K_r$ -form. Minimization of the length of the repeating part with (1) then yields a unique  $K_r$ -program (cf. the last example above).

Thread extraction is more straightforward when applied to second canonical  $K_r$ -forms: structural congruent  $K_r$ -programs define identical threads and because the infinite chaining of jumps is excluded, the rules in Table 2 are then complete and there is no more need for the *default-rule* (that stated that whenever the equations do not yield any action, the resulting behavior is D).

## 4 Discussion and conclusions

Our main motivation to undertake the current research is that in the setting of program algebra (PGA), the notion of a 'program notation' as defined in [4] should be strengthened, and we return to this question in Section 4.2. Program algebra was introduced as a general approach to model and analyze the notion of a sequential, imperative program in the form of a rather 'non-formal' and theoretical style. An algebra of these programs named PGA is used as the carrier for a further development of this matter, and the syntax of PGA serves as a very simple and basic program notation, underlying many other program notations. In Section 4.1 we relate our approach to PGA.

In Section 4.2 we conclude the paper with a consideration about PGLA, an earlier account of the semigroup  $K_r$  that was proposed as a machine-readable version of PGA and that underlies the current toolset for PGA [8]:  $K_r$ -expressions are precisely the programs that can be processed by this toolset.

PGA can be viewed as a theory of instruction sequences with our subsemigroup  $K_r^-$  or PGLA as one of its many representations. Unfortunately, we have not been able to identify any pre-existing theory by other authors to which this work can be related in a convincing manner. The phrase *instruction sequence* seems not to play a clear role in the theory of programming, and the software engineering literature at large features many uses of this phrase, but only in a casual setting.

### 4.1 Program algebra

PGA was set up in a very similar way as  $K_r$ , with the only difference that instead of repeat instructions, a unary operator called *repetition* is used. The notation for this operator is

 $(\_)^{\omega}$ 

and its relation with  $K_r$  is captured by the equation scheme

$$u_1;\ldots;u_k; \backslash\!\backslash\!/\# k = (u_1;\ldots;u_k)^{\omega} \quad \text{for } k \in \mathbb{N}_{>0}, \tag{e2}$$

where  $u_1, ..., u_k \in \mathcal{U}$ , the set of primitive instructions that PGA and  $K_r$  are based on.

The associativity of concatenation and all further axiomatizations discussed previously, that is, the axiomatizations for single-pass congruence and structural congruence for  $K_r$  are the direct counterparts of those provided for PGA when equation scheme  $(e_2)$  is applied, and the same holds for the equations that define thread extraction. From a mathematical point of view, all such axioms and equations formulated in the setting of PGA are more elegant. The axiomatization of single-pass congruence in PGA is indeed so simple that it can be easily remembered by heart:

(X;Y); Z = X; (Y;Z)	) (PGA1)	
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$$(X^n)^\omega = X^\omega \tag{PGA2}$$

$$X^{\omega}; Y = X^{\omega} \tag{PGA3}$$

 $(X;Y)^{\omega} = X; (Y;X)^{\omega}$ (PGA4)

However, in terms of a *program notation* for finite or periodic SPIs, there is something to be said *against* PGA: its notation is not conforming to ASCII and exploits a scope-dependent unary operator  $(\_)^{\omega}$ .

Because of the immediate correspondence between  $K_r$ -programs and PGA-programs as characterized by equation scheme  $(e_2)$ , many PGA-results also hold for  $K_r$ -programs. For example, each regular thread over A can be specified (programmed) by a  $K_r$ -program: assume a regular thread  $P_0$  is given by the linear recursive specification

$$\{P_i = t_i(\overline{P}) \mid i = 0, ..., n\}$$

with  $t_i(\overline{P})$  of the form S, D, or  $P_{i_1} \leq a_i \geq P_{i_2}$ , where  $a_i \in A$  and  $i_1, i_2 \leq n$ . Then

$$P_0 = \llbracket \sigma_0(P_0); \ldots; \sigma_n(P_n); \backslash \! \backslash \# 3n + 3 \rrbracket_{K_r},$$

where

$$\sigma_i(P_i) = \begin{cases} !; \#0^2 & \text{if } t_i = \mathsf{S}, \\ \#0^3 & \text{if } t_i = \mathsf{D}, \\ +\mathsf{a}; \#f(n, i_1, i_2); \#g(n, i_1, i_2) & \text{if } t_i = P_{i_1} \trianglelefteq \mathsf{a}_i \trianglerighteq P_{i_2} \end{cases}$$

for appropriate target functions f and g. For example, the regular thread Q discussed in Section 3.1 and specified by the equations

$$Q = \mathbf{a} \circ R,$$
  

$$R = \mathbf{c} \circ R \leq \mathbf{b} \succeq (\mathbf{S} \leq \mathbf{d} \succeq Q).$$

and thus by  $P_0$  in the linear recursive specification (see Definition 5) that consists of the linear equations

$$\begin{split} P_0 &= P_1 \trianglelefteq \mathbf{a} \trianglerighteq P_1, \\ P_1 &= P_2 \trianglelefteq \mathbf{b} \trianglerighteq P_3, \\ P_2 &= P_1 \trianglelefteq \mathbf{c} \trianglerighteq P_1, \\ P_3 &= P_4 \trianglelefteq \mathbf{d} \trianglerighteq P_0, \\ P_4 &= \mathsf{S}, \end{split}$$

satisfies

$$\begin{split} Q = P_0 = \llbracket + \mathbf{a}; \# 2; \# 1; \\ + \mathbf{b}; \# 2; \# 4; \\ + \mathbf{c}; \# 11; \# 10; \\ + \mathbf{d}; \# 2; \# 4; \\ !; \# 0; \# 0; \backslash \! \backslash \# 15 \\ \rrbracket_{K_r}. \end{split}$$

Observe that this result implies that negative test instructions and basic instructions do not increase expressiveness; indeed their sole purpose is to provide ease of specification. On the other hand, jump instructions with counters of unbounded size are crucial for the abovementioned expressiveness result (cf. [4, 9]).

In [7] we introduced an alternative for  $K_r$ : the set  $\mathcal{U}$  of primitive instructions that underlies PGA and  $K_r$  is replaced by a set of programming instructions that specifically prescribe whether the next instruction to be executed is concatenated to the right or to the left. The resulting semigroup C also produces all periodic SPIs over  $\mathcal{U}$ . More results on C are discussed in [10]. We also mention here [6], in which SPISA is extensively introduced (Single Pass Instruction Sequence Algebra), a variant of PGA that comprises next to the termination instruction also a positive termination instruction !t and a negative termination instruction !f.

## 4.2 PGLA

The program notation PGLA, which is in fact  $K_r$  as defined in this paper, was introduced in [4] as a first example of a 'programming language'. The criterion formulated in [4] to use this terminology is the existence of a projection function

### pgla2pga

(PGLA to PGA) that maps any PGLA-program ( $K_r$ -expression) to a PGA-program. In fact, PGLA inspired a toolset and programming environment for program algebra [8]. However, we now conclude that we did not deal in a proper way with the non-standard case of programs with repeaters with a counter that is too large: the projection function pgla2pga then adds #0-instructions to obtain a first canonical  $K_r$ -form. This solution does not combine in an elegant way with jumps as witnessed by the following examples, where we write |X| for the thread extraction of PGA-program X and use the following abbreviations:

 $|Y|_{pgla}$  for |pgla2pga(Y)| (as is done in [4]), and  $\mathbf{a}^{\infty}$  for the thread defined by  $P = \mathbf{a} \circ P$ .

Some examples:

$$\begin{aligned} |\mathbf{a}; \#1; \backslash \! \backslash \#3|_{pgla} &= |\mathbf{a}; \#1; \#0; \backslash \! \backslash \#3|_{pgla} = |(\mathbf{a}; \#1; \#0)^{\omega}| = \mathbf{a} \circ \mathsf{D}, \\ |\mathbf{a}; \#2; \backslash \! \backslash \#3|_{pgla} &= |\mathbf{a}; \#2; \#0; \backslash \! \backslash \#3|_{pgla} = |(\mathbf{a}; \#2; \#0)^{\omega}| = \mathbf{a}^{\infty}, \end{aligned}$$

and, more generally, for  $k \in \mathbb{N}$  we find

$$|\mathbf{a}; \#k; \backslash \backslash \#3|_{pgla} = |(\mathbf{a}; \#k; \#0)^{\omega}| = \begin{cases} \mathbf{a}^{\infty} & \text{if } k \mod 3 = 2, \\ \mathbf{a} \circ \mathsf{D} & \text{otherwise.} \end{cases}$$

Thus, our final conclusion is to avoid the question of "too large repeat counters" and to state that  $u_1; \ldots; u_k; \backslash \not\parallel n$  is not a program whenever k > n. This agrees with the point of view to consider PGA the more basic theory for providing semantics for sequential programming from a mathematical point of view (instead of  $K_r$  or  $K_r^-$ ) and with the point of departure adopted in [4]: a programming language is a pair  $(E, \phi)$  with E a set of expressions (the programs) and  $\phi$  a projection function to PGA. Finally, we note that  $K_r^-$  shares a property that is often seen in imperative programming: if

P;Q

is a  $K_r$ -program, then P and Q need not be  $K_r$ -programs (while  $K_r$ -expressions satisfy by definition the property that their decomposition yields  $K_r$ -expressions, and the same can be said for SPIs).

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