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Author(s): V. A. Shah, A. Dobbie, M. Myronov, D. J. F. Fulgoni, L. J. Nash, and D. R. Leadley

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## Reverse graded relaxed buffers for high Ge content SiGe virtual substrates

V. A. Shah,<sup>1,a)</sup> A. Dobbie,<sup>1</sup> M. Myronov,<sup>1</sup> D. J. F. Fulgoni,<sup>2</sup> L. J. Nash,<sup>2</sup> and D. R. Leadley<sup>1</sup>

<sup>1</sup>Department of Physics, The University of Warwick, Coventry CV4 7AL, United Kingdom

<sup>2</sup>AdvanceSis Ltd., Sir William Lyons Road, Coventry CV4 7EZ, United Kingdom

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An innovative approach is proposed for epitaxial growth of high Ge content, relaxed  $\text{Si}_{1-x}\text{Ge}_x$  buffer layers on a Si(001) substrate. The advantages of the technique are demonstrated by growing such structures via chemical vapor deposition and their characterization. Relaxed Ge is first grown on the substrate followed by the reverse grading approach to reach a final buffer composition of 0.78. The optimized buffer structure is only  $2.8\ \mu\text{m}$  thick and demonstrates a low surface threading dislocation density of  $4 \times 10^6\ \text{cm}^{-2}$ , with a surface roughness of 2.6 nm. The buffers demonstrate a relaxation of up to 107%. © 2008 American Institute of Physics. [DOI: 10.1063/1.3023068]

Strained Ge has been shown to offer increased hole mobility<sup>1</sup> and can be realized by epitaxial growth of Ge on a relaxed  $\text{Si}_{1-x}\text{Ge}_x/\text{Si}(001)$  substrate with a different lattice constant.<sup>2</sup> These lattice-parameter controlled substrates, known as “virtual substrates” (VSs), are required to provide an interface to the ubiquitous Si wafer and must accommodate the change in lattice parameter without compromising the final layer quality by introducing excessive threading dislocations or surface roughening. Commonly, they consist of relaxed  $\text{Si}_{1-x}\text{Ge}_x$  buffers grown on Si(001) substrate. A high Ge fraction in the range of  $0.6 \leq x \leq 0.9$  is required to allow a reasonable thickness of compressive strained Ge to be grown without exceeding the critical thickness. These lattice-parameter tunable substrates are also of great interest for optoelectronic applications, as they provide a route to integrating existing III-V technologies with Si.

Using reduced pressure chemical vapor deposition (RP-CVD), Si and SiGe epitaxial films can be grown of very high structural quality, often exceeding that obtained using molecular beam epitaxy (MBE). As these buffer structures are relatively thick, growth by solid-source MBE with growth rates of about 0.1 nm/s would be extremely slow whereas rates of 5 nm/s are easily achievable by RP-CVD. An added bonus with RP-CVD is that the residual  $\text{H}_2$  gas acts as a surfactant and suppresses surface roughness during growth.

The high quality high Ge content buffers are typically grown by two major techniques by CVD. The first is through a two temperature growth method directly on the Si(001) substrate.<sup>3,4</sup> The second is by a slow grading of a buffer structure,<sup>5,6</sup> which, for such high-composition layers leads to a relatively thick structure, typically 8–12  $\mu\text{m}$ , since a Ge grading rate of 10%/ $\mu\text{m}$  and a 1–2  $\mu\text{m}$   $\text{Si}_{1-x}\text{Ge}_x$  cap layer have to be grown to obtain a root mean square (rms) surface roughness of 8–15 nm (Refs. 6–8) and threading dislocation density (TDD) in the range  $10^6\ \text{cm}^{-2}$  or below. In this work, we introduce an innovative approach to producing high Ge composition  $\text{Si}_{1-x}\text{Ge}_x$  relaxed buffer of relatively small thickness and high quality.

The  $\text{Si}_{0.22}\text{Ge}_{0.78}/\text{RLG}/\text{Ge}/\text{Si}(001)$  VS design is shown in Fig. 1. The structures were grown by RP-CVD in an ASM Epsilon 2000 reactor with  $\text{GeH}_4$  and  $\text{SiH}_2\text{Cl}_2$  as gaseous precursors. Initially, a relatively high quality 1  $\mu\text{m}$  Ge layer was grown directly on the Si(001) substrate using the two tem-

perature growth method.<sup>3,4</sup> This allows dislocations to nucleate while keeping the roughness to a minimum at the initial lower temperature and then allows the dislocations to glide during the higher temperature phase, the temperatures used were 400 °C for the initial seed layer then 670 °C for the high temperature layer. After this, the reverse linear graded (RLG) layer was deposited at a high growth temperature of 850 °C, with the Ge content reducing linearly from  $x=1$  to  $x=0.78$  over a thickness  $t_{\text{LG}}$ , which was varied from 50 to 5000 nm corresponding to a range of effective grading rates between 440%/ $\mu\text{m}$  and 4.4%/ $\mu\text{m}$ . Finally, a 1.1  $\mu\text{m}$  constant composition  $\text{Si}_{0.22}\text{Ge}_{0.78}$  layer was grown. The grading was achieved by keeping the  $\text{GeH}_4$  flow constant and reducing the  $\text{SiH}_2\text{Cl}_2$  flow. The grading was calibrated in 5% composition steps to account for the increase in growth rate from 1.26 nm/s at pure Ge to 4.74 nm/s at  $\text{Si}_{0.2}\text{Ge}_{0.8}$ . The linear grading profile was confirmed through high energy secondary ion mass spectroscopy. Alongside these wafers, a pure Ge layer and another without a graded layer were also fabricated as controls. In this letter, we will demonstrate and discuss the results of characterization for the optimum structure with  $t_{\text{LG}}$  of 800 nm corresponding to an effective grading rate of 27.5%/ $\mu\text{m}$ .

The unique feature of the proposed VSs is that the alloy content is graded *down* from a relatively high quality, relaxed pure Ge interlayer to the required final composition instead of grading *up* from the Si wafer. As well as producing thinner VSs, the main advantage from having the buffer relax under biaxial tensile strain rather than the compressive strain is that the buffer layers will remain smoother during growth.<sup>9</sup>

To view threading dislocation formation and establish the actual layer thicknesses, cross-sectional transmission electron microscopy (TEM) investigations were performed along the  $\{110\}$  plane using a JEOL 2000FX TEM at 200 kV.

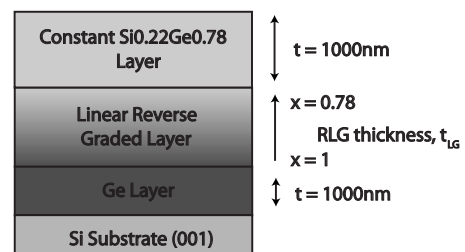


FIG. 1. Schematic design of the reverse linear graded  $\text{Si}_{0.22}\text{Ge}_{0.78}/\text{RLG}/\text{Ge}/\text{Si}(001)$  VS.

<sup>a)</sup>Electronic mail: vishal.shah@warwick.ac.uk.

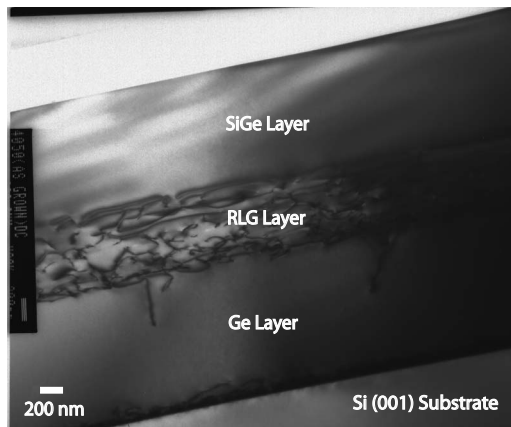


FIG. 2. A cross-sectional TEM image in the (220) diffraction condition. Shown are dislocations confined to the RLG region but not penetrating upwards.

Figure 2 shows how the initial 100 nm Ge seed is riddled with defects, while no defects can be observed in the high temperature Ge layer indicating a TDD below  $10^8 \text{ cm}^{-2}$ . From a sample of ten such images, the TDD reaching the surface of the Ge layer was estimated to be  $<5 \times 10^7 \text{ cm}^{-2}$ . Within the reverse graded layer misfit dislocations, needed to relax the top composition layer, are generated. These can be seen to be confined to this graded layer, as with linear “forward graded” structures. Due to the tensile strain imposed on the graded region by the Ge layer, microtwins are initially formed in the graded layer and extend down into the Ge layer. Finally, the constant composition  $\text{Si}_{0.22}\text{Ge}_{0.78}$  layer is seen to be 1.1  $\mu\text{m}$  thick, again with very few defects.

The TDD reaching the top surface was measured through selective etching and viewing any etch pits formed with a differential interference contrast microscope. To reveal threading dislocations in the  $\text{Si}_{0.22}\text{Ge}_{0.78}$  top layer alloy, a dilute Schimmel etchant was used,<sup>10</sup> with a bulk etching rate of 2.1 nm/s determined for 78% Ge. The TDD was obtained from the average of 30 images each with an area of  $8.2 \times 10^3 \mu\text{m}^2$ . After a 5 min Schimmel etch, a defect level of  $(4.2 \pm 0.1) \times 10^6 \text{ cm}^{-2}$  was measured. No dislocation pileup or stacking fault formation was seen, although a cross-hatch pattern is observed on the surface of the sample both before and after etching. This cross-hatch pattern is typical of graded structures due to the strain field generated by misfit dislocation running in orthogonal directions during growth of the graded layer.<sup>11</sup> A separate wafer was grown to evaluate the density of defects within the Ge base layer. Since the reactivity of Schimmel etchant decreases as the Ge fraction increases and it has virtually no effect on pure Ge, a dilute iodine etchant<sup>12</sup> was used. After a 15 s iodine etch, a TDD of  $\sim 2 \times 10^7 \text{ cm}^{-2}$  was found, consistent with the defect level found from TEM.

Having confirmed these low defect levels, the surface rms roughness was assessed from an average of eight  $10 \times 10 \mu\text{m}^2$  scans measured by a Veeco Nanoscope contact mode atomic force microscope (AFM). An average rms value of  $2.6 \pm 0.3 \text{ nm}$  was found, with a maximum height range of  $16.3 \pm 2.2 \text{ nm}$ . The cross-hatch pattern can now be resolved in more detail and was determined to have an average wavelength of  $1.5 \pm 0.2 \mu\text{m}$ . This is seen to be the main mechanism for roughening of the layers.

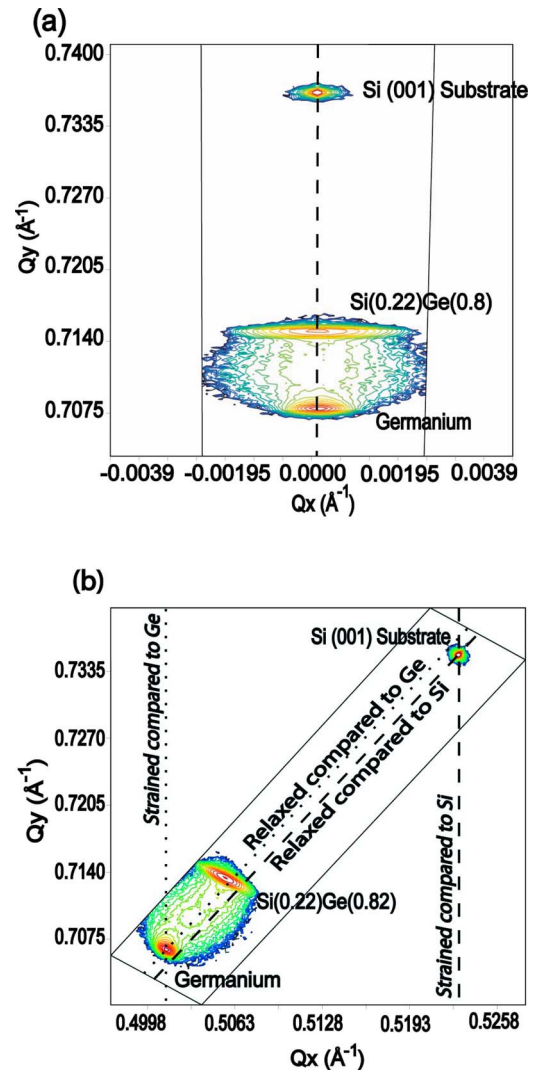


FIG. 3. (Color online) (a) Symmetrical (004) RSM, there is no tilt of the layers. (b) Asymmetrical (224) RSM, full relaxation of layers.

The relaxation and composition of the layers was determined by x-ray diffraction using a Philips X’pert MRD Pro single crystal high resolution x-ray diffractometer. Reciprocal space maps (RSMs) were performed along the symmetrical (004) and asymmetrical (224) orientations, which allowed the in-plane and out-of-plane lattice parameters to be calculated. The alignment of the peaks in the (004) scan [Fig. 3(a)] shows that there is minimal tilt to the epitaxial layers. As there are two different constant composition layers, the underlying relaxation of the Ge layer will influence the relaxation of the top alloy layer. The Ge layer is measured to be 104.2% relaxed compared to the Si(001) substrate. The slight tensile strain incurred in the layer has been calculated to result from the difference in thermal contraction of both materials as they cool from the growth temperature. This is represented in Fig. 3 by a deviation from the *diagonal dashed* line that represents the full relaxation of a layer compared to the Si substrate, whereas a fully pseudomorphic, and hence compressively strained, layer is represented by the *vertical dashed* line. The relaxation of the  $\text{Si}_{0.22}\text{Ge}_{0.78}$  layer was measured to be  $107.3\% \pm 0.2\%$  relative to the Si substrate. The relaxation of the  $\text{Si}_{0.22}\text{Ge}_{0.78}$  layer can also be compared to the underlying Ge layer. Within Fig. 3, the *diagonal dotted* line shows complete relaxation compared to

the Ge layer and the *vertical dotted* line shows 100% strain. A relaxation of  $85.8\% \pm 0.2\%$  was calculated for the  $\text{Si}_{0.22}\text{Ge}_{0.78}$  layer compared to the Ge layer. After annealing for 10 h at  $750^\circ\text{C}$ , the relaxation increases slightly to  $88.7\% \pm 0.1\%$  and  $106.1\% \pm 0.1\%$ , relative to Ge and Si, respectively. Again differential thermal contraction is responsible for a small compressive strain, which partially explains why the layer is not fully relaxed. As can be seen in both the (004) and the (224), a spreading of the peaks from the epitaxial layers is observed. This is due to the periodic strain field, which arises from small monocrystalline blocks formed from the crossing of misfit dislocations, termed “mosaicity.” During growth, this mosaicity forms the undulations which are more commonly known as crosshatch.

These RLG buffers can be compared to conventional CVD grown linear graded buffers with no intermediate steps where a TDD is reported in the range  $10^6$ – $10^7$   $\text{cm}^{-2}$  for structures having an overall buffer thickness of over  $8\ \mu\text{m}$ .<sup>6,13</sup> When chemical mechanical polishing (CMP) is employed within a forward graded structure TDD values of  $10^5$   $\text{cm}^{-2}$  are reported.<sup>8</sup> If a thick constant composition layer is deposited instead of grading of any kind, a TDD of  $10^8$   $\text{cm}^{-2}$  will arise.<sup>5</sup> We have found that a grading rate of  $31\%/\mu\text{m}$  can be used to obtain well relaxed, low TDD, and smooth buffers. The optimum structures, with a total thickness of just  $2.8\ \mu\text{m}$  above the Si wafer, have a high degree of relaxation and a relatively low TDD of  $(4.2 \pm 0.1) \times 10^6$   $\text{cm}^{-2}$ . Buffers grown by forward grading from Si have been shown to use an optimal grading rate, which is slower, typically  $10\%/\mu\text{m}$ , and make a much greater change in lattice parameter, hence they are much thicker, relieve more strain energy, and have a comparable TDD of around  $10^6$   $\text{cm}^{-2}$ . The low grading rate is required to limit nucleation of dislocations and ensure that relaxation occurs through the glide of dislocations. Isaacson *et al.*<sup>14</sup> suggest the dislocation nucleation regime is partly temperature limited; however, in our RLG buffers, there is already a reasonably high dislocation content at the top of the Ge layer and, as the energy for dislocation glide is lower than the energy of dislocation nucleation, we suggest that dislocation glide is the main mechanism for strain relief in the reverse graded layer. As glide dominates, rather than nucleation, we speculate the higher growth temperatures can be used that suppress further dislocation nucleation, which reduces the strain field associated with dislocations, resulting in reduced surface roughening. Further evidence for the importance of dislocation glide in the graded region is that the TDD reaching the top  $\text{Si}_{0.22}\text{Ge}_{0.78}$  surface is much lower than that at the Ge surface; however, further reduction of the final TDD is still limited by the number of threading dislocations in the underlying Ge layer. The higher growth temperature along with the fact that under tensile strain a smoother surface is achieved<sup>9</sup> avoids the vicious cycle<sup>6</sup> of surface morphology affecting dislocation glide, producing pileup, which in turn reduces the local growth rate around arrested dislocations and creates a rougher surface. Most importantly, the rms surface roughness in the RLG buffers, of  $2.6 \pm 0.3$  nm is at least a factor of 6 lower than in previous works.<sup>6,7</sup> In an attempt to produce smoother layers, Bogumilowicz *et al.*<sup>8</sup> employed a mid-growth CMP step after linearly grading to 50% Ge. After CMP, the rms roughness was 0.4 nm, but continued growth to 75% Ge again increased the roughness to 15 nm. Thus the

RLG buffers are shown to be comparable in terms of full relaxation of the layer and relatively low TDD, but considerably thinner and much better in terms of surface morphology compared to conventional forward graded substrates.<sup>5,6,13</sup>

Reverse grading has previously been exploited on low composition structures to produce thin buffers with low TDD.<sup>15,16</sup> In that case, there was no relaxed underlayer and the abrupt step to higher Ge content was used to nucleate misfit dislocations at the interface to the Si substrate. Residual strain in the reverse grading region was thought to act as threading dislocation filters.<sup>16</sup> By contrast, the technique outlined here uniquely incorporates a high-quality fully relaxed Ge layer underneath the RLG region and enables high Ge composition buffers to be developed.

In summary, an innovative approach to grading for high-composition  $\text{Si}_{1-x}\text{Ge}_x/\text{Si}(001)$  VSs has been proposed. The structures were grown using RP-CVD and a range of characterization techniques have been applied. A high degree of relaxation was obtained with a comparatively low TDD of  $4.2 \times 10^6$   $\text{cm}^{-2}$ , compared to forward graded structures typically with TDDs of  $10^6$ – $10^8$   $\text{cm}^{-2}$ . The higher grading rate of  $27.5\%/\mu\text{m}$  resulted in a much thinner structure of  $2.8\ \mu\text{m}$  when compared to similar structures of over  $8\ \mu\text{m}$ . Finally a surface rms roughness of 2.6 nm has been achieved, six times lower than traditional forward grading. These encouraging results show the potential of using reverse grading as part of the platform for strained Ge layers. The technique could similarly be applied to produce VSs for highly strained silicon, or SiGe, by varying the initial seed layer.

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