

Speed control in DC and AC drives

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Article Info

Article history:

Received May 22, 2021

Revised Sep 7, 2021

Accepted Sep 14, 2021

Keywords:

DC and AC drives

Motor control

Pole placement

State observer

State space

ABSTRACT

Three speed-control strategies for DC and AC drives are presented in this study: a proportional integral derivative (PID) control strategy; an internal model control (IMC); and a state-space control by pole assignment with full state observer (ESSO). The three strategies are applied to a case study, demonstrating the potential of each one. Experimental identification was used to obtain the drive models used for the synthesis of the controllers. The three strategies showed satisfactory results when compared with the requirements imposed on the system, in addition to the good rejection of disturbances. However, the IMC strategy showed itself to be a little softer and with no maximum overshoot, which in some cases and some applications is usually a restriction.

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1. INTRODUCTION

The development of science and technology has also increased the quality, efficiency, and productivity of industrial services and processes. Modern industry has a wide variety of mechanisms that require speed control at different points in the production process. These mechanisms are a fundamental requirement for rotating electrical machines, basically direct current (DC) and alternating current (AC) motors. The DC motor offers facilities for speed control, which can be activated, for example, through the insertion of resistors in the armature, and through the variation of armature voltage or current in the field. These motors, despite their limitations in use, have enjoyed total hegemony in the industrial field for decades [1]. On the one hand, the cost of the AC motor is one-sixth of that of a DC motor of the same power and requires less maintenance. On the other, the motor provides no speed control facilities, which were at first achieved through the Ward Leonard system, given the impossibility of implementing a variable speed drive from an electrical system of constant voltage and frequencies; especially if the driver must be of high reliability and efficiency, have a wide range of speeds and moderate initial costs. For all those reasons, the DC motor has virtually been unrivaled for some years [2], [3].

Variable frequency converters have been introduced with the development of semiconductor devices, improving the technical characteristics of this type of drive to the point where it is superior to its nearest rivals because it permits high-precision, efficient, and quality speed regulation [4]-[6]. Thus, in recent

years a wide variety of control techniques have been developed and applied, such as, for example, direct torque control (DTC) and field-oriented control (FOC), which have dominated the market for decades [7]-[9]. The proportional integral derivative (PID) controller has also been widely used in speed regulation, in combination with other techniques, such as fuzzy logic, neural networks, predictive control, and adaptive control, for proper tuning of such drivers [10]-[14]. Other more modern speed control techniques have also been applied, such as, for example, internal model control (IMC), control by pole assignment, model-based predictive control (MPC), and the use of state observers, among others [15]-[18].

The present work is based on the application of some control techniques in the speed control of DC and AC drives. Firstly, some theoretical aspects of PID control strategies, internal model control (IMC), and control by pole assignment are discussed in a general way. Since the objective of the control system is normally to ensure that the output follows a reference with zero error, it is, therefore, a type 1 follower system; finally, a complete order observer is considered in conjunction with the type 1 follower system. All the control strategies for speed control are simulated using Matlab and the results are validated in an experimental facility, including both the DC and the AC drive types.

2. RESEARCH METHOD

2.1. PID control strategy

The PID controller is the most popular and widely used in the industry, because of its low cost, simplicity, and the good dynamic behavior of the controlled process when properly tuned. It is estimated that about 95% of the control meshes use the PID and the proportional-integral (PI) [19], [20], which in its ideal form (ISA algorithm) is represented by (1).

$$G_c(s) = \frac{U(s)}{E(s)} = K_p \left(1 + \frac{1}{T_i s} + T_d s \right) \quad (1)$$

Where s is the Laplace complex variable, $U(s)$ the control signal, $E(s)$ the acting error, and K_p , T_i , and T_d represent the proportional gain, the integrative, and the derivative time, respectively

Tuning a controller is a matter of determining the best combination of parameters (K_p , T_i , T_d) so that the system will respond as desired. Among the various tuning methods, for example, are those based on the transient response, such as Ziegler-Nichols and Cohen-Coon, those based on comprehensive criteria, such as IAE and ITAE, and other methods based on computational and intelligent optimization techniques, such as genetic algorithms and fuzzy logic [21]-[23]. Such methods will not always guarantee acceptable determination of the parameters; however, they serve as a starting point for finer parametrical adjustments to satisfy the design conditions.

2.2. IMC control strategy

The internal model control (IMC) strategy considers the process model as part of the controller. The project is based on the cancellation of poles and zeros because the structure of the controller contains the inverse of the process model [24]-[26]. Figure 1 shows the typical block diagram of the IMC strategy.

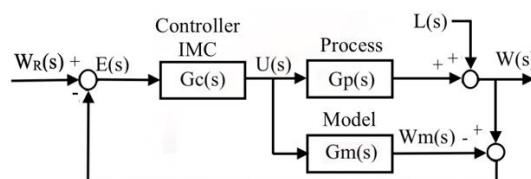


Figure 1. IMC control scheme [4]

Typically, the IMC controller design is done in two steps using the model factoring method.

Step 1 - Nominal behavior: The model is decomposed according to expression (2).

$$G_m(s) = G_{m+}(s) G_{m-}(s) \quad (2)$$

Where $G_{m+}(s)$ is the non-invertible portion, containing the zeros to the right of the complex half-plane, the unit gain and eventually the time delay, and $G_{m-}(s)$ is the invertible portion, containing the stable terms of the model. Thus, the ideal controller is the inverse of the invertible part of the model.

Step 2 - Performance and robust stability: A low-pass filter is added to the ideal controller to generate a proper transfer function, strictly speaking, and to ensure that the controller is physically feasible. In addition, unwanted high-frequency behavior is avoided, and measurement noise is eliminated. The IMC controller is represented in (3).

$$G_c(s) = \frac{1}{G_m(s)} \frac{1}{(T_f s + 1)^n} \tag{3}$$

Where n is the order of the filter and must be chosen in such a way as to guarantee that the controller is its own, which is usually equal to the order of the process whenever the problems of changes in the reference are addressed, and T_f is the filter setting constant, which varies the response speed of the closed-loop system.

2.3. Control strategy by pole placement

The design of state feedback controllers assumes that all state variables are measurable and available for input feedback through a matrix of gains, K , with constant coefficients; see Figure 2. This feedback means that the poles may be in a closed loop in the desired position, as long as the system is in a completely controllable state [26].

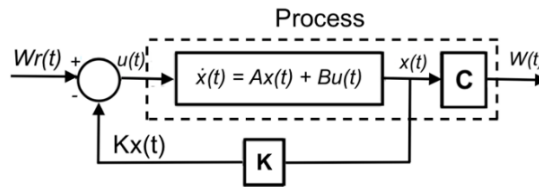


Figure 2. State feedback control scheme [27]

From the Figure 2 you get:

$$\frac{W(s)}{W_r(s)} = C(s) [sI - A(s) + B(s)K]^{-1} B(s) \tag{4}$$

Where $K = [k_1, k_2, \dots, k_n]$.

The closed-loop poles can be located by appropriate adjustment of the elements of the matrix K . If the desired location of the closed-loop poles is p_1, p_n , then the characteristic equation of the system given in (4) is:

$$|sI - A(s) + B(s)K| = (s - p_1) \dots (s - p_n) = 0 \tag{5}$$

K may be determined from the equality given in (5).

2.4. Type 1 follower system

In many practical applications, the output of the control system must follow the desired reference and electric drives are a typical case of such systems. If the process is of type 0, then it will for example be unable to follow a step type reference with a null error. Thus, it is convenient to use a type 1 follower system so that the system can follow the reference with zero error. Figure 3 shows the basic principle of this type of system in which an integrator block is inserted in the direct path between the error comparator and the process [27].

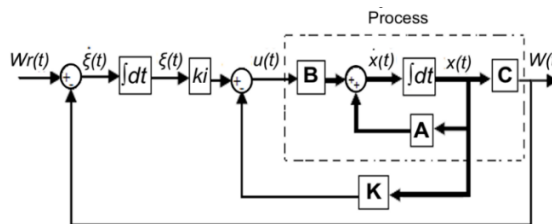


Figure 3. Type 1 follower system [27]

For this configuration, (6) is considered.

$$\dot{e}(t) = \hat{A}e(t) + \hat{B}u_e(t) \tag{6}$$

Where $\hat{A} = \begin{bmatrix} A & 0 \\ -C & 0 \end{bmatrix}$, $\hat{B} = \begin{bmatrix} B \\ 0 \end{bmatrix}$, and $e(t) = \begin{bmatrix} x(t) & -x(\infty) \\ \xi(t) & -\xi(\infty) \end{bmatrix}$ is the error vector of order $(n + 1)$, and $u_e(t) = u(t) - u(\infty)$ is the new control vector, given by (7).

$$u_e(t) = -\hat{K}e(t) \tag{7}$$

where $\hat{K} = [K - K_i]$

The objective of this system is to guarantee an asymptotically stable system using an acceptable determination of \hat{K} . If the system is in a completely controllable state, then it is possible to determine such a matrix using the pole assignment approach.

2.5. Follower system type 1 with state observer

In practice, it is not common to have all the state variables available for feedback, so it is necessary to estimate them from the measurement of others (state observer). If all state variables are estimated, see Figure 4, then it is a complete order observer [27]. In (8) defines the observer's model.

$$\dot{\tilde{x}}(t) = A\tilde{x}(t) - Bu(t) + K_e[W(t) - C\tilde{x}(t)] \tag{8}$$

Where K_e is the observer's earnings matrix, and the estimate of $x(t)$ is the vector of states

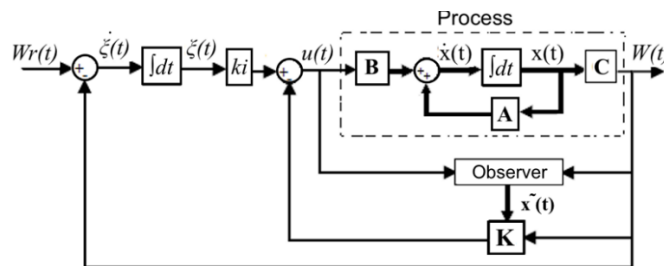


Figure 4. Type 1 follower system with state observer [27]

Starting from (8) we can arrive at (9), which defines the observer's error.

$$\dot{e}(t) = (A - K_e C)e(t) \tag{9}$$

where $\dot{e}(t) = x(t) - \tilde{x}(t)$

If the system is completely observable, then it will be possible to determine K_e so that $(A - K_e C)$ has arbitrarily desired eigenvalues, in such a way that the dynamic behavior of the error is asymptotically stable and sufficiently fast. K_e may be determined in the same way as for K in the assignment of poles.

3. RESULTS AND DISCUSSION

Next, the control strategies are synthesized, simulated, and experimentally validated in an experimental installation, including the two types of drives, DC and AC.

3.1. Description of the experimental facility

Figure 5 shows a block diagram of the experimental installation, which has a DC drive, consisting of a DC motor (5) of 10V / 1.5A and a maximum speed of 2500 rpm, a tachometer (6), model E5-600-5BZ-CV-2.5, coupled to the motor, and a power amplifier (4), to feed the DC motor and to give the speed command. There is likewise an AC drive, with an AC motor (8), three-phase induction, 80W/220V, and a maximum speed of 2460 rpm, with a frequency range of 3 to 60 Hz and a variable speed drive AF502-A40 (7), which is compliant with the constant V/f relationship. The entire installation is controlled from a computer (1) and a

SAD PCL818 (2) was used, internal to the computer, which has a maximum data transfer rate of 100 kHz. The input-output connections are made through the external module PCLD8115 (3). All signals are in a range from 0 to 10V and data transfer is done by the program.

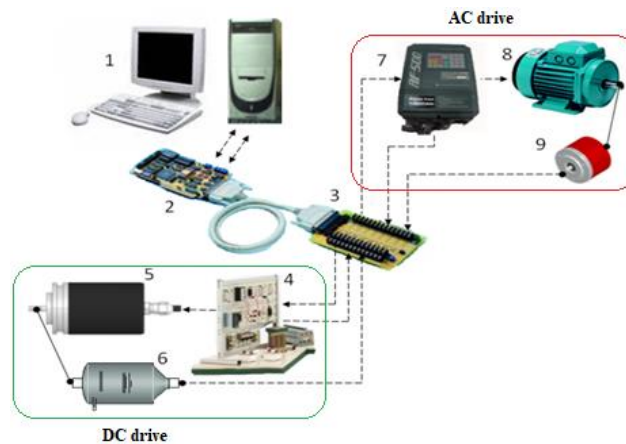


Figure 5. Experimental installation

3.2. Models of DC and AC drives

For the synthesis of the control strategies, the drives were modeled experimentally, applying a 10 V step as input, and as output, the speed was measured every 0.5 seconds. The input-output data were fed into the Matlab Identification Toolbox and the models for the DC and AC drives were obtained, given in (10) and (11), respectively. Figures 6 (a) and 6 (b) show the experimentally obtained responses and the responses from the model for both drives with an approximation of more than 96%.

$$G_{DC}(s) = \frac{250}{0.24s+1} \quad (10)$$

$$G_{AC}(s) = \frac{246.057}{0.125s^2+0.8273s+1} \quad (11)$$

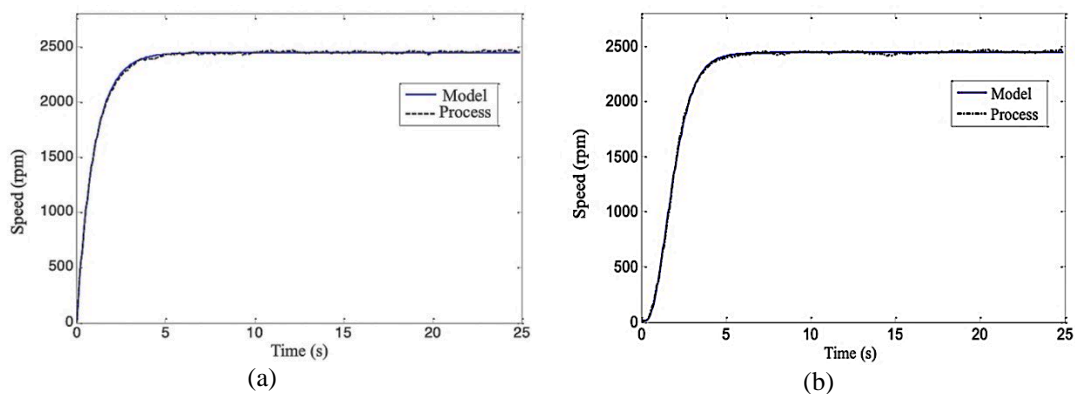


Figure 6. Model and process response: (a) DC drive, (b) AC drive

3.3. PI control strategy for the DC drive

The model given in (10) is a first-order model without delay, so a PI controller is sufficient to satisfy the design requirements. Its tuning, performed in Matlab, yielded the following:

$$G_{PIDC}(s) = 0.0003435 \left(1 + \frac{1}{0.0244s} \right) \quad (12)$$

The response presented a maximum overshoot (M_p) of 10% and settling time (t_s) of approximately 1.52 seconds.

3.4. IMC control strategy for DC drive

Considering the model given in (10), the IMC controller for the DC drive is:

$$G_{IMC_DC}(s) = \frac{0.004(0.24s+1)}{(0.4s+1)} \quad (13)$$

For which a first-order filter with an adjustment constant of 0.4 seconds was chosen.

3.5. ESSO control strategy for the DC drive

Expressing the model of the DC drive given in (10) in a representation in state variables, we have:

$$\dot{x} = [-4.16]x + [1041.66]u \quad (14)$$

$$y = [1]x + [0]u$$

where x is the state, and the output, and u the control signal.

3.6. Follower system type 1

Examining the MCD controllability matrix.

$$MCD = \begin{bmatrix} -4.16 & 1041.66 \\ -1 & 0 \end{bmatrix} \quad (15)$$

The range of MCD is 2 and its determinant anything other than zero when the system is in a completely controllable state. Assuming $M_p=10\%$ and a peak time (t_p) of 1 second, the desired characteristic is given by (16).

$$s^2 + 4.6080 s + 15.1632 = 0 \quad (16)$$

The closed-loop characteristic is:

$$|sI - \hat{A} + \hat{B}\hat{K}| = s^2 + (4.16 + 1041.66k_i)s + 1041.66k_i \quad (17)$$

Comparing (17) with (16), we can see that:

$$\hat{K} = [4.2797 \cdot 10^{-4} \quad 0.0146] \quad (18)$$

which ensures that the system is asymptotically stable.

3.7. Full order status observer

Examining the OCD observability matrix:

$$OCD = [C' \quad C'A' \quad C'A'^{n-1}] = [1] \quad (19)$$

Since the range of OCD is equal to 1, the system is fully observable, choosing the desired from the observer as:

$$s + 5 = 0 \quad (20)$$

The observer's characteristic is:

$$|sI - A + k_e C| = s + 4.16 + k_e = 0 \quad (21)$$

Comparing (21) with (22), we can see that $k_e = 0.84$.

3.8. Simulation of the control strategies for the DC drive

Figure 7 shows the performance of the three control strategies. First, a speed of 1200 rpm is assumed, at 4 seconds the reference is changed to a speed of 2300 rpm, and at 8 seconds a disturbance in the speed of 100 rpm is simulated.

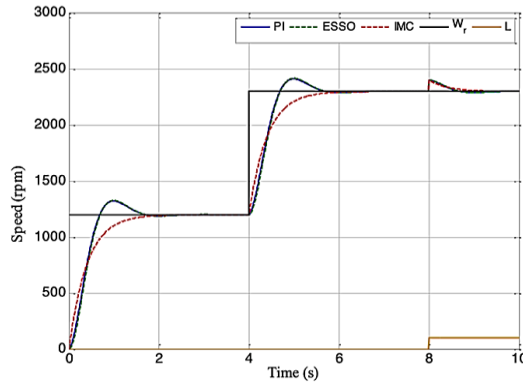


Figure 7. Control strategies in the DC drive

3.9. PID control strategy for AC drive

As the AC drive model is second order, a PID controller was used, and Matlab was also used for its tuning, resulting in the following controller:

$$G_{PID_AC} = 0.008 \left(1 + \frac{1}{5.5609s} + 0.01576s \right) \quad (22)$$

The response presents an $M_p \approx 10\%$ and a $t_s \approx 1.6$ seconds.

3.10. IMC control strategy for the AC Drive

Considering (11), the IMC controller is given by:

$$G_{IMC_AC}(s) = 0.0041 \left(\frac{0.125s^2 + 0.8273s + 1}{(0.2s + 1)^2} \right) \quad (23)$$

A second-order filter was chosen with an adjustment constant of 0.2 seconds

3.11. ESSO control strategy for the AC drive

A state-space representation of (11) is:

$$\begin{aligned} \dot{x} &= \begin{bmatrix} -6.6184 & -8 \\ 1 & 0 \end{bmatrix} x + \begin{bmatrix} 1 \\ 0 \end{bmatrix} u \\ y &= [0 \quad 1968.5] x \end{aligned} \quad (24)$$

Where the state vector $x = [x_1 \ x_2]^T$

3.12. Type 1 follower system

Examining the MCA controllability matrix:

$$MCA = \begin{bmatrix} -6.6184 & -8 & 1 \\ 1 & 0 & 0 \\ 0 & -1968.5 & 0 \end{bmatrix} \quad (25)$$

The range of MCA is 3 and its determinant differs from zero. Assuming $M_p = 10\%$ and $t_p = 1$ second, the dominant closed-loop poles are $p_{1,2} = -2.3029 \pm j3.1400$. Choosing a third pole $p_3 = -20$, the desired characteristic was:

$$s^3 + 24.6052s^2 + 107.2755s + 303.43 = 0 \quad (26)$$

The closed-loop characteristic equation was:

$$s^3 + (6.6184 + k_1)s^2 + (k_2 + 8)s - 19668.5k_i = 0 \quad (27)$$

Comparing (27) with (26), it follows that:

$$\hat{K} = [17.9868 \quad 99.2755 \quad 0.1541] \quad (28)$$

Which thereby ensures that the system is asymptotically stable.

3.13. Full order status observer

Examining the *OCA* observability matrix:

$$OCA = \begin{bmatrix} 0 & 1968.5 \\ 1968.5 & 0 \end{bmatrix} \quad (29)$$

The range of *OCA* is 2 and the nonzero determinant. Choosing the observer poles at $s_{1,2} = -5$, the desired characteristic is:

$$s^2 + 10s + 25 = 0 \quad (30)$$

On the other hand, the characteristic equation of the observer is:

$$s^2 + (6.6184 + 1968.5k_{e_2})s + (8 + 1968.5k_{e_1} + 13028k_{e_2}) = 0 \quad (31)$$

Comparing (31) with (30),

3.14. Simulation of control strategies for the AC drive

Figure 8 shows the performance of the three control strategies. First, a steady-state speed of 1200 rpm was assumed, at 4 seconds the reference was changed to 2300 rpm, and at 8 seconds a disturbance in the speed of 100 rpm was simulated.

As can be seen in Figure 9, the responses of the controlled system are satisfactory, in the PID and ESSO strategies the response was underdamped as designed, with $t_s \approx 1.6$ s. In the IMC strategy, the response showed no overshoot, was softer and therefore less aggressive, with a very similar t_s to the PID and ESSO. When the system was disturbed, the three strategies satisfactorily performed rejection and the BMI did so more smoothly and with no tendency towards oscillation.

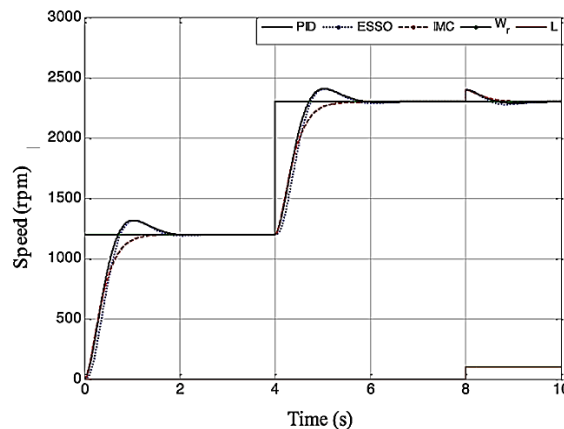


Figure 8. Control strategies in the AC drive

4. VALIDATION AND DISCUSSION OF RESULTS

A graphical interface for the validation was developed in LabWindows 6.5, allowing the selection of the drive and the control strategy, among other options, in Figure 9 and Figure 10. First, a reference speed of 1200 rpm was proposed, and then it was changed to 2300 rpm. Figure 11 (a), Figure 11 (b), and Figure 11 (c) show the performance of the system controlled with the PI, IMC, and ESSO strategies, respectively, for the DC drive. Finally, Figure 12 (a), Figure 12 (b), and Figure 12 (c) show the performance of the control system using the PID, IMC, and ESSO strategies, respectively, for the AC drive.

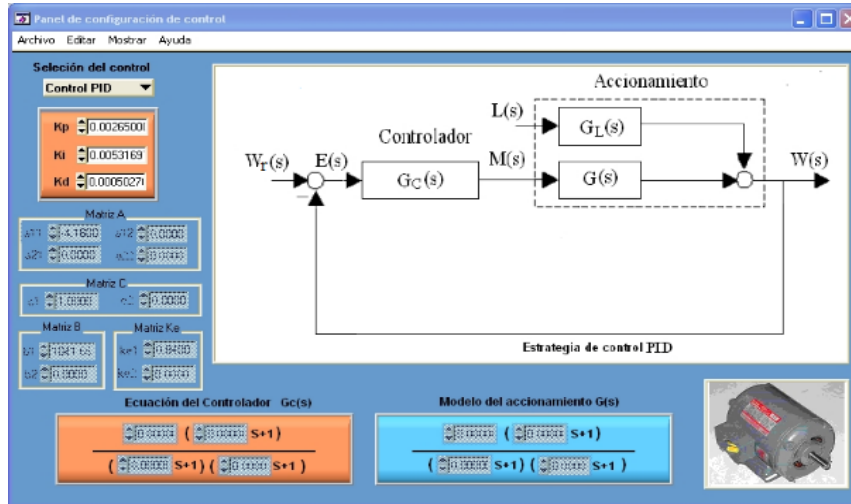


Figure 9. PID configuration panel for the DC drive

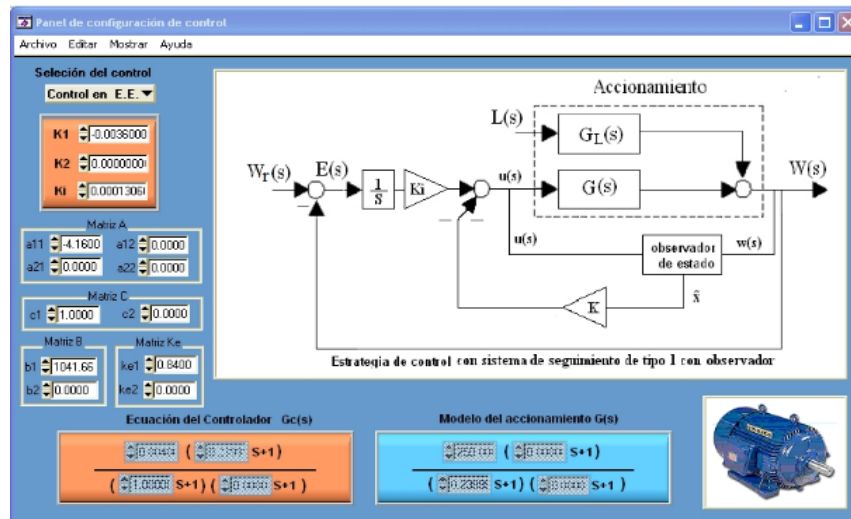


Figure 10. ESSO configuration panel for the AC drive

In all cases the results can be classified as satisfactory and in accordance with expectations. In the experiments carried out, the PI and PID control strategies showed better results at high speeds, since at low speeds the system started to oscillate, due to the mechanical characteristics of the experimental station, in which non-linearities and background noise can have more influence on the speed measurements of the control system.

The IMC control, moreover, has a broader speed control range in both electric drives, with values between 10 and 2500 rpm. For example, if we compare it with the PID variant, IMC showed a faster transient response and somewhat shorter settling times. However, it has been found that this control strategy sometimes tended to offer maximum overshoots during start-up when speeds below 500 rpm were taken as the starting point.

The ESSO control strategy offered a lower speed control range on both drives, around 500 to 2500 rpm, in addition to a transient response with settling times that depended on the location of the desired closed-loop poles and the speed of the state observer. Moreover, this strategy tended to offer maximum overshoots during start-up by the above. Finally, it can be said that in both drives the responses were satisfactory, with small differences between the simulated and practical responses, which can be explained by non-molded dynamics and the non-linearities present in the physical system under investigation.

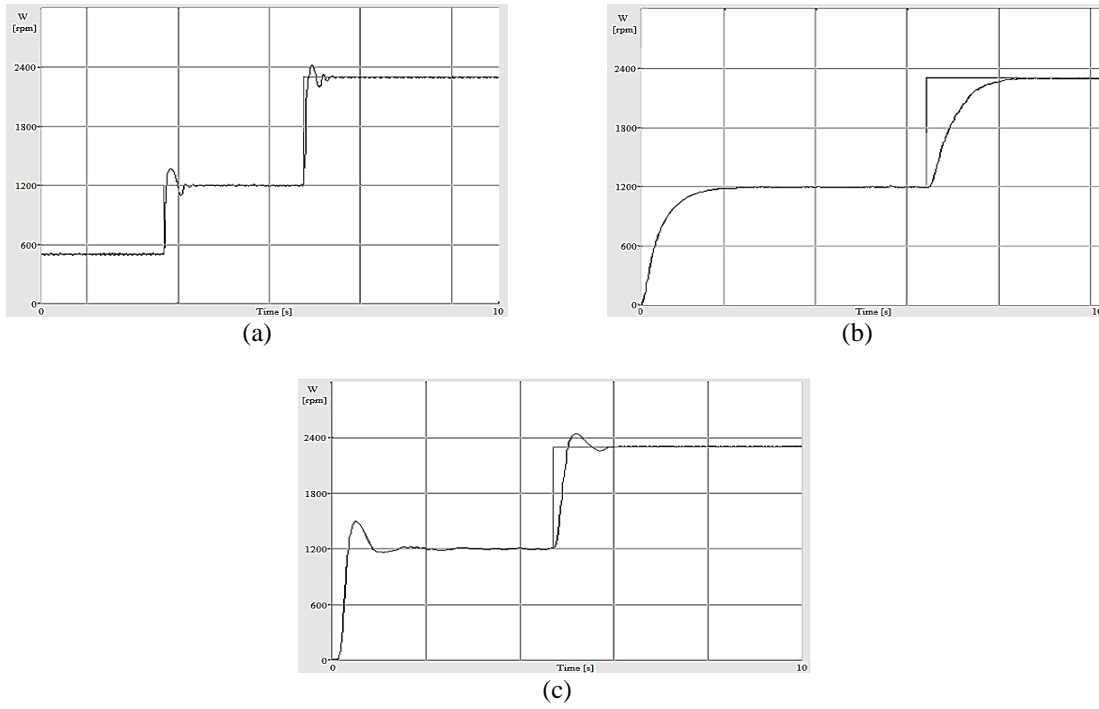


Figure 11. Controlled system performance using implemented strategies: (a) PI control strategy in the DC drive, (b) IMC control strategy in the DC drive, (c) ESSO control strategy in the DC drive

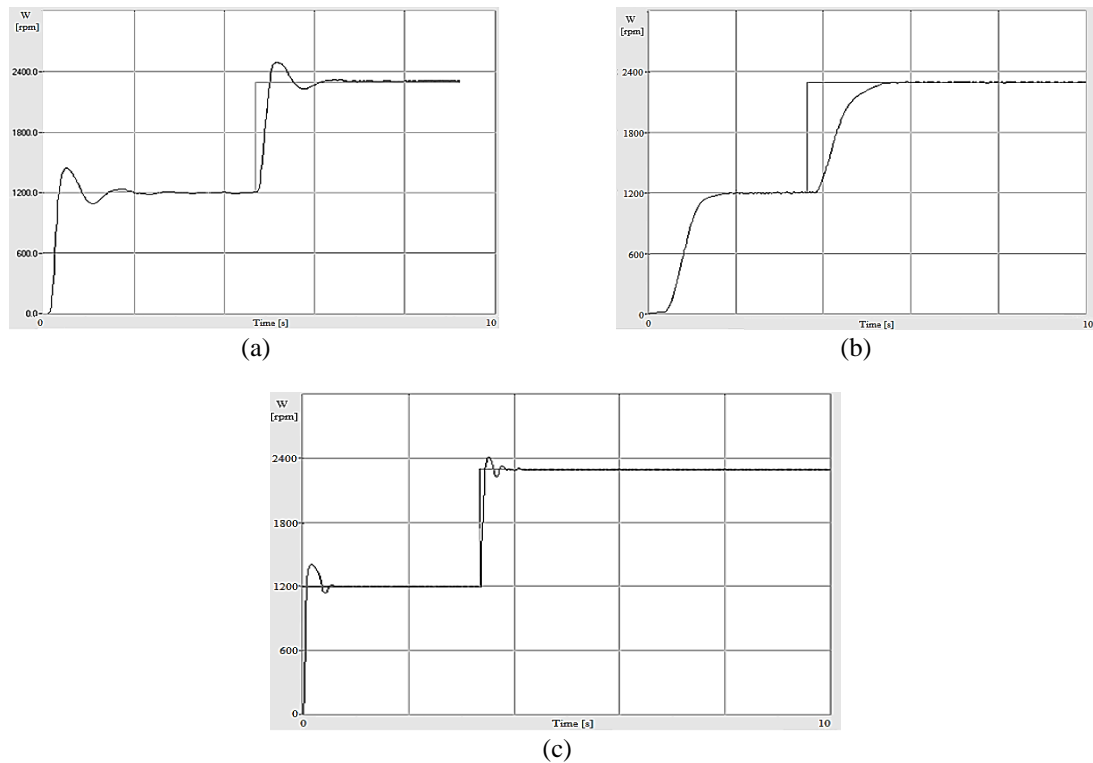


Figure 12. Control strategy: (a) PID control strategy in the AC drive, (b) IMC control strategy in the AC drive, (c) ESSO control strategy in AC drive

5. CONCLUSIONS

Three control strategies for DC and AC drives have been synthesized. The drives have been modeled via experimental identification, where a first-order model was obtained for the DC drive and a second-order model for the AC drive, both without time delay. A graphical interface was developed in LabWindows 6.5 for the experimental validation, with which it was possible to configure all the instrumentation, choose the drive and the speed control strategy that was implemented, and it was also possible to set indicators for the speed and limits of alarms for the protection of the drive against possible overloads, low line voltage, short circuits, and locked rotors, for example. The drive, in addition to being protected by a security code and alarms, has an emergency stop button in case of tampering.

In general, it can be said that the response of the experimental system coincided with the simulated one. Moreover, the PI and PID control strategies presented better results for high speeds. The IMC control permitted a greater range of speed control on both electric drives, in addition to the less aggressive controller action. The ESSO strategy offered a lower speed control range on both electric drives and the transient response depended on the location of the desired closed-loop poles and the speed of the state observer.

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