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ANALYZING THE SCALABILITY OF PARALLEL MICROWIRE ARRAYS FOR NEURAL RECORDING

A Thesis

Presented to

The Faculty of the Department of Biomedical Engineering

San José State University

In Partial Fulfillment

of the Requirements for the Degree

Master of Science

by

Yeena Ng

August 2021

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The Designated Reading Committee Approves the Project Titled

ANALYZING THE SCALABILITY OF PARALLEL MICROWIRE ARRAYS FOR NEURAL RECORDING

by

Yeena Ng

APPROVED FOR THE DEPARTMENT OF BIOMEDICAL ENGINEERING

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August 2021

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ABSTRACT

ANALYZING THE SCALABILITY OF PARALLEL MICROWIRE ARRAYS FOR NEURAL RECORDING

by Yeena Ng

Brain-computer interfaces (BCI) improve the quality of life for patients with severe motor disabilities and sensory impairment by providing them a direct way to communicate with the outside world through computers. To gain higher temporal resolution for better devices, intracortical neural electrodes, such as microwire arrays, are used. Microwire electrode arrays bonded to CMOS sensors, for intracortical neural recordings, have been claimed to be scalable. Microwire electrode arrays of varying diameters and densities were constructed and evaluated for percentage connectivity after interfacing with a custom-made CMOS sensor. The results demonstrate that there is no significant difference in the mean connectivity between a 3 mm and a 12 mm bundle as well as between arrays that have a wire-to-wire distance of 200 µm versus 100 µm, confirming the scalability of microwire electrode arrays. Understanding array scalability allows for better electrodes to be built for higher resolution neural recordings, which can help those who suffer from motor or sensory disabilities regain a better quality of life by re-establishing some independence.

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1 INTRODUCTION

1.1 Brain-Computer Interfaces

Patients who suffer from disabilities as a result of lack of neural function are missing a channel to connect them to the outside world. Neurological disorders or injuries that break down communication between the brain and the external world can cause severe sensory or motor impairment. One way to assist these patients in regaining their independence is through brain-computer interfaces (BCI), devices that connect the human brain to a machine that allows patients with limited muscular movements or other disabilities to communicate directly with the external world. BCIs utilize electrodes to record and relay information on neural activity back to a machine. The machine is then able to process and analyze these action potentials. Ultimately, BCI systems that can record and apply potentials have the potential to substantially improve the quality of life for patients by restoring some of their motor abilities, thus allowing them to live more independently⁽¹⁾.

1.2 Electrophysiology Modalities and Challenges

Modern neural electrophysiological technologies optimize for less invasiveness and higher signal resolution. Non-invasive modalities, such as electroencephalography (EEG), record neural activity over the surface of the scalp and have been successful in performing basic tasks on neuroprostheses and wheelchairs ^(2,3). However, EEG lacks the resolution to perform more advanced tasks, such as multi-dimensional control applications especially in neuroprostheses, and more invasive approaches would be required to gain signal resolution ^(2,3,4).

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More invasive approaches involve surgical interventions that pose far greater health risks but reward with higher spatial and temporal resolution gained from directly interfacing with brain tissue. Electrocorticography (ECoG) involves placing an electrode grid directly on the surface of the brain ⁽⁴⁾. ECoG can record from either directly on top of the dura or beneath the dura, providing substantially higher signal quality than EEG ⁽⁵⁾. Studies have demonstrated that ECoG signals can be used to decode two-dimensional joystick trajectories in humans ⁽⁶⁾.

Alternatively, intracortical neuron recording involves using penetrating electrodes to record from inside the cortex for the highest resolution, as these electrodes can come closest to firing neurons. As shown in Table 1, intracortical neuron recording demonstrates better temporal and spatial resolution than other methods. Electrodes implanted into the cortex can detect the spiking activity of single neurons, resolution unable to be obtained with the other modalities ⁽⁴⁾.

Modality	Temporal Resolution	Spatial Resolution	Risk
EEG	~0.05 s	~10 mm	Non-invasive
ECoG	~0.003 s	~5 mm	Invasive (on top of the dura or sub- dura)
Intracortical Neuron Recording	~0.003 s	~0.05-0.5 mm	Invasive (intracortical)

Table 1. Comparison of the resolution and risk for varying electrophysiology modalities

1.3 Refining Intracortical Neuron Recording Devices

Intracortical neuron recording provides higher quality recordings than other modalities, making it a popular choice for studies ^(7,8,9,10,11,12). Implanted electrodes

record the activity of nearby neurons. Neural activity can be broadly separated into two categories: multi-unit activity (MUA) and single-unit activity (SUA) ⁽¹³⁾. Multi-unit activity comprises recordings of many units within the proximity of the electrode, while single-unit activity is the activity of individual neurons. The capability to record SUA is highly sought after, as it provides substantially higher resolution than MUA ^(13,14).

The Utah Intracortical Electrode Array, a 100-electrode silicon array, has been evaluated in prior studies, including chronic human trials, due to its ability to record single action potential rather than thousands ^(15,16). However, there is interest in developing scalable microwire arrays that improve upon signal resolution by increasing the number of recording electrodes and reducing the damage upon penetration into the tissue ^(7,17,18). Electrode arrays that are easily fabricated to increase in size and density can open the doors to the rapid development of substantially more sensitive devices.

2 LITERATURE REVIEW

2.1 Intracortical Neural Electrodes

Intracortical neuron recording utilizes implanted electrodes to record neural activity within the cortex of the brain. This modality of neural recording can be done with an array of micromachined electrodes, such as the Utah Array, a lithographically-modified planar silicon shank, such as the Michigan probe, or microwire electrode arrays ^(10,15,18,19). Electrodes must be optimized for easy scalability, penetration with minimal damage, and good signal-to-noise ratio ⁽²⁰⁾. However, other factors must be taken into consideration as well, such as insertion mechanics, tissue damage, and material selection.

2.2 Factors to Consider for Electrode Design

2.2.1 Insertion Mechanics

One of the primary factors to be considered while designing intracortical neuron recording electrodes is their insertion mechanics. Electrode insertion is a non-trivial procedure due to the robustness of the pia mater, a meningeal layer covering the brain ^(22,22,23,24). Though flexible devices reduce chronic inflammation by minimizing material softness mismatch between the implant and the brain, they frequently have difficulties penetrating the pia ⁽²⁵⁾. While larger electrodes may seem more attractive for their mechanical strength, their large size can more substantially damage brain tissue, as described in Section 2.2.2 ^(9,26). Probes must also penetrate the pia while avoiding dimpling caused by the large amount of force required to insert an electrode relative to the resistance of the underlying cortical tissue ^(18,21). This dimpling is amplified in arrays of closely spaced electrodes, causing a bed-of-nails effect, deforming the brain tissue, and

causing long-term damage ^(8,11,21,27,28). Hence, it is prudent to consider how the shape and size of an electrode array affects its insertion capabilities.

2.2.2 Tissue Damage

Aside from insertion mechanics, tissue damage is also a critical factor to consider while designing intracortical neural electrodes. Probe insertion can rupture vasculature on the surface of the brain, leading to loss of blood to the damaged area of the brain ⁽⁹⁾. Injury to the blood-brain barrier can also cause microglia and astrocytes to elicit an immune response, damaging the probe and reducing recording performance ⁽⁹⁾. Microglial cells, as part of the immune response, encapsulate electrodes by forming a sheath, reducing ion and molecular exchange between the electrodes and the neurons ⁽²⁴⁾. Furthermore, common materials utilized to build implants are susceptible to absorption of proteins, such as proinflammatory molecules and cytokines that perpetuate a chronic tissue response ⁽²⁵⁾. This encourages cellular encapsulation of the electrodes, reducing recording quality ⁽²⁵⁾.

Intracortical electrode arrays displace brain tissue volume when inserted, placing more pressure on surrounding tissue ⁽²⁵⁾. Furthermore, intracerebral hemorrhage edema cause swelling and pinching of nearby blood vessels, causing loss of perfusion and a secondary ischemic injury ⁽²⁵⁾. Aside from tissue displacement, strain caused by mechanical mismatch between the implanted device and the brain is a source of chronic tissue reaction due to micromotion, causing persistent irritation and prolonged inflammation ^(9,25).

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Minimizing tissue damage is a non-trivial matter to consider for intracortical neuron recording modalities. Decreasing device size reduces tissue displacement and damage due to pressure but results in technical challenges to minimize feature sizes ⁽²⁵⁾. Other methods to reduce chronic damage due to micromotion include ways to decrease bending stiffness of the inserted electrodes ⁽²⁵⁾. Reducing material stiffness mismatch between brain tissue and the electrode by reducing the cross-sectional area or geometry of the device, increasing electrode length, and reducing the elastic modulus of the material have been proposed ^(25,29). However, if the electrodes are too thin, they lack the mechanical strength to penetrate the brain ⁽²⁵⁾. Hence, further improvements regarding electrode size and shape must be made.

2.2.3 Materials Selection

The biocompatibility of materials for intracortical neuron recording electrodes is important to ensure the fidelity of long-term recordings. Intracortical neural electrode arrays are predominantly made from silicon-based microelectrodes due to their ease of reproduction, low costs of manufacturing, and customizability ⁽³⁰⁾. For example, the Utah array is a micromachined silicon-based array sputtered with an iridium oxide or platinum layer to improve conductivity at the electrode tips and insulated along its shaft with a biocompatible polymer parylene-C ^(31,32). Similarly, the Michigan probe is built from a lithographically-modified planar silicon shank ^(19,31). More details regarding these two common types of intracortical electrode arrays are discussed later, in Sections 2.3.1 and 2.3.2.

Because of the manner in which microwire electrode arrays are produced, more materials have been explored. Microwire electrode arrays have typically been insulated with biocompatible materials, such as polyimide, parylene-C, Teflon, and quartz glass ^(10,28). Insulating materials with higher dielectric constants are more favorable for minimizing stray capacitances from affecting the electrode during recording, as shown in Table 2; however, biocompatibility and ease of process development must also be taken into consideration ^(7,23,29,30,33). Conductive metallic cores of these microelectrodes have been made from biocompatible materials such as platinum, iridium, platinum-iridium, gold, stainless steel, tungsten, and molybdenum ^(30,35,36).

Insulation Material	Dielectric Constant (1 kHz, Room
	Temperature)
Polyimide	3.4
Parylene-C	3.1
Teflon	2.002
Quartz glass	3.823

Note: Dielectric constant values were sourced from several works. (34,35,36,37,38,39)

2.3 Current Electrode Array Designs

2.3.1 Utah Array

Currently, the two most used electrode designs are micromachined arrays and planar multi-electrode shanks. The Utah Intracortical Electrode Array is a 10 x 10 array of silicon probes spaced 400 μ m apart, center-to-center, as shown in Figure 1 ⁽¹⁵⁾. The 100 electrodes in the array are each 1.5 mm long and taper from 80 μ m to 50 μ m in tip diameter ⁽¹⁵⁾. The Utah Array has been successfully utilized in neural prosthetic

applications despite the low density of electrodes and limitations inherent to a 4 mm x 4mm cortical recording area ^(32,33).

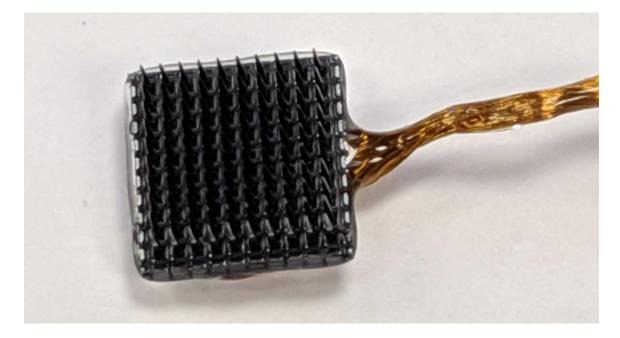


Figure 1. Photograph of a Utah Intracortical Electrode Array.

2.3.2 Michigan Probe

Another common form factor is a planar, shank-shaped electrode. The Michigan microelectrode system, or Michigan probe, is a silicon shank with lithographically patterned recording sites along the length of the substrate, allowing for electrophysiological information to be obtained at different depths of the neural cortex ⁽¹⁹⁾. This type of penetrating electrode, shown in Figure 2, can improve the resolution by scaling up the number of recording sites along the surface area of the flat part of the 10 mm long, 70 μ m wide, and 20 μ m thick substrate ⁽³¹⁾. However, the number of recording sites is limited to the surface area of the electrode.



Figure 2. Image of a Michigan microelectrode system, or Michigan probe, with lithographically-modified recording sites on the surface of the silicon planar shank ⁽²¹⁾.

2.3.3 Microwire Electrode Arrays

Neither of the two previously mentioned designs is easily scalable without major architectural changes, which raises the need for an alternative design. Microwire electrode arrays provide a possible solution for this problem. A microwire electrode array is built by arranging insulated wires into a bundle ^(7,17). Unlike micromachined arrays, such as the Utah array, that has a fixed number of electrodes, the number of electrodes within a microwire bundle can be increased by increasing the diameter or density ⁽¹⁸⁾.

Early microwire array bundles utilized less than 100 wires and penetrated the brain with the use of a metal carrier tube ^(7,12). However, more recent arrays have demonstrated much greater scalability with microwire arrays. Obaid *et al.*, 2020, built a 7 mm diameter

array with 40 μ m wires at center-to-center distance, producing an 8640-electrode bundle, revealing the potential for microwire electrode array scalability ⁽¹⁸⁾. This was produced with a fabrication process, shown in Figure 3, that bundled microwires by spacing wires with a sacrificial polymeric coating ⁽¹⁸⁾. The sacrificial layer was customizable to adjust the center-to-center distance, or pitch, between the wires ⁽¹⁸⁾. The coated electrodes were then formed into a round array by orienting the microwires in a hexagonal pattern, as shown in Figure 4 ⁽¹⁸⁾. This allows the array to theoretically be able to be scaled up to hundreds of thousands of microwires for high-bandwidth applications ⁽¹⁸⁾.

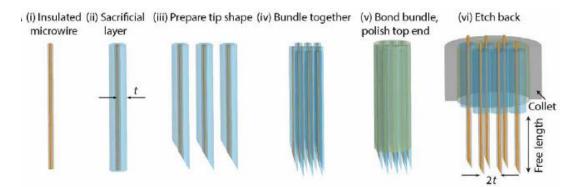


Figure 3. Fabrication procedure for microwire bundles, where t represents the adjustable thickness of the sacrificial polymeric coating and 2t represents the center-to-center, or pitch, between each microwire ⁽¹⁸⁾.

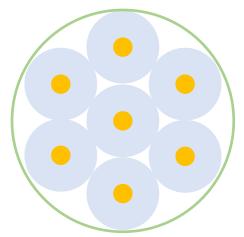


Figure 4. A cross-section view of hexagonally-packed microwires in a bundle. The gold circles represent the metallic core of the microwire and the pale blue surrounding rings represent the sacrificial polymeric coating.

In order to process the neural activity detected by the electrode array into meaningful information, the signal must amplified. The distal end of the array refers to the end that will be inserted into and recording from within the brain ⁽¹⁸⁾. The proximal end of microwire electrode array is connected to the CMOS amplifier array by compressing the wires mechanically onto the chip, creating ohmic contacts ⁽¹⁸⁾. This parallel bonding paradigm where the proximal end of the array is mated to CMOS electronics while the distal recording tips are inserted into the brain has been explored for many years ^(18,40). This strategy worked well with many different CMOS designs, ensuring that more than 90% of wires contact reproducibly ⁽¹⁸⁾.

2.3.4 The Argo System

In order to use microwire electrode arrays to record and decode neural activity, they must be paired with the appropriate electronics to read and amplify neural signals ⁽¹⁰⁾. Previously, microwire electrodes were not considered as scalable recording methods due to difficulties connecting large arrays to many amplifiers ^(8,11). However, recent successes

in connecting microwire arrays to high density CMOS sensor arrays have proved that this problem could be resolved, demonstrating the viability of recording from substantially larger, denser microwire electrode arrays ^(18,40). Sahasrabuddhe *et al.*, 2021, describes the Argo System in Figure 5, where microwire bundles interface with a CMOS amplifier array to send recorded neural signals over an optical data link to the server computers ⁽¹⁰⁾. The data are received by custom data acquisition software that displays it on a user interface on the client computer and saved into an offline processing computer for further analysis ⁽¹⁰⁾.

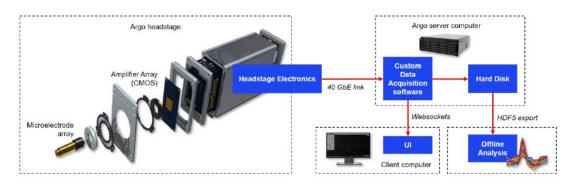


Figure 5. The Argo System utilizing fabricated microwire electrode arrays from Sahasrabuddhe *et al.*, 2021.

The Argo System CMOS sensor, utilized to amplify and filter recorded signals from the electrode array, is an application-specific integrated circuit (ASIC) consisting of a 256 x 256 pixel array ⁽¹⁰⁾. The pixels are spaced at 50 μ m pitch and contain a 40 μ m x 40 μ m metal landing pad on top of each pixel to connect with the microwire electrodes ⁽¹⁰⁾. Sahasrabuddhe *et al.*, 2021, describes the chip design and relevant electronics in further detail. The microwire electrode bundle connects to the CMOS by mechanically pressing the distal end of the microwire array onto the chip, simultaneously contacting all the electrodes in the array with the pixels on the CMOS ⁽¹⁰⁾. The bundle is held in place with a flexible diaphragm secured onto the front plate of the head stage of the Argo System as shown in Figure 6 and described by Obaid *et al.*, 2020 ⁽¹⁸⁾. Successes seen with the Argo System make it, along with microwire electrode arrays, viable for recording neural activity.

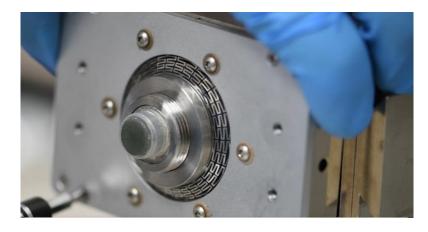


Figure 6. A microwire array secured onto the front plate of the Argo System head stage and held onto the CMOS sensor array with a flexible diaphragm.

2.4 Summary

Intracortical neuron recordings can capture neural signals with higher resolution, because the electrodes are substantially closer to the area of activity than other recording modalities. In order to develop electrodes, insertion mechanics, tissue damage, and materials selection must be taken into consideration. Microwire electrode arrays provide a scalable solution to these issues; the electrodes are much smaller in size than the traditional micromachined Utah Array, and microwire bundle architecture allows for scalability in terms of array size and density. Microwire electrodes also do not face the same surface area limitations as lithographically-modified planar shanks, such as the Michigan probe. Microwire electrode arrays, as demonstrated by the ones utilized in the Argo System, serve as viable intracortical neural recording devices that are easy to scale up in array size and electrode density. This opens doors for the development and manufacturing of high-resolution neural interfaces that can change the lives of patients with mobility impairment and sensory loss to potentially communicate directly with their electronics. This would provide patients with some independence as well as an improved quality of life.

3 RESEARCH OBJECTIVE

3.1 Objectives

My research objective was to evaluate the scalability of microwire electrode bundles by using the aforementioned Argo System to measure electrode connectivity as a function of increasing array diameter and electrode density. I hypothesized that if the array diameter and electrode density increased, then the average connectivity will not be significantly different between larger, denser arrays and smaller, sparser arrays. Average connectivity refers to the number of wires connected to the CMOS sensor in the Argo System divided by the total number of electrodes in the bundle.

3.2 Justification

Electrode scalability for improved neural recording quality is a problem that can be resolved through microwire bundles. Microwires produce less damage upon insertion, due to their smaller size, than the traditionally micromachined arrays and the number of electrodes can be increased for easy scaling, unlike lithographically-modified planar shanks that are limited by the surface area of the probe. The fabrication method for these arrays, as described in Obaid *et al.*, 2020, allows for an increase in size and density for use in different cortical areas for greater lateral coverage without implementing major changes to the manufacturing process. Increasing the array diameter can be done simply by incorporating more electrodes into the microwire bundle, and increasing the density merely involves reducing the thickness of the sacrificial spatial coating between each wire, allowing for more electrodes to be packed into the same volume. These small

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changes allow for simple customization of intracortical implants without drastically affecting the other steps in the manufacturing process

My hypothesis was that increasing the array diameter and electrode density would not cause a statistically significant change in the average connectivity of the electrode arrays. My null hypotheses were that arrays with 3 mm and 12 mm diameters would have the same mean percentage connectivity; similarly, arrays with 200 μ m and 100 μ m pitches would have the same mean percentage connectivity. My alternative hypotheses were that arrays with diameters of 3 mm and 12 mm diameter would have a statistically different mean percentage connectivity, and arrays with 200 μ m and 100 μ m pitches would also have a statistically significant difference in mean percentage connectivity.

4 MATERIALS AND METHODS

4.1 Materials

The materials required to build the microwire array bundles included $18 \,\mu m$ diameter platinum-iridium alloy (90% platinum, 10% iridium) wires purchased from TANAKA Precious Metals. Calcium chloride, deionized water, a programmable AC power source, a printed circuit board with a single strip of copper running from end to end, and silver epoxy H20E from Epoxy Technologies were used in the electrochemical sharpening process. A fume hood with carbon and HEPA filters was needed to electrochemically process the microwires. Parylene-C dimer DPX-C, Silane A-174, and Specialty Coating Systems PDS 2010 Labcoter were required to coat a spatial Parylene-C layer as well as the electrode insulation layer onto the wires. For the microwire array assembly, fluorinated ethylene propylene heat shrink tubing, a hot air gun, EPO-TEK 301 epoxy, a generic vacuum pump drying oven, and a custom 0.45-inch long stainless steel tube with inner diameters between 1.5 mm to 2 mm were used. 96% sulfuric acid, 30% hydrogen peroxide, hot plates, beakers, and an acid hood were used to etch the epoxy. A plasma etcher (SPI Plasma Prep II) and pure oxygen gas were needed to etch spatial Parylene-C. To smooth out the top of the array, a 5C collet, lathe, and 400, 600, 1000, and 1200 grit silicon carbide grinding paper were needed. Fluorescent dye Y3G mixed with EPO-TEK 301 for contrast, a confocal microscope to measure the lasing distance, and a UV laser were used to ablate the insulation from the tips of the electrodes. A scanning electron microscope was critical for evaluation of the quality of recording sites

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on the tips of the electrodes. A Leica DVM6 digital microscope and ImageJ were used to assess the packing of the microwires within each array.

To assess the connectivity of the array, the Argo System as described in Sahasrabuddhe *et al.*, 2021 and the custom CMOS chip which the electrode array mechanically presses onto were needed. The microwire arrays required custom hollowed ³/₄"-20 threaded rods to thread into the front plates, cyanoacrylate, custom front plates, flexible diaphragms, custom stainless steel housing, and M2 x 0.4 screws to secure all the parts. A modified vise was required to press the electrode array onto the CMOS sensor. 1X PBS, a beaker, and a custom user interface were used to perform the saline test to evaluate the connectivity of the electrodes within the array to the CMOS chip ⁽¹⁰⁾.

4.2 Methods

Understanding the effects of array diameter and electrode density of microelectrode arrays on the electrode array connectivity with the CMOS sensor results in better design decisions.

In order to do so, 3 mm and 12 mm diameters of microwire electrode arrays with low and high electrode densities were fabricated. Electrode densities were referenced by their "pitch," or distance from the center of one wire to the center of the next. Low-density arrays had a pitch of 200 μ m, and high-density arrays had a pitch of 100 μ m. Table 3 lists the combinations of array sizes and electrode pitches that were studied.

Array Size	Electrode Pitch	
3mm	200 µm (Low Density)	100 µm (High Density)
12mm	200 µm (Low Density)	100 µm (High Density)

Table 3. Combinations of array sizes and electrode densities evaluated.

For additional clarification, the "proximal" end of the electrode array as referenced within this thesis refers to the end of the bundle mechanically pressed onto the CMOS chip. The "distal" end of the array refers to the end that will be recording from within the brain.

4.2.1 Microwire Electrode Array Fabrication

Microwire electrode arrays were fabricated from 18 µm diameter platinum-iridium alloy (90% platinum, 10% iridium) wires purchased from TANAKA Precious Metals. The wires were annealed, cut into 1-inch long pieces, and one end of the wires was electrochemically processed in a 0.5 M calcium chloride solution to produce a sharp needle-shaped electrode. The electrodes were coated with a spatial layer of polymer Parylene-C (PaC) through chemical vapor deposition with a Specialty Coating Systems PDS 2010 Labcoter that determined the pitch between the wires. The coated wires were collected in a round piece of heat shrink and assembled into a round bundle with all the sharpened tips oriented in the same direction and on the same plane as shown in Figure 7. The electrodes were bound by epoxy EPO-TEK 301 from Epoxy Technology Inc and cured at 65°C for 2 hours. The array was then removed from the heat shrink and adhered with EPO-TEK 301 into a 0.45-inch long 316 stainless steel tube with inner diameters between 1.5 mm to 2 mm larger than the bundle to provide handling ease. The tube sat in the center of the bundle such that 0.25 inches of the distal end of the array extended beyond the edge of the tube.

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Figure 7. Assembled microwire arrays after electrodes were coated with spatial Parylene-C coating and assembled in a clear heat shrink.

The array was then chemically etched in a piranha solution consisting of a 2:1 solution of 96% sulfuric acid and 30% hydrogen peroxide at 100°C and then oxygen plasma etched to reveal the bare wire. In order to keep the lengths of the exposed wire consistent across the entire array, wet and dry etches were alternated and repeated until >1 mm of wire was exposed on the distal end of the bundle and 300-500 μ m of wire was exposed on the distal end of the bundle and 300-500 μ m of wire was exposed on the proximal end. The electrodes were coated with a 1.5 μ m layer of Parylene-C insulation paired with silane A-174 for improved adhesion. Fluorescent epoxy created as a mixture of EPO-TEK 301 and fluorescent dye Y3G was used to embed the insulated wires in preparation for confocal microscopy during the later tip deinsulation step. The proximal end of the array was embedded in non-fluorescent EPO-TEK 301. After curing at 65°C for 2 hours, both ends of the bundle of electrodes were machined down with a lathe until the wires were exposed on the proximal end of the

bundle and until approximately 300µm of epoxy remained between the tips of the electrodes and the surface of the epoxy. To eliminate any tooling marks from machining, both ends of the bundle were lightly polished by hand on silicon carbide paper up to 1200 grit until the epoxy was clear enough to be seen beneath the surface as shown in Figure 8.

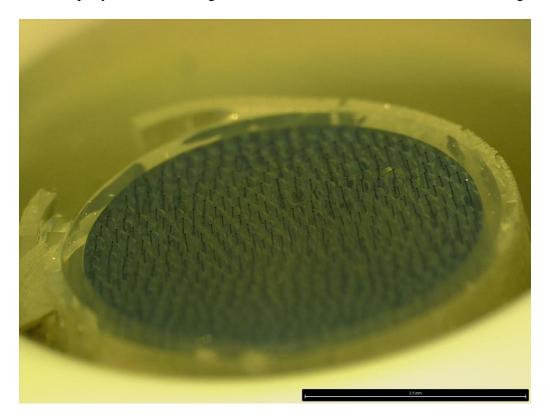


Figure 8. Proximal end of a 12 mm diameter, $200 \,\mu$ m pitch bundle polished and with a small length of exposed wire prior for pressing onto a CMOS chip.

To expose recording sites on the microwires, laser ablation was utilized to deinsulate the tips of the electrodes. Confocal microscopy verified the distance, approximately 300 μ m, between the surface of the epoxy and the tips of the electrodes. Using this information, UV laser ablation was used to etch the epoxy to expose 10-30 μ m of the distal tips, based on calculations for optimal impedance for neural recordings. The remaining Parylene-C insulation covering the tips was etched away with oxygen plasma. The wires on the proximal end of the bundle were also etched with oxygen plasma to expose $\sim 10 \,\mu\text{m}$ of bare wire for better connection with the CMOS chip. The electrodes were released from the epoxy with a final piranha etch until 800 μ m-1.5 mm of wire length was exposed as shown in Figure 9.

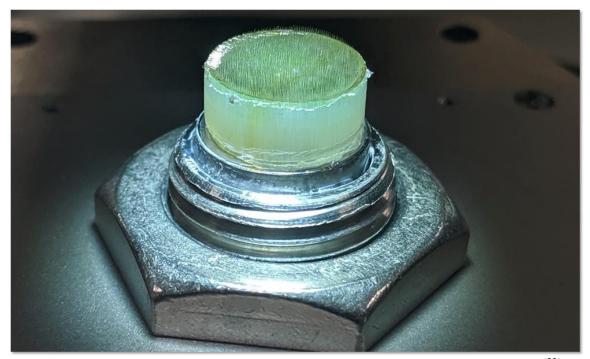


Figure 9. A fully fabricated microwire electrode array threaded onto the front plate ⁽³²⁾.

The arrays were imaged with a scanning electron microscope after the arrays were released from epoxy with piranha. Each array was examined to verify relatively uniform tip deinsulation, tip sharpness after processing, and the quality of the insulating layer. Photographs of these sharp tips were imaged with a scanning electron microscope, as shown in Figure 10.

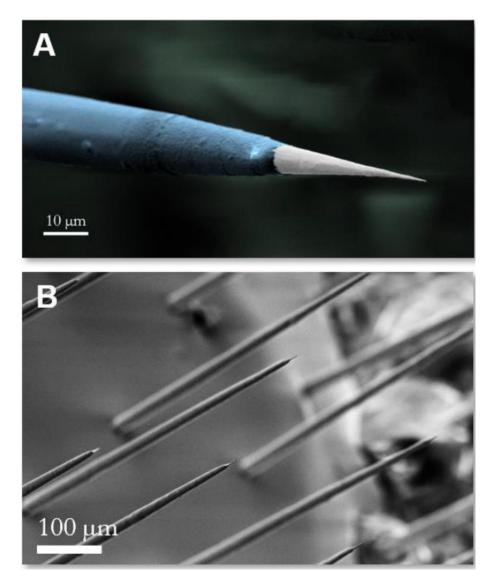


Figure 10. (A) False color image of an electrochemically sharpened microwire taken with a scanning electron microscope (B) Image of insulated microwire array taken with a scanning electron microscope ⁽¹⁾.

4.2.2 Connecting to a Sensor

The arrays were bonded to a CMOS amplifier array connected to the Argo System as described in Sahasrabuddhe *et al.*, 2021. In order to do so, the stainless steel tube surrounding the array was first bonded with cyanoacrylate into a custom hollowed 3/4"-20 threaded rod to screw into the center of the front plate with a flexible diaphragm

holding the array in place. The front plate was loaded into the top jaw of a modified vise standing on its side. The CMOS chip was seated in a stainless steel housing that was loaded into the bottom jaw of the vise. The vise came together to press the back of the array onto the CMOS sensor. When the array presses down onto the CMOS chip, the connected user interface displays a map of connected pixels on the chip to visualize the connectivity of the array to the CMOS sensor. After a satisfactory connectivity was achieved, when further pressing no longer yielded a higher connectivity on the user interface, the front plate was screwed on four corners into the housing for the CMOS chip to secure the bundle in its mechanically coupled state, as shown in Figure 11.

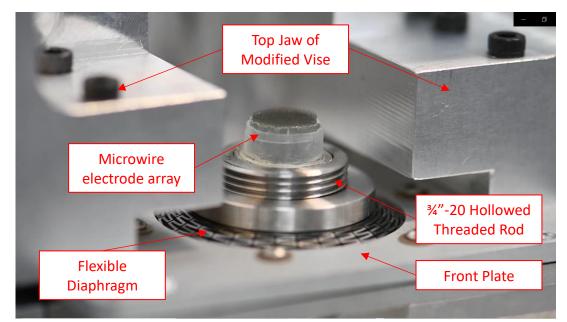


Figure 11. Microwire bundle array secured in vise during pressing onto CMOS sensor.4.2.3 Saline Bath Test

In order to evaluate the connectivity, or percentage of microwires that successfully bonded to the CMOS sensor, the functionality of the array was tested for ohmic contact to the chip in a saline bath test. While connected to the Argo System, the tips of the electrode array were submerged into a phosphate-buffered saline (PBS) bath ⁽¹⁰⁾. A 2 kHz, 2 mV_{pp} sine wave was applied through all the electrodes to verify connectivity to the CMOS chip, and a 2 kHz, 0.5 mV_{pp} sine wave was applied to calculate pixel gain from the recorded voltage measurement ⁽¹⁰⁾. Another voltage measurement was taken after shorting the saline bath to calculate the noise ⁽¹⁰⁾. The recording was band-pass filtered between 300 Hz and 6 kHz to isolate input-referred noise ⁽¹⁰⁾. Channels with optimal gain and low noise were chosen to form a connectivity mask, like the one shown in Figure 12, to represent the electrodes in the array suitable for neural recording *in vivo*. This mask also served as a baseline measurement to compare with data acquired from neural recordings.

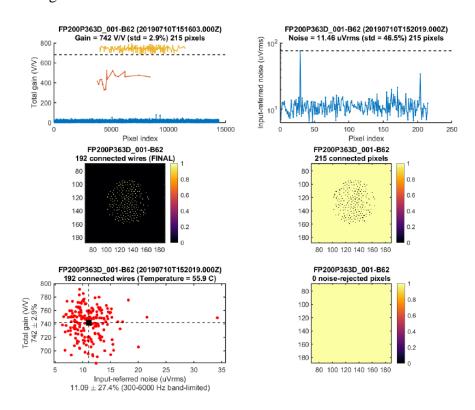


Figure 12. Example of a connectivity mask of a 3 mm diameter, $200 \,\mu\text{m}$ pitch bundle showing final connected wires and pixels after filtering for gain and noise.

4.2.4 Calculating Percentage Connectivity

The connectivity mask displays the number of connected electrodes and analyzes if they are well suited for recording neural signals. In order to find the percentage connectivity, the total number of wires per array must first be confirmed. Due to minor variations of $\pm 5\%$ in the Parylene-C coating across the deposition chamber, the spatial coating may not always be precise, particularly in arrays with larger spacing and larger diameters. In conjunction, manual aggregation of the wires does not guarantee the perfect hexagonal form, meaning there are frequently differences between each bundle in terms of electrode pitch and number of wires per array. To confirm the number of wires within each array, images of the proximal end of the bundle were taken, processed to 8bit as shown in Figure 13, and analyzed via the particle counter function on ImageJ prior to pressing onto the CMOS chip. The percentage connectivity was calculated by dividing the number of wires connected, as indicated on the connectivity mask, by the total number of wires in each array.

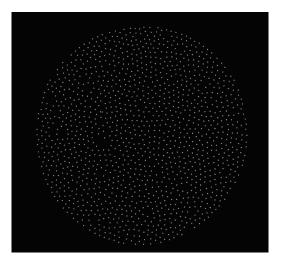


Figure 13. Example image of the proximal end of a 200 μ m array used to verify the number of wires within an array.

4.3 Safety

Risks of fabricating electrodes using this process involved the use of some dangerous chemicals listed in Table 4. Most of the materials utilized in this process were relatively harmless. Phosphate buffered saline relatively safe, and splash goggles and gloves were worn while working with it. Calcium chloride, utilized in electrochemical processing, is a salt that minimally affects human health. While mixing the salt in water was not harmful, the electrochemical sharpening process caused the solution to emit a dangerous chlorine gas. In order to keep users safe, this process was done under a fume hood with appropriate filters to minimize exposure. Gloves and goggles were used regularly during this process as well.

Other chemicals that required additional care are the following. Silane A-174 is a skin sensitizer and care was taken to make sure there was no contact with skin. Gloves and goggles were used each time with Silane A-174. EPO-TEK 301 Part A and B and EPO-TEK H20E Part A and B are irritants, toxic, and corrosive in their uncured state, so gloves and safety goggles were worn each time when handling the epoxy as well. Common solvents, including acetone and isopropanol, are flammable and need to be kept away from an open flame. Gloves and goggles were worn while handling these chemicals, and they were stored in a fireproof cabinet. More hazardous chemicals, such as hydrogen peroxide and sulfuric acid, require additional precautions. While handling these chemicals in a specialized acid fume hood, protective face shields, primary nitrile gloves, secondary rubber gloves, tertiary vinyl gloves, and full-length aprons were worn to protect the user. Any vinyl gloves that came into contact with these chemicals were

removed immediately and discarded into a specialized acid solid waste disposal, and secondary rubber gloves were thoroughly washed with water after each use to ensure the safety of future users. Aprons and face shields were examined prior to each use and replaced regularly to ensure the safety of users.

Upon contact with any of these materials, it is important to provide proper treatment in order to ensure little lasting damage to the health of the user. In case of eye contact, contact lenses must be removed and the eyes must be flushed with water for at least 15 minutes. If inhaled, the person utilizing the chemical must be moved to an area with fresh air and provided with artificial respiration if needed. When there is skin contact, it is recommended that the person flushes their skin with water and seek medical attention if their skin experiences any severe irritation.

Other hazards that are not related to chemicals include the utility of ovens, highenergy lasers, and machining equipment. Proper heat-resistant gloves were used while handling samples from ovens in order to ensure that users do not get burned. Laser cutter and other high-energy lasers raises the risk of vision damage if improperly used, so proper laser safety goggles were used each time. The laser was operated in a room with safety lights outside to indicate whether or not it was safe to enter the room. Heavy machining equipment, such as a lathe, are fast-spinning and dangerous. Users took extra care to ensure that there were no loose clothing items or hair that could potentially get caught on these tools.

Chemical	Health	Flammabilit	Reactivity	PPE	Other hazards
Acetone	5	ω	0	Splash goggles, gloves, protective apron, vapor respirator	Flammable
Calcium Chloride	1	0	-	Splash goggles, gloves, apron, dust respirator	N/A
EPO-TEK 301 Part A	7	1	0	Splash goggles, gloves, apron, vapor respirator	Irritant
EPO-TEK 301 Part B	3	1	0	Splash goggles, gloves, apron, vapor respirator	Toxic, corrosive
EPO-TEK H20E Part A	5	1	-	Splash goggles, gloves, protective clothing	Toxic, irritant
EPO-TEK H20E Part B	7	1	-	Splash goggles, gloves, respiratory protection, protective clothing	Toxic, corrosive, irritatnt

Table 4. List of chemicals used in this study

Chomical Haalth Flammahilit D	Hoolth	Flammahilit	Donotivity	DDG	Othor hozonde
Hydrogen Peroxide, 30%	ŝ	0	2	Splash goggles, face shield, gloves, apron, vapor respirator	Oxidizer, corrosive
Isopropanol	7	e	0	Splash goggles, gloves, apron, vapor respirator	Flammable
Parylene-C Dimer (DPX-C)	N/A	N/A	N/A	Splash goggles, gloves, dust respirator	No HMIS information found, classified as not dangerous
Phosphate Buffered Saline	1	0	0	Splash goggles, gloves	N/A
Silane A-174	N/A	N/A	N/A	Splash goggles, gloves, apron, vapor respirator	No HMIS information found, classified as skin sensitizer
Sulfuric Acid, 96%	3	0	2	Splash goggles, face shield, gloves, apron, vapor respirator	Corrosive

Table 4. List of chemicals used in this study (continued)

5 RESULTS

The percentage connectivity was measured for 21 different arrays. The raw data, including the total number of electrodes, number of connected electrodes, and the calculated percentage connectivity for each array, can be found in Appendix A. While all the electrodes demonstrated some percentage of connectivity, the mean connectivity of the entire sample (n = 21) was 71.64% with a standard deviation of 16.72. The median connectivity of the sample was 68.98%, seated between a minimum of 33.2% and a maximum of 97.92% connectivity. The arrays with lower conductivity tended to be ones that were built earlier in time, and the process was not yet as well developed, resulting in less ideal arrays with greater angular deviation on the proximal end. Because the mean and median were close, a normal distribution could be assumed. Graphing the percentage connectivity of every array built during this project on a normal probability plot in Figure 14 demonstrated that the data followed the theoretical normal distribution line relatively closely, meaning the sample approximately followed a normal distribution. The similarity between the mean and median further supported the assumption that the sample was normal.

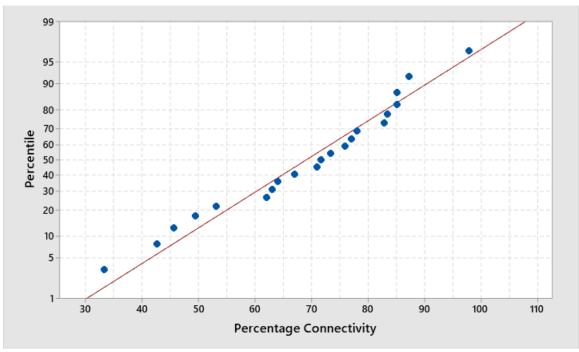


Figure 14. Normal probability plot of percentage connectivity of all microwire arrays. 5.1 Array Diameter

5.1.1 Parametric Tests

Bundles with diameters of 3 mm and 12 mm were bonded to evaluate the effect of increasing bundle size on connectivity. The 3 mm bundles (n = 10) had an average connectivity of 63.08%, represented by the white triangle outlined in black on the box-and-whiskers plot to the left in Figure 15, with a standard deviation of 16.46. The values ranged from a minimum of 33.20% to a maximum of 87.30%. Meanwhile, the 12 mm bundles (n = 11) had an average of 74.34%, represented by a white triangle outlined in black on the box-and-whiskers plot to the right, connectivity with a standard deviation of 15.77. The values ranged from a minimum of 42.65% to a maximum of 97.92%. Figure 15 demonstrates that the connectivity range of the two samples appeared similar. However, it appeared that the larger bundle, with a 12 mm diameter, had a greater mean

and median percentage connectivity than the 3 mm diameter bundle. This was an unexpected result, since arrays with larger diameters were more prone to misalignment or angular deviations which may have caused fewer electrodes to connect. However, taking care to do all processing steps well minimized the possibility of defects that decreased connectivity. A 95% confidence interval for the mean connectivity was determined to be between 52.42% and 73.73% for 3 mm arrays and 62.18% and 84.50% for 12 mm diameter bundles.

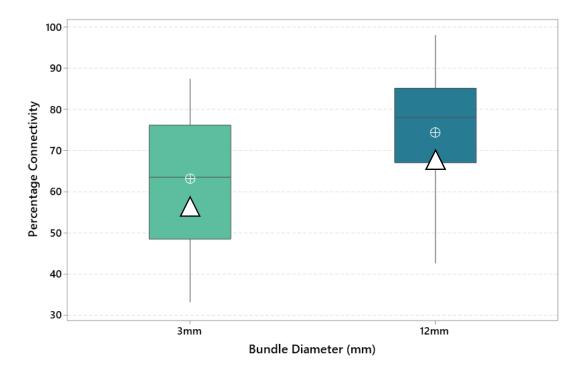


Figure 15. Box and whiskers plot showing the median, quartiles, and range of percentage connectivity of 3 mm and 12 mm diameter arrays.

To compare the two groups by testing the null hypothesis of similar average between two samples, one-way analysis of variance (ANOVA) with $\alpha = 0.05$ was performed on the samples with MiniTab 19. The results reveal an F-value of 2.85 (P < 0.126), as shown in Table 5, resulting in a failure to reject the null hypothesis; the means do not differ.

Table 5. Results of a one-way Analysis of Variance (ANOVA)

Source	DF	Adj SS	Adj MS	F-Value	P-Value
Factor	1	665.7	664.7	2.56	0.126
Error	19	4924.2	259.2		
Total	20	5588.9			

Note: For 3 mm diameter and 12 mm diameter arrays

5.1.2 Non-parametric Tests

For these two samples, normality was assumed because of similar means and medians; however, in the event that the population is actually not normal, non-parametric tests were utilized to evaluate these samples. Percentile bootstrapping involves repeatedly sampling from the experimental data at random with replacement to construct surrogate datasets with the same sample sizes that allow for statistical inferences to be made ⁽⁴¹⁾. Because the sample sizes were small and unequal, this method was particularly helpful. The connectivity data for 3 mm and 12 mm diameter bundles were randomly sampled with replacements to simulate a new data set from the pre-existing one, and the means of each new surrogate dataset was calculated. The difference between the new mean connectivity of the 3 mm and 12 mm samples were found and recorded. This process was completed 10,000 times in MATLAB to estimate the expected difference in each measurement and allowed for an even distribution of 3 mm and 12 mm diameter bundles and recorded in Figure 16. Based on these data, a 95% confidence interval was calculated and shown to bound between the red lines indicated as 5% CI. The null hypothesis, H_0 mean and indicated in blue in Figure 16, of no difference in means

between the two samples can be rejected if the observed difference fell outside the 2.5% to 97.5% percentile range of the bootstrapped distribution. However, as shown in Figure 16, this was not the case, since the null hypothesis fell within the 95% confidence interval indicating that there is no statistical difference at $\alpha = 0.05$ between the two means. Hence, the hypothesis that the two samples have the same population mean cannot be rejected, even using non-parametric statistical analysis.

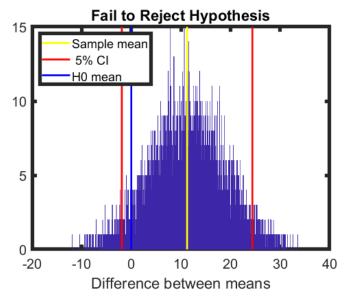
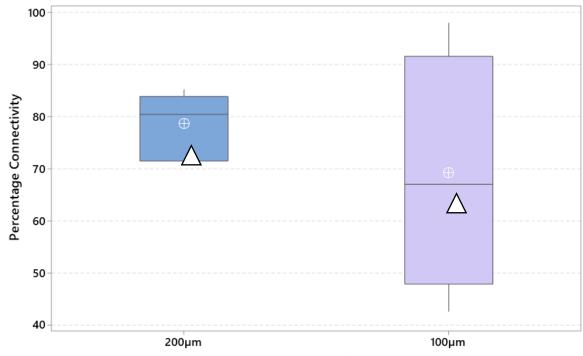


Figure 16. Percentile bootstrapping of the difference between means of 3 mm and 12 mm diameter bundles.

5.2 Electrode Density

5.2.1 Parametric Tests

The effect of electrode density on the connectivity of a microwire array was evaluated by comparing how well 12 mm diameter bundles with 200 μ m and 100 μ m wire-to-wire pitch connected to the CMOS chip. The 200 μ m pitch bundles (n = 6), with connectivity values ranging from 70.92% to 85.12%, demonstrated an average of 78.65% connectivity and a standard deviation of 6.19%. The 100 μ m pitch bundles (n = 5) had a 64.92% connectivity on average, ranging from a minimum of 42.65% to a maximum of 97.92%, with a standard deviation of 22.64%. Figure 16 shows a vast difference in range between the two samples; while the sample of 200 μ m pitch arrays have a range of 14.2%, the difference between the greatest and least connected of 100 μ m pitch arrays was 55.27%. This large difference in variation was also reflected in the difference between the standard deviations of the two samples. The means of the two samples were also different at $\alpha = 0.05$. Figure 17 indicates that the 200 μ m pitch arrays had a higher average connectivity than the 100 μ m pitch arrays. The 95% confidence interval of the mean connectivity was also calculated to be between 64.08% and 93.23% for the 200 μ m pitch arrays and between 52.3% and 85.1% for the 100 μ m pitch bundles.



Wire-to-wire Pitch (µm)

Figure 17. Box and whiskers plot showing the median, quartiles, and range of percentage connectivity of 200 μ m and 100 μ m pitch arrays.

Similarly, ANOVA with $\alpha = 0.05$ was also performed on these samples and are displayed in Table 6. The results show a F-value of 0.99 (P < 0.347), which is smaller than the F-critical value of 6.26. This indicates a failure to reject the null hypothesis of dissimilar population means. However, the small sample sizes render it difficult to draw more reliable conclusions.

Table 6. Results of a one-way Analysis of Variance (ANOVA)

Source	DF	Adj SS	Adj MS	F-Value	P-Value
Factor	1	245.4	245.4	0.99	0.347
Error	9	2241.3	249.0		
Total	10	2486.7			

Note: At $\alpha = 0.05$ for 200 µm and 100 µm pitch arrays

5.2.2 Non-parametric Tests

Because the samples evaluating the effects of density on connectivity were also very small, non-parametric tests were similarly used for analysis. Like the sample sizes used to evaluate diameter, the sample sizes for density were small and unequal, and percentile bootstrapping simulated repeated experiments 10,000 times in MATLAB to estimate the expected difference in each measurement and compensated for differences in sample size between 200 μ m and 100 μ m pitch arrays. The observed difference did not fall outside the 95% confidence interval, as indicated by Figure 18, failing to reject the null hypothesis that the two samples do not have different means.

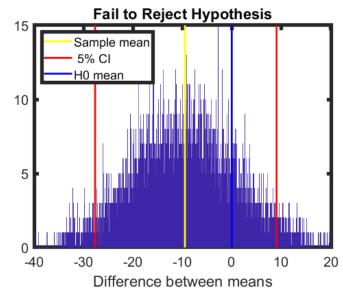


Figure 18. Percentile bootstrapping of the difference between means of 200 μm and 100 μm pitch arrays.

6 DISCUSSION

6.1 Array Diameter

The scalability of arrays by increasing bundle diameter was evaluated by analyzing the connectivity of 3 mm and 12 mm diameter bundles to a CMOS sensor. While the average connectivity and range of responses for 12 mm bundles appear slightly greater than the average connectivity and responses for 3 mm arrays, ANOVA indicated that there was no statistical difference between the means of the two samples. Similarly, the observed difference in the bootstrapped test did not fall outside the confidence interval either. Both parametric and non-parametric tests failed to reject the null hypothesis that the two populations have the same mean percentage connectivity, indicating that there was no statistically significant difference between the average connectivity of the 3 mm diameter and 12 mm diameter arrays.

Increasing the array diameter does not come without its issues. A minor angular deviation on the proximal end of the array that otherwise would not be apparent in a 3 mm array with a smaller radius would be magnified in a 12 mm array with a larger radius due to amplification of angular deviation across a larger surface area. For example, a one degree deviation across a 3 mm diameter array from end to end would result in a 52 μ m height difference across the surface, while the same angular deviation across a 12 mm diameter array from end to end would result in a 52 μ m nonplanarity negatively affects connectivity, as the electrodes would not properly interface with the CMOS chip if some parts of the bundle do not make contact with the pixels. Often, this is visible in larger arrays when a crescent moon-shaped shadow of no

connection appears along the outer perimeter of the connectivity map due to a bevel on the bundle. Any deviation and misalignment of the array on the CMOS sensor would also result in poor connectivity, particularly in larger electrode bundles with diameters close to the dimensions of the chip. With smaller diameter arrays, misalignment would likely play a far smaller role in connectivity issues, because the surface area of the proximal end of the array is much smaller than the surface area of the CMOS array. Minor deviations of even several hundred microns, in centering the array on the CMOS sensor would not be detrimental to the connectivity, since the surface area of the CMOS array was larger and could compensate for any slop. Bundles with diameters close to the maximum dimensions of the CMOS sensor must be aligned carefully such that the electrode array was perfectly centered on the chip; otherwise microwires on the periphery of the bundle may not make contact with the pixels on the sensor.

Another factor that may play a role in connectivity is the pressing force of the array onto the CMOS chip. Larger arrays have a greater number of wires and surface area to contact with the pixels, requiring a greater amount of force to fully connect the entire bundle. Optimal connectivity was achieved by securing the bundle onto the CMOS chip when it appears that most, if not all, of the array is within contact with the pixels. This technique allows for force required to connect each array to be tailored to that bundle and varies from array to array, causing differences in percentage connectivity.

6.2 Electrode Density

Microwire electrode array scalability was also examined by varying electrode density. Decreasing the wire-to-wire distance increased the array density. The 200 µm pitch

bundles had a higher average percentage connectivity than 100 μ m pitch arrays. The 100 μ m pitch arrays have a substantially higher variance and range of responses than the less dense bundles. This indicates that while 100 μ m pitch arrays demonstrate high percentage connectivity, the 200 μ m pitch bundles are more consistent. However, the failure to reject the null hypothesis that the two populations have the same mean percentage connectivity, despite these differences, demonstrating that decreasing from 200 μ m and 100 μ m pitch show no statistically significant change in average connectivity.

Electrode density also affects the pressing force required to connect the array to the CMOS. More wires require a greater amount of force to connect the array onto the pixels, as previously discussed regarding increasing array size. Bonding force is affected by electrode density as well, as decreasing the wire-to-wire distance increases the number of wires within a bundle. As mentioned before, the custom interfacing allows the amount of force required to be individual to each bundle, causing differences in percentage connectivity depending on how much force is required.

6.3 Other Limitations

Other possible variables that were not considered within this study include the human factors. Each of these bundles were assembled by hand, resulting in the possibility of differences between each array. As more arrays were made, the expertise in handling thin microwires and delicate electronics improved, simultaneously improving the quality of the bundles and the connectivity. It would be difficult to quantify and evaluate how strong of a role human factors played into creating arrays with greater

connectivity, including how chronology of assembly of the arrays in these samples affected the percentage connectivity may be interesting to examine in another study.

7 RECOMMENDATIONS FOR FUTURE RESEARCH

Due to the limitations of this project, future research is recommended for more conclusive results. To obtain statistically significant results, the experimental set up needs to be well-defined and more intentional. This chapter outlines recommendations for a more robust experimental design and analysis that would yield more meaningful conclusions. The recommended set up would be important for optimizing the process for mass-producing commercial electrode arrays by understanding the limitations of the manufacturing process.

7.1 Design of Experiments

7.1.1 Research Question and Hypothesis

The results of this project failed to disprove the null hypothesis, forcing its acceptance. This did not allow strong, meaningful conclusions to be drawn. Should the size of the effect be too small to statistically discern, increasing sample sizes in further studies or decreasing the significance level of the statistical analysis will yield more conclusive results.

A strong design of experiments first requires a clearly defined research question, null, and alternative hypotheses. The goal of this project is to analyze if array diameter and array density affect the percentage connectivity of the array when pressed on a CMOS chip. If so, how much do the factors and their interaction affect the response? In this case, the response variable in question is the percentage connectivity, while the factors are the array diameter and array density. The null hypothesis is that the array diameter and density, along with their interaction (array diameter*array density), will not affect the

percentage connectivity. The alternate hypothesis will be that the array diameter and density, along with their interaction (array diameter*array density) will affect the percentage connectivity when pressed onto a CMOS chip. While the hypothesis is similar to that of the previous study, the results may be strengthened by increasing the sample size to produce results of a higher power, making inconclusive assessments less likely.

7.1.2 Experimental Design

The two factors in question include array diameter (A) and array density (B), and each factor needs to be evaluated at low and high levels. Microwire electrode array diameters should be 3 mm and 12 mm. Arrays should also be made in low density (200 μ m pitch) and high density (100 μ m pitch). This information is summarized in Table 7. Table 7. Summary of Factors and Their Levels

Factor	Letter Corresponding to Factor	Low Level	High Level
Diameter	А	3mm	12mm
Density	В	200µm Pitch	100µm Pitch

A 2x2 factorial experiment should be used to test how the main effects and interaction affect response variable percentage connectivity. The main effects are diameter (A) and density (B). The two-way interaction between high levels of A*B should also be tested. Lastly, the low level of the two factors should be tested as well. Altogether, there should be 4 different groups of conditions to be evaluated within this experiment with 2 levels for each factor, or 1 degree of freedom for each factor and their interaction. Assuming a power of 80%, statistical significance of $\alpha = 0.05$, and an assumed medium effect size of 0.25, a calculated total sample size of 128, as calculated with G*Power, is needed to produce meaningful results ^(42,43). This will mean each set of conditions would require 32 iterations. In an optimal project, this sample size would generate strong results, but such a costly project would likely not be feasible from a business perspective unless these arrays were intended to enter production for commercial purposes. The arrays in this project were primarily built as a part of a proof-of-concept for a human implantable device and were not meant to be mass produced, but a similar study may be done to optimize specifications for a future commercial device.

To eliminate chronology from affecting the response, the run order of the experiments must be randomized. To create a completely randomized order of which these arrays must be fabricated and evaluated, the number of factors and replicates should be programmed into MiniTab to build a randomized final design, including the order of which the responses should be recorded. Blocking can be done to ensure that extraneous factors do not affect the response variable, but because each array is fabricated independently, it is unnecessary to build blocks and should be run as a full factorial design.

7.2 Statistical Analysis

7.2.1 Two-way Analysis of Variance

Results should be analyzed in Minitab to obtain a table of coded coefficients, where preliminary significance and effectiveness can be analyzed. True significance should be later evaluated via two-way ANOVA and the normal probability table. Minitab

calculates the effects of each factor and interaction and provides graphs to help visualize the effects between low and high levels of each factor.

While these graphs help describe how different factors may alter the response, it is also critical to evaluate the significance of these factors and interactions to understand if they truly matter. Two-way ANOVA should be done in Minitab. P-values, or probability values, are calculated by obtaining the sum of squares. Mean squares is calculated through the sum of squares. F-values should be calculated through mean square values. The P-value determines whether these factors or interactions will play a role in affecting the percentage connectivity at a specific significance level. In this case, $\alpha = 0.05$, meaning a P-value below 0.05 will be deemed significant, and would be sufficient for drawing strong statistical conclusions.

7.2.2 Regression Model

A preliminary regression model, taking the coefficients calculated in the effects table before, can be built. An intercept can be calculated as a mean of all the observations. The data can then be evaluated against this model for an R^2 value, an indicator of how much variability exists within the sample. Ideally, a model that most accurately represents a sample has an R^2 value close to 100%.

7.2.3 Error Analysis

The observations must be evaluated for any outliers within the sample as well as any trends due to experimental chronology. A normal probability plot of all the observations should be plotted against their residuals to observe any major outliers. If not, normality in the errors can also be assumed for the samples. Alternatively, the data can be evaluated through a histogram to visualize whether the sample follows roughly a normal distribution. This will also make outliers visible within the distribution. To evaluate whether chronology plays a role in the response, the residuals of the responses can be plotted in order of observation. Ideally, no visible patterns should be observed within this test, demonstrating that timing and experimental order did not affect the percentage connectivity.

7.3 Summary

Further studies may choose to focus on process optimization to produce the most value from these costly studies from a manufacturing standpoint. Understanding whether factors, such as array and diameter, affect percentage connectivity is important, but it would be more valuable to understand how these factors affect connectivity and whether the interactions do as well. Two-way Analysis of Variance enables this by comparing the results of low and high levels of each factor to show possible trends and interactions between these factors. Further improvements for these studies involve intentional randomization of experimental order to reduce biases and see if chronological trends in results exist. Should the results of future studies be inconclusive, the sample sizes may need adjustment to provide a study with more statistical power. The pre-assigned low and high levels of the factors may also need reevaluation, since the effects may be too small to yield meaningful results. These studies are critical, particularly when scaling up for producing mass-manufactured, commercialized medical devices for understanding process limitations and capabilities.

8 CONCLUSION

Intracortical neuron recordings are invaluable due to their high temporal resolution. Microwire electrode arrays, when paired with a CMOS amplifier array such as within the Argo System, are a scalable, cost-efficient modality for detecting neural signals. Microwire arrays can be scaled up by increasing the diameter and decreasing the pitch between the wires, because there is no significant difference in extent of connectivity between bundles of different sizes and densities. By quantifying this approach toward creating intracortical neuron recordings, new arrays with greater recording capabilities can be produced. Future experiments exploring novel materials and new fabrication schemes for bundled arrays can improve upon the current process. Improvements to build high-bandwidth neural interfaces open new doors to allow patients with significant mobility disorders and sensory impairment to regain their independence by communicating directly with a computer and the outside world.

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Diameter (mm)	Pitch (µm)	Total Microwires	Connected Microwires	Percentage Connectivity
		Micrownes	Where which	(%)
3	200	274	211	77.01
3	200	251	184	73.31
3	200	253	162	64.03
3	200	253	125	49.41
3	200	189	165	87.30
3	200	253	157	62.06
3	200	253	192	75.89
3	200	253	84	33.20
3	100	700	441	63.00
3	100	700	319	45.57
12	200	2500	1773	70.92
12	200	2500	2128	85.12
12	200	2500	2085	83.40
12	200	2500	2071	82.84
12	200	2500	1950	78.00
12	200	2500	1791	71.64
12	100	10000	9792	97.92
12	100	10000	5307	53.07
12	100	10000	6702	67.02
12	100	10000	8518	85.18
12	100	10000	4265	42.65

APPENDIX A: RAW PERCENTAGE CONNECTIVITY DATA