

Conversion Bridge of Sony SubLvds to MIPI CSI-2

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Affiliations
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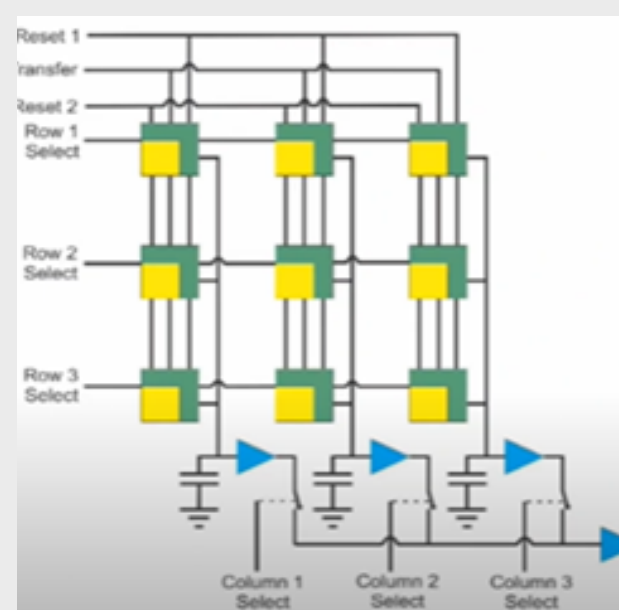
01 Objective

Create a circuit board design to convert Sony Sublvds output data into MIPI CSI-2 protocol

02 Initial steps

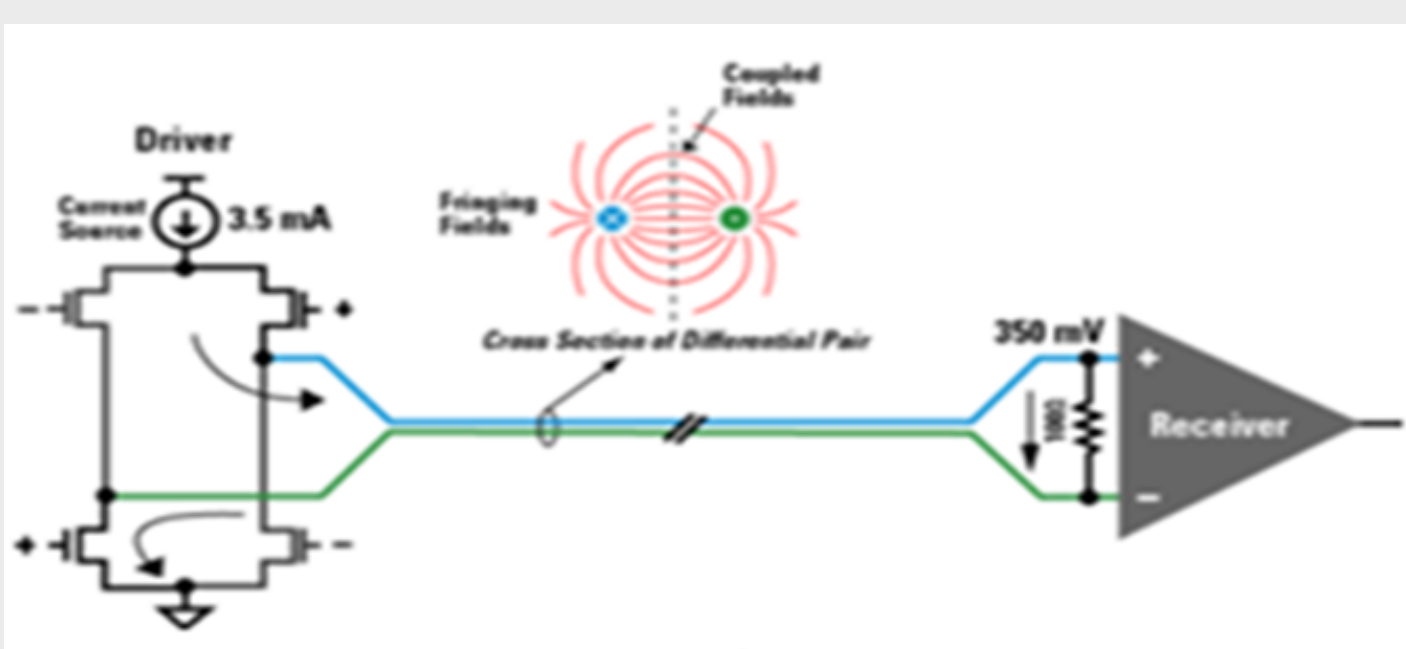
CMOS Image sensor

- Uses Mosfets to convert accumulated charge across photodiode into a voltage signal which can be used
- The voltage signal which corresponds to each pixel is called row by row
- There are two main types: Rolling shutter global shutter



SyBLvds

- Tight electromagnetic coupling between the wires, which reduces the generation of EM noise
- Reduces the vulnerability to EM noise
- Differential Voltage

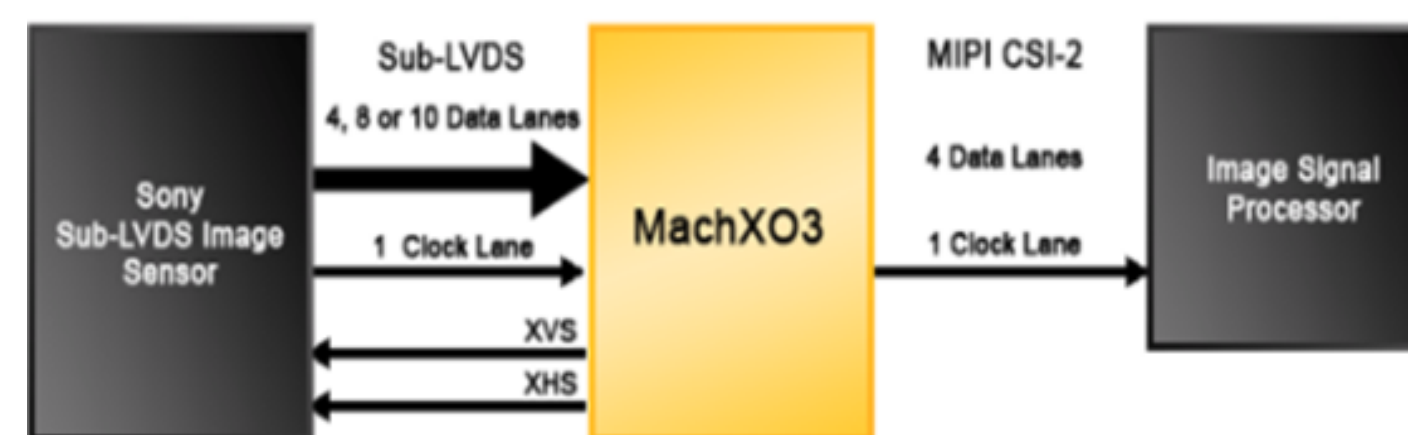


03 Available Technology

Few options available in choosing the proper fpga to complete the task. Some of them included the Trion T120 from Efinix, MachX02, MachX03, and Crosslink from Lattice Semiconductors.

The MachX02 was selected mainly because of the number of resources and documentation

provided by the company, along with a reference design that can be used to simulate the conversion between the two protocols

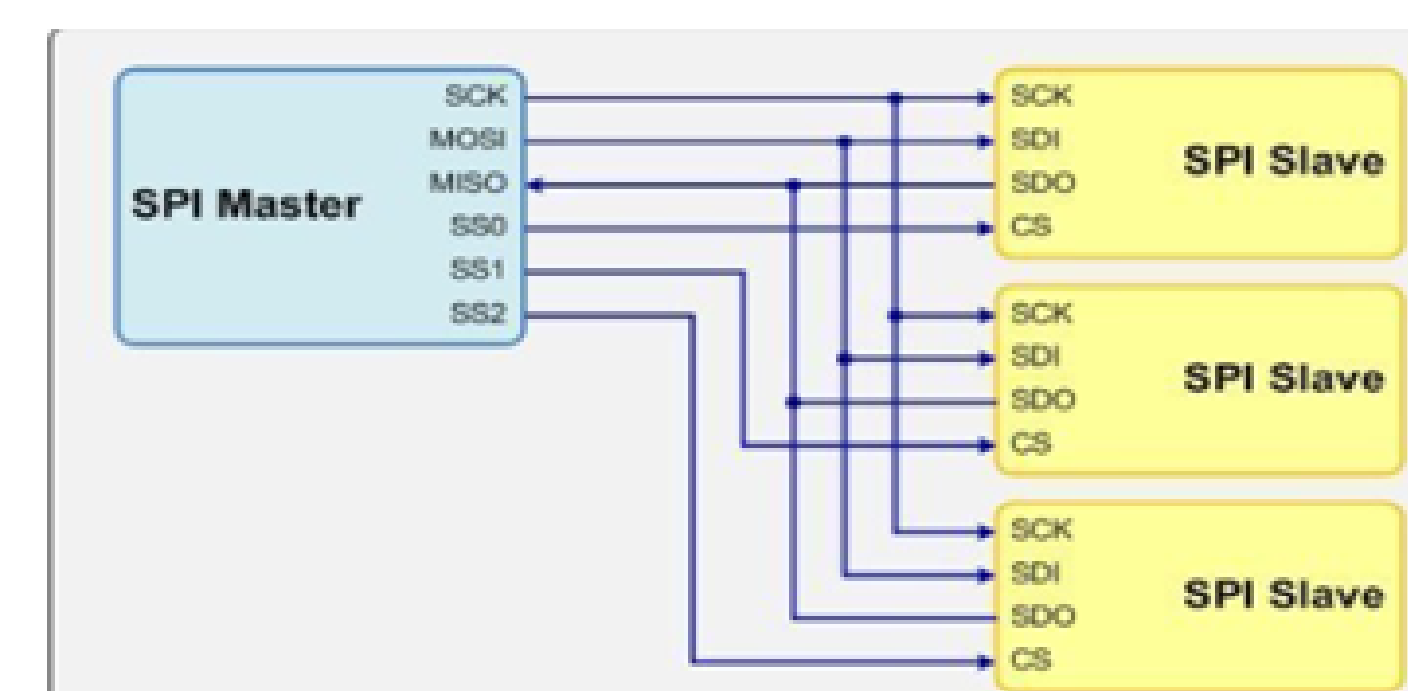
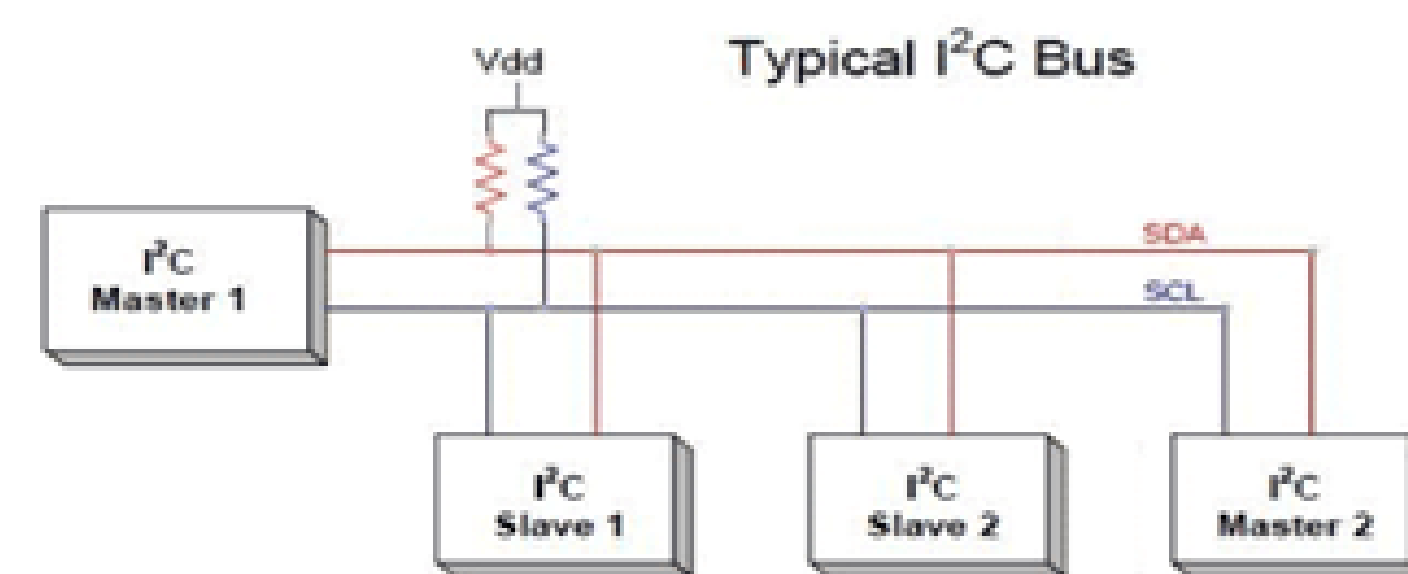


04 Peripheral Components

Some other components were needed in addition to the fpga such as: an oscillator, a clock buffer, an SPI to I2C, and voltage level translators

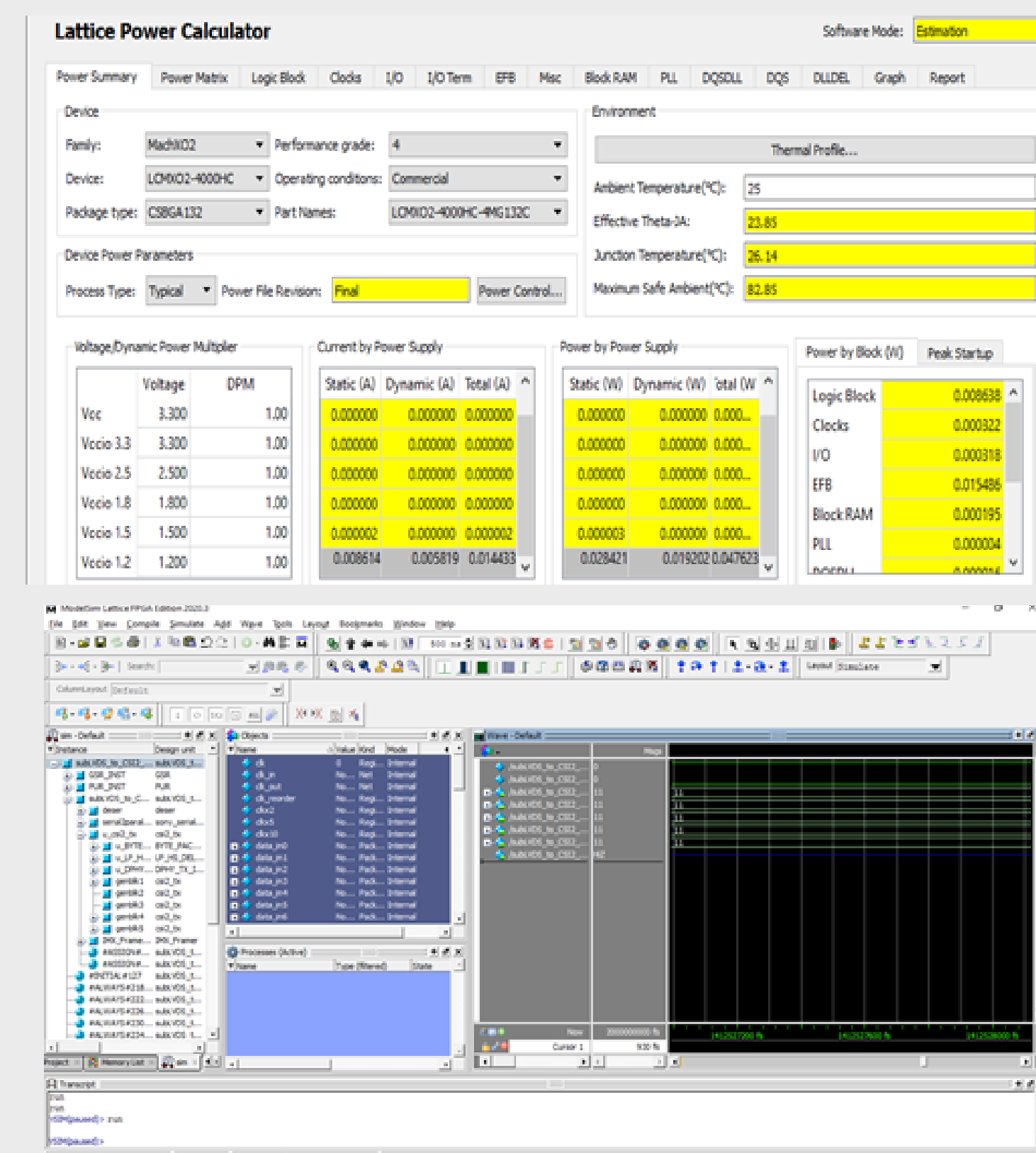
Learned the difference between SPI and I2C protocols, their advantages, and disadvantages

Created schematics and footprints for resistors and capacitors on Altium which were to be used for the peripheral components, the Sony image sensor and the fpga.



05 Implementation

- The MachX02 is programmed by using the Lattice Diamond software
- The software has timing analysis and power analysis which could be used
- I initially ran into a few problems
- The first problem was a licensing issue which took some time to be resolved
- The second issue was regarding the reference design which was provided by Lattice
- The code had multiple errors which Lattice needed to be contacted before they were resolved
- A documentation was also provided which was supposed to outline how to the diamond software should be used to run simulations for the reference design provided. This was however not the case
- Power analysis was able to provide an estimation regarding the power consumption for the given mode of operation



References (Images)

- <https://www.latticesemi.com/en/Products/DesignSoftwareAndIP/IntellectualProperty/ReferenceDesigns/ReferenceDesign04/SonySubLVDStoMIPICSI2SensorBridge?fbclid=IwAR0z4LotIIBIOTMGNpwZPjyW4KvWuTWYERsopPILodQwv93UDEN-p6QmWw>
- https://en.wikipedia.org/wiki/Low-voltage_differential_signaling?fbclid=IwAR27tBBA5mjYDZUQ7SdZdIXeeCYIb6QxYqYmF6aWBBvRVVHons-0Jx_SRO
- https://en.wikipedia.org/wiki/Serial_Peripheral_Interface?fbclid=IwARORQIKTIXJab1XBm5k7Wlp0_qz37kWhUU_w0uny7nNFXV0qZ2HjrWIOcdk
- https://www.cypress.com/documentation/application-notes/an50987-getting-started-i2c-psoc-1?fbclid=IwAR1mWaXeDKc2HhYh_d9u0UjRIGIQTZR6VkhWsmDIJkDJJ3wzwwB_FLc_j0
- https://l.facebook.com/l.php?u=https%3A%2F%2Fwww.youtube.com%2Fwatch%3Fv%3Dvpybs-LxmyA%26fbclid%3DIwAR2DvcqIFFzPvt-U7ahHN8_63D-f82jForwIDdpE5Zm2qSsZBxbkhjncYg&h-AT3MQ7TSPriUj6ySfrVg6B3xUOE5P5W27Tb1RfWSik0bLOHL2Cw3bZIKu2DgP4RreZvoIF-O2jBSWDP2CzXgzZD77zOCRNNocKIEf7HAPSQKnlF82ac8UzquGdE9vrmIKw