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# Wild Goats Optimization Approach for Capacitor Placement Problem

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**Abstract:** This paper deals with Capacitor Placement (CP) issue. The topic is an optimization problem including two types of variables: capacitor location as an integer variable, capacitor size as a continuous one. To cope with this problem, a new approach entitled Wild Goats Algorithm (WGA) is used. WGA is a new heuristic approach which has been proved recently. In this paper, WGA is successfully implemented to the CP problem with the objective of total loss reduction. Power flow criteria as well as operation constraints are all together accommodated in the process of optimization. Two various scenarios at three load levels are also recognized to cover all possible conditions. The validity of the WGA approach in handling CP problem is assured by testifying it on IEEE 33-bus and 69-bus test systems.

Keywords: Wild goats algorithm, capacitor placement, optimization problem

# 1. Introduction

Compensation capacitors have been usually utilized to manage reactive power return in radial/mesh distribution systems as well as diminishing power losses and enhancing network voltage profile [1-3]. The benefits of capacitor strongly depend on the optimal placing and sizing. Radial operating structure of network is established in order to decrease the losses of the system while meeting the system operating constrictions. Maximizing these advantages could be fulfilled by making use of capacitors. Non-linearity and expansive size of the capacitor placement issue make the global optimum result as well troublesome to find. Therefore because of the great capability of evolutionary methods to manage a huge number of variables without any reduction, they are so suitable applications on expansive optimization issues which have been utilized within the CP, too. Utilization of these methods can be so useful in gaining better optimum points but maybe assurance of global optimum point cannot be ensured by these strategies. Several algorithms have been introduced in order to solve the CP problem. In [4], Simulated Annealing-like, modified PSO (SA-PSO) algorithm has been proposed to solve the capacitor placement problem. A computer software has been developed in [5] for resolving the optimum size and location of a shunt capacitor. In [6], interactive bi-objective programming with the precious trade-off method has been used to solve the scheduling and capacitor placement problem. Limaçon inspired spider monkey optimization approach has been utilized to find the near-optimal sie and location of capacitor in [7]. VLCI approach based on hybrid PSOGSA optimization method has been displayed to solve the CP problem in [8]. In [9], ant colony algorithm has been employed to solve the CP problem in which at first by sensitivity analysis the candidate points are defined and after that optimal size and location of capacitors are achieved by applying the ant colony algorithm. An accelerated particle swarm optimization approach has been utilized in [10] to allocate optimal location of capacitors along radial distribution. In [11] for finding the optimal size and location of the capacitors in a distribution network, a newly generated algorithm, called crow search algorithm (CSA), is introduced. A novel hybrid algorithm has been introduced for optimal CP problem in [12].

Furthermore [13] is presented teaching learning based optimization algorithm (TLBO) to solve optimal capacitor placement problem. In [13] CP problem is considered with two various objectives of minimizing total cost and power loss. Some other works have considered extra constraints or objectives such as voltage improvement and energy loss reduction besides the CP issues principal problems. In [14] bacteria foraging (BF) algorithm has been used as an optimization device for solving the CP problem which considered peak power cost decreasing, voltage improvement and energy loss reduction as the objective functions. To solve the CP problem that includes resonance constraints the authors have introduced an extremal optimization method in [15]. In [16] the holy genetic algorithm is introduced for simultaneous fixed/switched capacitor banks and allocation of DG units and network reconfiguration in distribution systems considering various load levels. In recent years, power system utilizations have advanced from a recent and prevailing optimization algorithm known as Wild Goats Algorithm (WGA). WGA has been recently presented in [17]. In Table 1 the WGA algorithm is compared with plenty of popular heuristic methods in terms of fundamental directors of evolutionary algorithms.

Algorithm	Table 1- comparis           Swarm Intelligence	son of evolutionary algorith Searching In Groups	Memory based	Mutation
PSO	Yes	No	Yes	No
ICA	No	Yes	No	No
GA	No	No	No	Yes
WGA	Yes	Yes	Yes	Yes

According to the table I, the WGA algorithm is a completely general method. This paper considered WGA Method in order to solve power system CP problems. The CP is a nonlinear, non-convex optimization problem. Every well-known heuristic method has some individual features. Meanwhile, some of these predominant features can be categorized as: mutation, swarm intelligence, memory based, and searching in groups. Mutation is the individual feature of GA which also considered in WGA. PSO algorithm is firmly based on Swarm intelligence and also it's a perfectly memory based algorithm, both are considered in WGA. Searching in groups is another challenging and perfectly effective feature which is considered in ICA, this also considered in WGA. Also complete and well-sorted detail about WGA would found in [17]. As it obvious, WGA is a really powerful algorithm and this is the acceptable reason to consider this algorithm for complex non-linear and non-convex problems like CP.

The principal purpose of this paper is the whole active and reactive losses minimization of distribution system. The suggested method is testified on well-known 33-bus and 69-bus test systems. Simulation studies are carried out for two different scenarios in three load levels. Based on the comparison studies and simulations, the performance of the suggested approach could be concluded.

The residue of the paper is formed as follows: in Section 2, the CP problem and WGA formulation is discussed. In section 3 simulation studies are represented. Eventually, section 4 gathers the contributions and findings of the paper.

## 2. Problem formulation

## 2.1 Capacitor placement

Capacitor location and its rating is a vital factor which affects distribution system losses. Eventually, optimal capacitor sizing and siting from planning point of view could help minimizing system total losses. In this paper, CP issue is defined as a mathematical model to minimize total active and reactive losses of distribution system. Therefore, the problem could be expressed as follows:

$$Min: \ Z = \omega_1 \sum_{i=1}^{L} r_i I_i^2 + \omega_2 \sum_{i=1}^{L} x_i I_i^2 \tag{1}$$

where, Z is the objective function and L is the total number of branches.  $r_i$ ,  $x_i$  and  $I_i$  are the resistance, reactance and current of i<sup>th</sup> branch, respectively.  $\omega_1$  and  $\omega_2$  are the weighting coefficients of active and reactive power losses, respectively. It should be noted that the power flow equations should be considered as constraints. Also, the operational constraints should be satisfied, as following:

$$V_n^{\min} < V_n < V_n^{\max} \tag{2}$$

$$S_i \left| < S_i^{\max} \right| \tag{3}$$

where,  $V_n$  is voltage magnitude of nth bus and  $S_i$  is apparent power of i<sup>th</sup> branch.  $V_n^{\text{max}}$  and  $V_n^{\text{min}}$  are the upper and lower limits of voltage magnitude. Finally,  $S_i^{\text{max}}$  is upper limit of apparent power flow in i<sup>th</sup> branch. Moreover, capacitor rating constraint should be considered as follows:

$$Q_{C,i}^{\min} < Q_{C,i} < Q_{C,i}^{\max} \tag{4}$$

where,  $Q_{C,i}$  is the scheduled active power for i<sup>th</sup> capacitor.  $Q_{C,i}^{\text{max}}$  and  $Q_{C,i}^{\text{min}}$  are the upper and lower borders of active power for i<sup>th</sup> capacitor.

In this work, two variables are regarded as the optimization variables:

- The location of capacitor units as integer variable.
- Rating of capacitor units as continuous variable.

#### 2.2 Wild goats algorithm

Like other evolutionary algorithms, WGA have a set of variables which initializes all population members. wg is a solution for the problem and related to each wild goat. Consider *N* to be the dimension of the problem. Then:

$$wg_i = \begin{bmatrix} x_{i,1}, \dots, x_{i,N} \end{bmatrix}$$
(5)

Also,  $N_{wg}$  is the number of WGs.

After initializing the population members, f should evaluate. It supposes that f is the objective function of the problem. Wild goats evaluate respect to their objective function.

$$f(wg_i) = f\left(\left[x_{i,1}, \dots, x_{i,N}\right]\right)$$
(6)

From the objective function, the results have a big variation range and they aren't a proper tool for comparing wgs' values with each other. Therefore, here *W* is defined as a weight for each wg in order to introduce the quality of each wg.

$$W_{i} = exp\left(-N\frac{f\left(wg_{i}\right) - min_{j}\left\{f\left(wg_{i}\right)\right\}}{\sum_{j=1}^{N_{wg}}\left(f\left(wg_{i}\right) - min_{j}\left\{f\left(wg_{i}\right)\right\}\right)}\right)$$
(7)

W is the weight of each wg and the range of w is between 0 and 1. So, the value of optimal wg is equal to unit and it has the optimal value of the objective function.

wgs movements are highly dependent on three components: 1) its movement vector 2) its best possible, 3) number of followers of its group.

The current position of each wg at the iteration t+1 can be obtained from bellows formula:

$$wg_{i}(t+1) = wg_{i}(t) + v_{i}(t+1)$$
 (8)

As it obvious from the (8), position of every wg is directly depends on its position from last iteration (t) and its movement vector at the current iteration (t+1). The position of each wg updates every iteration by adopting its movement vector and past position in the search space.

The groups of wild goats can see the other groups of WGs, exactly when they climbing of the mountains. This time they share the experiments with others and the group with higher weight could attract the other groups' followers.

$$W_{G_{i}}(t) = \frac{W_{l_{i}}(t) - \sum_{j=1}^{N_{G_{i}}} W_{j}(t)}{1 + N_{G_{i}}}$$
(9)

Weight of the group leaders is  $W_{l_i}(t)$ , and  $W_{G_i}(t)$  is the weight of the i<sup>th</sup> group.

#### 3. Simulation studies

The simulation researches are performed by employing the WGA algorithm on the 33 and 69-node distribution standard networks. This study is implemented on MATLAB 7.6 in 2.5 GHz, i7, personal computer. In this paper, the values of weighting coefficients are assumed to be 1. The threshold amount of power flow study is considered to be 0.005. Moreover, the group size is 25 and the highest quantity of iterations is set to 200. The lower and upper voltage limits are fixed as Vmin = 0.9 p.u and Vmax = 1 p.u. For the given systems the maximum number of placed capacitors is confined to three units. In order to confirm the efficiency of the WGA approach, two various scenarios at three different load levels have been envisaged. The load levels include: 1) 0.5 (light), 2) 1.0 (nominal), and 3) 1.6 (heavy) p.u.

The first scenario studies the base case, in which no capacitor installation has been performed. The second scenario investigates capacitor problem.

#### 3.1 33-bus test system

The system information of 33-bus test system are adopted from [18]. The single line diagram associated with this system is depicted in Fig. 1 which includes five tie lines and 32 sectionalizing switches.

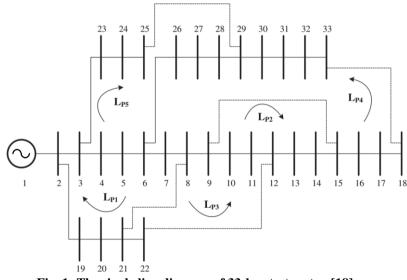


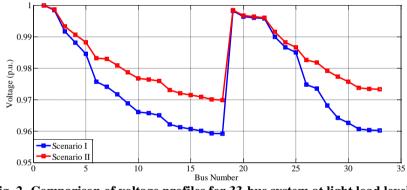
Fig. 1- The single line diagram of 33-bus test system[18]

The whole reactive and real power loads of the network are 2.3 MVAr and 3.72MW, respectively. Moreover, the initial total active loss of the system is 202.6 kW. The numerical outcomes of implementing the WGA algorithm at three various load levels are exhibited in Table 2. According to Table 2, for all the three load levels, active and reactive power loss minimization in the second scenario is highest, which obtains the efficiency of allocating capacitors. In addition, the lowest voltage amplitude for both scenarios are presented in Table 1. Referring to Table 1, it could be perceived that the minimum voltage magnitude of the network has been improved in the second scenario and for all load conditions in comparison with the base case. According to Table 1, the total power losses in scenarios II, in comparison with the base case, at light, normal and heavy load conditions have been decreased about 35.03, 34.2 and 45.6 percentages,

Scenario Item		Load Level					
		Light (0.5)		Normal (1)		Heavy (1.6)	
	Open switches	33-34-35-36-37		33-34-35-36-37		33-34-35-36-37	
Base Case Scenario I	Active Loss(kW)	47.26		203.80		577.23	
	Reactive Loss (kVAr)	31.34		134.88		383.76	
	Objective Function	78.61		338.68		960.99	
	V worst (p.u.)	0.9591		0.9171		0.8625	
		Method		Method		Method	
		WGA	PSO	WGA	PSO	WGA	PSO
	Open switches	33-34-35-36-37	33-34-35-36-37	33-34-35-36-37	33-34-35-36-37	33-34-35-36-37	33-34-35-36-37
		0.1916 at bus 8	0.1339 at bus 8	0.3868 at bus 8	0.2672 at bus 8	0.6160 at bus 8	0.4264 at bus 8
Scenario II	Cap. size (MVAr)	0.1232 at bus 14	0.1290 at bus 13	0.2434 at bus 14	0.2587 at bus 13	0.3919 at bus 14	0.4138 at bus 13
		0.4736 at bus 30	0.5508 at bus 27	0.9461 at bus 30	1.1014 at bus 27	1.5139 at bus 30	1.7635 at bus 27
	Active Loss (kW)	30.72	33.49	122.89	133.99	314.6	343.02
	Reactive Loss (kVAr)	20.35	22.62	81.39	90.50	208.38	231.70
	Objective Function	51.07	56.12	204.28	224.50	522.98	574.72
	V worst (p.u.)	0.9946	0.9946	0.9892	0.9891	0.987	0.9829

 Table 2 - Performance of WGA method on 33-bus system at different load levels.

respectively. The voltage profile of the system, for both scenarios, at the heavy, nominal and light load states are analyzed and depicted in Figs. 2-4, respectively. According to Figs. 2-4, for all load levels, the voltage profile has been developed in comparison with the base case. In this section, in order to confirm the superior efficiency of the WGA algorithm, the performance of WGA algorithm is compared with the results of particle swarm optimization algorithm (PSO). The comparison results for all scenarios are reported in Table 2. According to Table 2, the efficiency of the WGA in comparison with PSO algorithm could be concluded for all scenarios.





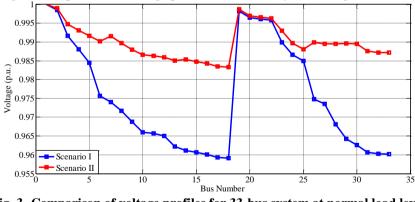


Fig. 3- Comparison of voltage profiles for 33-bus system at normal load level.

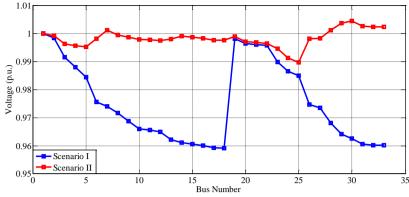
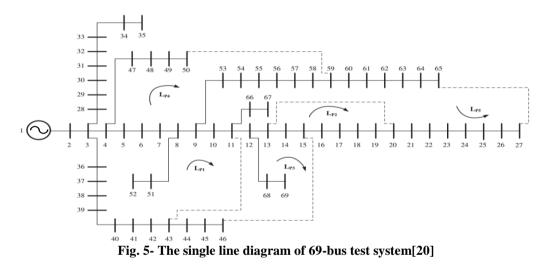


Fig. 4- Comparison of voltage profiles for 33-bus system at heavy load level.

# 3.2 69-bus test system

The single line diagram associated with this system is depicted in Fig. 5 which includes 69 nodes and 73 branches with 68 sectionalizing switches and 5 tie switches [19]. The system data is taken from [20]. The total reactive and active loads of the system are 2.69 MVAr and 3.80 MW, respectively. Similar to the pervious case, for 69 bus test system, two scenarios at three load levels are studied. The results concerning to 69-bus test system are presented in Table 3.



The whole power loss (kW) at three load conditions is decreased about 39.2, 41.6 and 45.3 in the scenario II, respectively. Moreover, according to Table 3, scenario II is more capable in reducing total active and reactive power losses, at all load levels. The efficiency of WGA on 69-bus system is compared with the outcomes of PSO method, and the results are reported in Table 3. It is clear that the WGA algorithm has a solid achievement in comparison with the PSO in terms of reactive and active power loss minimization. The voltage profile of the system, for each scenario and at all three load levels are compared and illustrated in Figs. 6-8, respectively. According to Figs. 6-8 and Table 3, the minimum voltage magnitude decreases as the load level increases from light to heavy in second scenario.

Scenario Item		Load Level						
		Light (0.5)		Normal (1)		Heavy (1.6)		
	Open switches	69-70-71-72-73		69-70-71-72-73		69-70-71-72-73		
Base Case Scenario I	Active Loss(kW)	51.58		224.96		648.90		
	Reactive Loss (kVAr)	23.54		101.99		292.71		
	Objective Function	75.12		326.60		941.62		
	V worst (p.u.)	0.9569		0.9492		0.8483		
		Method		Method		Method		
		WGA	PSO	WGA	PSO	WGA	PSO	
Scenario II	Open switches	69-70-71-72-73	69-70-71-72-73	69-70-71-72-73	69-70-71-72-73	69-70-71-72-73	69-70-71-72-73	
		0.598 at bus 63	0.0830 at bus 18	1.197at bus 63	0.2888 at bus 18	1.797 at bus 63	0.5471 bus 18	
	Cap. size (MVAr)	0.149 at bus 23	0.0864 at bus 24	0.300 at bus 23	0.1220 at bus 24	0.449 at bus 23	0.1755 at bus 24	
		0.0122 at bus 27	0.6425 at bus 57	0.0225 at bus 27	0.9 at bus 57	0.0342 at bus 27	0.900 at bus 57	
	Active Loss (kW)	34.84	36.51	148.70	165.23	417.83	446.48	
	Reactive Loss (kVAr)	16.33	16.76	69.38	75.38	193.74	201.34	
	Objective Function	51.18	53.28	218.089	240.62	611.58	647.76	
	V worst (p.u.)	0.9668	0.9519	0.9315	0.9300	0.8833	0.8726	

Table 3 - Performance of WGA method on 69-bus system at different load levels.

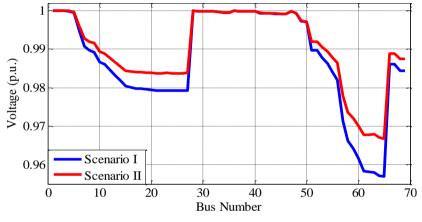


Fig. 6- Comparison of voltage profiles for 69-bus system at light load level.

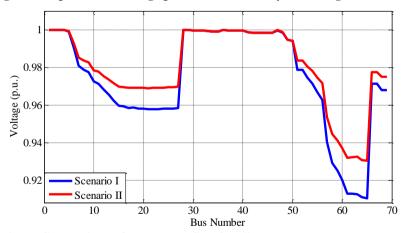


Fig. 7- Comparison of voltage profiles for 69-bus system at normal load

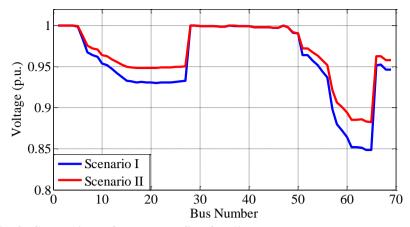


Fig. 8- Comparison of voltage profiles for 69-bus system at heavy load level.

#### 4. Conclusion

In this paper, a methodology for optimal capacitor placement has been presented. The WGA method has been employed to manage the problem. The principal objective of the paper is to minimize the total active and reactive power losses considering different load levels. The WGA method is examined on 33 and 69- test systems. Capacitor placement method has limited capability for loss reduction. The obtained results related to the scenario II (capacitor installation) results in lower power losses and improving the voltage profile. The association of WGA approach was very efficient considering the obtained comparative results with the PSO algorithm. According to the results of simulation studies, it could be concluded that, capacitor placement could be efficient for loss reduction in distribution systems. In the other words, when both operation issues such as loss reduction and planning issues such as capacitor placement are taken into account concurrently, handling operation concerns could be more efficient.

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