## Assessing the cost saving potential of shared product architectures - DTU Orbit (08/11/2017)

## Assessing the cost saving potential of shared product architectures

This article presents a method for calculating cost savings of shared architectures in industrial companies called Architecture Mapping and Evaluation. The main contribution is an operational method to evaluate the cost potential and evaluate the number of product architectures in an industrial company. Experiences from the case company show it is possible to reduce the number of architectures with 60% which leads to significant reduction in direct material and labor costs. This can be achieved without compromising the market offerings of products. Experiences from the case study indicate cost reductions between 0.5% and 2% of turnover. The main implication is that the method provides a quantitative basis for the discussion on whether or not to implement shared product architectures. This means a more fact-based approach is introduced.

## **General information**

State: Published Organisations: Department of Mechanical Engineering, Engineering Design and Product Development, Department of Management Engineering, Management Science Authors: Mortensen, N. H. (Intern), Hansen, C. L. (Intern), Løkkegaard, M. (Intern), Hvam, L. (Intern) Pages: 153-163 Publication date: 2016 Main Research Area: Technical/natural sciences

## **Publication information**

Journal: Concurrent Engineering: Research and Applications Volume: 24 Issue number: 2 ISSN (Print): 1063-293X Ratings: BFI (2017): BFI-level 1 Web of Science (2017): Indexed Yes BFI (2016): BFI-level 1 Scopus rating (2016): CiteScore 1.45 SJR 0.5 SNIP 1.121 Web of Science (2016): Indexed yes BFI (2015): BFI-level 1 Scopus rating (2015): SJR 0.594 SNIP 1.053 CiteScore 1.14 Web of Science (2015): Indexed yes BFI (2014): BFI-level 1 Scopus rating (2014): SJR 0.398 SNIP 0.876 CiteScore 1.08 Web of Science (2014): Indexed yes BFI (2013): BFI-level 1 Scopus rating (2013): SJR 0.495 SNIP 1.035 CiteScore 0.9 ISI indexed (2013): ISI indexed yes BFI (2012): BFI-level 1 Scopus rating (2012): SJR 0.453 SNIP 0.639 CiteScore 0.65 ISI indexed (2012): ISI indexed yes BFI (2011): BFI-level 1 Scopus rating (2011): SJR 0.377 SNIP 0.911 CiteScore 0.89 ISI indexed (2011): ISI indexed yes BFI (2010): BFI-level 1 Scopus rating (2010): SJR 1.008 SNIP 1.46 BFI (2009): BFI-level 1 Scopus rating (2009): SJR 0.56 SNIP 1.12 BFI (2008): BFI-level 1 Scopus rating (2008): SJR 0.6 SNIP 1.206 Web of Science (2008): Indexed yes Scopus rating (2007): SJR 0.616 SNIP 1.197 Scopus rating (2006): SJR 0.507 SNIP 1.003 Web of Science (2006): Indexed yes Scopus rating (2005): SJR 0.258 SNIP 0.642

Scopus rating (2004): SJR 0.598 SNIP 1.16 Scopus rating (2003): SJR 0.659 SNIP 0.933 Scopus rating (2002): SJR 0.624 SNIP 0.83 Scopus rating (2001): SJR 0.451 SNIP 1.101 Scopus rating (2000): SJR 0.483 SNIP 1.041 Scopus rating (1999): SJR 0.755 SNIP 1.079 Original language: English Product architecture, Manufacturing architecture, Modularization, Concurrent engineering Electronic versions: Assessing\_potential\_of\_shared\_product\_architectures\_ver\_8\_names.pdf. Embargo ended: 28/03/2017 DOIs: 10.1177/1063293X15624133 Source: FindIt Source-ID: 277685344 Publication: Research - peer-review > Journal article – Annual report year: 2016