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Model-Based Control for a Three-Phase Shunt Active Power Filter

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Abstract—This paper presents a robust model-based control in natural frame for a three-phase shunt active power filter. For the proposed control method a linear converter model is deduced. Then, this model is used in a Kalman filter in order to estimate the system state-space variables. Even though the states estimation do not match the variables of the real system, it has allowed to design three sliding mode controllers providing the following features to the closed loop system: a) robustness due to the fact that control specifications are met independently of any variation in the system parameters; b) noise immunity, since a Kalman filter is applied; c) a lower THD of the current delivered by the grid compared with the standard solution using measured variables; d) the fundamental component of the voltage at point of common coupling is estimated even in the case of a distorted grid; and e) a reduction in the number of sensors. Thanks to this solution the sliding surfaces for each controller are independent. This decoupling property of the three controllers allows using a fixed switching frequency algorithm that ensures a perfect current control. Finally, experimental results validate the proposed control strategy and illustrate all its interesting features.

Index Terms—current control, LCL filter, sliding mode control, Kalman filter, voltage sensorless control.

I. INTRODUCTION

HE use of nonlinear loads is a cause of current harmonics generation which degrade the quality of the power delivered by the grid. This fact causes non desirable effects on the power supply system. Ordinarily, passive power filters are used to suppress current harmonics due to their low cost [1], [2]. However, this kind of filters is sensitive to parameters variation and it may even produce a resonance problem. This resonance may lead to an over-current or over-voltage in the inductor and capacitor filter damaging the passive power filter.

As an alternative, shunt active power filters (SAPFs) have been used to avoid the aforementioned problems [3], [4]. In contrast to passive power filters, SAPFs are a flexible solution to compensate current harmonics generated by different types

Manuscript received August 10, 2015, revised November 16, 2015; accepted February 04, 2016. Date of current version xxxxx x, 2016. Recommended for publication by Associate Editor xxxxx.

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This work was supported by the Era-net project ELAC2014/ESE0034 and by the Ministry of Economy and Competitiveness of Spain under project PCIN-2015-001.

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Digital Object Identifier xx.xxxx/yyyy.xxxx.xxxxxxxx

of nonlinear loads, providing as well as a fast response to load variations.

In addition, the hybrid topology of SAPFs has been suggested in order to improve the filtering process. These kind of active filters incorporate an LC tank with the SAPF in order to reduce the effort of the control algorithm [5]–[7].

It is well known that the main purpose of a SAPF is to generate the harmonics caused by nonlinear loads, with the objective of providing a grid current with a low-harmonic content. Bearing this objective in mind, SAPF control has been widely studied, while considering two different control loops [3]–[5]:an outer voltage loop is designed in order to keep the voltage constant at the output capacitor filter, while the inner current loop is used to track the reference current, which can be generated by means of two different methods. The first method, the direct one, is based on extracting the harmonic content of the load current. Then, the filter current is used in the inner current loop in order to track the load current harmonics [8], [9]. The second method, the indirect one, is based on generating a sinusoidal reference current from the grid-voltage measurement. Then, the grid current is forced to track this sinusoidal current reference and the load current harmonics are provided by the filter inductor current in an indirect way [10]. The main advantages of this second approach are that only one low-bandwidth current sensor is required and a faster transient response is obtained. However, since the reference current is generated from measured voltages at the point of common coupling (PCC), when the grid is distorted, the reference current is not generated correctly. This issue is treated in detail in [11]. A selective harmonic-compensation control is used in order to mitigate the grid voltage harmonics, but with the inevitable trade-off between filtering performance and system stability, specially for the compensation of higher order harmonics [12].

Different control strategies for active power filters have been reported in the literature, such as linear feedback control [13], nonlinear control [14], repetitive control [15], neural network approach [16], [17], and adaptive fuzzy control [18], [19]. Any of the aforementioned control methods for three-phase SAPFs have been implemented in dq or $\alpha\beta$ frames. These control techniques have been widely accepted and used by researchers. Despite this, and with the aim of increasing robustness with a fast dynamic response, sliding mode control (SMC) is the best alternative to be used [20]. Here there is a lack of research using this technique due to the following drawbacks: a) the cross-coupling between controllers through the neutral point voltage which causes severe interferences between controllers [21] and b) the variable switching frequency of the SMC which

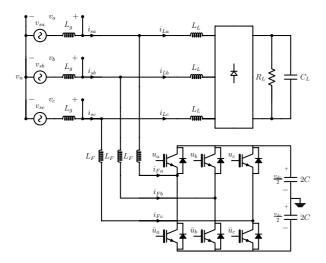


Fig. 1. Circuit diagram of three-phase SAPF

is unwanted in most industrial applications [22].

Some authors have reported several alternatives to fix the switching frequency, being the digital hysteretic modulators the most common solution [23], [24]. However, a high sampling frequency is needed for the correct implementation of these digital hysteretic modulators to increase the effectiveness of the control algorithm. This problem can be solved by using analog hysteretic comparators but at the expense of increasing the hardware [25]. Other solutions are adopted by using predictive control and DSP timers in order to decide the switching time [22]. It is worth to mention that the fixed switching frequency algorithms generally assume a decoupling between controllers, which is usually obtained by removing the neutral point voltage influence from the dynamics of the controllers. Some researchers have studied this issue but generally, their proposals have been based on a pure cancellation. At this point, it is important to highlight that the effectiveness of this cancellation has a clear dependence on the deviation of system parameters, and a high sensitivity to the system noise.

In order to avoid the aforementioned problems, a new model-based control solution is presented in this paper that uses SMC with estimated states. This solution is not usually applied to control power converters and, to our knowledge, it has been only applied to control a unity power factor rectifier [26]. Following a similar idea, this technique can be applied to control a SAPF. The proposed method is based on simple current sliding-mode controllers which use estimated variables. A lineal converter model is used in a Kalman filter (KF) in order to obtain the estimated states. This model allows a state estimation without the influence of the neutral point voltage, thus eliminating any interference between controllers in a simple and effective way. Then, these estimated states are used to obtain three independent sliding surfaces with a high robustness against variations of system parameters and noise immunity due to the application of the KF. With the proposed control method, the THD of the current delivered by the grid is improved compared with the standard SMC using measured variables. In addition, a variable hysteresis band comparator combined with a switching decision algorithm are used to

concentrate the switching spectrum, allowing to operate at fixed switching frequency.

The main advantage of this control method applied to a SAPF lies in the fact that the current reference is obtained from the fundamental component of the PCC voltage. This voltage is computed using the proposed model in a KF algorithm. With this procedure, the reference current is obtained without any distortion, even in the case of a highly distorted grid, without using any specific harmonic filters. An interesting side effect of the proposed control scheme is that in the case of voltage sags, the PCC voltage and its quadrature can be obtained, and positive and negative sequences can be derived directly without using a specific PLL algorithm for voltage sequence extraction, such as the algorithm presented in [27]. As shown in the experimental results section, very satisfactory results have been obtained using this control technique.

The main contributions of this paper are: 1) a linear converter model is derived, 2) a model-based control is used for the SAPF using the proposed linear converter model, which allows to estimate the fundamental component of the voltage at the PCC even in case of distortion in the grid, and also to remove the neutral point voltage influence from the dynamics of the controllers, and 3) an improvement in the THD of the currents delivered by the grid is achieved, compared with the THD obtained when the conventional SMC is applied.

The paper is organized as follows. In section II the nonlinear model of the SAPF is presented. In section III the problem formulation is introduced. Section IV presents the control proposal. The experimental results are presented in section V. Finally, section VI draws some conclusions.

II. NON-LINEAR MODEL OF THE SAPF

Fig.1 shows a circuit scheme of the SAPF. From this circuit the converter differential equations can be written as follows:

$$L_{F} \frac{d\mathbf{i}_{F}}{dt} = \mathbf{v} - \frac{v_{dc}}{2} \mathbf{u} + v_{n} \mathbf{1}$$

$$C \frac{dv_{dc}}{dt} = i_{Fa} \frac{u_{a}}{2} + i_{Fb} \frac{u_{b}}{2} + i_{Fc} \frac{u_{c}}{2}$$

$$v_{n} = \frac{v_{dc}}{6} (u_{a} + u_{b} + u_{c})$$
(1)
(2)

$$C\frac{dv_{dc}}{dt} = i_{Fa}\frac{u_a}{2} + i_{Fb}\frac{u_b}{2} + i_{Fc}\frac{u_c}{2}$$
 (2)

$$v_n = \frac{v_{dc}}{6}(u_a + u_b + u_c) \tag{3}$$

where v_n is the neutral point voltage, $\mathbf{i}_F = [i_{Fa} \ i_{Fb} \ i_{Fc}]^T$ is the inductor filter current vector, $\mathbf{v} = [v_a \quad v_b \quad v_c]^T$ is the voltage at the PCC $\mathbf{u} = [u_a \quad u_b \quad u_c]^T$ is the control variables vector with $u_{a,b,c} \in \{\pm 1\}$, v_{dc} is the dc voltage at the SAPF capacitor and 1 is defined as $[1 \ 1 \ 1]^T$.

In order to obtain the state-space model the equations mentioned above can be rewritten as follows:

$$\frac{d\mathbf{x}}{dt} = \mathbf{f}(\mathbf{x}, \mathbf{u}) + \mathbf{E}(\mathbf{v} + v_n) \tag{4}$$

$$\mathbf{y} = \mathbf{h}(\mathbf{x}, \mathbf{u}) \tag{5}$$

$$\mathbf{x} = \begin{bmatrix} i_{Fa} & i_{Fb} & i_{Fc} & v_{dc} \end{bmatrix}^T \tag{6}$$

$$\mathbf{f}(\mathbf{x}, \mathbf{u}) = \begin{pmatrix} 0 & 0 & 0 & -\frac{u_a}{2L_F} \\ 0 & 0 & 0 & -\frac{u_b}{2L_F} \\ 0 & 0 & 0 & -\frac{u_c}{2L_F} \\ \frac{u_a}{2C} & \frac{u_b}{2C} & \frac{u_c}{2C} & 0 \end{pmatrix} \mathbf{x}$$
(7)

$$\mathbf{E} = \frac{1}{L_F} [1 \ 1 \ 1 \ 0]^T \tag{8}$$

and the output matrix is given by h where is accomplished that

$$\mathbf{H} = \frac{\partial \mathbf{h}}{\partial \mathbf{x}} = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix}. \tag{9}$$

III. PROBLEM FORMULATION

The main control objective in a SAPF is to guarantee sinusoidal grid currents in phase with the grid voltages in presence of nonlinear loads. As a first approach, the sliding surfaces can be generated using the grid currents vector in order to obtain a grid current in phase with the voltage source:

$$\mathbf{S} = \mathbf{i}_s^* - \mathbf{i}_s \tag{10}$$

where $\mathbf{i}_s^* = k\mathbf{v} = [i_{sa}^* \quad i_{sb}^* \quad i_{sc}^*]^T$, is the reference current vector and $\mathbf{i}_s = \mathbf{i}_F + \mathbf{i}_L$ being $\mathbf{i}_L = [i_{La} \quad i_{Lb} \quad i_{Lc}]^T$ the load current vector.

Using (1), (3) and (10) the dynamics for each sliding surface is represented by the following expressions:

$$\frac{dS_a}{dt} = \frac{di_{sa}^*}{dt} - \frac{1}{L_F} \left(v_a + \frac{v_o}{2} \frac{-2u_a + u_b + u_c}{3} \right) - \frac{di_{La}}{dt}$$
(11)
$$\frac{dS_b}{dt} = \frac{di_{sb}^*}{dt} - \frac{1}{L_F} \left(v_b + \frac{v_o}{2} \frac{u_a - 2u_b + u_c}{3} \right) - \frac{di_{Lb}}{dt}$$
(12)
$$\frac{dS_c}{dt} = \frac{di_{sc}^*}{dt} - \frac{1}{L_F} \left(v_c + \frac{v_o}{2} \frac{u_a + u_b - 2u_c}{3} \right) - \frac{di_{Lc}}{dt}.$$
(13)

The aforementioned expressions show two different issues to take into account: 1) a cross-coupling term introduced by the neutral point voltage (3) which is expressed as a linear combination of the control signals, $u_{a,b,c}$, in each sliding surface dynamics. This term can produce severe irregularities in the ordinary hysteresis operation since each phase-leg can not be controlled independently [26], and 2) the reference currents are generated using the measured PCC voltages, $i_{sa,sb,sc}^* = kv_{a,b,c}$, and for this reason the distortion in the grid will involve grid currents with higher harmonic content.

As a solution, this paper suggest a model-based method in which three independent sliding mode controllers will be designed while obviating the neutral point voltage in the converter model used in a KF. This fact has a negligible effect in the average dynamics of the closed loop system. In addition, the fundamental component of the voltage at the PCC is estimated in order to generate the reference currents, even in the case of a distorted grid.

An interesting side effect of the proposed control scheme is the fact that the sliding surfaces for each controller will be designed independently. This decoupling property between the three current controllers allows using a fixed switching frequency algorithm. The proposed model to achieve all this aims will be introduced in the next section.

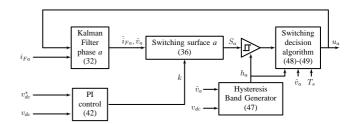


Fig. 2. Proposed control system for phase-leg a of the SAPF

IV. PROPOSED CONTROL SYSTEM

The control diagram for phase-leg a is depicted in Fig.2. Phase-legs b and c have the same control scheme. A KF is used together with the proposed converter model, as explained in the next subsection, in order to estimate the states. A PI controller is used to regulate the filter output voltage and to obtain the value of the gain k. This gain and the state estimation are the inputs of a sliding surface which computes the error between the estimated grid current and its reference, obtained as $i_{sa}^* = k\hat{v}_{sa}$. Finally this error is considered in a variable hysteresis comparator, which is used together with a switching decision algorithm. This combination allows to obtain a fixed switching frequency with an improved switching spectrum.

A. Proposed state-space model of the converter

The objective of this section is to obtain a linear simplified model for the SAPF in order to use it in the KF algorithm. This model will be designed with the aim of solving the problems exposed in section III. The first problem was related to the axis-decoupling and it can be solved by eliminating the dependence of the neutral point voltage from the controller dynamics (11)-(13). This is also an opportunity to reduce the number of sensors by adding two additional states, the voltage at the PCC v_i and its quadrature v_{iq} , in order to estimate the fundamental component of this voltage. Due to the grid inductance and grid voltage harmonics, the measured voltage at the PCC can be distorted. Thus, this estimation will improve the quality in the generation of reference currents, leading to a reduction in the THD of the grid currents. With this approach the second problem can be solved. In addition, the estimated PCC voltage can also be used in the hysteresis band expressions explained in section V-D.

Hence, in order to derive the adequate SAPF model, some considerations must be taken into account. Firstly, is to ensure that the model is a decoupled model (i.e. each phase only depends on its own control variable) and secondly, the model must be linear, accurate, and simple to reduce the computational time. For these reasons the following assumptions are made:

- 1) The neutral point voltage, v_n in the filter current equation (1) is not used in the proposed model, since it is a high frequency signal (it depends on the control actions u_a , u_b , u_c), and it only affects current inductors ripples. Thus, this approximation has a negligible effect on the average dynamics of the closed-loop system.
- 2) The SAPF capacitor is usually large and its voltage has a slow dynamics. Hence, the state variable v_{dc} can be assumed

to be constant between different sampling instants of the same switching period. Besides, since this variable is measured, it can be considered as a parameter and its dynamics can be neglected. With this consideration, the new state-space model for each phase-leg i could be considered linear in a switching

3) The voltage at the PCC v_i and its quadrature v_{iq} are added in the model in order to estimate the fundamental component of the PCC voltages. With this solution the generation of the reference currents does not rely on noise or harmonic distortion in the grid, thus improving the system robustness against grid disturbances.

The aforementioned considerations lead to redefine new differential equations for each phase-leg i:

$$\frac{di_{Fi}}{dt} = \frac{1}{L_F} v_i - \frac{v_{dc}}{2L_F} u_i \tag{14}$$

$$\frac{dv_i}{dt} = \omega_o v_{iq} \tag{15}$$

$$\frac{dv_i}{dt} = \omega_o v_{iq} \tag{15}$$

$$\frac{dv_{iq}}{dt} = -\omega_o v_i. \tag{16}$$

where ω_o is the angular grid frequency.

The proposed linear converter model can be obtained from these differential equations as follows:

$$\frac{d\mathbf{x}_i}{dt} = \mathbf{A}\mathbf{x}_i + \mathbf{B}v_{dc}u_i \tag{17}$$

$$y_i = \mathbf{C}\mathbf{x}_i \tag{18}$$

being

$$\mathbf{x}_i = \begin{bmatrix} i_{Fi} & v_i & v_{iq} \end{bmatrix}^T \tag{19}$$

(20)

$$\mathbf{A} = \begin{pmatrix} 0 & \frac{1}{L_F} & 0\\ 0 & 0 & \omega_o\\ 0 & -\omega_o & 0 \end{pmatrix}$$
 (21)

$$\mathbf{B} = \begin{bmatrix} -\frac{1}{2L_F} & 0 & 0 \end{bmatrix}^T \tag{22}$$

Using this model, a KF can be employed to estimate the filter current without the effect of the neutral point voltage and to extract the fundamental component and its quadrature of the PCC voltages. This fact will allow to generate sinusoidal reference currents even in case of a distorted grid [29].

Note that the control algorithm does not use grid voltage sensors and the value of the output matrix C is:

$$\mathbf{C} = \begin{bmatrix} 1 & 0 & 0 \end{bmatrix} \tag{23}$$

At this point it is important to prove the observability and controllability of the proposed converter model. Taking into account the aforementioned matrices, the following observability matrix is obtained

$$\mathbf{O} = [\mathbf{C} \ \mathbf{C} \mathbf{A} \ \mathbf{C} \mathbf{A}^2]^T. \tag{24}$$

Moreover the controllability matrix Γ is given by

$$\Gamma = [\mathbf{B} \ \mathbf{A}\mathbf{B} \ \mathbf{A}^2\mathbf{B}] \tag{25}$$

Matrices O and Γ are both of full-rank, (i.e. $rank\{O\}$) $rank\{\Gamma\} = 3$), so that the system is controllable and can be observed using only the measured filter current i_{Fi} .

B. Kalman Filter

It is well known that in nonlinear system applications an EKF is used in order to estimate the states, with the drawback of the long computational time required by this algorithm. This is the case of the SAPF which is clearly a nonlinear system (4)-(5). However, the proposed converter model defined by (17)-(18) is considered linear in a switching period, and it can be used for each phase-leg i in order to estimate the states. This fact allows to use a KF instead of an EKF, which reduces the computational load. For the digital KF implementation, the state-space model (17)-(18) is discretized, yielding

$$\mathbf{x}_i(k+1) = \mathbf{A}_d \mathbf{x}_i(k) + \mathbf{B}_d v_{dc}(k) u_i(k) + \boldsymbol{\eta}_i(k)$$
 (26)

$$y_i(k) = \mathbf{C}\mathbf{x}_i(k) + \mathbf{w}_i(k) \tag{27}$$

where $\eta_i(k)$ and $\mathbf{w}_i(k)$ are the process and measurement noise vectors respectively; their covariance matrices are given by:

$$\mathbf{R}_{i}(k) = E\{\mathbf{w}_{i}(k)\mathbf{w}_{i}^{T}(k)\}$$
(28)

$$\mathbf{Q}_i(k) = E\{\boldsymbol{\eta}_i(k)\boldsymbol{\eta}_i^T(k)\}$$
(29)

$$\mathbf{A}_d = e^{AT_s} \cong \mathbf{I} + \mathbf{A}T_s = \begin{pmatrix} 1 & \frac{T_s}{L} & 0\\ 0 & 1 & \omega_o T_s\\ 0 & -\omega_o T_s & 1 \end{pmatrix}$$
(30)

$$\mathbf{B}_{d} = \int_{0}^{T_{s}} Be^{A\lambda} d\lambda \cong \mathbf{B}T_{s} = \begin{bmatrix} -\frac{T_{s}}{2L} & 0 & 0 \end{bmatrix}^{T}$$
 (31)

being I the identity matrix and T_s the sampling time.

Here it is assumed that $v_{dc}(k)$ is a constant between samples of the same switching period, so that $v_{dc}(k+1) \cong v_{dc}(k)$, and the discrete model can be considered linear over the same switching period.

As mentioned above, the state-space model defined by (26)-(27) is used in a KF, and the estimated states can be used as inputs of the sliding surfaces providing three independent controllers with a high noise immunity and an improved THD of the currents injected to the grid. The main properties and the implementation of the KF are well explained in [30] so only the adaptive equation used in the estimation will be given in this section.

Based on the KF algorithm implementation, the equation for the state estimation can be expressed as follows:

$$\hat{\mathbf{x}}_i(k+1) = \mathbf{A}_d \mathbf{x}_i(k) + \mathbf{B}_d v_{dc}(k) u_i(k) + \mathbf{L}_i(k) (\mathbf{x}_i(k) - \hat{\mathbf{x}}_i(k))$$
(32)

where the Kalman gain is computed as:

$$\mathbf{L}_i(k) = \mathbf{P}_i(k)\mathbf{C}^T(\mathbf{C}\mathbf{P}_i(k)\mathbf{C}^T + \mathbf{R}_i(k))^{-1}.$$
 (33)

being P_i the error covariance matrix for phase-leg i, which is computed following the KF algorithm explained in [26].

1) Considerations about the Kalman gain computation: The high computational load is a major drawback in the KF algorithm implementation. Particularly, the Kalman gain (33) contains a matrix inversion which involves a long computational time. Using a KF for each phase-leg instead a single KF for the whole three-phase system, the matrix inversion in (33) can be reduced to a scalar inversion. Actually, according to (23) and with the condition that $\mathbf{P}_i(k)$ is a square matrix, the computation of $\mathbf{CP}_i(k)\mathbf{C}^T$ in the Kalman gain expression results in a scalar quantity. Then, to be consistent with the matrix addition operation in (33), the noise covariance matrix $\mathbf{R}_i(k)$ is considered as mean noise power in each phase leg, $R_i(k)$. In addition, considering that this noise is so similar in each phase leg, the Kalman gain can be assumed identical for all the phases (i.e. $\mathbf{L}_a(k) \cong \mathbf{L}_b(k) \cong \mathbf{L}_c(k)$). Thanks to this approximation, (33) is computed only for one phase and it is used in all the phases. Following this approach a notably reduction in the KF implementation is practically obtained.

2) Mean noise power estimation: The value of $R_i(k)$ can be estimated using the unbiased mean power estimator

$$R_i(k) = \frac{1}{NT_s} \sum_{k=0}^{N-1} |w_i(k)|^2$$
 (34)

where $w_i(k)$ is an additive white gaussian noise sample at time k, in phase-leg i, and N is the number of samples.

To obtain $w_i(k)$, the simplest way is to use a dc-voltage with additive white gaussian noise. In this case a reference dc-voltage used in the sensing system V_{ref} has been employed. This voltage is connected to one analog input of the DSP and is contaminated by the switching noise when the inverter is switching. The noise samples can be obtained by subtracting from the V_{ref} measures, the real value, V_{ref}^* (i.e. the measured value when the inverter is not switching), yielding

$$R_i(k) = \frac{1}{NT_s} \sum_{k=0}^{N-1} |V_{ref}(k) - V_{ref}^*(k)|^2$$
 (35)

The last expression allows to estimate the mean noise power in any of the three phases and it can be used to compute the Kalman gain. Assuming the noise power level has no important variations, $R_i(k)$ is computed at the beginning, remaining unchanged during all the time.

3) Process covariance matrix: Matrix $\mathbf{Q}_i(k)$ is not found so directly. It is generally derived intuitively, but there are some points that need to be regarded in its selection [30]. Unmodeled dynamics and parameter uncertainties are generally modeled as process noise. Usually $\mathbf{Q}_i(k)$ is taken as a diagonal matrix, which is of dimension 3 in this application. Its value has been obtained by means of simulations. In the simulation results an approximate value of $\mathbf{Q}_i(k) = 0.005\mathbf{I}_3$ provides an adequate behaviour, where \mathbf{I}_3 is a 3-dimensional unity matrix.

C. Sliding-Mode Control

The conventional SAPF control scheme consists of a fastinner input-current loop which ensures sinusoidal input currents in phase with the PCC voltages, $\mathbf{i}_s = k\mathbf{v}$, and a slowouter loop, usually a PI controller, the main task of which is to regulate the output voltage, modifying the input-current amplitudes. In this proposal, the inner control loop is designed using estimated variables as explained in previous sections. With this aim the following switching surfaces are proposed:

$$S_a = \hat{i}_{sa}^* - \hat{i}_{sa} \tag{36}$$

$$S_b = \hat{i}_{sb}^* - \hat{i}_{sb} \tag{37}$$

$$S_c = \hat{i}_{sc}^* - \hat{i}_{sc}. {38}$$

It should be noticed that using our proposed model, the dynamics of the aforementioned controllers only depends on its control variables u_a , u_b and u_c :

$$\frac{dS_a}{dt} = \frac{d\hat{i}_{sa}^*}{dt} - \frac{1}{L_F} \left(\hat{v}_a - \frac{v_{dc}}{2} u_a \right) - \frac{di_{La}}{dt} \tag{39}$$

$$\frac{dS_b}{dt} = \frac{d\hat{i}_{sb}^*}{dt} - \frac{1}{L_F} \left(\hat{v}_b - \frac{v_{dc}}{2} u_b \right) - \frac{di_{Lb}}{dt} \tag{40}$$

$$\frac{dS_c}{dt} = \frac{d\hat{i}_{sc}^*}{dt} - \frac{1}{L_F} \left(\hat{v}_c - \frac{v_{dc}}{2} u_c \right) - \frac{di_{Lc}}{dt} \tag{41}$$

where $\hat{i}_{sa,sb,sc}^* = k\hat{v}_{a,b,c}$ are the reference currents using the estimated inductor filter currents, $\hat{i}_{sa,sb,sc} = \hat{i}_{Fa,Fb,Fc} + i_{La,Lb,Lc}$ are the estimated grid currents and k is implemented in the outer control loop in order to regulate the output capacitor voltage using a PI controller [5], and expressed as:

$$k = k_p(v_{dc}^* - v_{dc}) + k_i \int_{-\infty}^{t} (v_{dc}^* - v_{dc}) d\tau$$
 (42)

being k_p and k_i the proportional and integral gains respectively.

The main requirement in the design of the SMC is to satisfy the reaching conditions, and also to guarantee the existence of a sliding regime in the switching surfaces S = 0. The most often used reaching conditions for each phase-leg i are given by

$$S_i \dot{S}_i < 0. (43)$$

Defining the control variable u_i as:

$$u_i = \begin{cases} u_i^+ & if \quad S_i > 0 \\ u_i^- & if \quad S_i < 0 \end{cases}$$
 (44)

where $u_i \in \{1, -1\}$, and taking into account (43), (14) and (36)-(38) the following expression can be deduced:

$$\frac{v_{dc}}{2L_F}(u_i^+ - u_i^-) < 0 (45)$$

which allows us to determine the switching action

$$u_{i} = \begin{cases} u_{i}^{+} = -1 & if \quad S_{i} > 0 \\ u_{i}^{-} = 1 & if \quad S_{i} < 0 \end{cases}$$
 (46)

D. Hysteresis Band Generator

This section deals with a fully digital hysteretic modulator to fix switching frequency of the SAPF. Conventionally, the expression for the hysteresis band is given by the following equation [25]

$$h_i = \frac{v_o}{8L_F f_{sw}} \left[1 - \left(\frac{2\hat{v}_i}{v_{dc}}\right)^2 \right] \tag{47}$$

where f_{sw} is the desired switching frequency.

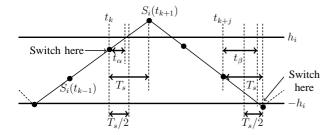


Fig. 3. Hysteresis band with the switching surface.

It is widely known that the finite sampling frequency can cause deviations in the desired the switching frequency due to the samples that appear out of the hysteresis limits. This problem is shown in Fig.3 and it can be avoided by using a simple switching decision algorithm presented in [26]. This algorithm, which has been adopted for this application, can decide the optimum switching instant and it concentrates the switching spectrum. Using this algorithm, the number of samples out of the hysteresis limits is drastically reduced. Assuming a slow variation in the hysteretic band in a sampling period, $h_i(t_k) \simeq h_i(t_{k+1})$, the algorithm can be implemented as follows: 1) When a sample is acquired, the time between the sample and the hysteresis band is calculated. If the sample is in the increasing slope of the switching surface, $(u_i = 1)$ the time t_{α} can be computed as:

$$t_{\alpha} \cong L_F \frac{h_i - S_i(t_k)}{v_{dc}/2 - \hat{v}_i} \tag{48}$$

otherwise, if the sample is in the decreasing slope of the switching surface $(u_i = -1)$, the time t_β can be approximated by:

$$t_{\beta} \cong L_F \frac{h_i + S_i(t_{k+j})}{v_{dc}/2 + \hat{v}_i} \tag{49}$$

being S_i the switching surface for the phase-leg i.

2) When the estimated times t_{α} and t_{β} are known, the switching decision is implemented as follows:

In the case of a positive slope:

- If $t_{\alpha} < \frac{T_s}{2}$, the control signal u_i is changed from 1 to -1, otherwise the control signal u_i is not changed.

In the case of a negative slope:

- If $t_{\beta} < \frac{T_s}{2}$, the control signal u_i is changed from -1 to 1, otherwise the control signal u_i is not changed.

This procedure is done for each sampling period.

V. EXPERIMENTAL RESULTS

Fig.4 shows the prototype of the SAPF built using a 4.5-kVA SEMIKRON full-bridge as the power converter and a TMS320F28M36 floating-point digital signal processor (DSP) as the control platform. The grid voltages have been generated using a PACIFIC 360-AMX source. Some experimental results have been exported to MATLAB© for the final representation. The system parameters are listed in Table I.

Using the approximations reported in section IV-B, the computational load of the KF algorithm can be reduced from $12\mu s$ to $3\mu s$. Note that in each phase-leg a different KF is used

TABLE I SYSTEM PARAMETERS

Symbol	Description	Value
L_F	Filter input inductance	5 mH
C	Output capacitor	1500 μ F
v_{dc}	dc voltage	400 V
f_s	Sampling frequency	40 kHz
f_{sw}	Switching frequency	4 kHz
f_{grid}	Grid frequency	60 Hz
V_{qrid}	Grid voltage	110 Vrms
L_g	Grid inductance	0.5 mH
k_p	Proportional gain	0.03
k_i	Integral gain	0.5
R_L	Load resistor	$48\Omega - 24\Omega$
L_L	Load inductance	5mH
C_L	Load capacitor	$100 \mu F$
$R_i(k)$	Single phase system noise power	$0.24V^{2}$
$Q_i(k)$	Process covariance matrix	$0.005 \mathbf{I}_{3}$

but the Kalman gain is the same for each phase-leg. This is an important reduction in the total algorithm time, which now is about $23\mu s$. This fact allows a sampling frequency f_s =40kHz and it makes possible the implementation of this proposal in the DSP. The switching frequency, f_{sw} , when a sliding mode control is used can be chosen as f_{sw} = $f_s/10$ to ensure enough samples for a proper performance of the variable hysteresis comparator. This relation provides a good trade-off between the use of the computational load in a switching period and the switching frequency accuracy. The following figures show the good performance of the proposed control system.

In Fig.5(a) the sliding surfaces with its corresponding control signals in the case of coupled controllers (10) are shown. In this case the three controllers are implemented with measured variables instead of estimated ones. The figure shows the effect of the neutral point voltage (3) in the dynamics of the controllers. As it can be seen, the slope of each sliding surface changes with any of the control signals losing the synchronization and making impossible to design a current control with variable hysteresis band operating at fixed switching frequency.

In Fig.5(b) the sliding surfaces with its corresponding control signals in the case of decoupled controllers (36)-(38) are shown. In this case the sliding mode controllers use the estimated estates provided by (32). As it can be seen, the axis-decoupling is effective, and the slope of each sliding surface only changes with its control variable. Using the proposed model the three controllers are independent and the variable hysteresis control can be performed ensuring a fixed switching frequency.

A. Response of the SAPF to load variations

Fig.6 represents the main waveforms of the SAPF when a sudden load step change occurs, from no load to full-load, and from full-load to half-load. This figure shows from top to bottom the grid currents, the non-linear load currents, the filter currents and the output voltage. Fig.6(a) shows the waveforms when the conventional SMC is used, while the waveforms in Fig.6(b) are obtained using the proposed control scheme. Although in both figures the transient response is similar, it

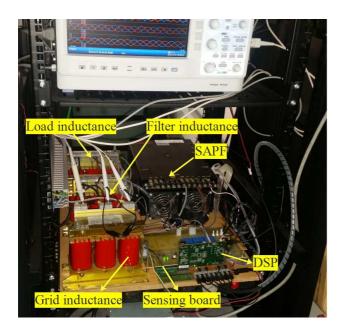
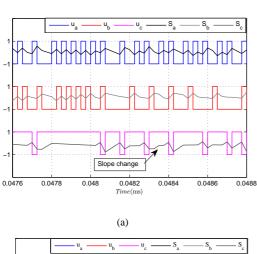


Fig. 4. SAPF Prototype



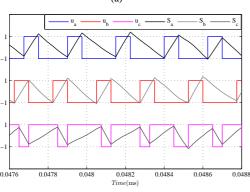


Fig. 5. Experimental control signals u_a , u_b and u_c with their corresponding sliding surfaces S_a , S_b and S_c (a) with coupled controllers, (b) with decoupled controllers operating at fixed switching frequency.

can be observed that when the proposed controller is used, the THD of the grid currents is improved.

Fig.7 shows the harmonic spectrum of the grid current for phase-leg a. The spectrum before compensation is shown in Fig.7(a), its THD is 28.45%. When the conventional SMC (10) is used, the THD is reduced to 5.36% as shown in Fig.7(b). In contrast, if the proposed controller (36)-(38) is employed, the THD is reduced to 2.51% as shown in Fig.7(c). A reduction of 46.8% in the THD is achieved with respect to the conventional SMC, which proves the effectiveness of the proposed control method. Note that in both cases the fundamental component is not represented in order to have a higher resolution in the rest of the harmonics.

B. SAPF performances under a distorted grid

Fig.8 compares the three-phase grid currents using a conventional SMC and the proposed one, in the case of a distorted grid. This figure illustrates from top to bottom: the distorted PCC voltages, being the THD about 14%, the load currents, and the grid currents. As it can be seen, when the conventional SMC is used from 0 to 50 ms, the grid currents are distorted since the reference current generation method uses distorted PCC voltages. In contrast, when the proposed controller is used from 50 ms to 100 ms, the grid currents are practically sinusoidal since the reference current is generated by using only the fundamental component of the PCC voltage obtained from the KF.

A detail of the distorted PCC voltage for phase-leg a together with the estimated fundamental component is shown in Fig.9. Using the estimated voltage at the PCC the reference current is sinusoidal. In addition, the estimated voltage is perfectly in phase with the measured one showing a good synchronization, at least as good as when using PLL.

C. SAPF performances under grid voltage sags

Fig.10 shows SAPF performances under grid voltage sags. Two different sag types are analyzed in this test: a onephase fault and a two-phase fault which are represented in Fig.10(a) and Fig.10(b), respectively. In both cases, the reference currents are obtained using the positive sequence of the PCC voltage using $i^*_{sa,sb,sc} = kv^{\bar{+}}_{a,b,c}$. The positive sequence is computed from the estimated PCC voltages and their quadratures obtained from the KF. With this solution, the use of a specific PLL algorithm for extracting the positive and negative sequence grid voltage components is not necessary. Even without using this algorithm, the quality of the grid current waveforms are good both in normal and abnormal (voltage sag) conditions. Due to the fact that the grid current tracks only the positive sequence of the PCC voltage, a ripple frequency component of $2\omega_o$ appears in the output voltage as expected.

D. Control signal frequency spectrum

The hysteresis band with its switching surface of phaseleg a is shown in the following figures. If an hysteresis band without the switching decision algorithm is used, an error

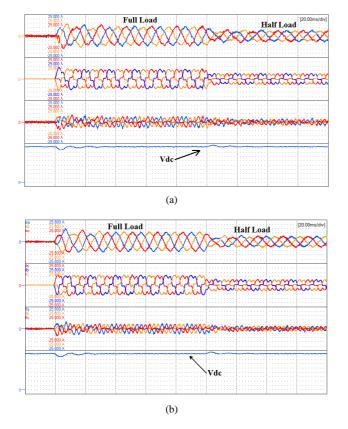


Fig. 6. A sudden load step change from no-load to full-load and full-load to half-load. From top to bottom, grid currents (5A/div), load currents (5A/div), filter currents (5A/div), output voltage (50V/div): a) using conventional SMC, and b) using proposed SMC.

on the desired switching frequency appears. In Fig.12(a) this problem can be clearly seen, where several samples are out of the hysteresis limits. The problem is solved using the switching decision algorithm presented in section V-C. Fig.11(b) shows how the number of samples that are out of the bounds of the hysteresis limits have been reduced using this algorithm, and the error has disappeared.

Finally, the spectrum of the switching frequency is shown in the following figures. Fig.12(a) shows the switching spectrum with hysteresis bands but without the switching decision algorithm. The spectrum is concentrated around a fixed switching frequency of 3kHz with an error around 1kHz, since the desired switching frequency is 4kHz. This problem is solved using the proposed algorithm. Its results are depicted on Fig.12(b). Now the switching frequency is the desired one, 4kHz.

VI. CONCLUSIONS

This paper has presented a model-based control algorithm for a three-phase SAPF. We have chosen a model-based solution using SMC in natural frame that is not usually employed in the control of power converters. The control algorithm works with a SMC which uses estimated variables from a KF achieving a high PF and reducing the THD. Besides, the use of a Kalman filter instead of a non-adaptive state observer improves the quality of the estimated signals in presence of noise, and increasing the noise immunity of the control

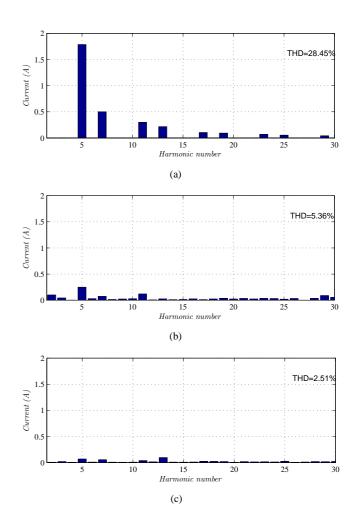


Fig. 7. Grid current harmonics for phase-leg a: (a) Before compensation, (b) after compensation but using conventional SMC and (c) after compensation but using the proposed control.

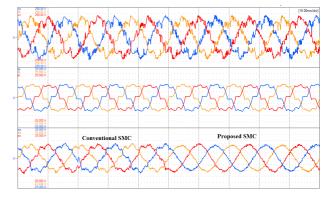


Fig. 8. From top to bottom: PCC voltages (50V/div) with THD=14%, load current (5A/div) and grid currents (5A/div)

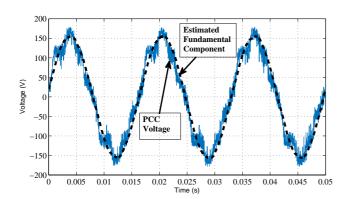


Fig. 9. Measured PCC voltage and estimated PCC voltage for phase-leg a.

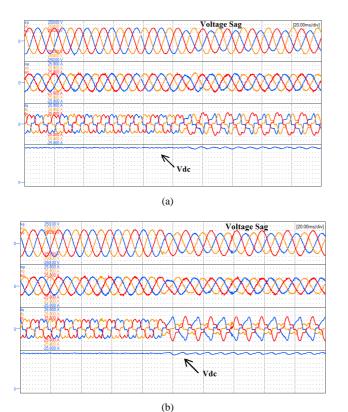
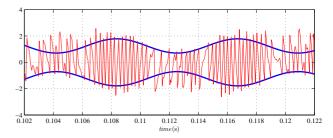


Fig. 10. From top to bottom: grid voltages (50V/div), grid currents (5A/div), load currents (5A/div) and v_{dc} (50V/div) under unbalanced grid fault: a) one-phase fault and b) two-phase fault.

loop. A useful approximation has been adopted to reduce the computational time needed for the KF implementation, reducing the total time employed for the control algorithm. In addition, a fixed switching frequency algorithm has been used to improve the switching spectrum of the controllers. The corresponding experimental results show the validity of this proposal. In the near future, this technique may be applied to other converters using the most appropriate converter for each model.

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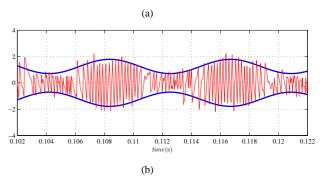


Fig. 11. Experimental hysteresis band and its switching surface for phase-leg a: a) without switching decision algorithm and b) with switching decision algorithm.

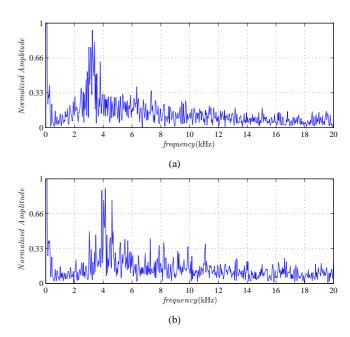


Fig. 12. switching spectrum: a) without switching decision algorithm and b) with switching decision algorithm.

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