Error Analysis and Reduction for a Simple Sensor-Microcontroller Interface

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Abstract—Error analysis of a resistive sensor-to-microcontroller interface based on pulse-width modulation and time-ratio measurement shows that internal input and output resistances in microcontroller digital ports produce zero, gain and nonlinearity errors. The time-ratio measurement technique cancels these errors when the sensor resistance equals the reference resistor and reduces errors around that point. We propose two simple methods of reducing those errors for sensors with a wide dynamic range. Both methods use time-ratio measurements. The first method uses several reference resistors covering the sensor resistance range; the second method uses two-point calibration. The second method is more efficient and yields errors that can be smaller than 0.5 Ω for a sensor resistance from about 600 Ω to 3550 Ω .

Index Terms—Error analysis, error reduction, microcontroller interface, resistive sensor, sensor interface, time-ratio measurement.

I. Introduction

IRECT sensor-to-microcontroller interfaces without intervening analog-to-digital converters (ADC) are simple to design and less expensive than interfaces based on the classical sensor-amplifier-ADC approach. Daugherty [1] described a simple analog-to-digital conversion technique based on pulsewidth modulation (PWM) using a microcontroller and Cox [2] applied it to resistive sensor interfacing (Fig. 1). In general, the time t_t needed to charge a capacitor C (initially charged at V_0) through a resistor R to a threshold voltage V_t from a source V_r is

$$t_t = -RC\ln\frac{V_r - V_t}{V_r - V_0} \tag{1}$$

In Fig. 1, programmming a "high" level (V_{OH}) at output O1 charges C through the sensor R_{S} up to a voltage level determined by the logic "1" threshold (V_{IH}) of input I1. Then C is discharged through a protection resistor R_{P} by setting I1 low ("0", V_{OL}). Next, O1 is brought to a high-impedance state by programming it as input and O2 is set "high" to charge C through

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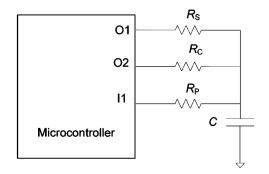


Fig. 1. Analog-to-digital conversion by pulse-width modulation (PWM).

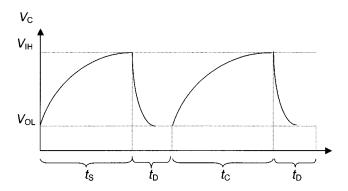


Fig. 2. Voltage waveform at port I1 (Fig. 1). $t_{\rm S}$ and $t_{\rm C}$ are the respective time intervals needed to charge C through $R_{\rm S}$ and $R_{\rm C}$. The discharge time $t_{\rm D}$ through $R_{\rm P}$ is the same in both cases.

the reference resistor $R_{\rm C}$. The time taken to charge is monitored again by checking the voltage level at input I1 and C is discharged again through $R_{\rm P}$. The voltage at I1 changes between $V_{\rm IH}$ and $V_{\rm OL}$ (Fig. 2) and its pulse width depends on the time needed to charge C. Since C is the same for both time measurements, calculating their ratio cancels the contribution of C and also any possible multiplicative interference [3]. The result is

$$R_{\rm S}^* = \frac{t_{\rm S}}{t_{\rm C}} R_{\rm C} \tag{2}$$

where $R_{\rm S}^*$ is the resulting estimate of the actual sensor resistance $R_{\rm S}$

Uncertainty in output and threshold voltages involved in the modulation process and internal microcontroller parameters such as output and input resistances and leakage currents limit the resolution and accuracy of the process depending on the range of the sensor resistance. This paper analyzes these errors and provides two simple methods of reducing them.

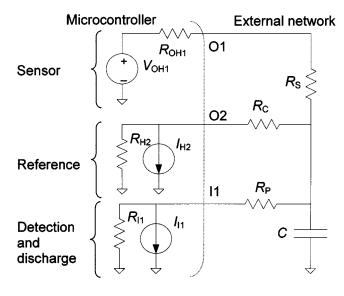


Fig. 3. Complete model for the circuit in Fig. 1 including all intervening resistances and current sources.

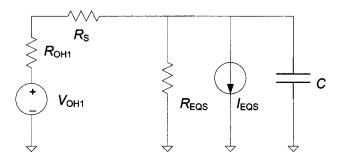


Fig. 4. Circuit equivalent to that in Fig. 3.

II. CIRCUIT MODEL

Fig. 3 shows the model for the circuit in Fig. 1 when Ccharges through the sensor. The active output port has a nonzero internal resistance $R_{\rm OH1}$. The port connected to the reference resistor $R_{\rm C}$ is set at high impedance and modeled by a finite resistance $R_{\rm H2}$ and a leakage current $I_{\rm H2}$. Input I1 is also modeled by a finite resistance (R_{I1}) and a leakage current source (I_{I1}) . Ideally, R_{H2} and R_{I1} should be infinite and I_{H2} and I_{I1} should be zero. In practice, however, they are not and therefore not all the current through the sensor charges C. Instead, part of it flows to the other microcontroller terminals. In addition, C charges not only through the sensor but also through $R_{\rm C}$ and $R_{\rm P}$.

The circuit in Fig. 3 can be simplified as shown in Fig. 4 where

$$R_{\text{EQS}} = (R_{\text{I1}} + R_{\text{P}}) || (R_{\text{H2}} + R_{\text{C}})$$
 (3)

$$I_{\text{EQS}} = I_{\text{II}} \frac{R_{\text{II}}}{R_{\text{II}} + R_{\text{P}}} + I_{\text{H2}} \frac{R_{\text{H2}}}{R_{\text{H2}} + R_{\text{C}}}$$
 (4)

When C charges through $R_{\rm C}$, the model and equivalent circuit are similar to those in Figs. 3 and 4. However, in the port connected to the sensor $R_{\rm S}$ we now have $R_{\rm H1}$ and $I_{\rm H1}$, instead of $R_{\rm H2}$ and $I_{\rm H2}$ for the output port connected to the reference resistor $R_{\rm C}$. Thus, the equivalent elements in Fig. 4 are $R_{\rm EQC}$ and I_{EQC} obtained from (3) and (4) by replacing R_{C} with R_{S} . From (1), the respective times to charge C, $t_{\rm S}$ and $t_{\rm C}$ are

$$t_{\rm S} = -R_{\rm EQSS} \operatorname{C} \ln \left| \frac{V_{\rm S}(t_{\infty}) - V_{\rm S}(t_f)}{V_{\rm S}(t_{\infty}) - V_{\rm S}(t_0)} \right| \tag{5}$$

$$t_{\rm C} = -R_{\rm EQCC} \operatorname{Cln} \left| \frac{V_{\rm C}(t_{\infty}) - V_{\rm C}(t_f)}{V_{\rm C}(t_{\infty}) - V_{\rm C}(t_0)} \right|$$
 (6)

where $R_{\rm EQSS}$ and $R_{\rm EQCC}$ are the respective equivalent resistances to charge C through $R_{\rm S}$ and $R_{\rm C}$, which are

$$R_{\text{EQSS}} = \frac{\left(R_{\text{OH1}} + R_{\text{S}}\right) R_{\text{EQS}}}{R_{\text{OH1}} + R_{\text{S}} + R_{\text{EQS}}} \tag{7}$$

$$R_{\text{EQCC}} = \frac{(R_{\text{OH2}} + R_{\text{C}}) R_{\text{EQC}}}{R_{\text{OH2}} + R_{\text{C}} + R_{\text{EQC}}}.$$
 (8)

From Fig. 2, $V_{\rm S}(t_f) = V_{\rm C}(t_f) = V_{\rm IH}$ (high-level input voltage) and $V_{\rm S}(t_0) = V_{\rm C}(t_0) = V_{\rm OL}$ (low-level output voltage). Also, from Fig. 4, the respective final voltages at the capacitor terminals are

$$V_{\rm S}(t_{\infty}) = V_{\rm AS} = \frac{V_{\rm OH1} - I_{\rm EQS}(R_{\rm OH1} + R_{\rm S})}{R_{\rm OH1} + R_{\rm S} + R_{\rm EQS}} R_{\rm EQS}$$
 (9)

$$V_{\rm C}(t_{\infty}) = V_{\rm AC} = \frac{V_{\rm OH2} - I_{\rm EQC}(R_{\rm OH2} + R_{\rm C})}{R_{\rm OH2} + R_{\rm C} + R_{\rm EQC}} R_{\rm EQC}.$$
(10)

Because of the high value for $R_{\rm H1}$ and $R_{\rm H2}$ as compared to $R_{\rm S}$, $R_{\rm C}$ and $R_{\rm P}$, in (3) we infer $R_{\rm EQS} \approx R_{\rm EQC}$, so that when replacing (5) and (6) in (2), we can simplify this last equation to

$$R_{S}^{*} = \frac{t_{S}}{t_{C}} R_{C}$$

$$= \frac{R_{EQSS}}{R_{EQCC}} \frac{\ln \frac{V_{AS} - V_{IH}}{V_{AS} - V_{OL}}}{\ln \frac{V_{AC} - V_{IH}}{V_{AC} - V_{OL}}} R_{C}$$

$$= \frac{R_{OH1} + R_{S}}{R_{OH2} + R_{C}} \left(\frac{\ln A}{\ln B}\right) R_{C}$$
(11)

$$A = \frac{V_{\rm AS} - V_{\rm IH}}{V_{\rm AS} - V_{\rm OI}} \tag{12}$$

$$A = \frac{V_{\text{AS}} - V_{\text{IH}}}{V_{\text{AS}} - V_{\text{OL}}}$$

$$B = \frac{V_{\text{AC}} - V_{\text{IH}}}{V_{\text{AC}} - V_{\text{OI}}}.$$
(12)

Note that when $R_{\rm C}=R_{\rm S},\,A\approx B.$ Rearranging (11) yields

$$R_{\rm S}^* = (R_{\rm OH1} + R_{\rm S}) K \left(\frac{\ln A}{\ln B}\right) \frac{1}{\left(1 + \frac{R_{\rm S}}{R_{\rm OH1} + R_{\rm FOS}}\right)}$$
 (14)

where

$$K = \frac{(R_{\text{OH2}} + R_{\text{C}} + R_{\text{EQC}})R_{\text{C}}}{(R_{\text{OH2}} + R_{\text{C}})(R_{\text{OH1}} + R_{\text{EQS}})}.$$
 (15)

Finally, if $R_{\rm S} < R_{\rm OH1} + R_{\rm EQS}$ the term after the quotient of logarithms in (14) can be expressed as a series expansion of $(1+z)^{-1}$ to obtain, by keeping only terms up to the second power:

$$R_{\rm S}^* \cong (R_{\rm OH1} + R_{\rm S}) K \left(\frac{\ln A}{\ln B}\right) \times \left(1 - \frac{R_{\rm S}}{R_{\rm OH1} + R_{\rm EQS}} + \left(\frac{R_{\rm S}}{R_{\rm OH1} + R_{\rm EQS}}\right)^2\right). \tag{16}$$

This approximation yields an error smaller than 1/2 LSB for an n bit digitizer (such as an analog-to-digital converter or a time counter) when

$$1 - \frac{1 - \frac{R_{\rm S}}{R_{\rm OH1} + R_{\rm EQS}} + \left(\frac{R_{\rm S}}{R_{\rm OH1} + R_{\rm EQS}}\right)^2}{\frac{1}{1 + \frac{R_{\rm S}}{R_{\rm OH1} + R_{\rm EQS}}}} < \frac{1}{2^{n+1}}$$
(17)

which leads to

$$R_{\rm S} < \frac{R_{\rm OH1} + R_{\rm EQS}}{2(n+1)/3}.$$
 (18)

This condition can be less restrictive than the condition necessary to consider $R_{\rm EQS} \approx R_{\rm EQC}$ as assumed to obtain (11). Equation (14) shows that there is a zero error (R_{OH1}) , a gain error (because $K \ln A / \ln B \neq 1$) and a nonlinearity error. If ports O1 and O2 were identical, these errors would be zero for $R_{\rm S}=R_{\rm C}$. This implies selecting $R_{\rm C}=R_0$, the mid range value for $R_{\rm S}$. Errors for other sensor resistances depend on $R_{\rm OH1}$, $R_{\rm OH2},\,R_{\rm H1},\,R_{\rm H2},\,V_{\rm IH},\,V_{\rm IL},\,V_{\rm OH1},\,V_{\rm OH2},\,I_{\rm I1},\,I_{\rm H1},\,{\rm and}\,\,I_{\rm H2}.$ These internal microcontroller parameters depend on the technology and manufacturers do not wholly specify them. In any case, (14) shows that nonlinearity increases when $R_{\rm S}$ is close to $R_{\rm OH1} + R_{\rm EQS}$. Since we need $R_{\rm OH1} \ll R_{\rm S}$ to reduce the zero error, it must be $R_{\rm EQS} \gg R_{\rm S}$. The actual limits depend on the particular microcontroller and sensor used. Nevertheless, there are no limits imposed on $R_{\rm S}$ if it is close to $R_{\rm C}$. Therefore, one possible error-reduction method is to divide the sensor range into several sub-ranges and use a different reference resistor at each sub-range. This method, however, does not use the information contributed by (14).

An alternative method is to approximate (14) (or (16)) by a linear response and use two calibration resistors to determine the actual (linearized) response, in addition to the reference resistor needed for time—ratio measurement. This method is valid if the nonlinearity error does not exceed a given error target. For example, if the nonlinearity error must be less than the maximal quantization error for an n bit digitizer and the condition in (18) is fulfilled, the right-most factor in (16) must not differ from 1 by more than $(1/2)^{n+1}$. That is

$$1 - 2^{-(n+1)} < 1 - \frac{R_0 + (\Delta R_S/2)}{R_{\text{OH1}} + R_{\text{EQS}}} + \left(\frac{R_0 + (\Delta R_S/2)}{R_{\text{OH1}} + R_{\text{EQS}}}\right)^2 < 1 + 2^{-(n+1)}$$
(19)

where $R_0 - \Delta R_{\rm S}/2 < R_{\rm S} < R_0 + \Delta R_{\rm S}/2$. Hence, the sensor resistance variation ($\Delta R_{\rm S}$) must fulfill the condition

$$|\Delta R_{\rm S}| < (1 - \sqrt{1 - 2^{1-n}}) (R_{\rm OH1} + R_{\rm EQS}) - 2R_0.$$
 (20)

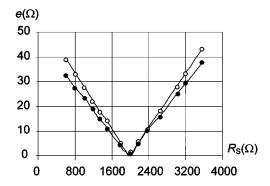


Fig. 5. Absolute error when the sensor resistance $R_{\rm S}$ in Fig. 1 is calculated theoretically with (14) (circles) and experimentally using (2) (dots) with $R_C=2000~\Omega$ and $618.7~\Omega < R_{\rm S} < 3553.2~\Omega$.

If $\Delta R_{\rm S}$ exceeds the limit above, or if the sensor itself is nonlinear, the measurement range can be divided into several sub-ranges and the same two-point calibration procedure applied in each sub-range.

III. EXPERIMENTAL RESULTS

We have built the circuit in Fig. 1 using the a PIC16C71 microcontroller and $R_{\rm P}=220~\Omega$. Because of the inherent ± 1 count uncertainty in time measurements C must be large enough to allow sufficient time to achieve the desired resolution. We use (1) with $V_r=V_{\rm OH}=5$ V, $V_t=V_{\rm IH}=3.24$ V and $V_0=V_{\rm OL}=0.002$ V. To obtain 2^n counts for the difference between the charging time when measuring the minimal $R_{\rm S}(=R_0-\Delta R_{\rm S}/2)$ and the charging time when measuring the maximum $R_{\rm S}(=R_0+\Delta R_{\rm S}/2)$ we need

$$f_e t_{t \text{ max}} - f_e t_{t \text{ min}} = 2^n \tag{21}$$

where $f_{\rm e}$ is the equivalent frequency counted during the time interval to measure. Therefore, C must fulfill the condition

$$C > \frac{2^n}{f_e \Delta R_S \ln \frac{V_{\text{OH}} - V_{\text{OL}}}{V_{\text{OH}} - V_{\text{IH}}}}.$$
 (22)

We have measured the time interval with the internal timer of the microcontroller. The program takes three machine cycles. Since a machine cycle of the PIC16C71 takes four clock cycles (11 MHz), we have $f_{\rm e}=(11$ MHz)/12. Hence, for $\Delta R_{\rm S}=3553.2~\Omega-618.7~\Omega=2934.5~\Omega,$ which is a typical range for the Pt1000 temperature sensor and, for example, n=13 bits, we need $C>2.9~\mu{\rm F}.$ We have used $C=4.1~\mu{\rm F},$ which improves the resolution to 0.25 $\Omega.$ We have also used $R_{\rm C}=2000~\Omega,$ that is, close to the midrange value for $R_{\rm S}.$

We measured the microcontroller parameters not specified in data sheets and calculated the coefficients in (14). Fig. 5 shows the absolute error between the estimated $R_{\rm S}^*$ [using (14) and experimentally using (2)] and the actual $R_{\rm S}$. The error is zero for $R_{\rm S}=R_{\rm C}$ and increases when $R_{\rm S}$ separates from $R_{\rm C}$. Using different reference resistors would keep that error below a given target.

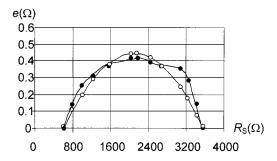


Fig. 6. Theoretical (circles) (predicted) and experimental (dots) absolute error when using time—ratio measurements and the transfer characteristic is estimated from two calibration resistors.

The least squares method shows that the best straight-line approach to (14) is

$$R_{\rm S}^* = 0.9667 R_{\rm S} + 66.394 \,\Omega. \tag{23}$$

Similarly, the best straight-line approach to the experimentally measured sensor resistance is

$$R_{\rm S}^* = 0.97 R_{\rm S} + 55.83 \,\Omega.$$
 (24)

Therefore, the experimental zero and gain errors are close to those predicted by (23). From (18), for n=13 bit, it should be $R_{\rm S} < 300~{\rm k}\Omega$; hence, the approximation in (16) is acceptable. Therefore, instead of measuring the microcontroller parameters, if $\Delta R_{\rm S}$ fulfills the criterion in (20), we can use two calibration resistors to find the approximate transfer characteristic for a given microcontroller and use it to find the actual $R_{\rm S}$ from the measured $R_{\rm S}^*$ when in (2) we select $R_{\rm C}=R_0$. From (20), it should be $\Delta R_{\rm S} < 3000~\Omega$, which is true in our experiment. If $R_{\rm S2}^*$ and $R_{\rm S1}^*$ are the estimated values for the two calibration resistors $R_{\rm S2}$ and $R_{\rm S1}$, the linear transfer characteristic used to find the actual $R_{\rm S}$ from the measured value $R_{\rm S}^*$ when $R_{\rm C}=R_0$ is

$$R_{\rm S} = \frac{(R_{\rm S}^* - R_{\rm S1}^*)(R_{\rm S2} - R_{\rm S1})}{R_{\rm S2}^* - R_{\rm S1}^*} + R_{\rm S1}.$$
 (25)

Fig. 6 shows the predicted and experimental absolute error when applying this procedure using calibration resistors equal to the theoretical extreme for $R_{\rm S}$. The maximumal error is 0.5 Ω and depends on the available measurement resolution, which was limited to 0.25 Ω for the time-interval measurement method implemented.

IV. CONCLUSION

Sensor-to-microcontroller interfaces based on PWM and time-ratio measurement have zero, gain and nonlinearity errors due to the nonideal internal input and output resistances and leakage currents of digital ports [(14) and (16)]. Ratio measurements according to (2) reduce errors only for sensor resistances close to the reference resistor. Therefore, the first error-reduction method measures the time ratio using a reference resistor selected according to the measured sensor

resistance: for a sensor with a wide resistance range, divide that range into sub-ranges and use a different reference resistors in each sub-range. However, this procedure does not use all the information provided by these reference resistors. An improved method uses a pair of calibration resistors to estimate the actual (linearized) transfer characteristic in a given sensor range and measures the time ratio using a reference resistor close to the mid range value for the sensor. This procedure, when applied to a sensor whose resistance goes ranges from about 600 Ω to 3550 Ω , yields an absolute error of 0.5 Ω .

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