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An Improved Buffer for Bioelectric Signals

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Abstract—We propose an ac-coupled amplifier that offers a high input impedance, thus making it suitable for bioelectric signal amplification. We also present the necessary formulas for calculating its input impedance and transfer function in order to facilitate its adaptation to different applications.

I. INTRODUCTION

The amplification of bioelectric signals requires them to be ac coupled before high gain amplification, either to avoid saturations due to contact potentials arising at electrode-electrolyte interfaces [1] or for further band-limiting purposes. At the same time, in order to reduce interferences due to the usually high electrode-skin impedance and its variability [2], the amplifier needs to have a very high input impedance. This calls for an input stage acting as buffer.

In most designs, each one of these functions—ac coupling and buffering—is implemented at different stages of the circuit. In this work, we prove, both analytically and experimentally, that it is possible to merge both functions in a single stage. We also present the formulas enabling us to apply the proposed circuit to different signals in order to tailor it to particular needs.

II. AC COUPLING AND INPUT IMPEDANCE IN BIOELECTRIC SIGNAL AMPLIFIERS

A. AC Coupling

In order to measure bioelectric signals, it is necessary to use electrodes. At any electrode-electrolyte interface, a contact potential arises, whose magnitude is very high and subject to variations due to many parameters [1]. Thus, in spite of two electrodes being involved in any biopotential measurement, the prospect of a tight balance between them is so remote that, when designing the amplifier, the possible presence of a dc potential higher in magnitude than the expected signal must be taken into consideration. The measurement is certainly a differential one, but contact potentials would seldom cancel each other out.

The usual solution is to decouple dc voltages to prevent offset potentials from saturating the amplifier whenever its voltage gain has a value of 40 dB or even lower. This need of an ac coupling is a serious drawback for various reasons. In low-noise design, for example, it is important to have as much gain as possible concentrated in the first stage of the amplifier. Bioelectric signals normally require voltage gains in excess of 60 dB.

Therefore, if ac coupling cannot be implemented before the first amplifier, as happens with ordinary circuits, the designer is prevented from taking advantage of the improved performances of

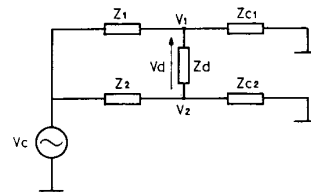


Fig. 1. Circuit model for the input of a biopotential amplifier showing how any imbalance in the front end converts a common mode signal into a differential one. The imbalance can be on the electrode side or on the amplifier side.

modern operational amplifiers which offer very low noise levels because he or she is compelled to postpone high-voltage gains until an ac signal is obtained [3]. Furthermore, for a given tolerance in components, the CMRR attained increases if voltage gain is large in differential stages, although it is unaffected by voltage gain in nondifferential ones.

Also, when dealing with several electrodes and only one or a few differential amplifiers, it is necessary to place a buffer for each electrode. If they are dc coupled, it may happen that a saturation occurs in the amplifier when switching from one lead to another because of electrode offset potentials. Therefore, some provision must be made in order to rapidly restore voltage levels. An ac-coupled buffer might eliminate this problem.

B. Input Impedances

Another requirement when measuring bioelectric signals is the need for differential amplifiers to have very high input impedances, both differential and common mode. A high differential mode input impedance minimizes loading effects. A high common mode input impedance reduces the differential mode voltage that results from common mode input signals [2]. This common mode-to-differential mode conversion results from any imbalance between both input terminals, as indicated in Fig. 1, and shows up as an increased interference at power line frequency.

In Fig. 1, Z_1 and Z_2 are any impedances in front of the amplifier. Whenever either $Z_1 \neq Z_2$ or $Z_{c1} \neq Z_{c2}$, the former being the usual situation, then $v_1 \neq v_2$, and an interfering signal will result. In most designs, Z_1 and Z_2 are mainly attributable to electrode-skin impedances. The resulting interference can be effectively reduced by making Z_{c1} and Z_{c2} very high. Buffering each electrode with a unity gain voltage follower or a buffer with gain is the usual solution.

The placement of a capacitor in series with each electrode would certainly block any dc voltage, and therefore might be considered for decoupling. However, there is a simultaneous need for a path for dc bias currents entering the amplifier, and the blocking capacitor would prevent any dc current from coming through the electrodes. Placing a resistor from each input of the amplifier to ground would solve this problem, but it would be necessary to use rather unusual components in order to obtain the high input impedance required for a bearable interference and the optimum low-frequency response.

For these reasons, typical biopotential amplifiers have a dc-coupled moderate-gain input stage, thus biasing through the electrodes and reducing the risk of saturation at this stage. Afterwards—preferably once dealing with a nondifferential signal—an ac-coupled stage offering an additional gain is put in place. A typical circuit is the one proposed by Neuman [4].

III. ANALYSIS OF AN AC-COUPLED BUFFER

In accordance with the above reasons, the placement of a series capacitor in front of an amplifier will be a satisfactory solution, provided that a suitable way of biasing the circuit is offered. This

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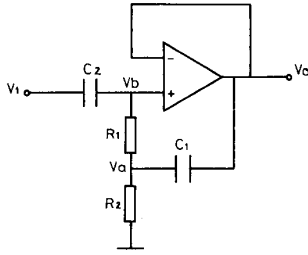


Fig. 2. Proposed ac-coupled buffer. A series capacitor blocks dc currents.

means that unusual resistors need not be required and that input impedances must be kept high in any case.

Thakor and Webster [5] proposed a double-ended two-electrode design based upon a bootstrapped operational amplifier as an input stage. This stage provided a high common mode input impedance and a path for the dc bias current of the operational amplifier without using high-value resistors. The effective input impedance increased with frequency and was very high at power line frequency. A refined design was later presented by Winter and Webster [2].

On the face of it, it would seem that by merely placing a suitable capacitor in series with the input of such a circuit, an ac-coupled buffer will be obtained. The resulting circuit is the one in Fig. 2. The validity of this assumption, however, must be checked by examining both the characteristics we are interested in, that is, the transfer function and input impedance.

A. Transfer Function

The relationship between v_0 and v_1 can be found by combining the following equations, deduced from a simple inspection of the circuit in Fig. 2:

$$C_2 s(v_1 - v_b) = (v_b - v_a)/R_1 \quad (1)$$

$$(v_b - v_a)/R_1 + (v_0 - v_a)C_1 s = v_a/R_2 \quad (2)$$

$$(v_b - v_0)A_0\omega_a/(s + \omega_a) = v_0 \quad (3)$$

where A_0 is the open-loop gain of the operational amplifier and ω_a is its corner frequency. Given that $A_0 \gg 1$, the desired relationship proves to be

$$\frac{V_0(s)}{V_1(s)} = \frac{s\tau_2(1 + \tau_1 s)}{1 + s[\tau_2 + (\tau_m + \tau_a)/A_0] + s^2[\tau_n^2 + \tau_a(\tau_m + \tau_2)/A_0] + s^3\tau_n^2\tau_a/A_0} \quad (4)$$

where

$$\tau_2 = (R_1 + R_2)C_2$$

$$\tau_1 = (R_1//R_2)C_1$$

$$\tau_m = R_2C_1$$

$$\tau_a = 1/\omega_a$$

$$\tau_n^2 = R_1R_2C_2C_1 = \tau_2\tau_1.$$

For high-frequency signals, expression (4) indicates that there is an increasing attenuation. The circuit will behave as a unity gain voltage follower only at low frequencies, excluding dc.

For low-frequency signals, the circuit time constants can be designed in such a way that all the terms including A_0 in the denominator become negligible. If this is the case, that is, for frequencies below $A_0/(\tau_m + \tau_a)$, below $[A_0/(\tau_m + \tau_2)\tau_a]^{1/2}$, and below $[A_0/\tau_n^2\tau_a]^{1/3}$, then expression (4) can be simplified to

$$\frac{V_0(s)}{V_1(s)} = \frac{s(s + 1/\tau_1)}{s^2 + s/\tau_1 + 1/\tau_1\tau_2} \quad (5)$$

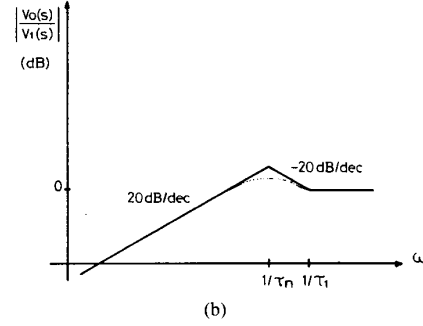
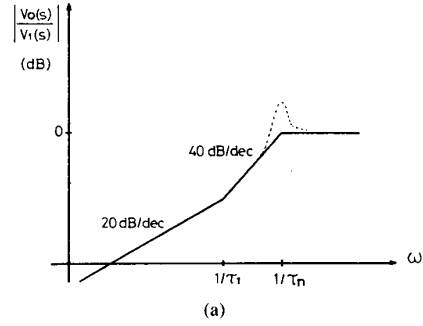


Fig. 3. Frequency characteristic of circuit in Fig. 2—see (5). Depending on the relative value of the components, two cases, (a) $\tau_1 > \tau_n$, (b) $\tau_1 < \tau_n$ will result. A flat frequency response is not granted in all cases. The frequency where there is the maximum amplitude is given by (14) and the maximum amplitude is given by (15). Corner frequency (-3 dB) is given by (16).

The damping factor ϵ and the natural frequency ω_n are given, respectively, by

$$\epsilon = (\tau_2/\tau_1)^{1/2}/2 \quad (6)$$

$$\omega_n = (1/\tau_1\tau_2)^{1/2} = 1/\tau_n. \quad (7)$$

Thus, while the frequency response will always be high pass at low frequencies, its shape will depend on the relative magnitude of τ_1 and τ_n , as indicated in Fig. 3.

The decision about whether it is better to have τ_1 greater, lower, or equal to τ_2 cannot be taken, however, before examining its effect on the input impedance.

B. Input Impedance

The input impedance for the circuit in Fig. 2 is given by

$$Z = \frac{v_1}{(v_1 - v_b)C_2 s} \quad (8)$$

After some manipulations of expressions (1)–(3), and given that $A_0 \gg 1$, the following is obtained:

$$Z = \frac{1}{C_2 s} \left(1 + \frac{s\tau_2 + s^2(\tau_n^2 + \tau_a\tau_2/A_0) + s^3\tau_n^2\tau_a/A_0}{1 + s(\tau_m + \tau_a)/A_0 + s^2\tau_a\tau_m/A_0} \right) \quad (9)$$

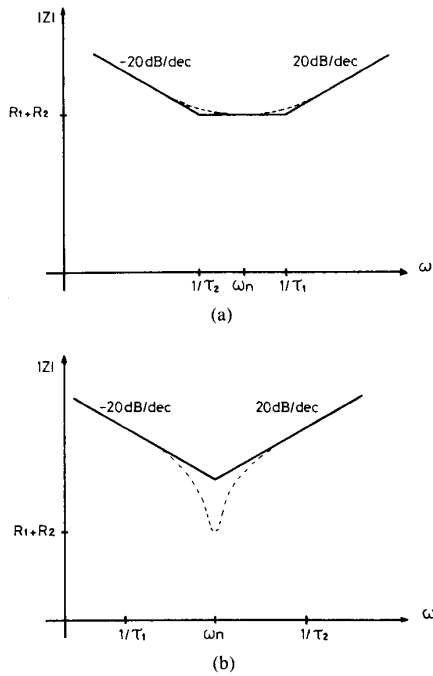


Fig. 4. Input impedance of circuit in Fig. 2—see (11). Depending on the relative value of the components, two cases, (a) $\tau_1 < \tau_2$, (b) $\tau_1 > \tau_2$ will result. A high input impedance is not granted in all cases.

Here, too, if only the low-frequency part of the spectrum is considered, the following simplified formula is obtained:

$$Z = \frac{1}{C_2 s} (1 + s\tau_2 + s^2\tau_1\tau_2) \quad (10)$$

and by replacing time constants by their expressions, the following results:

$$Z = \frac{1}{C_2 s} + R_1 + R_2 + sR_1R_2C_1. \quad (11)$$

This is the same result as that obtained by Winter and Webster [2] with an additional term in our case contributed by series capacitor C_2 . Fig. 4 shows this input impedance as a function of frequency for the cases $\tau_2 > \tau_1$ and $\tau_2 < \tau_1$.

IV. DESIGN OF AN AC-COUPLED BUFFER

An appropriate ac-coupled buffer must simultaneously have a flat frequency response and a very high input impedance. In order to ensure a high input impedance at power line frequency and its harmonics, without using high-value resistors, it is necessary for these frequencies to fall within the zone where Z looks like an inductor, that is,

$$R_1 + R_2 < \omega_i R_1 R_2 C_1 \quad (12)$$

where ω_i is the frequency of the interfering signal considered.

The frequency response in the passband must be flat according to some criterion. The American Heart Association, for example, recommends a response for direct-writing electrocardiographs which is flat to within ± 0.5 dB over the frequency range from 0.14 to 25 Hz. Therefore, it is necessary to ensure both that at the lower frequency of interest, the attenuation, if any, does not exceed 0.5 dB, and that the damping factor in (5) has a value which does not lead to overshooting in the frequency response.

By equating the numerator and denominator modulus in (5), it can easily be deduced that the modulus of the transfer function

takes a value of 1 at a frequency ω_0 given by

$$\omega_0 = \omega_n / \sqrt{2}. \quad (13)$$

At the same time, by observing the approximated transfer function for $\omega \gg \omega_n$, it is concluded that the modulus is also 1 at frequencies well above ω_n .

Therefore, a second condition to be considered when designing such a buffer is for the minimum frequency of interest not to be lower than ω_0 . It is important to observe that this condition is independent of the damping factor.

The transfer function (5) has a maximum amplitude at a frequency ω_r given by

$$\omega_r = \omega_n [1 + (1 + 8\epsilon^2)^{1/2}]^{1/2} / \sqrt{2}. \quad (14)$$

The magnitude of this maximum is given by

$$\left| \frac{V_0(s)}{V_1(s)} \right|^2 = \frac{1 + 8\epsilon^2 + (4\epsilon^2 + 1)(1 + 8\epsilon^2)^{1/2}}{1 + 8\epsilon^2 + (4\epsilon^2 - 1)(1 + 8\epsilon^2)^{1/2}}. \quad (15)$$

By subtracting 1 to the square root of this expression, the magnitude of the overshooting in the frequency response is obtained. It has been represented in Fig. 5 as a function of ϵ . According to the latter, if the maximum deviation allowed with respect to a flat response is of 0.5 dB, then ϵ must be higher than 1.76.

The value of ϵ will influence the rate of attenuation at frequencies below ω_0 . The frequency ω_m at which there is a 3 dB attenuation is easily deduced from (5) to be

$$\omega_m = \omega_n \left\{ [(2\epsilon^2 + 1)^2 + 1]^{1/2} - (2\epsilon^2 + 1) \right\}^{1/2}. \quad (16)$$

Therefore, the lower the value of ϵ , the higher the value of ω_m , as shown in Fig. 5 (right vertical axis).

Example: Let us suppose that a buffer for the ECG signal is desired, with an input impedance equal to or higher than 100 M Ω at 50 Hz, a flat frequency response (± 0.5 dB) from 0.14 Hz, and without using resistors higher in value than 10 M Ω .

From (12) and (13), it is deduced that

$$R_1 R_2 C_1 100\pi = 10^8. \quad (17)$$

From (13), it follows

$$2\pi 0.14 = \frac{1}{(2R_1 R_2 C_1 C_2)^{1/2}}. \quad (18)$$

If a maximum attenuation is desired for frequencies below 0.14 Hz, then according to Fig. 5 and (16), ϵ must be 1.76. From (6), it follows that

$$(R_1 + R_2)^2 C_2 = 12.4 R_1 R_2 C_1. \quad (19)$$

Expressions (17) and (18) determine $C_2 = 2 \mu\text{F}$. Expressions (17) and (19) determine the condition $R_1 + R_2 = 1.4 \text{ M}\Omega$. If, in order to use resistors with the lowest possible value, the additional condition $R_1 = R_2 = R$ is imposed, then $R = 720 \text{ k}\Omega$ and from (17), $C_1 = 650 \text{ nF}$.

We measured the transfer function and input impedance of a circuit such as that in Fig. 2 implemented with $R_1 = R_2 = 680 \text{ k}\Omega$, tolerance 5 percent, $C_1 = 680 \text{ nF}$, and $C_2 = 2 \mu\text{F}$, tolerance 10 percent. The operational amplifier was a TL071 and the circuit was arranged in order to minimize parasitic capacitances. The following numerical results were read from the display on a Hewlett-Packard Spectrum Analyzer 3582A: minimum frequency where gain was 0 dB, 0.16 Hz; frequency of -3 dB gain, 0.052 Hz; and maximum gain, 0.4 dB at frequencies between 0.288 and 0.56 Hz. By calculation, the following results were deduced: input impedance at 50 Hz, 141 M Ω ; maximum input impedance, 1.63 G Ω at 124 Hz.

The preceding results show that the input impedance at 50 Hz is higher than predicted, but by making allowance for the tolerances of the components, it can be accepted. Yet the decreasing of the impedance starting at 124 Hz cannot be explained by the model

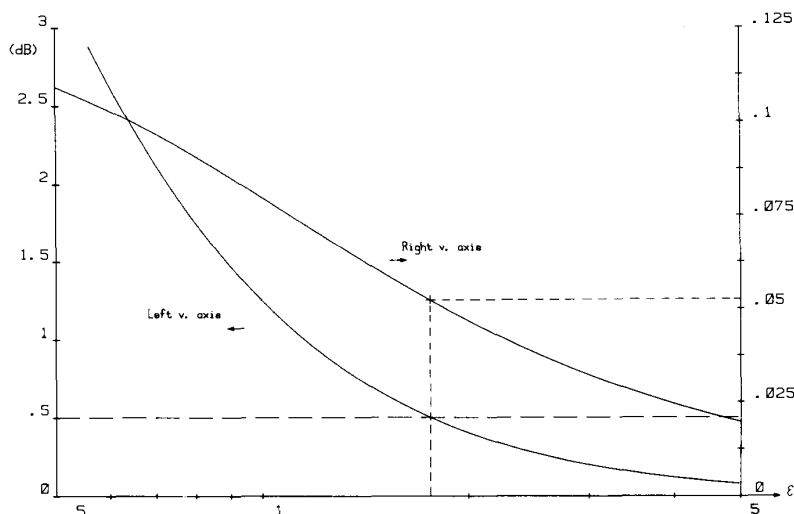


Fig. 5. Horizontal axis: damping factor ϵ . Left vertical axis: overshooting of the transfer function of the buffer see (15). Right vertical axis: frequency where the gain is -3 dB [ω_m , (16)] when the frequency, where gain is 0 dB (ω_0), has been chosen to be 0.14 Hz.

used for the circuit because gain limitations of the operational amplifier do not have any noticeable effect at these low frequencies.

The fact that the impedance at 124 Hz is 11.5 times that at 50 Hz, instead of 2.5 times as predicted by an inductive-looking impedance, suggests that there is some resonance effect. Assuming this, if an input capacitance C paralleling the equivalent input inductor L is considered, then from the value of the impedance at 50 and 124 Hz, it is deduced that $C \approx 3$ pF and $L \approx 0.4$ MH. At 50 Hz, this inductance has an impedance of 120 M Ω . The frequency of resonance is near 145 Hz.

V. CONCLUSIONS

By adding a single capacitor to a well-known dc-coupled buffer, it is feasible to obtain an ac-coupled buffer with very high input impedance without resorting to high-value resistors. The necessary formulas for adapting this circuit to any particular need when dealing with low-frequency signals are relatively simple. By using the appropriate equations, it is possible to choose the value of the input impedance, the maximum overshooting in the frequency response, and the "low corner" frequency.

The effective input impedance will, in fact, be limited by common mode input capacitances associated not only with the operational amplifier, but also with input protections, RF filters, and circuit layout. These capacitances will, however, provide an increased impedance near the frequency of resonance.

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Two-Dimensional Fourier Spectrum of QRST Integral Maps in Classification of Patients Prone to Ventricular Arrhythmia

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Abstract—Two-dimensional Fourier spectra of QRST integral maps, obtained by body surface potential mapping, were analyzed to identify subjects prone to ventricular arrhythmia, when they have not been identified by the extrema count method. The diagnostic performance (84.38 percent) of the peak value of the Fourier spectrum as a classifier for subjects prone to ventricular arrhythmia showed an improvement of 3.65 percent over the use of the extrema count method as a classifier.

INTRODUCTION

Body surface potential maps have been found to reveal more information about the electrical activity of the heart than can be found from conventional electrocardiograms [1], [2]. The extensive amount of data involved in a total body surface potential map permits its visual analysis only in a global fashion. The maps are qualitatively analyzed in terms of simple parameters such as the location of maxima and minima [3], [4]. The application of analytical representation techniques to body surface potential mapping allows visual study of the maps in an orthogonal or transform domain and the assessment of the maps on the basis of other criteria; for example, these techniques have been applied to the determination of potentials at sites between electrodes [5].

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