

# Fuzzy Controlled THD Reduction and Performance Improvement of a PMBLDC Motor Using MMLI

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**Abstract:** In this study, an improved chopper is implemented for current extraction, enhanced solar power generation control, and increased voltage. A simplified space vector control technique is applied to operate the permanent-magnet brushless DC drive (PMBLDC) by controlling the inverter operation. This is used as a special electrical machine with reliable operation and maintenance control. This system uses the Perturb and observer (P&O) algorithm which maximises the PV-system output power and reduces the overall cost. A fuzzy logic controller (FLC) is used to control the duty cycle and increase the PV voltage. The modified multilevel inverter (MMLI) reduces harmonic distortions and low-switching losses by using a self-tuning filter (STF). This study achieved a better PMBLDC system performance, an improved efficiency, and eliminated the total harmonic distortion (THD) by the self-tuning filter with and without using MATLAB/SIMULINK.

**Keywords:** Active Power Filter; Boost converter; Fuzzy logic controller; Maximum Power Point Tracking; Multilevel Inverter; Permanent Magnet Brushless DC Drive; photovoltaic array; Self-Tuning Filter

## 1 INTRODUCTION

The photovoltaic framework as a sustainable power source is popular in several countries. In this systems sun oriented vitality becomes significant for the vitality supply. PV clusters (19) generate power from sunlight and its different parts transform, disperse, and store the vitality delivered by the framework. Sun-oriented vitality is utilised by charging batteries for different applications. A sun-oriented PV [1, 7-10] exhibits generated power by accomplishing MPPT calculations that utilises a DC-DC converter [18, 20]. The chopper converter (support converter) [6] is used to increase the info voltage. The essential information voltage is low and yield voltage is high. The aim of this study is to make a chopper circuit with an info voltage of 12 V and yield voltage of 24 V.

The motivation behind innovating fuzzy logic controllers (FLCs) [2, 5] was to control engine drives. In this study, an FLC controls the obligation cycle and builds up the voltage. The FLC can effectively deal with heap contrasts and disturbances on the grounds where the controller is used. Brushless DC (BLDC) engines [5, 7-9] are synchronous engines with perpetual magnets on the rotor and an armature twisting on the stator. It can operate rapidly under different conditions. It has an advanced engine that provides a huge torque and requires low maintenance because of the annihilation of a mechanical commutator. The inverter switches the scaffold ON and OFF to ensure a current flow in separate loops. The separate loops depend upon the data of the appropriate commutator, engine pivot, and rotor position. In the rotor structure, windings were placed at the core of the motor. The rotor magnets are used as a covert o dissipates the heat from the motor. One of the advantages of a staggered inverter is their low yield voltage e that results in a high yield quality, low consonant segment, easy calculability, and low exchanging misfortunes. Therefore, in this frame work we chose a sun-powered PV, DC-DC converter [15], PMBLDC [15], and an adjusted staggered inverter which can operate in load and barometric conditions. A MATLAB/SIMULINK is utilized to confirm the exhibition of the PMBLDC engine utilizing high advance up adjusted multi-inverter. In Existing systems have high

ripple (THD) and more switching loss. The challenges of the paper are sorted out as follows: in area 2 is clarifies the difficult plan; in segment 3 depicted the framework arrangement and rule of activity; in segment 4 clarifies the structure of PMBLDC engine utilizing high advance up altered multi-inverter; Results has been talked about in segment 5 and segment 6 at last closes the paper.

## 2 PROBLEM FORMULATION

The aim of this paper is to propose a PMBLDC motor using the high step up the modified multi-level inverter. To Design of proper various control system parameters, maximum power point tracking (MPPT) and FLC technique used for Solar power generation. An effective photovoltaic system to achieve without loss and the analysis of PMBLDC motors are made with the boost converter, interleaved boost converter with Fuzzy logic control circuit and MPPT. The boost converter interleaved with PMBLDC Drive the modified multi-level inverter and self-tuning filter to eliminate harmonics. This paper presents how reducing harmonics is achieved to the new topology of modified multilevel inverter. In order to achieve the better system performance, efficiency and reliability by Brushless DC motor.

## 3 SYSTEM CONFIGURATION AND PRINCIPLES OF OPERATION

In this proposed work, a solar array fed Chopper i.e. Boost converter which is used to increase level of voltage. The DC - DC converter is to increase the PV voltage and balance the current level. Fuzzy logic is utilized to control the duty cycle and to increase the voltage. The MMLI has been used for adjustable speed drives, to eliminate harmonics and high or medium power system because of static reactive power compensation. Hall effect sensor is to control speed and position of the PMBLDC. In this PMBLDC motor using the high step up [6] the modified multi-level inverter (MMLI) system achieve better system performance, improve PMBLDC motor efficiency and to eliminate harmonics using self-tuning filter [2]. Fig. 1

shows the block diagram of PMLBDC Motor using a high step up MMLI with STF filter.

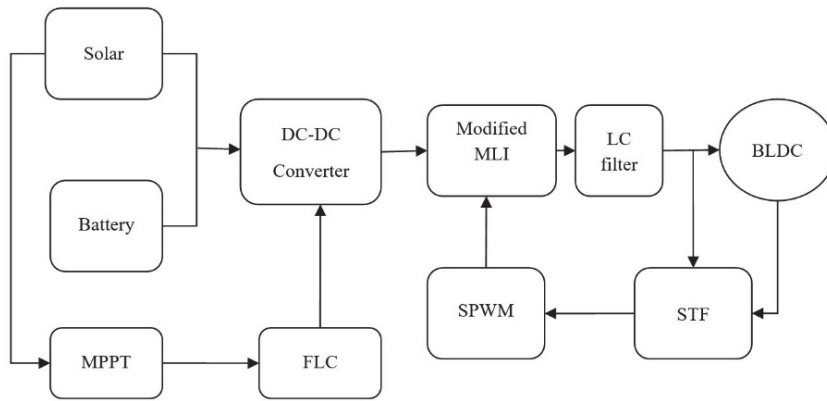


Figure 1 PMLBDC Motor Using High Step up Modified Multi-inverter Block Diagram

#### 4 DESIGN OF PMLBDC MOTOR USING HIGH STEP UP MODIFIED MULTI-INVERTER

simulation consists of PMLBDC motor, Solar PV, Battery, DC-DC Converter, SPWM, MMLI, FLC, STF and Hall Effect sensor.

In the Fig. 2 shows the simulation for PMLBDC Motor using a high step up modified multi-inverter. This

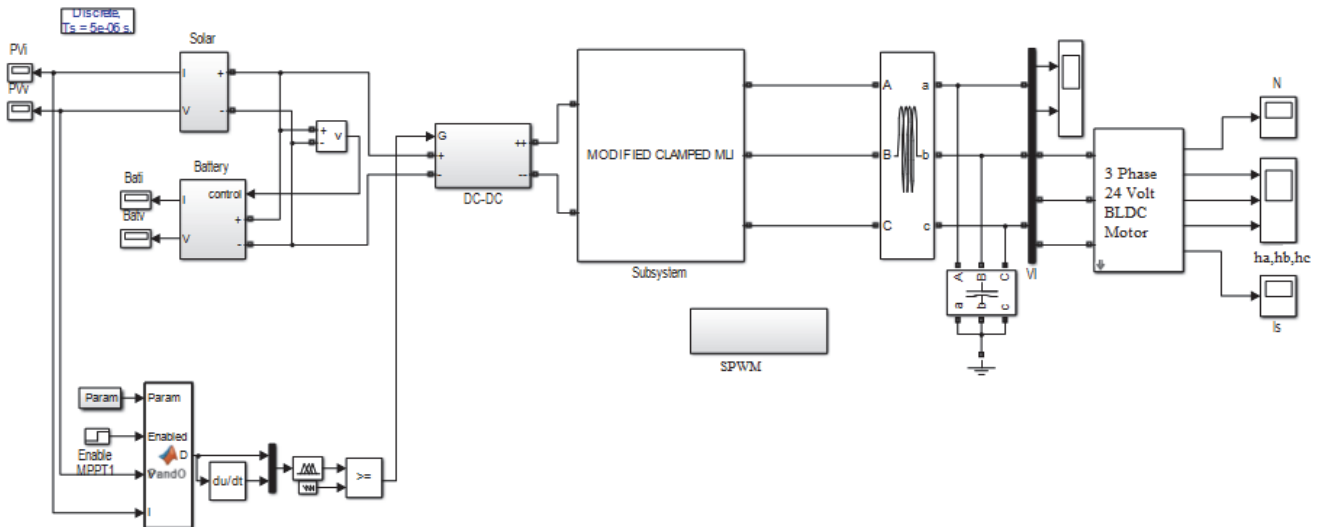


Figure 2 Simulation for PMLBDC Motor Using High Step up Modified Multi-inverter

#### 4.1 Maximum Power Point Tracker

Maximum power point tracking methodology is adopted for extracting maximum power from the PV module. The process is implemented by varying the duty cycle of DC to DC converter [5]. The energy is extracted in the greatest possible way from the PV module clusters. The most effective voltage is obtained from the maximum point tracked in the PV module. To obtain this Perturb and Observance (P&O) algorithm is adopted [10, 16]. In this proposed technique, if the battery framework input voltage is not exactly the yield voltage so help converter ought to be picked for MPPT [26] P&O (Perturb and Observer) calculation. In the Fig. 3 shows the P&O calculation flowchart.

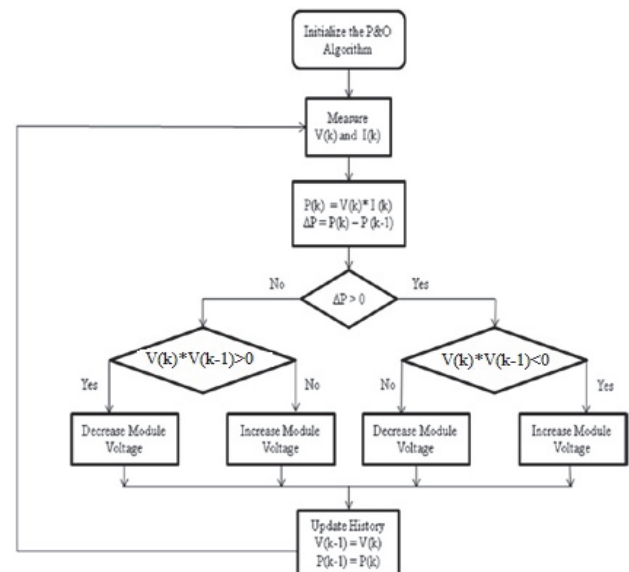


Figure 3 Perturb and Observer flowchart

P&O calculation causes the intensity of the sun-oriented PV [16] cluster to change continually and if the force increments because of annoyance. After that irritation is preceded a similar way then the force diminishes after that pinnacle power is reached and after that bother turns around. The calculation vacillates around the pinnacle moment that the consistent state is reached.

### 4.2 DC - DC Converter (Boost Converter)

PMBLDC requires 24V for its operation so the voltage obtained from the battery is not sufficient. In our application, the voltage obtained from solar is 12V so it is not enough to drive a PMBLDC motor [19-20] as it requires 24 V for its operation. The boost converter is constantly increasing the input voltage. So, it is known as a step-up converter.

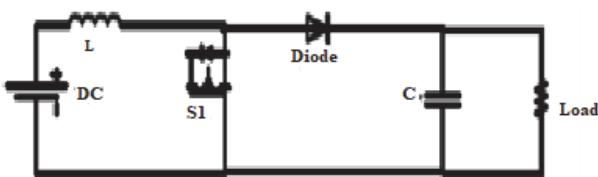


Figure 4 Boost Converter

In Fig. 3 shows the circuit diagram of the boost converter. DC converter [5] can be used as a switching diode; it converts a fixed DC voltage into variable DC voltage. In DC-DC converter [17]. Tab. 1 shows the parameters are used in the boost converter.

Table 1 Parameters used in the boost converter

| S.No | Parameters                | Value |
|------|---------------------------|-------|
| 1    | Switching frequency / kHz | 5     |
| 2    | Input Voltage / V         | 12    |
| 3    | Inductance / mh           | 0.12  |
| 4    | Capacitor / mf            | 4.55  |
| 5    | Load Resistance / Ω       | 0.96  |

### 4.3 Fuzzy Logic Controller

Fuzzy logic [2] is capable of making effective decisions and this technique is widely applied today. To implement fuzzy logic [12] is applied to an application requires the following three steps. The first step is called fuzzification. It is changing of real values into fuzzy values. Fuzzy inference process is the second step combined the fuzzy variables with the control rules to obtain the fuzzy output. The finally is defuzzification it is reconverted fuzzy variables into real world signal.

Table 2 Fuzzy Rules

| $dE$ \ $E$ | ZE | PS | PM | PB |
|------------|----|----|----|----|
| ZE         | ZE | ZE | ZE | ZE |
| PS         | PS | PS | PS | PM |
| PM         | PM | PS | PM | PM |
| P          | PB | PM | PM | PB |

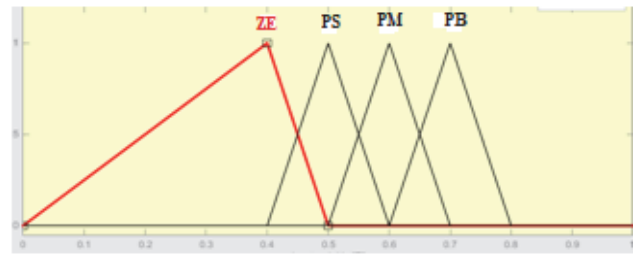


Figure 5 Fuzzy Diagrammatic Representation

The contribution to the fluffy rationale is a mistake ( $E$ ) and change in blunder ( $dE$ ) and the yield obtained is the dynamic segment of current. Both  $E$  and  $dE$  are changed from ceaseless factors to fluffy factors by utilizing four part works. The yield of the fluffy rationale controller is assessed utilizing fluffy principle appeared in Tab. 2 for four participation capacity of each info, the yield will be 16. Fluffy arrangements of a blunder and change in mistake are consolidated to create a standard for the framework. These principles are known as rule base framework. Fig. 4 shows the fluffy control rule diagrammatic portrayal.

### 4.4 Modified Multilevel Inverter

The staggered inverter [11-14] has been utilized for medium or high force framework in view of static responsive force remuneration, movable speed drives and to lessen consonant mutilation. A modified 7 level and 13 level inverter is proposed in the methodology.

An ' $n - 1$ ' bearer is utilized for ' $n$ ' level SPWM inverter. SPWM utilizes triangular wave for sinusoidal reference wave structure. The reference is sine wave structure and triangular bearer wave structure utilized for staggered SPWM. Semiconductor stress is reduced with usage of multilevel inverter and thus reducing the exchanging misfortunes.

Table 3 Parameters of Multilevel Inverter

| SI No | Parameters                                   | Range        |
|-------|--|--------------|
| 1     | 7-level and 13-level inverters Input Voltage | 24 V         |
| 2     | IGBT Frequency                               | 5 kHz        |
| 3     | Inductor                                     | $2 e^{-3}$   |
| 4     | Capacitor                                    | $0.5 e^{-6}$ |

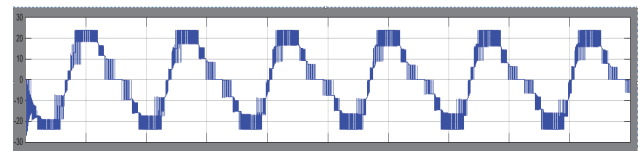


Figure 6 Re-enactment yield of 7 - level inverter

In Fig. 6 shows the recreation yield of a 7-level inverter. A 7-level inverter offers great outcomes yielded wanted 7-level yield with low complete consonant mutilation contrasted with the regular strategy.

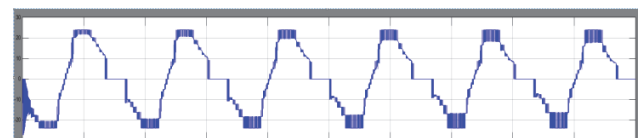


Figure 7 Re-enactment yield of 13-level inverter

In the Fig. 7 shows the mimicked yield of the 13-level inverter. The 13-level inverter offers great outcome yielded wanted 13-level yield with exceptionally low symphonious contortion contrasted with the 7-level inverter. From the past topology made far progress in the examination.

**4.5 Self-tuning Filter**

A self-tuning channel (STF) is to supplant a low pass channel and it is an arrangement dynamic channel. The STF is to diminish the most extreme degree of symphonies end and improve the framework execution. In this proposed framework, the all out symphonies decrease could be seen with self-tuning channel. One self-tuning channel [2] is utilized to remove the essential part straightforwardly from misshaped volt and amps signals in the  $\alpha - \beta$  reference outline. The PC reproductions under mutilated voltage conditions are utilized to figure the reference current age methodologies.

The transfer function equivalent of the integration in the frame SRF by:

$$V_{x,y}(t) = e^{-j\omega t} \int e^{-j\omega t} u_{x,y}(t) dt \tag{1}$$

The instantaneous signals, Which  $u_{x,y}$  and  $V_{x,y}$  are respectively before and after integration in the SRF. In the Eq. (1) transfer function expressed after Laplace transformation.

$$H(s) = \frac{V_{x,y}(s)}{u_{x,y}(s)} = \frac{s + j\omega}{s^2 + \omega^2} \tag{2}$$

In this paper to get  $\omega_c$  so Eq. (2) becomes:

$$H(s) = \frac{V_{x,y}(s)}{u_{x,y}(s)} = \frac{(s + k) + j\omega_c}{(s + k)^2 + \omega_c^2} \tag{3}$$

Where  $\omega_c$  is STF with cut off frequency.

To modified the Eq. (2) and Eq. (3) by substituting the input signals  $u_{x,y}(s)$  by  $x_{\alpha\beta}(s)$  and the output signals  $v_{x,y}(s)$  by  $\hat{x}_{\alpha\beta}(s)$ , Eq. (4) and Eq. (5) can be obtained.

$$\hat{x}_{\alpha}(s) = \frac{k}{s} [x_{\alpha}(s) - \hat{x}_{\alpha}(s)] - \frac{\omega_c}{s} \hat{x}_{\beta}(s) \tag{4}$$

$$\hat{x}_{\beta}(s) = \frac{k}{s} [x_{\beta}(s) - \hat{x}_{\beta}(s)] - \frac{\omega_c}{s} \hat{x}_{\alpha}(s) \tag{5}$$

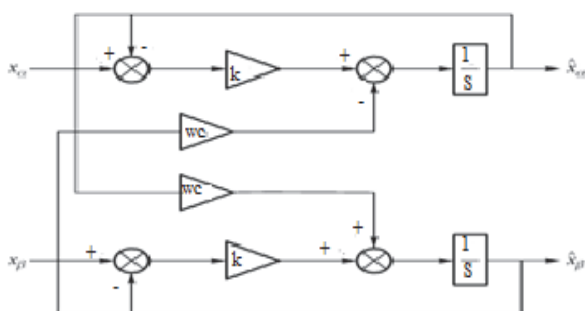


Figure 8 Square diagram of STF tuned  $\omega_c$

The STF tuned to the pulsation  $\omega_c$  is delineated in the figure. Simultaneously as various estimations of the parameters  $k$  versus the recurrence reaction of the STF. The STF can be seen that no removal is built up by this channel at the framework throbb. In STF, the major part can be separated from contorted electrical signs immediately and sufficiency evolves.

The load current  $i_{La}$ ,  $i_{Lb}$  and  $i_{Lc}$  the  $\alpha - \beta$  axis which transformed by the  $3\phi$  three wire system.

$$\begin{bmatrix} i_{\alpha} \\ i_{\beta} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} i_{La} \\ i_{Lb} \\ i_{Lc} \end{bmatrix} \tag{6}$$

Here the currents in the  $\alpha - \beta$  axis can be decomposed components by:

$$i_{\alpha} = \hat{i}_{\alpha} + \hat{i}_{\alpha} \tag{7}$$

$$i_{\beta} = \hat{i}_{\beta} + \hat{i}_{\beta} \tag{8}$$

The STF removes the major segments at the throbb  $\omega_c$  straightforwardly from the flows in the  $\alpha - \beta$  pivot. The heap flows of the  $\alpha - \beta$  consonant parts are determined by deducting the STF input signals from the proportional yields. In Fig. 9 shows the square chart of STF-based symphonious isolator.

This self-tuning filter is reducing harmonics of the distorted voltages. According to this filter is to improve the harmonic isolator performance. After that evaluation of the fundamental component  $\hat{v}_{\alpha\beta}$  and harmonic currents  $\hat{i}_{\alpha\beta}$ , the  $p$  and  $q$  powers are given as the Eq. (6) and Eq. (7).

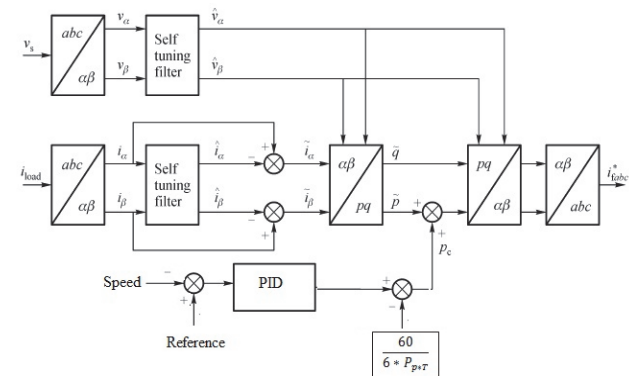


Figure 9 Square Diagram of STF-based consonant isolator

$$\begin{bmatrix} v_{\alpha} \\ v_{\beta} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} v_{sa} \\ v_{sb} \\ v_{sc} \end{bmatrix} \tag{9}$$

Instantaneous active power:

$$p = i_{\alpha} \hat{v}_{\alpha} + i_{\beta} \hat{v}_{\beta} \tag{10}$$

Instantaneous reactive power:

$$q = i_{\beta} \hat{v}_{\alpha} - i_{\alpha} \hat{v}_{\beta} \quad (11)$$

Where:

$$p = \hat{p} + \hat{p} \quad (12)$$

$$q = \hat{q} + \hat{q} \quad (13)$$

The  $\alpha - \beta$  related to the power components  $\hat{p}$  and  $\hat{q}$  can be written as:

$$\begin{bmatrix} \hat{p} \\ \hat{q} \end{bmatrix} = \begin{bmatrix} \hat{v}_{\alpha} & \hat{v}_{\beta} \\ \hat{v}_{\beta} & \hat{v}_{\alpha} \end{bmatrix} \begin{bmatrix} \hat{i}_{\alpha} \\ \hat{i}_{\beta} \end{bmatrix} \quad (14)$$

Current references frame  $i_{\alpha\beta}^*$  are written by:

$$i_{\alpha}^* = \frac{\hat{v}_{\alpha}}{\hat{v}_{\alpha}^2 + \hat{v}_{\beta}^2} (\hat{p} + p_c) - \frac{\hat{v}_{\beta}}{\hat{v}_{\alpha}^2 + \hat{v}_{\beta}^2} \hat{q} \quad (15)$$

$$i_{\beta}^* = \frac{\hat{v}_{\beta}}{\hat{v}_{\alpha}^2 + \hat{v}_{\beta}^2} (\hat{p} + p_c) - \frac{\hat{v}_{\alpha}}{\hat{v}_{\alpha}^2 + \hat{v}_{\beta}^2} \hat{q} \quad (16)$$

Then, the filter reference currents  $x - y - z$  are defined by:

$$\begin{bmatrix} i_{fx}^* \\ i_{fy}^* \\ i_{fz}^* \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & 0 \\ \frac{1}{2} & \frac{\sqrt{3}}{2} \\ -\frac{1}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} i_{\alpha}^* \\ i_{\beta}^* \end{bmatrix} \quad (17)$$

#### 4.6 Hall-Effect Sensors

The Hall Effect is a perfect detecting innovation and it is an attractive field sensor so it tends to be utilized in PMBLDC engine. The Hall Effect sensor detects the attractive field and it creates an electric sign. It is that changes over the electrical sign to one that meets the prerequisite of the framework. In PMBLDC, the changeless magnet material mounted on the rotor shaft works the Hall Effect sensors. The Hall Effect sensors sense the rakish situation of the engine and feed this data into a rationale circuit. The rationale circuit encodes this data and controls switches in a drive circuit.

Fig. 9 shows the Hall Effect signal. The sensor feedback is performed by Hall- Effect sensors. At any new signal is identified, the different switching pattern is applied. The Hall Effect sensors [4, 5, 20] are located inside the motor as position sensors. The sensor feedback method used three hall sensors are fixed inside the motor.

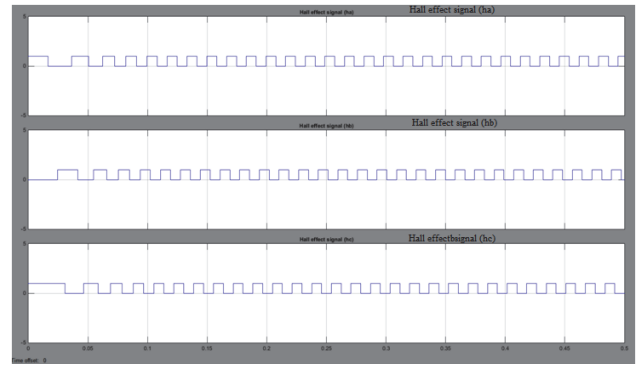


Figure 10 Hall Effect Signal

## 5 SIMULATION RESULT

A Simulation model of a Permanent BLDC engine utilizing the high advance up adjusted multi-inverter with self-tuning channel is structured utilizing MATLAB/SIMULINK apparatus. The Simulink model comprises Solar PV, Boost converter, FLC, MMLI, PMBLDC, and STF. The above Simulink model has been structured utilizing the accompanying parameters as appeared in Tab. 3.

Table 3 Parameters Used In Simulink Model

| Parameters                                | Value   |
|---|---------|
| Power / Watts                             | 600     |
| Speed / RPM                               | 3300    |
| Poles of the motor / P                    | 4       |
| Moment of inertia / J (kgm <sup>2</sup> ) | 0.00062 |
| Voltage / V/krpm                          | 24      |
| Torque / N-m                              | 1.33    |
| Stator Phase Resistance / $\Omega$        | 0.09    |

#### 5.1 Multilevel Sinusoidal Pulse Width Modulation

SPWM for the MMLI based on typical two levels of the waveform i.e., triangular carrier and the sinusoidal reference waveform.

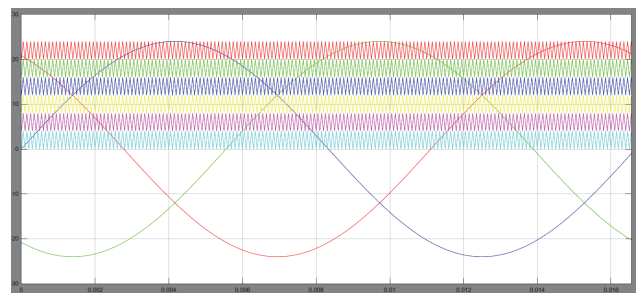


Figure 11 Reference waveform of modified MMLI using multi carrier PWM

The transporter arrangement for the bearer based SPWM method is appeared in Fig. 11 is the stage restriction mien (POD) procedure for 7-level, 6 conveyers are adjusted as symmetric identical representations over the zero-reference pivot. In Fig. 10 shows the reference sine and triangular bearers of changed staggered inverter utilizing multi-transporter PWM.



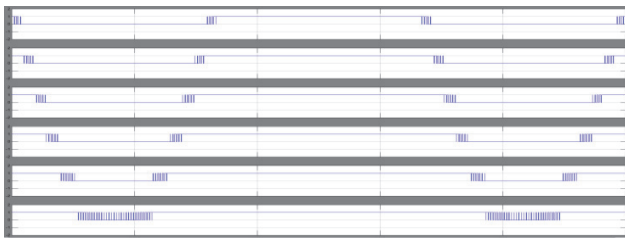


Figure 12 PWM signal for Inverter

In Fig. 12 shows the PWM signal for the inverter. The Pulse width modulation [3] output has six phases of the flight of stairs yield for 7-level inverter.

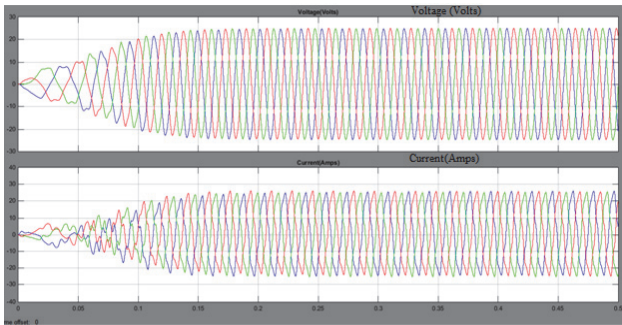


Figure 13 With Filter Output Wave Form

In Fig. 13 represents the channel yield waveform. The main distinction between two level SPWM and staggered SPWM is no. of bearers utilized. Producing a correlative pair of switches in the diode clipped in the staggered inverter is the association of specific transporter and reference is utilized.

### 5.2 FFT Analysis for Without Self-Tuning Filter

In the THD analysis voltage utilizing 13 - level inverter without channel, graph is shown in Fig. 14. The THD of the load voltage is 2.22% (supply voltage without STF).

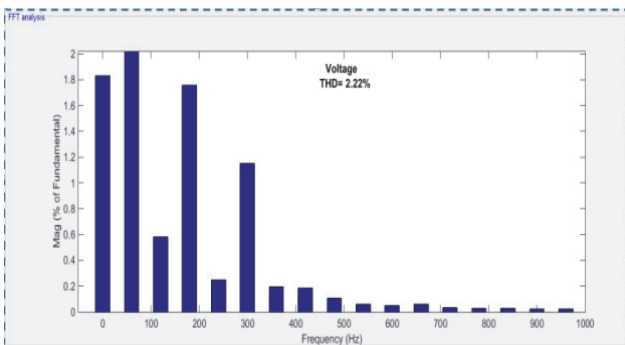


Figure 14 THD analysis voltage utilizing 13-level inverter without channel

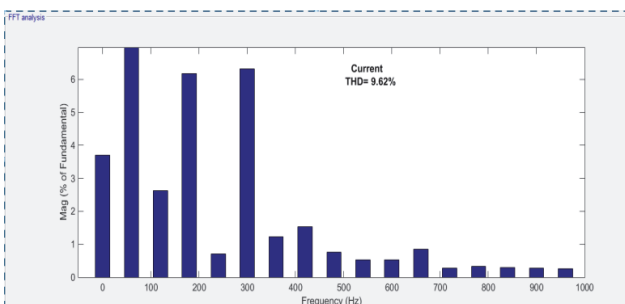


Figure 15 THD analysis current utilizing 13-level inverter without channel

In the THD analysis current utilizing 13-level inverter without channel graph is shown in Fig. 15. The THD of the load current is 9.62% without self-tuning filter.

### 5.3 FFT Analysis for With Self-Tuning Filter

In the FFT investigation of THD voltage utilizing with self-tuning channel, diagram is appeared in Fig. 16. The complete symphonious mutilation (THD) of the heap voltage is 1.53% (supply voltage with self-tuning channel).

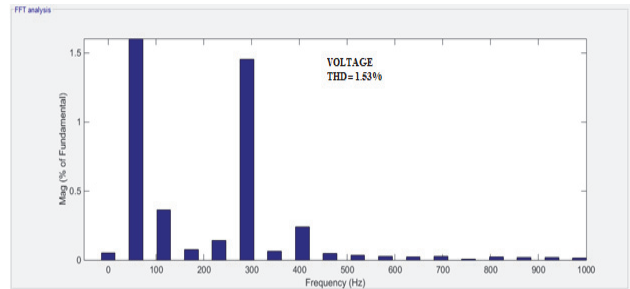


Figure 16 THD Analysis of voltage utilizing 13-level inverter with STF

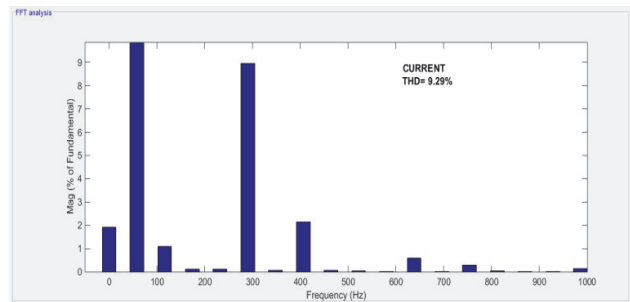


Figure 17 THD Analysis of current utilizing 13-level inverter with STF

In the analysis of THD current utilizing the 13-level inverter with self-tuning filter graph is shown in Fig. 16. THD) of the load current is 9.29%.

### 5.4 Comparisons of Different Types of Inverter THD

From Fig. 18 shows the examinations of the THD voltage for without channel and with channel. In the 7-level inverter absolute symphonious bending voltage for without channel is 38.77%, 13-level inverter all out consonant mutilation voltage for without channel is 36.58% and 13-level inverter complete consonant contortion voltage for with channel is 2.22%. From this outcome shows the more significant level inverter with channel has low symphonious twisting.

Table 4 Different types of Inverters THD Level

| Module       | Topology   | THD / % |
|--------------|--|---------|
| Conventional | A cascaded bridge 13-level inverter topology     | 9.14    |
| Existing     | Hybrid Thirteen Level Cascaded H-Bridge Inverter | 8.74    |
| Proposed     | Modified MLI                                     | 1.53    |

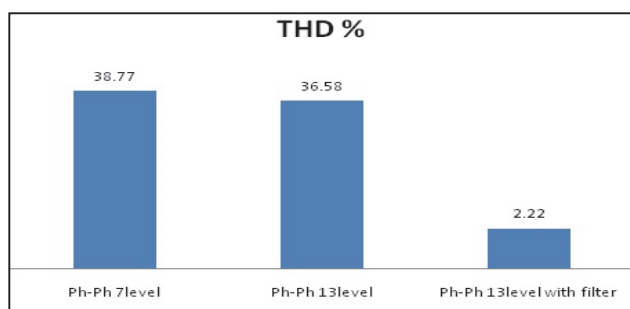


Figure 18 Comparisons of Total Harmonic Distortion voltage for without filter and with filter

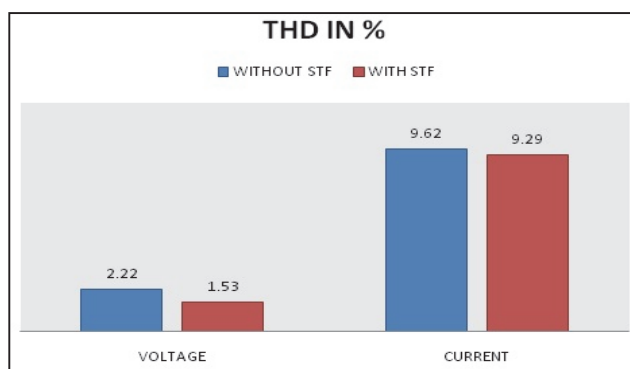


Figure 19 Comparisons of the 13-Level Inverter THD voltage & current in with and without STF

From Fig. 19 shows that the correlations of THD voltage and current in without self-tuning channel and with self-tuning channel. The all out symphonious twisting (THD) of the heap voltage is 2.22% (supply voltage without STF) and the heap voltage is 1.53% (supply voltage with STF). The THD of the heap current is 9.62% (supply current without STF) and the heap current is 9.29% (supply current with STF). It shows 13-level with STF inverter have low consonant twisting.

Table 5 Comparisons between THD in without Self-tuning Filter and with Self-tuning Filter

| Total Harmonic Distortion (THD) | Without Self-tuning Filter THD / % | With Self-tuning Filter THD / % |
|---------------------------------|------------------------------------|---------------------------------|
| Voltage                         | 2.22                               | 1.53                            |
| Current                         | 9.62                               | 9.29                            |

## 6 CONCLUSION

The proposed solar energy-based PMLDC motor drive using a high step-up modified multi-inverter with a self-tuning filter was analysed and successfully simulated using MATLAB/SIMULINK. We use an MPPT controller with the P&O algorithm that maximises the solar PV output power. A chopper converter is used to increase the solar voltage level. The multilevel inverter achieved the lowest value of total harmonic distortion (THD) and reduced switching losses. Finally, the THD in the multilevel inverter at various stages were compared. From the comparison, we found that the 13-level inverter with a self-tuning filter had the lowest value. The voltage and current THD was 1.53 and 9.29%, respectively. To maintain the voltage balance between the inverter capacitor and decrease the reactive power. In this PMLDC motor, using a high step-up modified multilevel inverter, we gained performance, a better system efficiency, and eliminated the harmonics using a self-tuning filter. The

hardware implementation of this study and the application of soft computing techniques to improve the performance of the motor will be carried out in the future.

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