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Fractional Order Identification Method and Control: Development of Control for Non-Minimum Phase Fractional Order System

A dissertation submitted in partial fulfillment of the requirements for the degree of Doctor of Philosophy in Engineering with a concentration in Electrical Engineering

by

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December 2020 University of Arkansas

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ABSTRACT

The increasing use of renewable energy has resulted in the need for improved a dc-dc converters. This type of electronic-based equipment is needed to interface the dc voltages normally encountered with solar arrays and battery systems to voltage levels suitable for connecting three phase inverters to distribution level networks. As grid-connected solar power levels continue to increase, there is a corresponding need for improved modeling and control of power electronic converters. In particular, higher levels of boost ratios are needed to connect low voltage circuits (less than 1000 V) to medium voltage levels in the range of 13 kV to 34 kV. With boost ratios now exceeding a factor of 10, the inherent nonlinearities of boost converter circuits become more prominent and thereby lead to stability concerns under variable load conditions. This dissertation presents a new method for analyzing dc-dc converters using fractional order calculus. This provides control systems designers the ability to analyze converter frequency response with Bode plots that have pole-zero contributions other than +/- 20 dB/decade. This dissertation details a systematic method of deriving the optimal frequency-domain fit of nonlinear dc-dc converter operation by use of a modified describing function technique. Results are presented by comparing a conventional linearization technique (i.e., integer-order transfer functions) to the describingfunction derived equivalent fractional-order model. The benefits of this approach in achieving improved stability margins with high-ratio dc-dc converters are presented.

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DEDICATION

To my parents, my wife and my children Abdullah, Hessah and my big family.

TABLE OF CONTENTS

1	Intro	luction	1
	1.1	Overview	1
	1.2	Motivation and Contribution of the Dissertation	2
	1.3	Dissertation Outline	2
2	Preli	ninary Mathematical Knowledge	4
	2.1	Introduction for Fractional Calculus	
		2.1.1 Gamma Function	4
		2.1.2 Mittag-Leffler Function	5
	2.2	Fractional Order Calculus	
		2.2.1 System Definitions	
	2.3	Laplace Transform of Fractional Operator	9
	2.4	The Fractional Fourier Transform	
	2.5	Fractional-Order Differential Equation	
		2.5.1 Linear Fractional Order Differential Equation	
		2.5.2 Nonlinear Fractional-Order Differential Equations	
	2.6	State-Space Representation	
	2.7	Stability of Fractional Differential Equations	14
	2.8	Fractional Order Transfer Functions	15
		2.8.1 Fractional-Order Transfer Function Stability	
		2.8.2 Discrete Fractional-Order Systems	
		2.8.3 Discrete-Time Fractional-Order State-Space Models	
3	Iden	ification of Fractional-Order Models	

	3.1	System Identification
	3.2	Identification Concepts
		3.2.1 Open Loop Identification
	3.3	System Identification Procedure
	3.4	Sine-Wave Response
	3.5	Time-Domain Identification
	3.6	Frequency-domain Identification
	3.7	Nonlinear Fractional-Order System Identification
		3.7.1 Linearization of nonlinear fractional order systems
	3.8	Discrete-Time Delta Operator
4	Sinu	soidal Pulse Width Modulation
	4.1	Sinusoids
	4.2	Describing Function (DF) 40
		4.2.1 PWM Effects
		4.2.2 DC-DC Converter Nonlinear Modeling with New Describing Function
		4.2.3 Property of Modified Describe Function
		4.2.4 Harmonic of Nonlinearity
		4.2.5 Accuracy
	4.3	Harmonic-Based Fractional Oder Modelling
	4.4	The Bode Plot
	4.5	Approximation of Fractional Order Systems 49
		4.5.1 Oustaloup's Method
		4.5.2 Levy's method

	4.6	Comn	nensurate Fractional-Order Transfer Functions	51
5	Fract	Fractional PI Controller		
	5.1	Fractional Order Robust Control		
	5.2	Optimization Controller Design		
	5.3	FO-PI Tuning Formulation		
	5.4	Gain Scheduling Methods		
	5.5 Linear Quadratic		r Quadratic Regulator (LQR) Design	
		5.5.1	First Order Plus Dead Time (FOPDT)	64
		5.5.2	The Fractional First Order Plu Dead-Time (FFOPDT)	65
		5.5.3	Review of Conventional PI Controller Design Methods	69
6	Simulation and Experiment Identification of DC-DC Converter			71
	6.1	Introduction		71
	6.2	Identification of a DC-DC Buck Converter71		
		6.2.1	Fast Fourier Transform (FFT) Algorithm for the Buck Converter	72
		6.2.2	Bode Plot Formulation for the Buck Converter	75
		6.2.3	Controller for the DC-DC Buck Converter	77
		6.2.4	Experimental Verification for the Buck Converter	
	6.3	Identi	fication of DC-DC Boost Converter	86
		6.3.1	Fast Fourier Transform (FFT) Algorithm for Boost Converter	87
		6.3.2	Bode Plot Generation for Boost Converter	89
		6.3.3	Controller for the DC-DC Boost Converter	
		6.3.4	Experimental Reults for Boost Converter	
	6.4	Identi	fication of a DC-DC Boost Converter with Constant Power Load (CP	'L) 103

		6.4.1	FFT Algorithm for Boost Converter with CPL 105
		6.4.2	Bode Plot Generation for Boost Converter with CPL 109
		6.4.3	Modeling and Analysis of a DC-DC Boost Converter with CPL 113
		6.4.4	Controller for the DC-DC Boost Converter with CPL 115
	6.5	Identif	fication of a Boost-Buck Converter Circuit
		6.5.1	Modeling and Analysis of a DC-DC Boost-Buck Converter 127
		6.5.2	PI Controller for the DC-DC Boost-buck Converter
		6.5.3	Experimental Part for the DC-DC Boost-Buck Converter
	6.6	Identif	Fication of a DC-DC Interleaved Boost Converter with (CPL) 142
		6.6.1	Fast Fourier Transform (FFT) Algorithm for Interleaved Boost Converter with
		(CPL)	143
		6.6.2	Bode plot generation for Interleaved Boost Converter with CPL 146
		6.6.3	Modeling and Analysis of a DC-DC Interleaved Boost Converter with (CPL)
			149
		6.6.4	Controller for the DC-DC Interleaved Boost with CPL Converter
7	Conc	lusion.	
Ref	ference	9	
Ap	Appendix		

LIST OF FIGURE

Figure 2.1: Graph of Gamma function	5
Figure 2.2: Mittag-Leffler function for E α , 1(x) for α = 0.5, 1, 1.5	7
Figure 2.3: Mittag-Leffler function for E1, $\beta(x)$ for $\beta = 0.5, 1, 1.5$	7
Figure 2.4: Classification of LTI system.	13
Figure 2.5: Stability region for all position	15
Figure 2.6: Bode diagrams of (s) =1/ (Ts α) systems for α = 0.5, 0.8, 1, 1.2, 1.5.	17
Figure 3.1: A system with input and output signal.	23
Figure 3.2: The system identification procedure.	24
Figure 3.3: Example of signal input-output signals.	25
Figure 3.4: Example of sinusoidal wave input and output	25
Figure 3.5: Example of signal input-output phase angle shift.	26
Figure 3.6: The system identification flowchart	28
Figure 3.7: One signal responses the gain and phase angle of input-output	30
Figure 4.1: Principal of Sinusoidal Pulse Width Modulation.	36
Figure 4.2: Sine and cosine functions.	37
Figure 4.3: complex number as a vector	38
Figure 4.4: complex number of $z=2+j5$, $=4-j3$, $z=-5+j0$, and $z=-3-j3$.	39
Figure 4.5: Complex number of $x=2\angle 45^\circ$, $x=3\angle 150^\circ$, and $x=3\angle -80^\circ$	39
Figure 4.6: Boost curve and sine input-output	43
Figure 4.7: Fast Fourier Transform for input	47
Figure 4.8: Fast Fourier Transform for output	47
Figure 4.9: The magnitude in dB and phase angle in degree	48

Figure 5.1: Fractional-order proportional and integrator controller model
Figure 5.2: The $PI^{\lambda}D^{\mu}$ controller plane
Figure 5.3: Step responses FOPI with variant integral of Tj
Figure 5.4: Step responses FO PI controller with variant integration order λ
Figure 5.5: Closed loop step responses for $G_{FO}(s)$ under various λ
Figure 5.6: Nyquist Bode plot of ideal transfer function
Figure 5.7: Control diagram of voltage to dc-dc converter
Figure 5.8: Inner current control for converter
Figure 5.9: Control diagram of inner loop and outer loop controller
Figure 6.1: Identification of dc-dc buck converter
Figure 6.2: Example of control signal for buck converter of 1500Hz72
Figure 6.3: DC-DC Buck converter Matlab Simulink72
Figure 6.4: Example of FFT analysis for buck converter of 1500Hz
Figure 6.5: Bode Plot of buck converter form the Powersim simulation data75
Figure 6.6: Bode Plot of buck converter simulated and estimated system
Figure 6.7: Bode Plot of buck converter simulated and approximation system
Figure 6.8: Movement of poles in LQR of buck converter
Figure 6.9: Poles location of system of buck converter
Figure 6.10: Simulation of fractional-order and integer-order controller of buck converter 80
Figure 6.11: Result of fractional-order and integer-order controller of buck converter
Figure 6.12: Experimental configuration for the buck converter controller
Figure 6.13: Simulation of control signal at 60Hz and output voltage of the buck converter 82
Figure 6.14: Experimental control of 60Hz reference and output voltage of the buck converter. 83

Figure 6.15: Experimental analysis of 60Hz control signal by FFT of the buck converter
Figure 6.16: Experimental analysis of output voltage by FFT of the buck converter
Figure 6.17: Experimental control by 10 KHz at output voltage of the buck converter
Figure 6.18: Experimental analyzing 10 KHz control signal by FFT of buck converter
Figure 6.19: Experimental analyzing output voltage signal by FFT of the buck converter 85
Figure 6.20: DC-DC Boost Converter
Figure 6.21: DC-DC Boost Converter Matlab Simulink
Figure 6.22: Example of the (FFT) analysis for buck converter of 500Hz
Figure 6.23:Bode Plot of Boost converter Powersim simulation data
Figure 6.24: Simulated data Bode plot of boost converter and estimation data
Figure 6.25: Simulation data Bode plot boost converter and approximation data
Figure 6.26: Simulation of boost converter actual Bode Plot and FO model
Figure 6.27: Root locus of closed loop poles for boost converter inner-loop cpntroller
Figure 6.28: Poles of current controller location of the boost converter
Figure 6.29: Movement of closed loop poles for voltage controller of the boost converter
Figure 6.30: Poles of voltage controller location of the boost converter
Figure 6.31: Controller configuration for the boost converter
Figure 6.32: Integer-order controller with fractional-order controller for boost converter
Figure 6.33: Experimental control of the boost converter
Figure 6.34: Simulation of reference signal at 60Hz and output voltage of the boost converter. 99
Figure 6.35: Simulation control signal 60Hz and output voltage of the boost converter 100
Figure 6.36: Experimental FFT analysis of control signal 60Hz the boost converter
Figure 6.37: Experimental FFT analysis of output voltage boost converter

Figure 6.38: Simulation control signal of 100Hz and output voltage boost converter
Figure 6.39: Experimental control signal of 100Hz and output voltage boost converter
Figure 6.40: Experimentalal FFT analysis of control signal of 100Hz boost converter 102
Figure 6.41: Experimental analysis FFT of output voltage boost converter
Figure 6.42: DC-DC boost converter with CPL
Figure 6.43: Example of control signal for boost converter with CPL at 1000Hz 104
Figure 6.44: Inductor current and capacitor voltage with CPL at 1000Hz
Figure 6.45: DC-DC boost converter with CPL in Matlab-Simulink
Figure 6.46: Example of FFT analysis for boost converter with CPL of 1000Hz 106
Figure 6.47: Bode Plot of boost with CPL from simulation data
Figure 6.48: Bode plot comparison of the boost converter with CPL data
Figure 6.49: Bode plot of boost converter with CPL 112
Figure 6.50: Boost converter with CPL 113
Figure 6.51: Bode plot comparison of boost converter with CPL (actual data and linearized
model)
Figure 6.52: Movement of closed loop poles for current control loop of the boost with CPL 116
Figure 6.53: Poles of current control loop location of the boost with CPL
Figure 6.54: Movement of closed loop poles for voltage controller of the boost with CPL 118
Figure 6.55: Poles of voltage controller location of the boost with CPL
Figure 6.56: Integer-Order controller with fractional-order controller for boost with CPL 119
Figure 6.57: Fractional order controller and integer-order controller for boost with CPL 120
Figure 6.58: Boost-buck frequency response simulation
Figure 6.59: Switching input for boost converter

Figure 6.60: Switching input for buck converter
Figure 6.61: Input-Output frequency response of boost-buck converter
Figure 6.62: Boost-buck Matlab simulation
Figure 6.63: Bode plot of boost-buck converter form collected simulation data
Figure 6.64: Bode plot of boost-buck simulation data and estimated system
Figure 6.65: Boost-buck circuit
Figure 6.66: Bode plot for boost-buck of actual versus linearized data
Figure 6.67: Bode plot for boost-buck actual versus fractional order
Figure 6.68: System block diagram of boost-buck converter
Figure 6.69: Movement of closed loop poles current controller of boost-buck converter 133
Figure 6.70: Poles of current controller location boost-buck converter
Figure 6.71: Movement of closed loop poles for voltage controller of the boost-buck converter.
Figure 6.72: Poles of voltage controller location of the boost-buck converter
Figure 6.73: Movement of poles in LQR boot-buck converter
Figure 6.74: Poles of the boost-buck System
Figure 6.75: Simulation fractional-order, integer-order controller of boost-buck converter 138
Figure 6.76: Simulation result for fractional and integer order controller of boost-buck converter.
Figure 6.77: Experiment of identify and control boost-buck converter
Figure 6.78: Open loop switching and voltages output of boost-buck converter
Figure 6.79: Experimental result of output voltages and currents of boost-buck converter 141
Figure 6.80: DC-DC interleaved boost Converter with CPL

Figure 6.81: Control signals for interleaved boost converter with CPL of 2000Hz 142
Figure 6.82: Inductor current signal for interleaved boost converter with CPL of 2000Hz 143
Figure 6.83: DC-DC Interleaved Boost Converter with CPL Matlab Simulink
Figure 6.84: FFT analysis of interleaved boost converter with CPL of 2000Hz 144
Figure 6.85: Bode plot of interleaved boost converter with CPL form obtained data 147
Figure 6.86: Bode plot of the interleaved boost converter with CPLVs estimation data
Figure 6.87: Bode Plot of interleaved boost converter with CPL 149
Figure 6.88: Bode plot of interleaved boost converter with CPL 151
Figure 6.89: Movement of closed loop poles current controller (interleaved boost with CPL). 152
Figure 6.90: Poles of current controller location of interleaved boost converter with CPL 153
Figure 6.91: Closed loop poles location of voltage controller interleaved boost with CPL 154
Figure 6.92: Poles of voltage controller location of interleaved boost converter with CPL 155
Figure 6.93: Simulation design of fractional-order versus integer-order controller of interleaved
boost converter with CPL 155
Figure 6.94: Simulation result Fractional Order controller-Integer order controller of interleaved
boost converter with CPL

LIST OF TABLES

Table 6.1: Collected data bode plot for buck converter	73
Table 6.2: Collected data bode plot for boost converter.	88
Table 6.3: Collected data bode plot for boost with CPL converter	106
Table 6.4: Data of Bode plot for boost-buck converter.	
Table 6.5: Collected data of bode plot for Interleaved Boost Converter with CPL	144

1 Introduction

1.1 Overview

Methods for identifying the dynamic behavior of systems have long been a topic of research. Traditional methods are based on sets of differential equations the model the behavior of engineered systems. This involves the use of relationships derived from well-established analysis techniques such as Kirchhoff's current and voltage laws for electrical circuits and Newton's laws for mechanical elements. These are typically applied using conventional integer-order calculus. However, these are based on idealized mechanical and electrical components, whereas actual physical devices have been observer to behave in a manner that is more accurately described by fractional-order derivatives [1]. A number of researchers have documented the fractional order behavior of many physical systems [2] and has led to ongoing efforts to establish the theoretical foundations for fractional-order modeling techniques [3]. These methods are often extensions from standard integer-order calculus, such as state-space representations, Laplace transforms of linear differential equations and discrete-time equivalents. However, there is a tradeoff between the accuracy versus complexity in the mathematical modeling of a physical system. Most physical system exhibit varying degrees of nonlinear behavior. Given the rich theoretical and design capabilities associated with linear systems, then there is a preference for performing a linearization of the system dynamics to provide a model that is reasonably accurate for a least a limited range near its nominal operating condition. Switched-mode power electronics that are employed in dcdc boost converters exhibit nonlinear behavior that becomes more acute as the boost ratios are increased. Consequently, conventional linearization methods result in progressively degraded accuracy under conditions where an accurate dynamic model is most needed. . In particular, dc-dc boost converters exhibit non-minimum phase effects in the linearized equivalent models associated

with emergent right-half plane (RHP) zeros. This research proposes a novel method the modeling dc-dc converters based on a fractional-order representation of the system dynamics.

1.2 Motivation and Contribution of the Dissertation

The principal aim of the dissertation is the development of a new modeling method that more accurately represents the actual behavior of dc-dc converter systems. The identification of dc-dc converters from data that includes nonlinear behavior is analyzed in the context of fractional-order converters from data that includes nonlinear behavior is analyzed in the context of fractional-order system theory. The modeling method begins with discrete-time (sampled) input-output data. By utilizing fractional-order calculus, the input-out data is processed to derive a Laplace-domain non-integer order transfer function. This provides systems designers the ability to analyze converter frequency response with Bode plots that have asymptotic pole-zero contributions other than +/- 20 dB/decade. The details of the proposed method derives an optimal fit of the actual dc-dc converter data through a novel describing function technique. The benefits of this approach in achieving improved stability margins with high ratio dc-dc boost converters are presented: First, the proposed methods are applied to data from a detailed circuit simulation of a switched-mode dc-dc boost converter. Secondly, the system modeling method is applied to a laboratory prototype dc-dc boost converter to further verify the effectiveness of the proposed methods.

1.3 Dissertation Outline

This dissertation has seven chapters: Chapter 2 discusses the mathematical tools that will be needed for understanding fractional-order calculus. Chapter 3 explains the identification of fractional-order systems based on input-output data collected from open-loop measurements and the derivation of a fractional order transfer function for a dc-dc converter. Chapter 4 describes a novel describing function method that can be used to quantify the nonlinear effects of dc-dc converter operation. Chapter 5 discusses system stability and robustness when a fractional-order controller is used.

Chapter 6 provides step-by-step procedures on how to identify the nonlinear behavior of a dc-dc converter using detailed simulations and experimental results from a laboratory prototype. A conclusion is provided in Chapter 7 for the results of the proposed new method and potential future research directions.

2 Preliminary Mathematical Knowledge

This chapter reviews the necessary background knowledge needed for developing fractional-order methods of system modeling and identification. Details in applying identification methods to dc-dc converters from input-output operational data is provided as a foundation for subsequent chapters.

2.1 Introduction for Fractional Calculus

The source of fractional-order behavior in electrical components is associated with the distributed internal losses present in all physical devices [4] [5]. The history of fractional calculus is well known and dates back to the original work by Newton and Leibniz [6]. Leibniz postulated derivatives and integrals of fractional order and the potential implications [7]. In recent times, there has been renewed interest in fractional-order calculus in order to meet challenges in systems analysis and engineering [8] [9]. Formal methods have been developed which encompasses and extends the concepts normally taken in conventional integer-order systems analysis [10].

2.1.1 Gamma Function

The gamma function is widely used in defining fractional-order integration. The gamma function integral is expressed as

$$\Gamma(x) = \int_{0}^{\infty} e^{-n} u^{x-1} du, Re(x) > 0$$
(2.1)

which has properties

$$\Gamma(x) = 1, \tag{2.2}$$

$$\Gamma(m+1) = m!$$
, where is $(m = 0, 1, 2...),$ (2.3)

$$\Gamma\left(\frac{1}{2}\right) = \sqrt{\pi},\tag{2.4}$$

$$\Gamma(x+1) = x\Gamma(x). \tag{2.5}$$

Integer-order and the fractional-order derivatives of a single variable defined as

$$\frac{d^{\alpha}}{dx^{\alpha}}x^{\alpha} = \frac{\Gamma(\alpha+1)}{\Gamma(\alpha-\alpha+1)}x^{\alpha-\alpha} = \Gamma(\alpha+1).$$
(2.6)

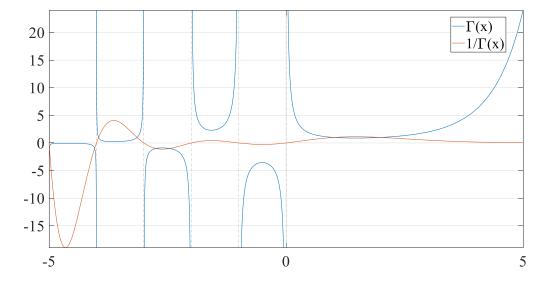


Figure 2.1: Graph of Gamma function.

 $\Gamma(x)$ and $1/\Gamma(x)$ is defined over the complex plane for positive and negative values of x is shown in Figure 2.1.

2.1.2 Mittag-Leffler Function

The Mittag-Leffler function is a generalization of the exponential function. That is needed in the resolution of fractional-order differential equations. There are several commonly used forms [11] [12], however this research relies upon the following version [13] for one parameter as

$$E_{\alpha}(t) = \sum_{k=0}^{\infty} \frac{t^n}{\Gamma(\alpha k + 1)},$$
(2.7)

and for two parameters as represented by

$$E_{\alpha,\beta}(t) = \sum_{k=0}^{\infty} \frac{t^n}{\Gamma(\alpha k + \beta)},$$
(2.8)

where is $\Re(\alpha) > 0, \Re(\beta) > 0$. The Mittag-Leffler function E(.) is defined as

$$E_{1,1}(t) = e^k, (2.9)$$

$$E_{1,2}(t) = \frac{e^k - 1}{t}.$$
(2.10)

For example,

$$\alpha = 1 \tag{2.11}$$

$$E_{\alpha}(t) = \sum_{k=0}^{\infty} \frac{t^n}{\Gamma(\alpha k + 1)} = e^k.$$
(2.12)

With
$$\alpha = 1$$
 and $\beta = 2$, (2.13)

$$E_{\alpha,\beta}(t) = \sum_{k=0}^{\infty} \frac{t^n}{\Gamma(\alpha k + \beta)} = \frac{e^k - 1}{t}$$
(2.14)

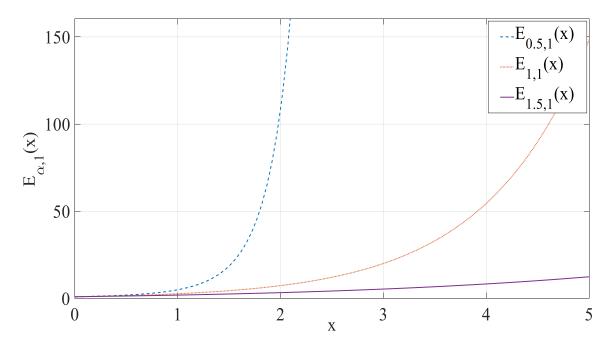


Figure 2.2: Mittag-Leffler function for Ea, 1(x) for $\alpha = 0.5, 1, 1.5$.

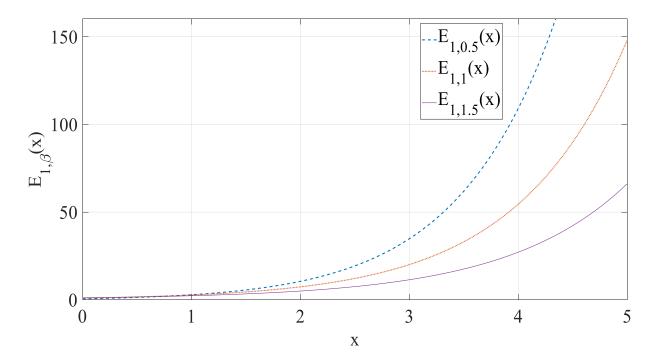


Figure 2.3: Mittag-Leffler function for E1, $\beta(x)$ for $\beta = 0.5, 1, 1.5$.

2.2 Fractional Order Calculus

Fractional calculus is a general form of integral and derivative terms for non-integer order operators. It is an improvement for dynamic systems that are described by integration and differentiation operators D_t and $_a \mathcal{D}_t^{\alpha}$:

$${}_{a}\mathcal{D}_{t}^{\alpha} = \begin{cases} \frac{d^{\alpha}}{dt^{\alpha}} & \Re(\alpha) > 0, \\ 1 & \Re(\alpha) = 0, \\ \int_{a}^{t} (dt)^{-\alpha} & \Re(\alpha) < 0, \end{cases}$$
(2.15)

where the operator *D* and the parameter α and variable *t* express the limits of the operations and signifies the fractional-order operator, where $\alpha \in \mathbb{R}$ [14].

2.2.1 System Definitions

There are several definitions commonly applied for a fractional-order derivative of a function: Riemann Liouville's definition; Caputo's definition; and Grunwald-Letnikov's definition. The Riemann Liouville's definition is often chosen be to be applied for Laplace transform analysis. It is a method that begins with a generalization of Cauchy's theorems for integration with extension to non-integer order conditions. The fractional-order integral becomes

$$I_c^{\alpha}f(t) = \mathcal{D}_t^{-\alpha}(t) = \frac{1}{\Gamma(\alpha)} \int_c^t (t-\tau)^{\alpha-1} \int (\tau) \, d\tau, \quad t > c, \quad \alpha \in \mathbb{R}^+$$
(2.16)

where $\Gamma(\alpha)$, is the gamma function represented with the following definition

$$\Gamma(\alpha) = \int_0^\infty e^{-n} u^{\alpha - 1} du.$$
 (2.17)

If α is a natural numbers then Eq. (2.17) then

$$\Gamma(n) = (n-1)!, \quad n \in N.$$
 (2.18)

The fractional derivative as derived by the Rieman-Liouville definition for a fractional order derivative $\alpha \in R^+$, $\alpha > 0$ is

$${}^{R}_{c}D^{\alpha}_{t}f(t) \triangleq D^{k}I^{k-\alpha}f(t),$$

$$= \frac{d^{k}}{dt^{k}} \left[\frac{1}{\Gamma(k-\alpha)} \int_{0}^{t} \frac{f(\tau)}{(t-\tau)^{\alpha-k-1}} d\tau \right],$$
(2.19)

where $k - 1 < \alpha \le k, k = 0$, and $k \in N$. The representation of the Rieman-Liouville definition is *R* [15]. Caputo's definition with fractional derivative is shown as

$${}_{c}D^{\alpha}f(t) \triangleq I^{k-\alpha}D^{k}f(t) = \frac{1}{\Gamma(k-\alpha)}\int_{0}^{t}\frac{f^{k}(\tau)}{(t-\tau)^{\alpha-k+1}}d\tau.$$
(2.20)

This form applies to integer-order conditions if using a Laplace transform to solve fractional order differential equations, where $k - 1 < \alpha < k$, $k \in N$ and C represents the Caputo definition. Grünwald determines a discrete time definition of fractional-order differentiation as [16]

$${}_{c}\mathcal{D}_{t}^{\alpha}f(t) = \lim_{m \to 0} \frac{1}{h^{\alpha}} \sum_{\tau=0}^{\left[\frac{t-c}{m}\right]} (-1)^{\tau} {\alpha \choose \tau} f(t-\tau m), \qquad (2.21)$$

where *m* is a sampling period and $\binom{\alpha}{\tau}$ is Newton's binomial function.

2.3 Laplace Transform of Fractional Operator

The Laplace transform is one of the fundamental tools in dynamical systems and controls engineering. It is not only applied for functions of integer order operators; it can be applied for non-integer order functions as [17]

$$F(K) = \mathcal{L}(f(t)) = \int_0^\infty e^{-Nt} f(t) dt, \qquad (2.22)$$

where the function f(s) is obtained as the Laplace transform F(K) by using the inverse Laplace transform represented as

$$f(s) = \mathcal{L}^{-1}[F(N)] = \frac{1}{j2\pi} \int_{c-j\infty}^{c+j\infty} e^{st} F(N) ds$$
 (2.23)

where *c* is large and all the poles of function F(z) are real valued [18]. The Laplace transform for fractional order versions of the Grünwald-Letnikov definition is

$$\mathcal{L}[D^{\alpha}f(s)] = s^{\alpha}F(z).$$
(2.24)

Laplace transform is defined of the Riemann-Liouville fractional operator definition for $f - 1 \le \alpha < f$ as:

$$\mathcal{L}[_{c}D^{\alpha}f(t)] = s^{\alpha}F(s) - \sum_{k=0}^{f-1} s^{k} \times [D^{\alpha-k-1}f(u)]_{u=0},$$
(2.25)

and it is defined with the Caputo fractional operator definition as

$$\mathcal{L}[_{c}D^{\alpha}f(u)] = s^{\alpha}F(s) - \sum_{k=0}^{n-1} s^{\alpha-k-1}f^{(k)}(0).$$
(2.26)

Example 2.1

Calculating the inverse Laplace transform for the following example [18]:

$$F(s) = \left\{\frac{1}{s\sqrt{s+nm}}\right\}$$
where *c* is a real-valued constant (2.27)

$$\mathcal{L}^{-1}[F(s)] \tag{2.28}$$

By applying the shift $\mathcal{L}[e^{a\tau}f(\tau)] = F(s - nm)$

$$\mathcal{L}^{-1}\left[\frac{1}{s\sqrt{s+nm}}\right] = \frac{e^{-a\tau}}{\sqrt{\pi t}}$$
(2.29)

Laplace transform integral

$$\mathcal{L}^{-1}\left[\frac{1}{s\sqrt{s+nm}}\right] = \int_0^\tau \frac{e^{-a\tau}}{\sqrt{\pi\tau}} d\tau$$
(2.30)

That is represented as for $u^2 = nm\tau$

$$\mathcal{L}^{-1}\left[\frac{1}{s\sqrt{s+c}}\right] = \frac{1}{\sqrt{c}}\operatorname{erf}(\sqrt{nm\tau}).$$
(2.31)

2.4 The Fractional Fourier Transform

The fractional-order integral and derivative is applied to the as well to the Fourier transform. The fractional Fourier transform is [19]:

$$X_{\alpha}(u) = \mathcal{F}_{\alpha}(x(\tau)) = \int_{-\infty}^{\infty} x(\tau) K_{\alpha}(\tau, u) d\tau, \qquad (2.32)$$

where $K_{\alpha}(\tau, u)$ is the kernel function determinate:

$$K_{\alpha}(\tau, u) = \begin{cases} \sqrt{\frac{1-j\cot\alpha}{2}} \times e^{j\left(\frac{u^2}{2}\right)\cot\alpha} e^{j\left(\frac{\tau^2}{2}\right)\cot\alpha - ju\tau\cos(ec\alpha)} & For \ \alpha \neq 2\pi \\ \delta(\tau-u) & For \ \alpha = 2\pi \\ \delta(\tau+ut) & For \ \alpha = (2n-)\pi \end{cases}$$
(2.33)

This becomes the conventional Fourier transform when $\alpha = 2\pi$, and $\frac{\pi}{2}$ multiples of α . Properties of the fractional Fourier transform are detailed in [20].

2.5 Fractional-Order Differential Equation

The Fractional-order differential equation is fundamental for expressing the fractionalorder dynamical behavior of electrical systems. This section describes the needed background for applying and solving fractional-order differential equations.

2.5.1 Linear Fractional Order Differential Equation

Fractional-order differential equations in this research will typically be in the form:

$$a_{c}\mathcal{D}^{\alpha_{c}}y(t) + a_{c-1}\mathcal{D}^{\alpha_{c-1}}y(t) + \dots + a_{0}\mathcal{D}^{\alpha_{0}}y(t)$$

= $b_{n}\mathcal{D}^{\beta_{n}}u(t) + b_{n-1}\mathcal{D}^{\beta_{n-1}}u(t) + \dots + b_{0}\mathcal{D}^{\beta_{0}}u(t)$ (2.34)

where is, $a_i, b_j \in \mathbb{R}$, and y(t) and u(t) represent the input and output of the system. The form given by (2.34) is defined as commensurate-order when the orders of the system derivatives are integer multiples of a common factor, q such as $\alpha_k, \beta_k = kq, q \in \mathbb{R}^+$. The system is represented as follows [21]:

$$\sum_{c=0}^{n} a_c \mathcal{D}^{cq} y(t) = \sum_{c=0}^{m} b_c \mathcal{D}^{cq} \gamma(t), \qquad (2.35)$$

where is $q = \frac{1}{l}$, and $l \in \mathbb{Z}_+$.

The relationships among the various forms for linear time-invariant (LTI) systems analysis are summarized in Figure 2.4.

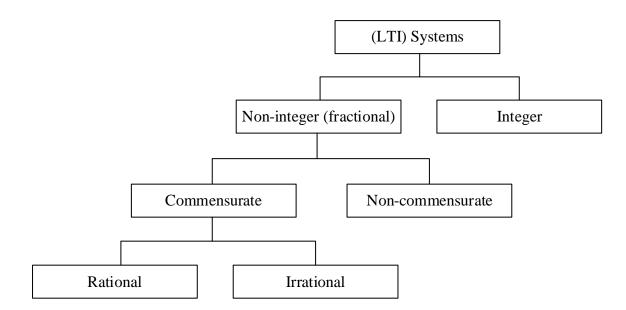


Figure 2.4: Classification of LTI system.

2.5.2 Nonlinear Fractional-Order Differential Equations

Pereira et al. [22] examined the fractional derivative for the Van der Pol equations by considering capacitance with a fractional order in a nonlinear electrical circuit design,

$$D^{\alpha}x + \gamma(x^2 - 1)\dot{x} + x = 0, \qquad 1 < \alpha < 2.$$
(2.36)

Barbosa et al. [23] presented fractional order Van der Pol equations by two part derivatives in fractional form as,

$$D^{1+\alpha}x + \gamma(x^2 - 1)D^{\alpha}x + x = 0, \qquad 0 < \alpha < 1.$$
(2.37)

2.6 State-Space Representation

The state-space description is often the most useful form for designing feedback control systems. When the Fractional-order differential equations have commensurate order then the state-space equations can be represented as:

$${}_{0}D_{t}^{\alpha}x(\tau) = f(x, u, \tau), \tag{2.38}$$

$$y(\tau) = g(\tau), \tag{2.39}$$

where is $0 < \alpha < 2$, and $x \in \mathbb{R}^n$ state vector of *n* dimension. For linear fractional-order differential equations as in (2.34) can then be expressed in matrix form as

$${}_0D_t^{\alpha}x(\tau) = Ax(\tau) + Bu(\tau), \qquad (2.40)$$

$$y(\tau) = Cx(\tau), \tag{2.41}$$

where are A, B and C real-values system matrices of appropriate dimensions.

2.7 Stability of Fractional Differential Equations

Understanding and ensuring the stability of feedback control systems is essential for developing power electronic dc-dc converters. Stability analysis is carried out using a modified Mittag-Letter form [24]. Analyzing the stability of nonlinear fractional differential equations is considerably more complicated than the linear case. Stability in general is determined from a fractional-order description for a state vector x,

$$D^{\alpha}x = f(x), \tag{2.42}$$

where is $x \in \mathbb{R}^n$ and $0 < \alpha < 1$. For the f(x) = 0 defines an equilibrium point of the system. It is asymptotically stable when the eigenvalues of a Jacobian matrix meets the condition [25]

 $|\angle(eig(\mathbf{J}))| = |\angle(\lambda_j)| > \alpha \frac{\pi}{2},$

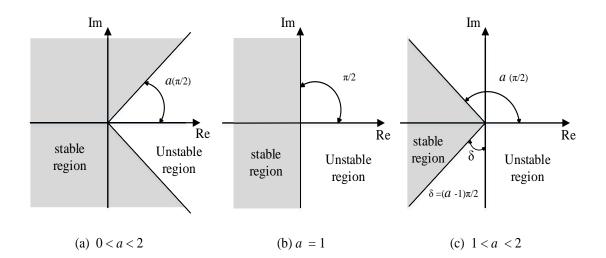


Figure 2.5: Stability region for all position.

where is λ_j the eigenvalues and $\mathbf{J}|_{x=x_0} = \partial f / \partial x$, and J = 1, 2, ..., n.

2.8 Fractional Order Transfer Functions

A fractional order transfer function represents the input-output relationship for a linear time-invariant continuous time dynamical system. Using the Laplace transform based on zero initial conditions, the input-output description of a fractional-order system is expressed as:

$$G(s) = \frac{Y(s)}{U(s)} = \frac{b_m s^{\beta_m} + b_{m-1} s^{\beta_{m-1}} + \dots + b_0 s^{\beta_0}}{a_n s^{\alpha_n} + a_{n-1} s^{\alpha_{n-1}} + \dots + a_0 s^{\alpha_0}}$$
(2.43)

where the Laplace transform is given for the input-output signals Y(s), U(s), for $m, n \in \mathbb{N}_+$. It is considered the zero initial condition of y(s) = 0 and u(s) = 0. The continuous-time transfer function for the case of a commensurate-order system is :

$$G(s) = \frac{Y(s)}{U(s)} = \frac{\sum_{k=0}^{m} b_k(s^{\alpha})^k}{\sum_{k=0}^{n} a_k(s^{\alpha})^k}.$$
(2.44)

Example 2.2

For the fractional-order system [25]:

$$G(s) = \frac{1}{(Ts)^{\alpha}} \tag{2.45}$$

The associated transfer function with $s = j\omega$ is the following:

$$G(s) = \frac{1}{(Tj\omega)^{\alpha}} = \frac{1}{T\omega^{\alpha}(\cos\frac{\pi}{2}\alpha + j\sin\frac{\pi}{2}\alpha)}$$
(2.46)

The magnitude is:

$$A(\omega) = \sqrt{\frac{(\cos^2 \frac{\pi}{2}\alpha + j\sin^2 \frac{\pi}{2}\alpha)}{(T\omega)^{2\alpha}}} = \frac{1}{(T\omega)^{\alpha}},$$
(2.47)

$$M(\omega) = 20 \log A(\omega) = -\alpha 20 \log(T) - \alpha 20 \log(\omega)$$
(2.48)

The phase of the transfer function is defined as:

$$\phi(\omega) = \arg\left[\frac{1}{(T\omega)^{\alpha}}j^{-\alpha}\right] = -\alpha \frac{\pi}{2},$$
(2.49)

The information is provided from the amplitude and phase can be used to create a Bode diagram of the fractional-order frequency response for various values $\alpha = 0.5, .08, 1, 1.2, 1.5$ as shown in Figure. 2.6.

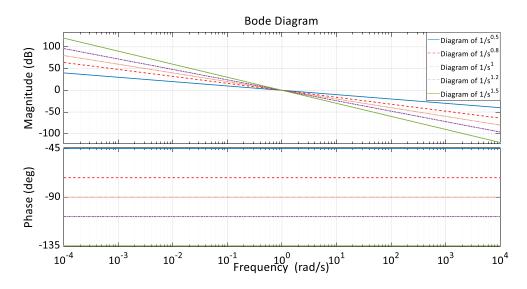


Figure 2.6: Bode diagrams of (s) =1/ (Ts α) systems for α = 0.5, 0.8, 1, 1.2, 1.5.

The fractional order state-space representation by a fractional-order transfer function expression as [26]

$$s^{\alpha}X(s) = AX(s) + BU(s), \qquad (2.50)$$

$$Y(s) = CX(s), \tag{2.51}$$

with assumed zero initial conditions. The system transfer function is determined by

$$G(s) = \frac{Y(s)}{U(s)} = N(s^{\alpha}I - A)^{-1}B.$$
(2.52)

The obtained G(s) of fractional order transfer function is a matrix when B is multi-input (multiple columns) and output C matrix has multiple rows (multi-output). The numerator and denominator of fractional order polynomials are of the general form

$$N(s) = a_n s^{\alpha_n} + a_{n-1} s^{\alpha_{n-1}} + \dots + a_0 s^{\alpha_0},$$
(2.53)

where is $\alpha_i \in \mathbb{N}_0^+$ and $\alpha_i \in \mathbb{R}$. The corresponding commensurate fractional degree polynomial is

$$N(s) = a_n s^{\frac{\lambda_n}{\lambda}} + a_{n-1} s^{\frac{\lambda_{n-1}}{\lambda}} + \dots + a_0 s^{\frac{\lambda_0}{\lambda}} \quad \lambda_i \in \mathbb{R}.$$
 (2.54)

2.8.1 Fractional-Order Transfer Function Stability

The stability of a fractional-order system can be determined by extending root locus methods or the Nyquist theorem [27] concepts. The root locus method approach analyzes the pole locations of a linear input-output feedback system with respect a variation of a parameter in terms of the corresponding loop gain. Stability in the time-domain for an LTI system is ensured when it satisfies the condition of its impulse response of h(t) [28] as

$$\int_0^\infty \|h(\tau)\| d\tau < \infty \,. \tag{2.55}$$

The *s*-domain fractional-order transfer function is stable when there are no poles located in the right-half complex plane [29]. A simplified Nyquist principle for fractional-order transfer function that is presented by Trigeassou et. When G(s) operated in a unity-gain closed-loop, the input-output transfer function bacomes

$$\widetilde{G_{nm}}(s) = \frac{G_{nm}(s)}{G_{nm}(s) + 1},$$
(2.56)

$$G_{nm}(s) = \frac{1}{a_{nm}s^{\alpha_{nm}} + a_{nm-1}s^{\alpha_{nm-1}} + \dots + a_0s^{\alpha_0}}$$
 (2.57)

By factoring,

$$G_{nm}(s) = \frac{1}{a_1 s^{\alpha_1}} \frac{1}{a_{nm} s^{\alpha_{nm}-a_1} + \frac{a_{nm-1}}{a_1} s^{\alpha_{nm-1}-a_1} + \dots + 1}$$
(2.58)

$$=\frac{1}{a_{1}s^{\alpha_{1}}}\widetilde{G_{nm-1}}(s).$$
(2.59)

 $\widetilde{G_{nm-1}}(s)$ is then written as a closed-loop system:

$$\widetilde{G_{nm-1}}(s) = \frac{G_{nm-1}(s)}{G_{nm-1}(s) + 1},$$
(2.60)

with the following

$$G_{nm-1}(s) = \frac{1}{\frac{a_2}{a_1}s^{a_2-\alpha_1}} \frac{1}{\frac{a_{nm}}{a_2}s^{\alpha_{nm}-a_2} + \frac{a_{nm-1}}{a_2}s^{\alpha_{nm-1}-a_2} + \dots + 1},$$
(2.61)

$$=\frac{1}{\frac{a_2}{a_1}s^{a_2-\alpha_1}}\widetilde{G_{nm-2}}(s), \qquad (2.62)$$

where the stability is determined by utilizing the Nyquist principle for the system loop gain.

2.8.2 Discrete Fractional-Order Systems

A brief summary of discrete-time fractional-order systems is given in the following. This definition is the generalization of the conventional definition for the fractional-order systems. Grünwald-Letnikov extended the definition for the fixed time-step of h for the first and second order increments as

$$\Delta x_k = x_k - x_{k-1} \,, \tag{2.63}$$

$$\Delta^2 x_k = x_k - 2x_{k-1} + x_{k-2}. \tag{2.64}$$

For generalization form of order n this becomes:

$$\Delta^{n} x_{k} = \sum_{a=0}^{n} (-1)^{a} {n \choose r} x_{k-a.}$$
(2.65)

The fractional-order difference equation can be determined for the α fractional order as

$$\Delta^{\alpha} x_{k} = \sum_{l=0}^{k} (-1)^{l} {\binom{\alpha}{l}} x_{k-l}, \qquad (2.67)$$

where is $\alpha \in \mathbb{R}$ and $k \in \mathbb{N}$ number of sampling.

2.8.3 Discrete-Time Fractional-Order State-Space Models

Dzieliński and Sierociuk have introduced fractional-order state-space models [30] [31]. The state-space description of the fractional-order system in discrete-time is represented as

$$\Delta^{\alpha} x_{k+1} = A_a x_k + B u_k, \tag{2.68}$$

$$x_{k+1} = \Delta^{\alpha} x_{k+1} - \sum_{l=1}^{k+1} (-1)^l {\alpha \choose l} x_{k-l+1},$$
(2.69)

$$y_k = Cx_k + Du_k, \tag{2.70}$$

where $x_k \in \mathbb{R}^N, A_a \in \mathbb{R}^{N \times N}, B \in \mathbb{R}^{N \times m}, C \in \mathbb{R}^{p \times N}, D \in \mathbb{R}^{p \times m}, m = input \text{ and } p = output.$ The nonlinear discrete-time model of a fractional-order system for the state-space description is represented as

$$\Delta^{\alpha} x_{k+1} = f(x_k, u_k), \qquad (2.71)$$

$$x_{k+1} = \Delta^{\alpha} x_{k+1} - \sum_{l=1}^{k+1} (-1)^l {\alpha \choose l} x_{k-l+1}, \qquad (2.72)$$

$$y_k = h(x_k), \tag{2.73}$$

where $\alpha \in \mathbb{R}$ and f(x), h(x) are nonlinear functions.

3 Identification of Fractional-Order Models

The research now proceeds with developing methods for identification of transfer functions for unknown systems. This is needed in order to develop dynamic models for dc-dc boost converters that encompasses their complex behavior in manner such that closed-loop feedback controllers can be developed.

3.1 System Identification

The objective of system identification is to find a suitable dynamical system model by using either experimental or simulation-based data to determine input-output behavior. This is accomplished by injecting a known set of input signals with corresponding observations of the system output response. These observations are repeated over a range of possible input frequencies in order to map the system behavior over a complete operational range. Most typically, sinusoidal input signals are applied with a set of specified magnitudes u_0 and frequencies ω [rad/s] [11].

$$u(t) = u_0 \sin(\omega t + \alpha), \qquad (3.1)$$

Fourier analysis of the output is performed and the component that corresponds to the same fundamental frequency of the input is identified [12]:

$$y(t) = y_0 \sin(\omega t + \beta)$$
(3.2)

where u_0 and y_0 denote magnitudes and are phase angle both are non-negative. It is noted that the output sinusoid has different amplitude y_0 and is shifted in phase from the input:

$$\phi = \triangleq \beta - \alpha. \tag{3.2}$$

In general, the output will contain harmonic components that are created due to nonlinearities and fractional order effects:

$$y(t) = \alpha \sum_{z=-\infty}^{\infty} g(z) \operatorname{Im} \left(e^{j\omega(t-z)} \right) = \alpha \operatorname{Im} \sum_{z=-\infty}^{\infty} g(z) e^{-j\omega(t-z)}.$$
(3.3)

$$y(t) = \alpha \operatorname{Im}\left\{e^{j\omega t} \sum_{z=-\infty}^{\infty} g(z)e^{-j\omega z}\right\} = \alpha \operatorname{Im}\left\{e^{j\omega t}G(e^{j\omega})\right\}$$
(3.4)

$$= \alpha \left| G(e^{j\omega}) \right| \sin(\omega t + \emptyset) \tag{3.5}$$

3.2 Identification Concepts

The identification of a linear dynamical system has been established in manner that provides this capability in various software and laboratory instruments ([32] - [38]). Nonlinear system identification is applied when linear system identification is unsuccessful in proving a suitable model ([39] - [42]). In this research, an approach is developed based on extracting a fractional order derived from either detailed simulation results or experimentally collected data. In either case, this approach uses a sinusoidal varying signal applied to the input [43]:

$$u(t) = u_0 \sin(\omega t + \alpha). \tag{3.6}$$

A frequency dependent function $G(e^{\omega k})$ is defined from the discrete Fourier transform of u(k):

$$G(e^{\omega k}) = \sum_{k=-\infty}^{\infty} g(k)e^{-j\omega k}$$
(3.7)

For this research, the input signal is the duty-cycle applied to a dc-dc converter and the output is a voltage with a conceptual relationship provided in Figure 3.1.



Figure 3.1: A system with input and output signal.

3.2.1 Open Loop Identification

The open loop Identification method is extracted from experimental or simulation data obtained from what is generally a nonlinear system. The frequency range of the input is smaller than the cut-off frequency of the system state variables [44][45]. The output is analyzed using a fast Fourier transform (FFT) method to obtain the magnitude and phase angles of all of the output signal components. The FFT component for the fundamental frequency associated with the sinusoidal input is retained. The method repeats for series of input frequencies. The result of this method is identify the general response for a particular dc-dc converter configuration. This method is automated using MATLAB [46] [47].

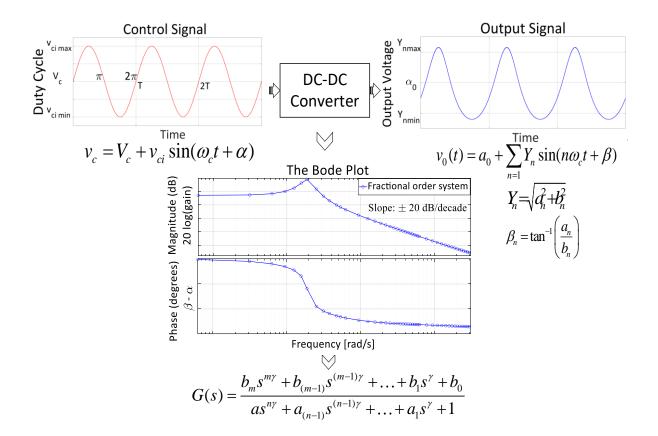


Figure 3.2: The system identification procedure.

Algorithm for the proposed method:

Step 1: Choose the frequency limit of concern where *f* is an array of frequencies.

Step 2: Assign a variable PWM corresponding to a desired nominal offset (boost gain) for the injected sinusoidal modulation such that the converter operates in continues conduction mode.

Step 3: Implement a simulation or experiment (time-domain) and record the V_{in} , and V_{out} signals. Resample y on a uniform grid tt = min(t) : dt : max(t). The y may have n columns but should have length (t) rows. This calls Matlab interp1 (with 'linear' as METHOD), and preprocess t (and y) so that tt is monotonically increasing. The *dtol* is a threshold such that t(j+1)-t(j) < dtol, then t(j+1) and y(j+1,:) is removed. NN is the number of new sample points (defaults to length(*t*)), given *dt* above as $dt = (\max(t) - \min(t))/(NN-1)$, (refer to MATLAB code in the Appendix A).

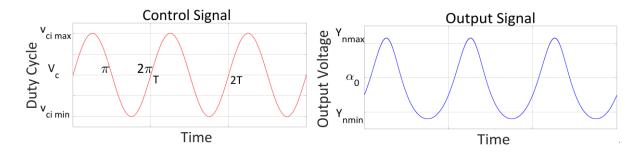


Figure 3.3: Example of signal input-output signals.

Step 4: Select the injected sinusoidal signal cycle (*t* are sampled points of each signal) of *input* signal and out signal in sampled points (*t*). Sort *y* and *t* for increasing *t* and remove duplicate entries in *t* (assuming y = y(t)). Duplicates are defined by t(j+1)-t(j) < dtol. The *p* is a permutation into 1: length (*t*), so that yy = y(p,:) and tt = t(p). If *y* is a (2*d*) matrix, then one of its dimensions must be length (*t*). If number of rows(*y*) is ~= length (*t*), then it is transposed. The output number of rows (*yy*) = length (*tt*), and *tt* is a column vector (refer to MATLAB code in Appendix B).

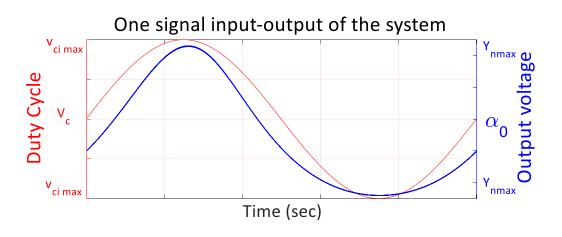


Figure 3.4: Example of sinusoidal wave input and output.

Step 5: Compute the respective FFT for (*Vin and Vout*), ft_i=fft(y_i or y_o) where y_i or y_o are one of the time-domain samples, in evaluating the series of input amplitudes and the series of output amplitudes with the phase angle for input and output suitably correlated. The fi_i(2) or if_o are the complex value of the fundamental frequency in V_{in} and V_{out} , where the gain and phase angles form the FFT are coded that in Matlab as:

Gain = *abs*(*value of ft_i or ft_o*)

Phase = tan (imaginary of of ft_i or ft_o, real value of ft_i or ft_o))

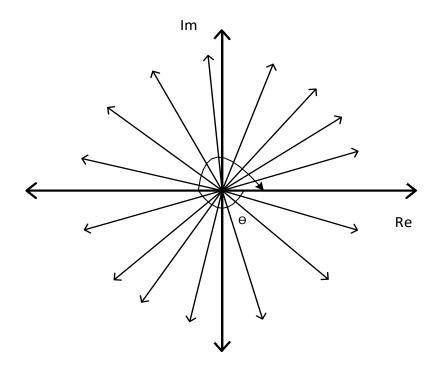


Figure 3.5: Example of signal input-output phase angle shift.

Step 6: Record the gain and phase angle of *input signal* and *output signal*. Generate a Bode plot by graphing the gain and phase shift of the open-loop transfer function using Matlab as:

Magnitude =
$$20 * Log \left[\frac{Gain(ft_{i(2)})}{Gain(ft_{o(2)})} \right] dB$$
,

$$\theta = (Phase(ft_i(2)) - Phase(ft_o(2))) * 180/\pi degrees.$$

Step 7: Plot the collected data as a Bode plot.

Step 8: From the Bode plot defined the fractional order system by using an approximation of the fractional order representation.

3.3 System Identification Procedure

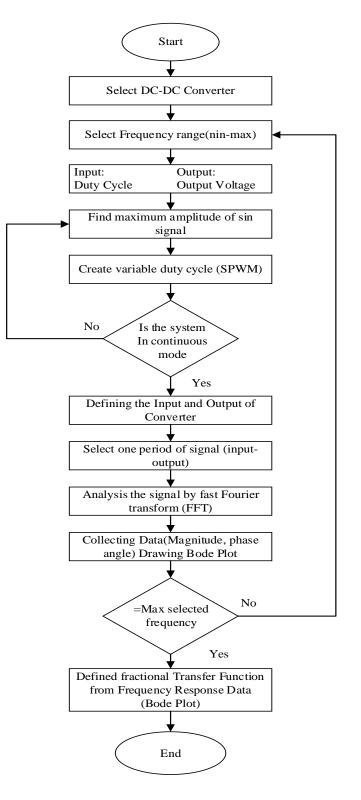


Figure 3.6: The system identification flowchart.

The general method of system identification is shown in Figure 3. 6. The analytical process is described in the following:

Step 1: Design the simulation or hardware prototype with methods for collecting the output FFT (magnitude and phase angle).

Step 2: Select a range of frequencies based on the type of power electronic converter.

Step 3: Determine the largest allowable amplitude for the injected control signal (duty cycle) to operate the system in continues conduction mode.

Step 4: Select a one period input switching sinusoidal signal function and determine the number of sample per period the will be used for the FFT.

Step 5: Determine and record the amplitude and phase shift using Fast Fourier Transform (FFT).

Step 6: Record the data of the amplitude and phase shift. The data is collected is used as one point of a Bode plot.

Step 7: Repeat this process for a frequencies defined from Step 2.

Step 7: Compile the tabulate date to determine the corresponding fractional-order transferfunction.

3.4 Sine-Wave Response

The fractional order system identification begins with the Bode plot derived from the fundamental component of the FFT that was obtained in Section 3.3. The Bode plot representation analogous to a describing function method that would produce a discrete set of points. Each particular input frequency has a corresponding fundamental component that is observed at the output. The relationship is shown at one particular case in Fig. 3.7. This process is repeated for all frequencies within the range of interest determined in Section 3.3. The resulting response $H(j\omega)$ has a magnitude and its phase angle:

$$H(s)|_{s=i\omega} = H(j\omega) = |H(j\omega)|e^{j\angle H(j\omega)}$$
(3.8)

where is $|H(j\omega)|$ the amplitude and $\angle H(j\omega)$ is the phase angle.

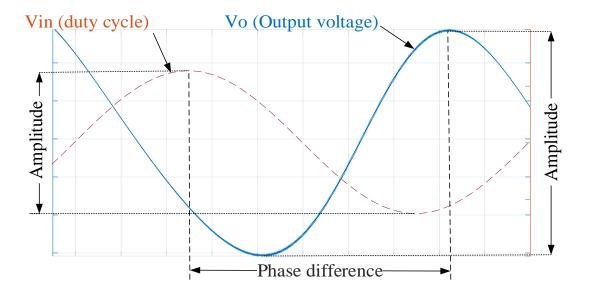


Figure 3.7: One signal responses the gain and phase angle of input-output.

3.5 Time-Domain Identification

The identification of fractional-order models in the time-domain is now described. Analyzing input-output frequency response achieves the fractional-order transfer function for a set of collected data. The general form of the transfer function [48]:

$$G(s) = \frac{b_m s^{\beta_m} + b_{m-1} s^{\beta_{m-1}} + \dots + b_0 s^{\beta_0}}{a_n s^{\alpha_n} + a_{n-1} s^{\alpha_{n-1}} + \dots + a_0 s^{\alpha_0}}$$
(3.9)

where is ak and bk indicate to poles and zeros of polynomial differential operator coefficients. By optimizing:

$$b_z = [b_m \quad b_{m-1} \quad \cdots \quad b_0],$$
 (3.10)

$$\beta_z = [\beta_n \quad \beta_{n-1} \quad \cdots \quad \beta_0], \tag{3.11}$$

$$a_p = [a_n \quad a_{n-1} \quad \cdots \quad a_0], \tag{3.12}$$

$$\alpha_p = \begin{bmatrix} \alpha_n & \alpha_{n-1} & \cdots & \alpha_0 \end{bmatrix}, \tag{3.13}$$

where a_p and b_z are pole and zero polynomial and α_p , β_z the orders of differentiation.

3.6 Frequency-domain Identification

Hartley and Lorenzo provide an approach to achieve a fractional-order model by substitution of $s = j\omega$ as all zeros [49]:

$$G(s) = c_n s^{n\gamma} + c_{n-1} s^{(n-1)\gamma} + \dots + c_1 s^{\gamma} + c_0.$$
(3.14)

The all-pole form is given by:

$$G(s) = \frac{1}{c_n s^{n\gamma} + c_{n-1} s^{(n-1)\gamma} + \dots + c_1 s^{\gamma} + c_0}.$$
(3.15)

where *n* is the integer order of the system and γ is the commensurate operator. Duarte Valério and Levy propose the frequency-domain identification approach. The complex term of a frequency $\omega \in (0; \infty)$ and *j* is the imaginary part as the following:

$$G(j\omega) = \frac{1}{c_n(j\omega)^{n\gamma} + c_{n-1}(j\omega)^{(n-1)\gamma} + \dots + c_1(j\omega)^{\gamma} + c_0},$$
(3.16)

Or more generally as

$$G(j\omega) = \frac{P(j\omega)}{Q(j\omega)} = \frac{b_m(j\omega)^{m\gamma} + b_{m-1}(j\omega)^{(m-1)\gamma} + \dots + b_1(j\omega)^{\gamma} + b_0}{a(j\omega)^{n\gamma} + a_{n-1}(j\omega)^{(n-1)\gamma} + \dots + a_1(j\omega)^{\gamma} + 1},$$
(3.17)

minimizing the square norm as:

$$\varepsilon = G(j\omega) [a_n (j\omega)^{n\gamma} + a_{n-1} (j\omega)^{(n-1)\gamma} + \dots + a_1 (j\omega)^{\gamma} + 1] - [b_m (j\omega)^{m\gamma} + b_{m-1} (j\omega)^{(m-1)\gamma} + \dots + b_1 (j\omega)^{\gamma} + b_0]$$
(3.18)

with a resulting matrix form expressed as:

$$\begin{bmatrix} \frac{1}{G(j\omega_{1})} \\ \frac{1}{G(j\omega_{2})} \\ \vdots \\ \frac{1}{G(j\omega_{m})} \end{bmatrix} = \begin{bmatrix} 1 & (j\omega_{1})^{\gamma} & (j\omega_{1})^{2\gamma} & \cdots & (j\omega_{1})^{n\gamma} \\ 1 & (j\omega_{2})^{\gamma} & (j\omega_{2})^{2\gamma} & \cdots & (j\omega_{2})^{n\gamma} \\ \vdots & \vdots & \vdots & \ddots & \vdots \\ 1 & (j\omega_{m})^{\gamma} & (j\omega_{m})^{2\gamma} & \cdots & (j\omega_{m})^{n\gamma} \end{bmatrix} \begin{bmatrix} c_{0} \\ c_{1} \\ c_{2} \\ \vdots \\ c_{n} \end{bmatrix},$$
(3.19)

where $\omega_1, \omega_2, \dots, \omega_n$ indicates the frequencies that have been selected. When using this form the operator must satisfy the order n and commensurate order γ . This method determines parameters from an experiment or simulated response of $G(j\omega) = \Re(\omega) + j\Im(\omega)$ [49]:

$$\begin{bmatrix} A & B \\ C & D \end{bmatrix} \begin{bmatrix} b_0 \\ \vdots \\ b_m \\ a_1 \\ \vdots \\ a_n \end{bmatrix} = \begin{bmatrix} e \\ g \end{bmatrix},$$
(3.20)

where are A, B, C, D are the following:

$$A_{k,l} = \sum_{c=1}^{f} (-\Re[(j\omega_c)^{k\gamma}]\Re[(j\omega_c)^{l\gamma}] - \Im[(j\omega_c)^{k\gamma}]\Im[(j\omega_c)^{l\gamma}]),$$

$$k = 0, \dots, m, l = 1, \dots, m.$$
 (3.21)

$$B_{k,l} = \sum_{c=1}^{f} \left(\Re[(j\omega_{c})^{k\gamma}] \Re[(j\omega_{p})^{l\gamma}] R_{p} + \Im[(j\omega_{c})^{k\gamma}] \Re[(j\omega_{c})^{l\gamma}] I_{c} - \Re[(j\omega_{c})^{k\gamma}] \Im[(j\omega_{c})^{l\gamma}] I_{c} + \Im[(j\omega_{c})^{k\gamma}] \Im[(j\omega_{c})^{l\gamma}] R_{c} \right), k = 0, ..., m \text{ and } l = 1, ..., n.$$
(3.22)

$$\begin{aligned} C_{k,l} &= \sum_{c=1}^{f} \{\{\Im[(j\omega_{c})^{k\gamma}]I_{c} - \Re[(j\omega_{c})^{k\gamma}]R_{c}\}\Re[(j\omega_{c})^{l\gamma}] \\ &+ \{-\Re[(j\omega_{c})^{k\gamma}]I_{c} - \Im[(j\omega_{c})^{k\gamma}]R_{c}\}\Im[(j\omega_{c})^{l\gamma}]\}, k = 1, \dots, n. \, l = 0, \dots, m. \, (3.23) \end{aligned}$$

$$D_{k,l} = \sum_{c=1}^{f} [(R_c^2 + I_c^2) \{ \Re[(j\omega_c)^{k\gamma}] \Re[(j\omega_c)^{l\gamma}] + \Im[(j\omega_c)^{k\gamma}] \Im[(j\omega_c)^{l\gamma}] \}], k = 1, ..., n, l$$

$$= 1, \dots, m. \tag{3.24}$$

$$e_{k,l} = \sum_{c=1}^{f} \{-\Re[(j\omega_c)^{k\gamma}]R_c - \Im[(j\omega_c)^{k\gamma}]I_c\}, k = 0, \dots, m,$$
(3.25)

$$g_{k,l} = \sum_{c=1}^{f} \{-\Re[(j\omega_c)^{k\gamma}](R_c^2 + I_c^2)\}, k = 1, \dots, n.$$
(3.26)

3.7 Nonlinear Fractional-Order System Identification

Nonlinear effects are accounted for with a new method of extending the describing function approach. This goes beyond the existing methods as in [50] to utilize fractional-order dynamical characteristics

$$\hat{y}(t) = \mathcal{L}^{-} \left[\frac{b_m s^{\beta_m} + b_{m-1} s^{\beta_{m-1}} + \dots + b_0 s^{\beta_0}}{a_n s^{\alpha_n} + a_{n-1} s^{\alpha_{n-1}} + \dots + a_0 s^{\alpha_0}} \right] * \sum_{k=1}^M \alpha_k u^k(t)$$
(3.27)

considering that the nonlinearity produces a polynomial model $\varphi(u) = \sum_{i=0}^{n} \beta_i u^i$, and the linear term from the commensurate fractional-order described as:

$$G(p) = \frac{B(p)}{A(p)} = \frac{\sum_{i=0}^{m} b_i p^{i\alpha}}{1 + \sum_{j=0}^{n} a_j p^{j\alpha}}$$
(3.28)

The proposed approach of system identification is to identify parameters associated with the nonlinear parts for a best fit equivalent fractional-order transfer functions.

3.7.1 Linearization of nonlinear fractional order systems

Conventional linearization techniques are commonly applied to achieve a linear approximation of a nonlinear system. This is ordinarily valid for a small region near the nominal operating point. The proposed new nonlinear fractional-order approach provides a wider region of valid operating conditions from the nominal operating point. This is a new extension to fractional-order models. In the following, recall that the fractional differential equation in the Caputo function are given by

$${}_{0}^{c}D_{t}^{\alpha}x(t) = f(x(t)),$$
(3.29)

$$Df(x)|_{x=x_{\omega}}. (3.30)$$

Linearization is with respect to the point x_{ω} . The following are defined:

Definition 1:

$$E \in \mathbb{R}^{n_a \times n_p}, F \in \mathbb{R}^{1 \times n_p}$$

$$n_{p = \binom{n_a + 1 + d}{d} - n_a - 2}$$
(3.31)

$$n_{\theta} = (n_a + 1)n_p \tag{3.32}$$

$$r(\theta) \triangleq \tilde{y}(\theta) - y \in \mathbb{R}^m \tag{3.33}$$

where is $m > n_{\theta}$.

$$f(\theta) \triangleq \frac{1}{2} \|r(\theta)\|^2, \in \mathbb{R}$$
 (3.34)

Definition 2:

Jacobian of residual vector r:

$$J(\theta) \triangleq \frac{\partial r}{\partial \theta} \in \mathbb{R}^{n_{\theta} \times m}$$
(3.35)

Definition 3:

Gradient of function (f):

$$\nabla f(\theta) \triangleq \frac{\partial r}{\partial \theta} = \left(\frac{\partial r^T}{\partial \theta}\right) r = J^T r \in \mathbb{R}^{n_\theta \times m}$$
(3.36)

Definition 4:

Hessian of function (*f*):

$$H(\theta) \triangleq \frac{\partial^2 f}{\partial \theta^2} \approx J^T J \in \mathbb{R}^{n_\theta \times m}.$$
(3.37)

3.8 Discrete-Time Delta Operator

Discretization is needed for the implementation of feedback controllers. The discrete-time techniques is extended to fractional-order systems. The discrete-time approximation for the continuous-time of transfer functions can be based on an Oustaloup filter [50]:

$$\frac{dy}{dz} \approx \frac{y(z+1) - y(z)}{T}$$
$$= \frac{(q-1)}{T} y(z)$$
$$= \delta y(t)$$
(3.38)

where δ is an operator that provides an equivalence of continuous and discrete-time systems [49].

4 Sinusoidal Pulse Width Modulation

This research is based on pulse width modulation (PWM) for switched-mode dc-dc converters . The switching state is obtained by comparing the desired reference wave with a carrier triangular waveform. When the reference signal is higher or lower than the carrier waveform, the active power transistor goes to the corresponding high (on) or low (off) state. Hard-switching (power transistors that change from on-state to off-state during a period of electrical conduction) tend to further increase the harmonic content of the associated state variables (inductor currents and capacitor voltages). Harmonic elements that are artifacts of PWM operation are filtered based on the inductance and capacitance of the overall systems [51].

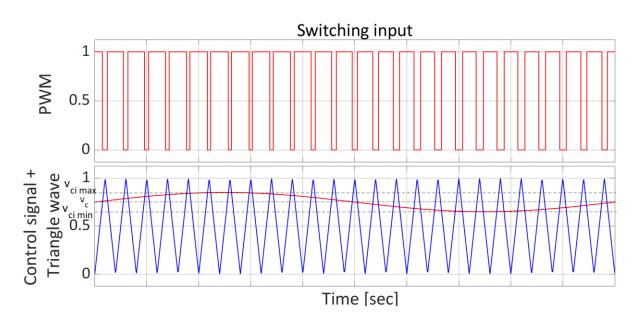


Figure 4.1: Principal of Sinusoidal Pulse Width Modulation.

As an example, the reference modulation signal is V(t) and V_m is an amplitude of the carrier triangular function as is shown in Fig. 4.1.

4.1 Sinusoids

For this research, the following notation is used for a reference sinusoidal modulating function:

$$x(t) = A\sin(\omega_0 t + \emptyset) \tag{4.1}$$

where *A* represents the amplitude, $\omega_0 = 2\pi f_0$ denotes the frequency(radian frequency), and \emptyset represents the phase angle [52].

$$x(t) = A\sin(\omega_0 t + \emptyset) = A\cos(\omega_0 t + \emptyset' - \pi/2)$$
(4.2)

where is $\phi = \phi' - \pi/2$. Trigonometric relationships to describe sin and cos functions are represented as follows:

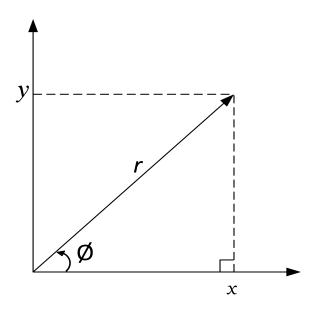


Figure 4.2: Sine and cosine functions.

$$\sin \phi = \frac{y}{r} \tag{4.3}$$

$$y = r \sin \emptyset. \tag{4.4}$$

For cosine

$$\cos \phi = \frac{x}{r} \tag{4.5}$$

$$x = r \cos \emptyset. \tag{4.6}$$

The triangle appeared of the first quadrant of length x and y, and hypotenuse of length r. The angle of sine described as y/r and cosine described as x/r. A complex number is an obtained couple from real numbers expressed as:

$$D = (x, y) \tag{4.7}$$

where is *D* the real part and *y* the imaginary part. Algebraically the following is noted:

$$(D, y) = D + iy = D + iy$$
, which is $i = j = \sqrt{-1}$. (4.8)

Recall that $j^2 = (\sqrt{-1})^2 = -1$. The rectangular model of a complex number is determined [53]:

$$x = (D, y) = D + jy = D + iy$$
 (4.9)

The polar form is given as:

$$x = re^{j\emptyset} = r \angle \emptyset = |x|e^{j \arg D} \tag{4.10}$$

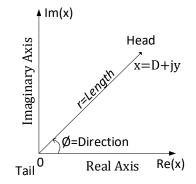


Figure 4.3: complex number as a vector.

Example 4.1

For complex numbers as, 1 - D = 2 + j5. 2 - D = 4 - j3. 3 - D = -5 + j0. 4 - D = -3 - j3.

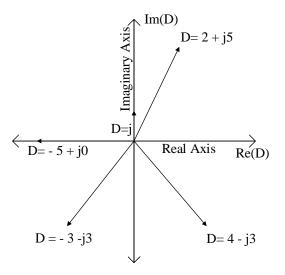


Figure 4.4: complex number of z=2+j5, =4-j3, z=-5+j0, and z=-3-j3.

Example 4.2

For complex numbers as $1 - x = 2 \angle 45^\circ$, $2 - x = 3 \angle 150^\circ$, $3 - x = 3 \angle -80^\circ$.

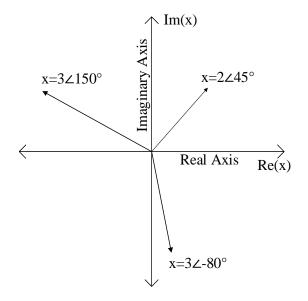


Figure 4.5: Complex number of $x=2\angle 45^\circ$, $x=3\angle 150^\circ$, and $x=3\angle -80^\circ$.

4.2 Describing Function (DF)

Nikolay Bogoliubov developed the describing function approach in 1930s [54] - [56]. It is a standard method for investigating limit cycle instabilities in a nonlinear system. The DF of a nonlinear component is comparable to the transfer function of a linear component. However, the DF leads to simplistic analyses and synthesis methods for nonlinear systems. Therefore this research developed an improved method of the input-output characteristics that encompass nonlinear dc-dc converters. The PWM duty-cycle command is considered as the input:

$$x(t) = A\sin(\omega t) \tag{4.11}$$

and the output signal (converter output voltage under load) is given by its Fourier decomposition:

$$y(t) = \frac{a_0}{2} + \sum_{n=1}^{\infty} (a_n \cos(n\omega_t t) + b_n \sin(n\omega_t t))$$
(4.12)

The Fourier description includes the dc offset $a_0 = 0$ which corresponds to a nominal dc boost gain for a dc-dc converters considered in this research. Using the first harmonic n = 1 of the Fourier series of the signal is:

$$\frac{y(t)}{x(t)} = \frac{a_1 \cos(\omega_t t) + b_n \sin(\omega_t t)}{A \sin(\omega t)}.$$
(4.13)

Applying the trigonometric identity:

$$a_1 \cos(\omega_t t) + b_n \sin(\omega_t t) = \sqrt{a_1^2 + b_1^2} \sin(\omega_t t + \emptyset)$$
 (4.14)

where is $\phi = \tan^{-1} \frac{a_1}{b_1}$

$$\frac{y(t)}{x(t)} = \frac{\sqrt{a_1^2 + b_1^2} \sin(\omega_t t + \emptyset)}{A \sin(\omega t)} .$$
(4.15)

The conventional describing function $N(A, \omega)$ is determined the ratio of the phasor description of input element and output element by frequency ω [57].

$$N(A,\omega) = \frac{\left(\sqrt{a_1^2 + b_1^2}\right)e^{j(\omega t + \phi)}}{Ae^{j\omega t}} = \frac{1}{A}(b_1 + ja_1)$$
(4.16)

where a_1 and b_1 are coefficients from the first harmonic of Fourier description provided as [58]:

$$a_1 = \frac{\omega}{\pi} \int_0^{2\pi/\omega} y(\tau) \cos(\omega \tau) \, d\tau, \tag{4.17}$$

$$b_1 = \frac{\omega}{\pi} \int_0^{2\pi/\omega} y(\tau) \sin(\omega \tau) \, d\tau \tag{4.18}$$

By substitution for a_1 and b_1

$$N(A,\omega) = \frac{\omega}{\pi A} \int_0^{2\pi/\omega} y(\tau) \sin(\omega\tau) \, d\tau + \frac{j\omega}{\pi A} \int_0^{2\pi/\omega} y(\tau) \cos(\omega\tau) \, d\tau.$$
(4.19)

4.2.1 PWM Effects

The effects of PWM harmonics challenging in order to achieve a good overall accounting for the nonlinear behavior of the overall system.

Example: PWM harmonic elements with fundamental of peak magnitude as:

$$h_1 = \left(4\frac{E}{\pi}\right)\left[1 - 2\cos\alpha_1 + 2\cos\alpha_2 - 2\cos\alpha_3 \cdots 2\cos\alpha_n\right]$$
(4.20)

$$h_2 = \left(4\frac{E}{2\pi}\right) \left[1 - 2\cos 2\alpha_1 + 2\cos 2\alpha_2 - 2\cos 2\alpha_3 \cdots 2\cos 2\alpha_n\right]$$
(4.21)

$$h_m = \left(4\frac{E}{m\pi}\right)\left[1 - 2\cos m\alpha_1 + 2\cos m\alpha_2 - 2\cos m\alpha_3 \cdots 2\cos m\alpha_n\right]$$
(4.22)

÷

where *E* is the dc bus voltage, h_m represents the magnitude of *m*, and α_n switching angles. The period of 2π Fourier coefficients are represented as:

$$a_{0} = \frac{1}{2\pi} \int_{0}^{2\pi} f(\theta) d\theta,$$
 (4.23)

$$a_k = \frac{1}{\pi} \int_0^{2\pi} f(k\theta) \cos(k\theta) \, d\theta, \qquad (4.24)$$

$$b_k = \frac{1}{\pi} \int_0^{2\pi} f(k\theta) \sin(k\theta) \, d\theta.$$
(4.25)

Based on symmetry, a quarter-cycle is determined by the Fourier coefficients as fowling [59]:

$$b_k = \frac{4}{\pi} \int_0^{2\pi} f(k\theta) \sin(k\theta) \, d\theta, \qquad (4.26)$$

$$b_{n} = \left(4\frac{E}{\pi}\right) \left[\int_{0}^{\alpha_{1}} \sin(k\theta) \, d\theta - \int_{\alpha_{1}}^{\alpha_{2}} \sin(k\theta) \, d\theta + \int_{\alpha_{2}}^{\alpha_{3}} \sin(nk) \, d\theta \cdots \int_{\alpha_{n}}^{\frac{n}{2}} \sin(k\theta) \, d\theta\right]$$
$$= \left(4\frac{E}{\pi}\right) \left[-\cos k\theta \Big|_{0}^{\alpha_{1}} + \cos k\theta \Big|_{\alpha_{1}}^{\alpha_{2}} - \cos k\theta \Big|_{\alpha_{2}}^{\alpha_{3}} \dots\right]$$
$$= \left(4\frac{E}{m\pi}\right) \left[1 - 2\cos m\alpha_{1} + 2\cos n\alpha_{2} - 2\cos n\alpha_{3} \cdots 2\cos n\alpha_{m}\right]. \tag{4.127}$$

4.2.2 DC-DC Converter Nonlinear Modeling with New Describing Function

The following is a new type of describing function technique compared to traditional methods as in [60]. Considering the input of the nonlinear behavior is x_{in} and the output is y_{out} . The input-output behavior of a nonlinear model for the converter is assumed by replacing the describing function definition for the nonlinearty of the converter as shown in Figurer 4.6.

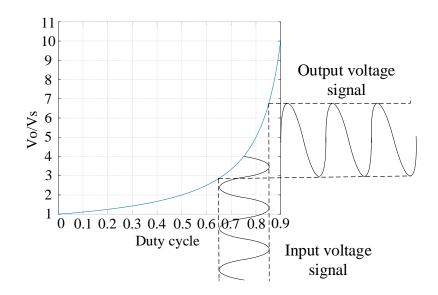


Figure 4.6: Boost curve and sine input-output.

The modified describing function method defines the model based on the input of the switching of the dc-dc converter output voltage. The input contains a low frequency sinusoidal signal (PWM duty cycle) as $v_c = V_{c+}v_{ci}\sin(\omega_c t)$ is compared with a high frequency carrier (saw tooth or triangular waveform). This produces a switching a waveform of $v_c(t)$ with the amplitude of V_c . The output voltage signal of the converter is $v_o(t) = v_m \sin(\omega_c t + \theta)$. The modified describing function is defined as $(j\omega) = \frac{v_m e^{j\theta}}{v_{ci}}$, which is defined as a Fourier series:

$$v_o(t) = a_0 + \sum_{n=1}^{\infty} (a_n \cos \omega_t t + b_n \sin \omega_t t),$$
 (4.28)

$$a_n = \frac{2}{T_x} \int_0^{T_x} v_o(\tau) \cos \omega \, \tau \, d\tau, \qquad (4.29)$$

$$b_n = \frac{2}{T_x} \int_0^{T_x} v_o(\tau) \sin \omega \tau \, d\tau, \qquad (4.30)$$

It is found that

$$v_m e^{-i\theta} = \frac{2}{T_x} \int_0^{T_x} v_o(\tau) e^{-i\omega\tau} d\tau, \qquad (4.31)$$

where T_x is the one period.

4.2.3 Property of Modified Describe Function

The modified DF (MDF) in this research accounts for the nonlinear boost characteristics of the converter. It is defined as linearization and minimization of the mean-squared approximation error, which results from the analysis.

$$\overline{e^2} = \frac{\omega}{2\pi} \int_0^{2\pi/\omega} e^2(t) dt$$
(4.32)

where

$$e = y(x, \dot{x}) - y_{approx}(A, \omega)$$
(4.33)

$$= y (A \sin \emptyset, A\omega \cos \emptyset) - A_{pN} \sin(\omega t + \vartheta_N), \qquad (4.34)$$

where *PN* is gain and ϑ_N , for stationary point as:

$$\frac{\partial \overline{e^2}}{\partial_{PN}} = \frac{\partial \overline{e^2}}{\partial \vartheta_N} = 0.$$
(4.35)

By differentiation respect to PN:

$$PN = \frac{\omega}{A\pi} \int_0^{2\pi/\omega} y(A\sin\phi, A\omega\cos\phi)\sin(\omega t + \vartheta_N) dt, \qquad (4.36)$$

and by differentiation respect to ϑ_N :

$$\frac{\omega}{A\pi} \int_0^{2\pi/\omega} y(A\sin\phi, A\omega\cos\phi)\cos(\omega t + \vartheta_N) dt = 0$$
(4.37)

and combining results in

$$PN = \frac{\omega}{A\pi} \int_0^{2\pi/\omega} y(A\sin\phi, A\omega\cos\phi) \left[\sin(\omega t + \vartheta_N) + j\cos(\omega t + \vartheta_N)\right] dt$$
(4.38)

$$j\frac{\omega}{A\pi}e^{-j\vartheta_N}\int_0^{2\pi/\omega} y(A\sin\emptyset,A\omega\cos\emptyset)e^{-i\omega t}dt.$$
(4.39)

4.2.4 Harmonic of Nonlinearity

The MDF represents the fundamental harmonic gain with inclusion on nonlinearities. The input-output gain due to nonlinearities from a sinusoid input with dc offset has been analyzed as an average of the effects of Fourier components. Therefore, when the input sinusoid represents the fundamental harmonic gain of the real nonlinearities as $v_c = V_{c+}v_{ci}\sin(\omega_c t)$. The MDF for harmonic nonlinearities is represented by [62]:

$$N(A) = \frac{4}{A\pi} \int_0^{\pi/2} y(A\sin\phi) \sin\phi \, d\phi$$
$$= \frac{4(V_c + v_{ci})}{A\pi} \int_0^{\pi/2} \sin(\omega_c \sin\phi) \sin\phi \, d\phi$$
$$= \frac{2(V_c + v_{ci})j(\omega_c)}{A}$$
(4.40)

where $(V_{c+}v_{ci})j$ is the Bessel functions.

4.2.5 Accuracy

Accuracy in the modeling of dynamical systems is an essential part in designing control systems. However, system analysis and identification utilizing conventional integer order differential equations has limitations ([63] - [68]) in systems that exhibit fractional order behavior [65].

4.3 Harmonic-Based Fractional Oder Modelling

The previous analysis assumed the availability of the fast Fourier transform (FFT) as a method to quickly analyze large amounts of input-output data. Another use of the FFT is to improve the accuracy of multiplication of high-degree order polynomials. This can be leverage to improve the ability to derive an equivalent fractional order transfer function realization that can be used in control system design. Transfer function polynomials coefficients, p and q are [69]:

$$r(x) = p(x)q(x) = \left(\sum_{m=0}^{N-1} f_m x^m\right) \left(\sum_{n=0}^{N-1} g_n x^n\right)$$

= $(f_0 + f_1 x + f_2 x^2 + \dots) (g_0 + g_1 x + g_2 x^2 + \dots)$
= $f_0 g_0 + (f_0 g_0 + f_1 g_0) x + (f_0 g_2 + f_1 g_1 + f_2 g_0) x^2 + \dots$
= $\sum_{m=0}^{2N-2} h_m x^m$ (4.41)

Whereas, $\sum_{i=0}^{N-1} f_i g_{n-i}$ and $h = f \circledast g$. Therefore, calculating the production of polynomials includes the convolution of the coefficient sequence.

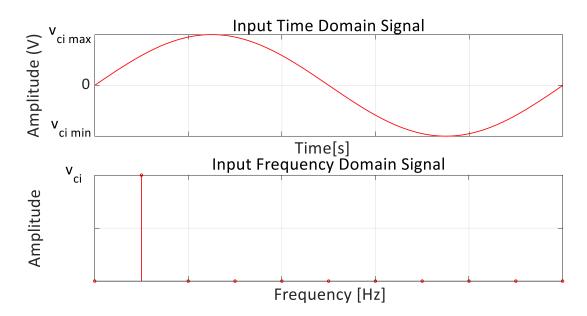


Figure 4.7: Fast Fourier Transform for input.

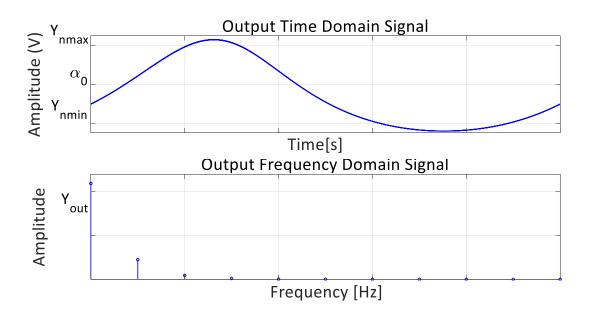


Figure 4.8: Fast Fourier Transform for output.

The amplitude for the input switching sinusoidal signal and output voltage across the load is utilizing by an FFT as shown in the Figs. 4.7 - 4. 8.

4.4 The Bode Plot

The Bode plot is description of the frequency response behavior of the system. A general example of the magnitude and phase plots are shown as in Fig. 4.9:

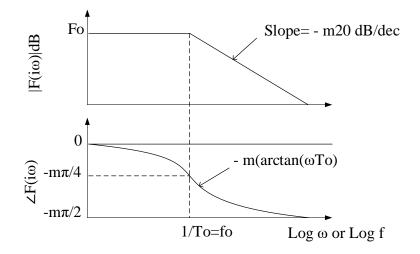


Figure 4.9: The magnitude in dB and phase angle in degree.

The first plot shows the magnitude in decibels (dB) [70] and the second plot is r the phase shift in radians. The construction of Bode plots for integer order transfer functions is explained in [71], with dB log scale

$$Lm(K) = 20\log|K| \tag{4.42}$$

where Lm is magnitude, and K is constant. For log of the magnitude and the phase shift.

$$Lm(j\omega)^{\mp k} = \mp 20k \log \omega, \qquad (4.43)$$

$$\angle (j\omega)^{\mp} = \pm \frac{k\pi}{2}, \qquad (4.44)$$

where the slope is $\mp 20k$ dB/decade and the phase shift $\mp \frac{k\pi}{2}$ is constant. For complex numbers $z = a + bj \in \mathbb{C}$ and $a, b \in \mathbb{R}$, $|z| = \sqrt{a^2 + b^2}$ and $\angle z = \tan^{-1} \frac{\operatorname{Im} z}{\operatorname{Re} z} = \tan^{-1} \frac{b}{a}$.

$$Lm(1+j\omega T)^{-1} = [20] * \log \left| \frac{1}{1+j\omega T} \right|,$$
(4.45)

$$= [20] * \log \left| \frac{1 - j\omega T}{1 + j\omega T} \right|, \qquad (4.46)$$

$$= [20] * \log \sqrt{\frac{1}{(1+\omega^2 T^2)^2} + \frac{\omega^2 T^2}{(1+\omega^2 T^2)^2}}, \quad (4.47)$$

$$= [20] * \log \sqrt{\frac{1}{(1+\omega^2 T^2)}}, \qquad (4.48)$$

$$= [-20] * \log \sqrt{1 + \omega^2 T^2}$$
(4.49)

$$\angle (1 + i\omega T)^{-1} = \tan^{-1} \frac{-\omega T}{1} = -\tan^{-1} \omega T.$$
(4.50)

For log magnitude and phase shift by $e^{\pm j\omega t}$

$$Lme^{\mp i\omega t} = [20] * \log |e^{\mp j\omega t}|$$
$$= [20] * \log |\cos(\mp \omega t) + j\sin(\mp \omega t)|$$
$$= [20] * \log 1 = 0$$
(4.51)

$$\angle e^{\mp i\omega t} = \tan^{-1} \frac{\sin \mp \omega t}{\cos \mp \omega t} = \mp \omega t.$$
(4.52)

At 0 dB the magnitude is equal to one, which is the magnitude plotted of a horizontal line and the phase shift is corresponded to the frequency ω .

4.5 Approximation of Fractional Order Systems

The rational approximation of the higher order transfer functions is employed in order to increase the modeling accuracy. The approximation of fractional order transfer functions can be

applied on continuous-time and discrete-time systems. The fractional-order approximation method is detailed in [72], [73].

4.5.1 Oustaloup's Method

Oustaloup's recursive filter is provides a band-limited version of fractional-order (FO) elements. The frequency range is (ω_b, ω_h) , with operation as s^{γ} , $0 < \gamma < 1$ and order *N* as:

$$G_f(s) = K \prod_{m=-N}^{N} \frac{s + \omega'_m}{s + \omega_m},$$
(4.53)

where

$$\omega_m' = \omega_b \left(\frac{\omega_h}{\omega_b}\right)^{\frac{m+N+\frac{1}{2}(1-\gamma)}{2N+1}},\tag{4.54}$$

and

$$\omega_m = \omega_b \left(\frac{\omega_h}{\omega_b}\right)^{\frac{m+N+\frac{1}{2}(1+\gamma)}{2N+1}}, \quad K = \omega_h^{\gamma}.$$
(4.55)

When the output is considered as an approximation of the fractional differentiated or integrated signal, the Oustaloup filter is purposed as [74] [75]:

$$s^{\alpha} \approx \left(\frac{d\omega_h}{b}\right)^{\alpha} \left(\frac{ds^2 + b\omega_h s}{d(1-\alpha)s^2 + b\omega_s s + d\alpha}\right) \prod_{m=-N}^{N} \frac{s + \omega'_m}{s + \omega_m},\tag{4.56}$$

where

$$\omega_m' = \left(\frac{d\omega_b}{b}\right)^{\frac{\alpha - 2m}{2N+1}}, \quad \omega_m = \left(\frac{b\omega_b}{d}\right)^{\frac{\alpha + 2m}{2N+1}}.$$
(4.57)

Applying the Oustaloup filter is often recommended to obtain a good results with b = 10and d = 9. For fractional orders $\alpha \ge 1$:

$$s^{\alpha} = s^m s^{\gamma}, \tag{4.58}$$

where $m = \alpha - \gamma$ indicates the integer term of α and the Oustaloup Approximation is represented by s^{γ} .

4.5.2 Levy's method

Levy's method of least-squares fitting of data can be extended to fractional-order functions. This includes transfer function approximations applying the input and output as in [76]:

$$\phi_j = |e_j| \sin(\omega t + \vartheta_j) \tag{4.59}$$

and

$$\phi_0 = |e_0|\sin(\omega t + \vartheta_0) \tag{4.60}$$

where $\frac{E_0(\omega)}{E_j(\omega)}$ amplitude ratio and $\theta_0(\omega) - \theta_j(\omega) = \Delta \theta(\omega)$ for frequencies within the range of modeling concerns.

4.6 Commensurate Fractional-Order Transfer Functions

The Levy identification approach is applied for identifying the fractional-order model represented in (4.60). It calculates the parameters of data frequency response by $G(j\omega) = \Re(\omega) + j\Im(\omega)$ as in [77]:

$$\begin{bmatrix} A & B \\ C & D \end{bmatrix} \begin{bmatrix} b_0 \\ \vdots \\ b_m \\ a_1 \\ \vdots \\ a_n \end{bmatrix} = \begin{bmatrix} e \\ g \end{bmatrix},$$
(4.61)

where $b_0, \ldots b_m$ and $a_1, \ldots a_n$ represents the identified parameters defined as *A*, *B*, *C*, *D*, *e* and *g* to obtained from the collected data [78]. The identification of fractional-order versions of the transfer function by using Levy's technique results in

$$\epsilon = \mathcal{G}(j\omega)[a_n(j\omega)^{n\gamma} + \dots + a_1(j\omega)^{\gamma} + 1] - [b_m(j\omega)^{m\gamma} + \dots + b_1(j\omega)^{\gamma} + b_0].$$
(4.62)

The Vinagre technique supplements weights for the norm of approximation at any frequency $\epsilon' = w \cdot \epsilon$, whereas weight w is based on a frequency, frequencies ω_i , i = 1, ..., f are defined as [79].

$$w = \begin{cases} \frac{\omega_2 - \omega_1}{2\omega_1^2} & , i = 1, \\ \frac{\omega_{i+1} - \omega_{i-1}}{2\omega_i^2} & , 1 < i < f, \\ \frac{\omega_f - \omega_{f-1}}{2\omega_f^2} & , i = f. \end{cases}$$
(4.63)

The operator must provides the commensurate order and fractional polynomial orders of *n* with m for an additional optimization of parameters $\theta = [\gamma \ n \ m]$. The function for minimization is of the form:

$$J = \frac{1}{n_{\omega}} \sum_{i=1}^{n_{\omega}} \left| G(i\omega) - \hat{G}(i\omega) \right|^2, \qquad (4.64)$$

whereas n_{ω} signify the number of frequencies in ω , \hat{G} denotes the identified plant *G*. The error index is utilized to calculate the identification accuracy [78] [79]. The time-domain identification is labelled as a data structure using a MATLAB (ffidata) command as follows:

id1 = ffidata(mag, ph, w), the mag characterizes to determine magnitude in dB, ph represents the phase angle by degree and w in rad/s.

5 Fractional PI Controller

There is increasing attention in using fractional calculus to control dynamical systems. After applying identification techniques to achieve the plant transfer function, the feedback control system for a non-integer order can be designed ([80], [81]). The use of a feedback controller requires measuring a performance error. This is typically determined as difference between the various outputs of the system and the desired value. The controller parameters are adjusted based on the system performance objectives such as settling time, steady-state tracking and percent overshoot ([82], [83]). The fractional-order proportional-integral (PI^{λ}) and lead-lag compensators have potential advantages compared to classical integer-order controllers ([84], [85]). Podlubny ([86]) introduced a form of the PI controller using fractional order operator as PI^{λ} (FO-PI) controller, for which an integral term of order λ is provided by assuming real-values non-integer numbers. Consideration of the general dynamical system as in [86]:

$$\dot{x}(t) = f[x(t), u(t)],$$
(5.1)

$$y = h[x(t)], \qquad (5.2)$$

where $x(t) \in \mathbb{R}^m$, $u(t) \in \mathbb{R}$, $f(x, u) \in \mathbb{R}^m \times \mathbb{R} \to \mathbb{R}^m$ and $h: \mathbb{R}^m \to \mathbb{R}$. The control operation of the PI^{λ} controller express as following:

$$u(t) = K_p e(t) + K_i \mathcal{D}^{-\lambda} e(t), \qquad (5.3)$$

where e(t) = -y(t) + s(t) is an error signal and s(t) is the desired control reference value. The Laplace transform is applied with zero initial conditions. The fractional order (FO) order controller has shown advantages compared to conventional integer order controllers [87].

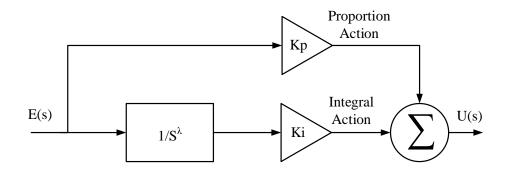


Figure 5.1: Fractional-order proportional and integrator controller model.

The transfer function of the FO PI^{λ} controller general expression is

$$G_c(s) = K_p + \frac{K_i}{s^{\lambda}}$$
(5.4)

where K_p , and K_i are the proportional and integral gains, respectively, of the PI^{λ} controller. The PI^{λ} controller retains the general properties of the integer order PI controller. The advantages of additional parameter λ is that it provides additional freedom in designing the controller. The fractional PI controller is particularly advantageous to for nonlinear systems. However, the tuning of the controller of the FO controller is more complicated compared to a conventional PI element. The representation of fractional-order proportional-integral-derivative (FO-PID) controller

$$G_c(s) = K_p + \frac{K_i}{s^{\lambda}} + K_d s^{\mu}, \qquad (5.5)$$

can similarly be defined. For the PI^{λ} controller, the effect of $\frac{1}{s^{\lambda}}$ is to produce an integral effect to guarantee there is no residual error in the closed-loop system [83].

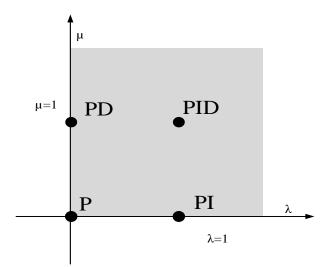


Figure 5.2: The $PI^{\lambda}D^{\mu}$ controller plane.

It is noted that selecting $\lambda = \mu = 1$ resulting that is classic integer-order PID controller. The FO-PI controller has more freedom of tuning the controller, duo to the additional parameter such λ and μ . The PID controller plane of the four-point PID diagram as showing in Figure 5.2. However, the derivative term μ will not be considered in this research due to noise present in switched-mode power converters that results in poor results for PID controllers.

5.1 Fractional Order Robust Control

The robustness of the fractional order controllers is particularly effective when there are uncertainties and the toleration of error. Research in the advances of fractional controllers is demonstrated in the robustness of tuning FO-PI controller and the H_{∞} controller in [88]. Tavazoei proposed designs for robust control with constant phase margin using FO models [89]:

$$G(s) = (hs^{\alpha} + L)\hat{G}^{-1}(s),$$
(5.6)

with feedback for the resulting FO system:

$$G(s) = \frac{1}{Ts^{\beta} + 1}\hat{G}(s),$$
(5.7)

where $\beta \in (0,2)$ and T > 0, is the relationship between the gain (0 dB) crossover frequencies and phase margins. Extending integration and derivative control operations is used in the fractional operation is given in [90] as

$$C(s) = K s^{\gamma}, \tag{5.8}$$

where $\gamma \in [-1:1]$ the fractional integrator.

Example 5.1

Consider an integer-order system with given transfer function as the following:

$$G(s) = \frac{1}{(s+1)^3},\tag{5.9}$$

with a FO-PI controller as:

$$C(s) = K_p \left(1 + \frac{1}{T_j s^{\lambda}} \right)$$
(5.10)

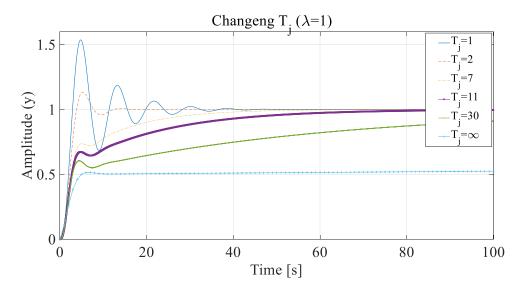


Figure 5.3: Step responses FOPI with variant integral of Tj.

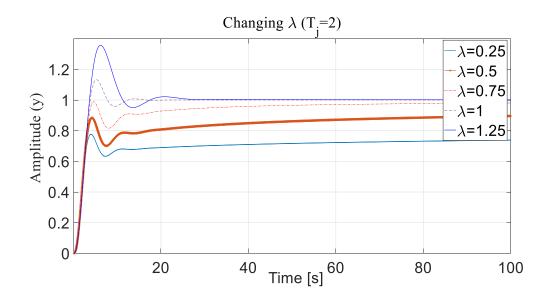


Figure 5.4: Step responses FO PI controller with variant integration order λ .

To demonstrate the characteristics of integration part with fractional order is shown in Figure 5.3 and Figure 5.4, which presents unit step response for the closed-loop of the system. Selecting proportional gain is $K_p = 1$ as a fixed value but the integral time of T_j are varied independently (Fig. 5.3 - 5.4). The integer case of $\lambda = 1$ are considered in Figure 5.3, with a steady-state error approaching zero. Furthermore, when T_j small there is a faster response with larger oscillatory overshoots. In Figure 5.4, T_j is constant with varied λ showing that the parameter does not affect oscillations. Overall, the FO-PI controller shows improved capability for tuning the systems response.

5.2 Optimization Controller Design

There are many perspectives that make designing a FO-PI controller difficult. Nevertheless, several approaches for tuning techniques have been developed ([91] - [92]). The most common optimization criteria is to minimize the integral squared error:

$$ISE = \int_{0}^{t} e^{2}(t)dt.$$
 (5.13)

Also considered are the integral absolute error,

$$IAE = \int_0^t |e(t)| dt, \qquad (5.14)$$

the integral time-square error as,

$$ITSE = \int_0^t te(t)^2 dt, \qquad (5.15)$$

And the integral time-absolute error as:

$$ITAE = \int_{0}^{t} t |e(t)| dt.$$
 (5.16)

The frequency domain specifications provide accuracy and stability for the control system. However, specifications can be commanded of open-loop frequency response $F(j\omega) = C(j\omega)G_p(j\omega)$, where is $C(j\omega)$ a FO-PI controller [93], with gain margin A_m :

$$A_m = 1 - \left| F(j\omega_g) \right|,\tag{5.17}$$

$$\arg\left(F(j\omega_g)\right) = -\pi.$$
 (5.18)

Phase margin φ_m and critical frequency ω_c are given by

$$\arg(F(j\omega_c)) = -\pi + \varphi_m, \qquad (5.19)$$

$$|F(j\omega_c)| = 1. (5.20)$$

Gain variation for a plant robustness is defined by

$$\frac{d \arg(F(j\omega))}{d\omega}\Big|_{\omega=\omega_c} = 0.$$
(5.21)

Noise rejection at high frequency is specified by

$$\left| T(j\omega) = \frac{F(j\omega)}{1 + F(j\omega)} \right|_{dB} \le N \, dB,$$
(5.22)

where frequencies $\omega \ge \omega_t$ rad/s, and *N* dB is noise attenuation.

Disturbance rejection is defined as

$$\left| \mathcal{C}(j\omega) = \frac{F(j\omega)}{1 + F(j\omega)} \right|_{dB} \le K \, dB, \tag{5.23}$$

where K dB is a constraint on the sensitivity function.

Example 5.6

A dc-dc converter fractional order transfer function is modeled by a fractional-order differential equation. The fractional transfer function and controller is determined to be

$$G(s)_{FO} = \frac{-0.323s^{0.78} - 15.58s^{0.52} - 96.03s^{0.26} + 340.9}{0.00156s^{1.3} - 0.02s^{1.04} - 0.1002s^{0.78} + 0.295s^{0.52} - 0.67s^{0.26} + 1},$$
 (5.24)

$$C_{FO}(s) = K_p + \frac{K_i}{s^{\lambda}}.$$
(5.25)

Applying the Ziegler-Nichols PI tuning method, PI parameters are determined as $K_p =$ 476.7 and $K_i = 0.011$ where λ is the parameter for optimizing the performance.

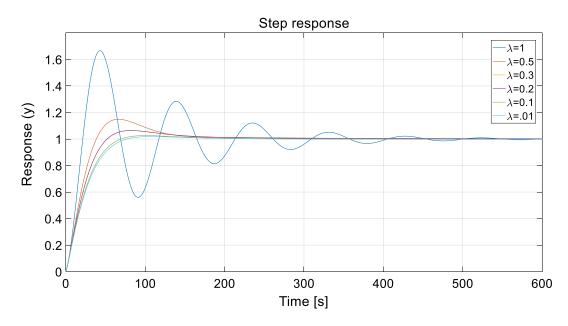


Figure 5.5: Closed loop step responses for $G_{FO}(s)$ under various λ .

5.3 FO-PI Tuning Formulation

The transfer function of the open-loop is represented by its Bode plot. Designing the feedback FO-PI controller as [80]:

$$G_{ref}(s) = \frac{K_r}{s^{\lambda}}, \qquad (1 < \lambda < 2) \tag{5.26}$$

Achieving the best performance of the feedback loop controller is investigated. With respect to the Bode plot:

Magnitude curve is constant slope of -20λ dB/dec, the critical pint of $\omega_c = (Kr)^{\frac{1}{\lambda}}$

Phase angle curve is horizontal at $-\frac{\lambda\pi}{2}$

Nyquist curve line at arg $-\frac{\lambda\pi}{2}$

These are shown in Figure 5.6. Robustness of the closed-loop system is achieved when the loop gain achieves [80]

$$G_{refCL}(s) = \frac{1}{\left(\frac{1}{K_r}\right)s^{\lambda} + 1}.$$
(5.27)

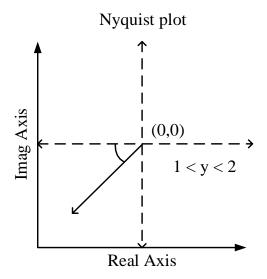


Figure 5.6: Nyquist Bode plot of ideal transfer function.

5.4 Gain Scheduling Methods

Gain scheduling methods assist in obtaining robust controller operation for a wider range of operating conditions. A fractional-order transfer equation can be achieved for a set of operating points $\{(u_k; y_k), k = 1, 2, ..., n\}$ for the system [87] as the set

$$\Psi = \{G_1, G_2, \dots, G_n\},\tag{5.28}$$

where $G_i \in \Psi$ narrowly meets a set of performance designations

$$\Omega = \{C_1, C_2, \dots, C_n\}.$$
 (5.29)

Considering a controller defined as

$$\Upsilon(x,f) = \sum_{s=1}^{k} \beta_k(x) C_s(f),$$
(5.30)

where β_s is weighting function and x(t) is scheduled state, $C_s(f) \in \Omega$. suppose that it is given that k = 2. Then the following controller is defined as

$$\Upsilon(x, f) = \beta_1(x)C_1(s) + \beta_2(x)C_2(f),$$
(5.31)

where $x(t) \in [0, x_{max}]$ and that

$$\beta_1(x) := \frac{[1 - \lambda(x)]}{2},\tag{5.32}$$

$$\beta_2(x) := \frac{\lambda(x)}{2},\tag{5.33}$$

$$\lambda(x) := \frac{x(t)}{x_{max}}.$$
(5.34)

5.5 Linear Quadratic Regulator (LQR) Design

The LQR design is a commonly used approach in an optimal control design. The LQR design method provides assurances for phase and gain margins, and some tolerance for nonlinearities [94]. Connections between robustness and LQR designs have been established ([95], [96]). The LQR design technique adds additional state variable as:

$$\dot{x}_1 = e(t) = V_{ref} - V_{out} \tag{5.35}$$

Consideration of the tracking error is given by an integral term,

$$x_1 = \int e(t). \tag{5.36}$$

Optimization with respect to the input is minimized by the cost function

$$J(t) = \int_0^\infty (z^T Q z + v^2) dt,$$
 (5.37)

where *Q* represents a symmetric positive semidefinite matrix and is often selected as Q = diag(q1, q2, q3). The q_i parameters are adjustable to meet a particular performance objective. The design of a closed-loop controller for a dc-dc converter is given in Figure 5.7.

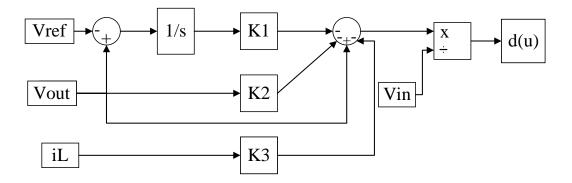


Figure 5.7: Control diagram of voltage to dc-dc converter.

The controller for a dc-dc converter is often configured with an inner and outer loop feedback path. The inner-loop is controlling the inductor current which is tracked as a reference value

$$\dot{x}_1 = e(t) = i_{lref} - i_l, \tag{5.38}$$

Where an augmented state variable is defined from the tracking error as:

$$x_1 = \int e(t), \tag{5.39}$$

The current controller is shown in Figure 5.8.

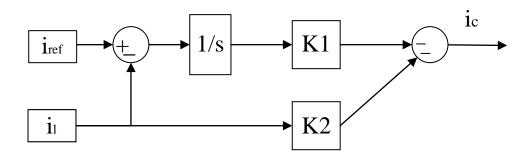


Figure 5.8: Inner current control for converter.

The outer-loop is controlling output voltage of the converter. The LQR design technique defines an error state as

$$\dot{x}_1 = e(t) = V_{ref} - V_{out} \,. \tag{5.40}$$

with an additional state variable of the tracking error as

$$x_1 = \int e(t). \tag{5.41}$$

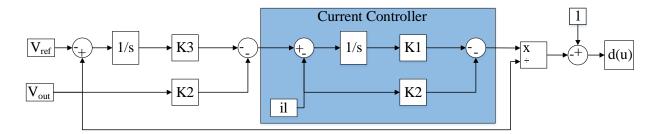


Figure 5.9: Control diagram of inner loop and outer loop controller.

5.5.1 First Order Plus Dead Time (FOPDT)

The (FOPDT) models are commonly applied in machine control design [94]. As an example,

$$G(j\omega) = \frac{Ke^{-Lj\omega}}{T(j\omega) + 1},$$
(5.35)

where *K* is the static gain of the model:

$$K = \frac{\Delta y}{\Delta u},\tag{5.36}$$

where Δy is the incremental change of the output and Δu an incremental change of the input. Lag L is defined as

$$L = t_u - t_r, \tag{5.37}$$

where t_u the time instance of the control input, and t_r is the time instance of system output.

Define the control dead-band as

$$\delta = \{ (1 - \gamma)u_s, (1 + \gamma)u_s \}, \ \gamma \in (0, 1)$$
(5.38)

where u_s represents the current steady-state control. A control input signal is defined as:

$$u(t) = \overline{+}\delta \tag{5.39}$$

After many cycles, the ultimate gain A_u and final period T_u are determined as an ultimate frequency ω_u :

$$\omega_u = \frac{2\pi}{T_u}, A_u = \frac{4\delta}{\pi a}.$$
(5.40)

5.5.2 The Fractional First Order Plu Dead-Time (FFOPDT)

The FFOPDT model is similar to the FOPDT design methods ([95], [96]). Consider the FFOPDT by

$$G(j\omega) = \frac{Ke^{-Lj\omega}}{T(j\omega)^{\alpha} + 1},$$
(5.41)

where it is assumed that K > 0, L > 0, T > 0 and $\alpha \in (0,2)$, for obtaining the magnitude and phase angle of $G(j\omega)$ from the following:

$$|G(j\omega)| = \frac{|K|}{\sqrt{1 + T^2 \omega^{2\alpha} + 2T \omega^{\alpha} \cos\left(\frac{\alpha\pi}{2}\right)}},$$
(5.42)

and

$$\arg(G(j\omega)) = -L\omega - \tan^{-1}\left(\frac{T\sin\left(\frac{\alpha\pi}{2}\right)}{\omega^{-\alpha} + T\cos\left(\frac{\alpha\pi}{2}\right)}\right).$$
(5.43)

To obtain the gain crossover frequency $|G(j\omega_c)| = 1$,

$$\omega_c = \left(\frac{\sqrt{\theta(K,\alpha)} - \cos\left(\frac{\alpha\pi}{2}\right)}{T}\right)^{\frac{1}{\alpha}},\tag{5.44}$$

where $\sqrt{\theta(K, \alpha)} = K^2 + \cos^2\left(\frac{\alpha\pi}{2}\right) - 1$ and $\omega_c \in \mathbb{R}_+$.

$$\theta(K,\alpha) \ge 0, \tag{5.45}$$

$$\sqrt{\theta(K,\alpha)} - \cos\left(\frac{\alpha\pi}{2}\right) \ge 0.$$
 (5.46)

The phase margin of the system is defined by:

$$\varphi_m = \pi - \arg(G(j\omega_c)) + 2\pi n \quad , \quad n \ge 0.$$
(5.47)

The transcendental equation

$$-L\omega_u - \tan^{-1}\left(\frac{T\sin\left(\frac{\alpha\pi}{2}\right)}{\omega_u^{-\alpha} + T\cos\left(\frac{\alpha\pi}{2}\right)}\right) = -\pi - 2\pi n,$$
(5.48)

where *n* is defined by obtaining a minimum gain margin of $1/|G(\omega_c)|$ is achieved in practice by tuning the system response [92]. It is analogous to the case of a conventional PI controller [97]. A helpful summary of accurate approximations of fractional operatives is presented in [98]. The time

constant T_c can be calculated from the frequency ω_u . The final gain of the system can be observed as:

$$T_c = \frac{\tan(\pi - L_c \omega_u)}{\omega_u}.$$
(5.49)

FOPI controller design seeks a suitable integrator order of λ , which can be evaluated through the closed-loop gain relationship:

$$\tau_c = \frac{L_g}{L_c + L_g},\tag{5.50}$$

where τ_c is the dead-time parameter. The approximate guideline for λ is given by:

$$\lambda = \begin{cases} 1.1 & \tau_c \ge 0.6, \\ 1.0 & 0.4 \le \tau_c < 0.6, \\ 0.9 & 0.1 \le \tau_c < 0.4, \\ 0.7 & \tau_c < 0.1. \end{cases}$$
(5.51)

The gain robustness is given by:

$$\psi'_g(\omega_c) = 0, \tag{5.52}$$

$$\psi_g(\omega) = \arg(\mathcal{C}(j\omega)) + \arg(\mathcal{G}(j\omega)) + \pi + 2\pi n.$$
(5.53)

The minimal gain margin G_m of the resulting functions can be determined from

$$k1(K_p, K_i, K_d) = |C(j\omega)| \cdot |G(j\omega)| - 1,$$
(5.54)

$$k2(K_p, K_i, K_d) = \arg(\mathcal{C}(j\omega)) + \arg(\mathcal{G}(j\omega)) + \pi - \varphi_m - 2\pi n, \qquad (5.55)$$

$$k3(K_p, K_i, K_d) = \psi'_{gm}(\omega), \qquad (5.56)$$

where $\omega = \omega_c$. Finding gains of the FOPI controller is notated by:

$$g = \left[K_p, K_i \right]^T.$$
(5.57)

To determine the control coefficients a control vector is defined with a constraint:

$$F_s = [k1(\cdot) \ k2(\cdot)]^T = 0.$$
(5.58)

A solution can be found numerically to the relationship

$$J\Delta g = -F_s, \tag{5.59}$$

With the control gain vector of g^+ is calculated:

$$g^+ = g + \Delta g. \tag{5.60}$$

Elements of the Jacobian matrix J are defined by

$$J_{n,1} = \frac{\partial k_n}{\partial K_p},\tag{5.61}$$

$$J_{n,2} = \frac{\partial k_n}{\partial K_i}, \quad \text{For } n = 1,2:$$
(5.62)

$$J_{1,1} = \frac{A_G A_{CR}}{A_C},$$
(5.63)

$$J_{1,2} = \frac{A_G A_{12}}{A_C},\tag{5.64}$$

where $A_G = |G(j\omega)|, A_C = |C(j\omega)|$, and $A_{CR} = C_R(\omega)$,

$$A_{12} = \omega^{-2\lambda} \left(K_i + \omega^{\lambda} \cos\left(\frac{\lambda\pi}{2}\right) K_p \right), \tag{5.65}$$

$$J_{2,1} = \frac{A_{21}}{A_2},\tag{5.66}$$

$$J_{2,2} = \frac{A_{22}}{A_2},\tag{5.67}$$

$$A_{21} = \omega^{\lambda} \left(\sin\left(\frac{\lambda\pi}{2}\right) K_i \right), \tag{5.68}$$

$$A_{22} = \omega^{\lambda} \left(\sin \left(\frac{\lambda \pi}{2} \right) K_p \right), \tag{5.69}$$

j is determined by a stopping criterion for the iteration method,

$$\|F_s(\cdot)\|_2 < \epsilon. \tag{5.70}$$

5.5.3 Review of Conventional PI Controller Design Methods

Prior to showing the experimental results of a prototype dc-dc converter, it is important to establish a baseline reference for which the resulting FO-PI controller can be compared. The section provides a brief review of root locus based design methods for integer order PI controllers. For obtaining values for K_p and K_i , the root locus method can be applied ([101], [102], [103]):

$$\Delta(s) = 1 + PI(s) G(s) = 0$$
(5.71)

$$C(s) = K_p + \frac{K_i}{s} \tag{5.72}$$

The root locus for the open loop system (K_pG) is obtained using commercially available software tools. Specifications are usually given in terms percentage overshoot, rise time, gain margin, phase margin, and settling of time to first determine the proportional gain:

$$K_p = \overline{K_P} \tag{5.73}$$

The integral term is found from and evaluation of the characteristic equation:

$$1 + (PI)(G(s)) = 0$$

No solving for the new root locus expression where $\overline{K_P}$ is held fixed and K_m is to be determined

$$1 + \left(\frac{s\overline{K_p} + K_m}{s}\right)[G(s)] = 0$$
(5.74)

$$1 + \left(\frac{s\overline{K_p} + K_m}{s}\right)[G(s)] = 0$$
(5.74)

$$\overline{K_p}(G) + \frac{K_m G}{s} + 1 = 0$$
 (5.75)

$$\overline{K_p}\frac{N(s)}{D(s)} + \left(\frac{K_m}{s}\right)\frac{N(s)}{D(s)} + 1 = 0$$
(5.76)

$$s\overline{K_p}N(s) + K_mN(s) + sD(s) = 0$$
(5.77)

$$sK_pN(s) + sD(s) + K_mN(s) = 0$$
 (5.78)

$$1 + K_m * \frac{N(s)}{sK_p N(s) + sD(s)}$$
(5.79)

A root locus plot is then used to determine the integral term K_m ,

$$root \ locus \ \left(\frac{N(s)}{sK_pN(s) + sD(s)}\right).$$
(5.80)

- 6 Simulation and Experiment Identification of DC-DC Converter
- 6.1 Introduction

In this research, the previously defined method for fractional order identification and control is applied to switched-mode power electronic dc-dc converters. Results confirm the benefits of the new analysis method and it applicability to the improved operation of switched-mode power converters.

6.2 Identification of a DC-DC Buck Converter

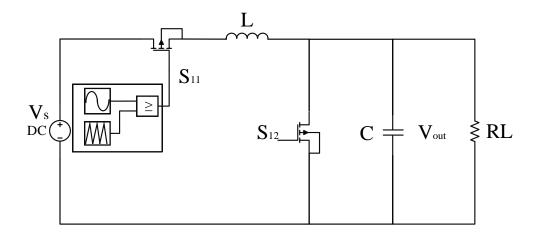


Figure 6.1: Identification of dc-dc buck converter.

The circuit configuration of the buck converter is presented in Figure. 6.1. The input voltage is a constant value of V_s=20 V. A time-varying reference signal is a combination of a dc voltage with an added sinusoidal component over a frequency range of $f = \{5 - 5K\}$ Hz. The average duty ratio is d(t)=0.6.

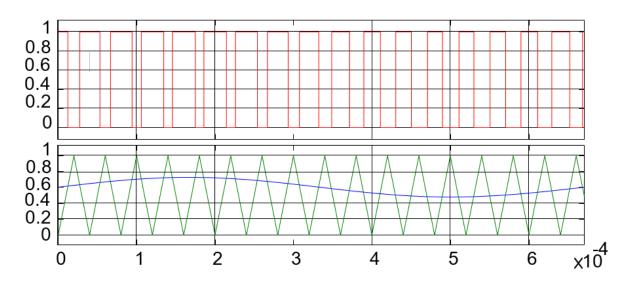


Figure 6.2: Example of control signal for buck converter of 1500Hz.

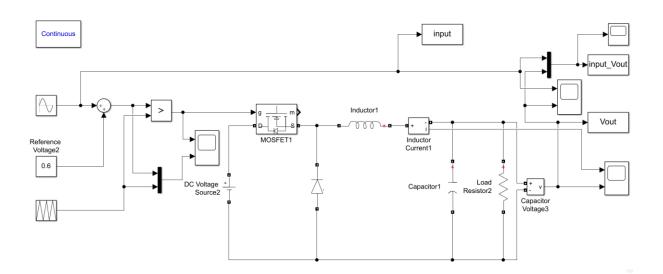


Figure 6.3: DC-DC Buck converter Matlab Simulink.

6.2.1 Fast Fourier Transform (FFT) Algorithm for the Buck Converter

The FFTs is applied to define the amplitude and phase angle from the output signal to determine the gain and the phase shift between the input and output signal as shown in Figure 6.4.

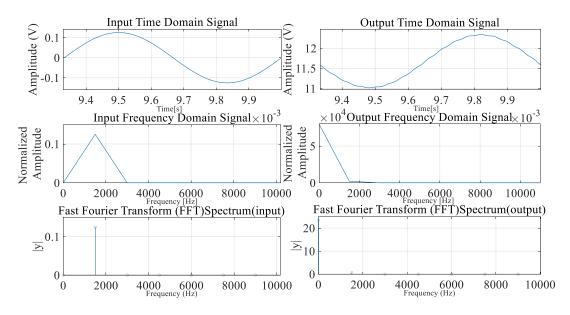


Figure 6.4: Example of FFT analysis for buck converter of 1500Hz.

Consequently, the magnitude and the phase shift for each frequency is tabulated for producing a Bode plot for the buck converter with values shown in Table 6.1.

Frequency (Hz)	Magnitude (dB)	Phase shift (degrees)
5	26.3518	-0.1019
10	26.3532	-0.2038
20	26.3588	-0.4079
100	26.5396	-2.0829
200	27.1286	-4.4615
300	28.2006	-7.5865
500	31.2667	-46.6346

Table 6.1: Collected data bode plot for buck converter.

Frequency(Hz)	Magnitude (dB)=20*log(gine)	Phase shift Degree
600	30.2814	-83.0687
700	28.6248	-108.1775
800	26.8768	-126.7498
900	25.1362	-141.3072
1000	23.34	-153.3525
1500	14.4072	-172.1569
2000	8.6764	-174.1601
2500	4.6062	-175.2553
3000	1.1723	-175.8845
3500	-1.7045	-176.1434
4000	-3.9727	-176.2405
4500	-6.1259	-177.142
4700	-6.8691	-176.3831
4800	-6.8691	-176.3831
5000	-6.8691	-176.3831

Continued Table 6.1: Collected data bode plot for boost converter.

6.2.2 Bode Plot Formulation for the Buck Converter

From the frequency response derived from simulation data, the Bode plot is created for the system. Results ar shown in Fig. 6.5.

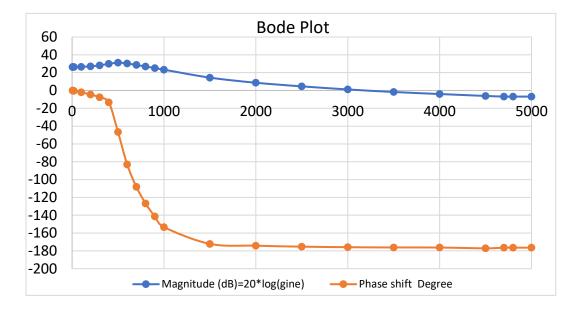


Figure 6.5: Bode Plot of buck converter form the Powersim simulation data.

The simulated buck converter is compared with a high-order integer transfer function by using the estimation Matlab command (TFEST). The system estimation fits 84.01% of the buck converter simulated results

$$G(s)_{sose} = \frac{3.226 \times 10^8}{s^2 + 2271 \, s + 1.601 \times 10^7}$$

A third-order integer transfer function is estimated for the buck converter. The results is fitted to estimation data of 85.98% of the simulated buck converter:

$$G(s)_{tose} = \frac{1.063 \times 10^{13}}{s^3 + 3.315 \times 10^4 s^2 + 5.939 \times 10^7 s + 5.27 \times 10^{11}}$$

A third-order system is estimated system with a zero included in the transfer function. The results fit the data by 84.95% of the simulated behaviour system as:

$$G(s)_{tosez} = \frac{2.545 \times 10^8 \,\mathrm{s} - 9.328 \times 10^{11}}{\mathrm{s}^3 + 925.8 \,\mathrm{s}^2 + 9.286 \times 10^6 \,\mathrm{s} + 4.4 \times 10^{10}}$$

A sixth-order integer estimation function is fitted to the data by 97.3% of the converter hehavior.

 $G(s)_{sxose}$

$$=\frac{4.041\times10^8\,\mathrm{s}^4\,-\,9.786e10\,\mathrm{s}^3\,+\,7.506e15\,\mathrm{s}^2\,+\,1.116\times10^{18}\mathrm{s}\,+\,6.171\times10^{22}}{\mathrm{s}^6\,+\,1338\,\mathrm{s}^5\,+\,3.382\times10^7\,\mathrm{s}^4\,+\,7.949\times10^9\,\mathrm{s}^3\,+\,4.971\times10^{14}\,\mathrm{s}^2\,+\,1.539\times10^{17}\,\mathrm{s}\,+\,2.951\times10^{21}}.$$

The comparison between all the estimated systems for simulation data is made to show the high order system is equivalent to the original system behavior as shown in the Figs. 6.6 and 6.7.

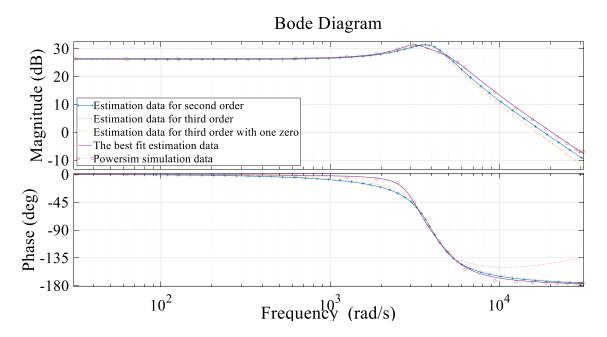


Figure 6.6: Bode Plot of buck converter simulated and estimated system.

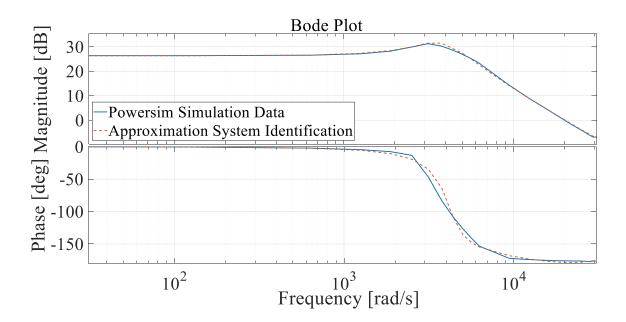


Figure 6.7: Bode Plot of buck converter simulated and approximation system.

The actual simulated response of the buck converter demonstrates a fractional order behavior since the asymptotic magnitude sloes at a rete different from -20 dB/decade. A fractional order transfer function is derived using Levy's method. The transfer function results the estimation error by 9% as showing:

$$G(s)_{FO} = \frac{-8.8396e - 06s^{1.5} + 0.00077031s^{1.2} - 0.028416s^{0.9} + 0.54375s^{0.6} - 5.2383s^{0.3} + 20.793}{1.6537e - 07s^{1.8} + 5.2915e - 06s^{1.5} + 9.2978e - 05s^{1.2} + 0.0016539s^{0.9} + 0.026898s^{0.6} + 0.2521s^{0.3} + 1.5638s^{0.6} + 1.5688s^{0.6} + 1$$

6.2.3 Controller for the DC-DC Buck Converter

A controller for the output voltage of the dc-dc buck converter is designed. The LQR design technique is used with error function defined:

$$\dot{x}_1 = e(t) = V_{ref} - V_{out},$$

with the buck converter model with a tracking error state variable defined as

$$x_1 = \int e(t),$$

$$x_{2} = i_{l},$$

$$x_{3} = V_{out},$$

$$\dot{x}_{1} = V_{ref} - x_{3}$$

$$\dot{x}_{2} = \frac{1}{C}x_{2} - \frac{1}{CR_{L}}x_{3}$$

$$\dot{x}_{3} = -\frac{rL}{L}x_{2} - \frac{1}{L}x_{3} + \frac{V_{in}}{L}u$$

For determine the tracking error behavior is the following:

$$\dot{z}_1 = -z_3$$
$$\dot{z}_2 = \frac{1}{C} z_2 - \frac{1}{C R_L} z_3$$
$$\dot{z}_3 = -\frac{rL}{L} z_2 - \frac{1}{L} z_3 + \frac{V_{in}}{L} u$$

For control input *u* is optimized by the cost quadratic cost function:

$$J(t) = \int_0^\infty (z^T Q z + v^2) dt,$$

where Q is a symmetric positive semi-definite matrix. The root-locus plot for the buck converter is shown in Fig. 6.8.

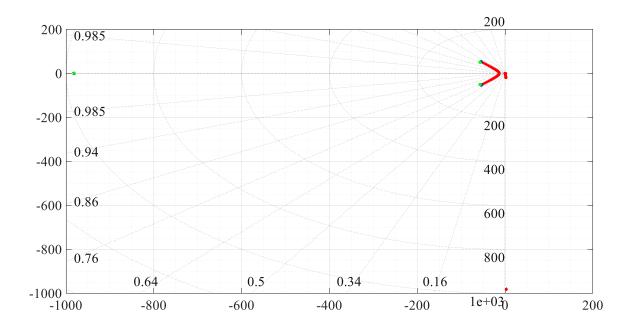


Figure 6.8: Movement of poles in LQR of buck converter.

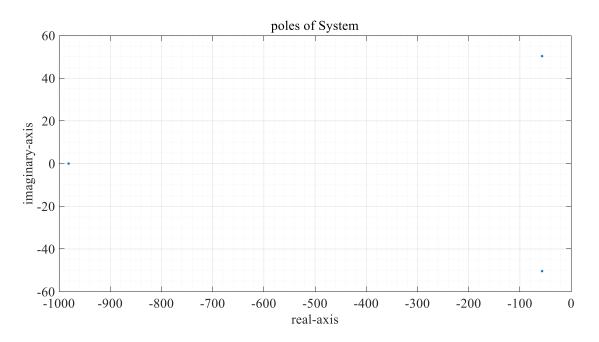


Figure 6.9: Poles location of system of buck converter.

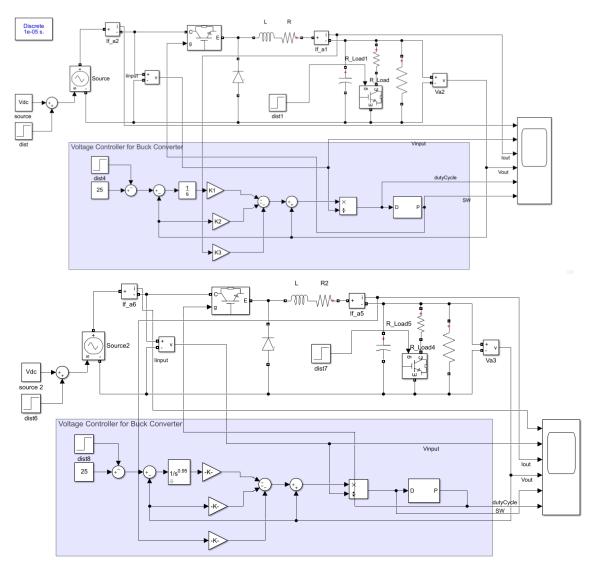


Figure 6.10: Simulation of fractional-order and integer-order controller of buck converter.

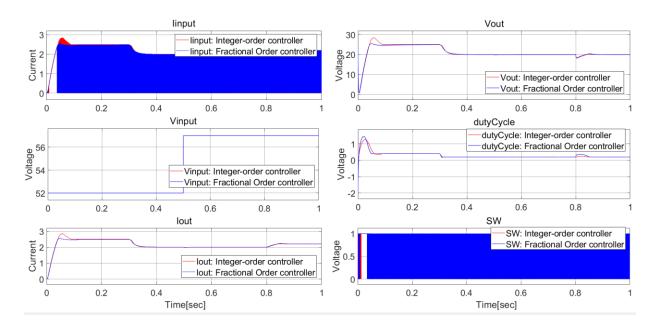


Figure 6.11: Result of fractional-order and integer-order controller of buck converter.

In Fig. 6.11 it is shown that the system behavior with a fractional-order controller has reduced overshoot. Disturbances are applied to the systems as a load change. At 0.3 s the output voltage is tracking the reference command value. At 0.5 s the input voltage is increased to evaluate tracking of the output value. At 0.8 s the resistive load is decreased to show the performance of the controller in regulating the output voltage.

6.2.4 Experimental Verification for the Buck Converter

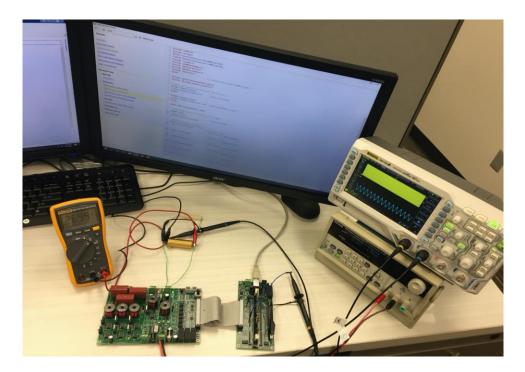


Figure 6.12: Experimental configuration for the buck converter controller.

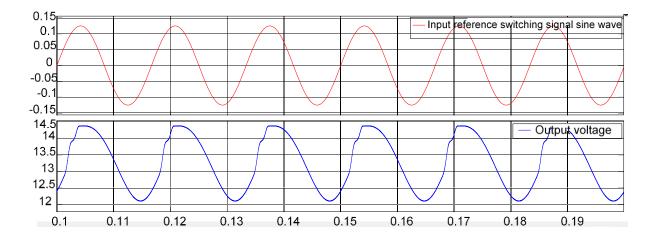


Figure 6.13: Simulation of control signal at 60Hz and output voltage of the buck converter.

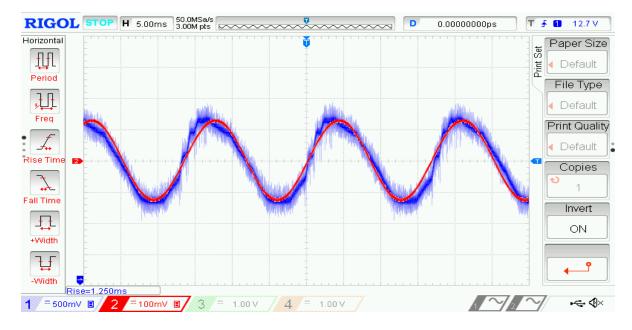


Figure 6.14: Experimental control of 60Hz reference and output voltage of the buck converter.

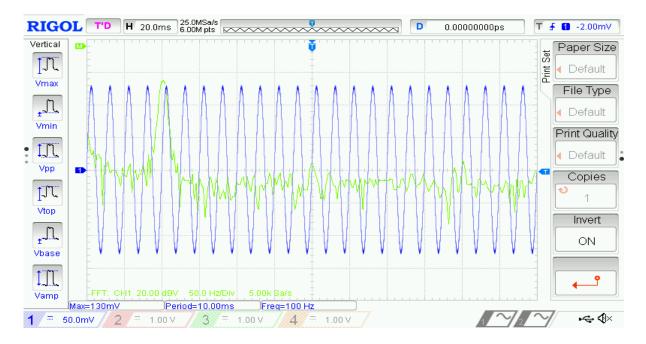


Figure 6.15: Experimental analysis of 60Hz control signal by FFT of the buck converter.

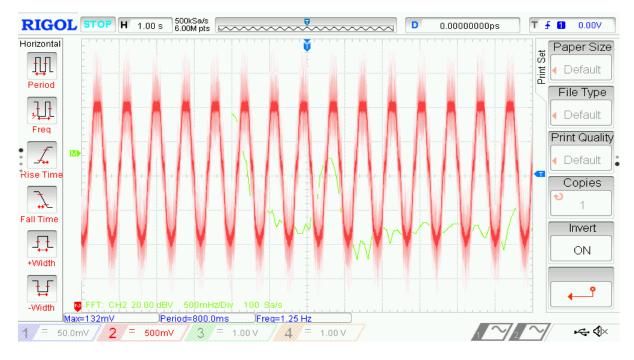


Figure 6.16: Experimental analysis of output voltage by FFT of the buck converter.

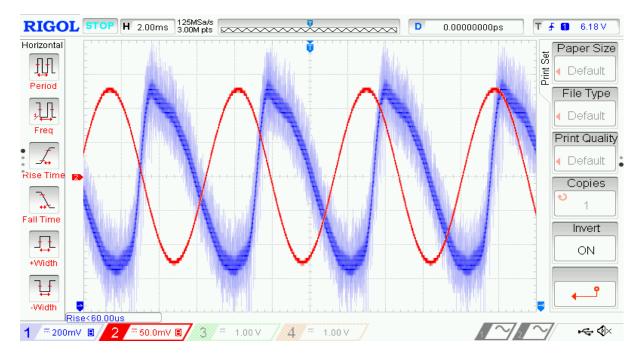


Figure 6.17: Experimental control by 10 KHz at output voltage of the buck converter.



Figure 6.18: Experimental analyzing 10 KHz control signal by FFT of buck converter.

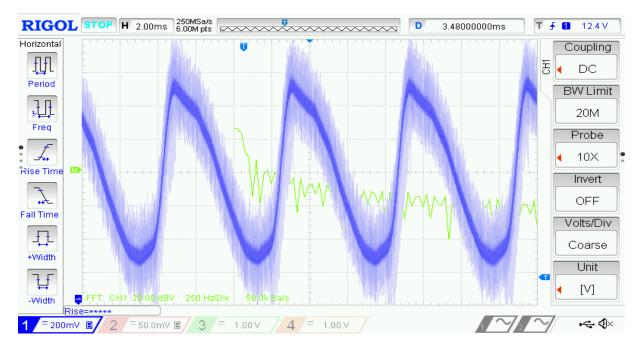


Figure 6.19: Experimental analyzing output voltage signal by FFT of the buck converter.

The experimental verification of the input (control) to output voltage signal by FFT of buck converter is very close to the simulation results.

6.3 Identification of DC-DC Boost Converter

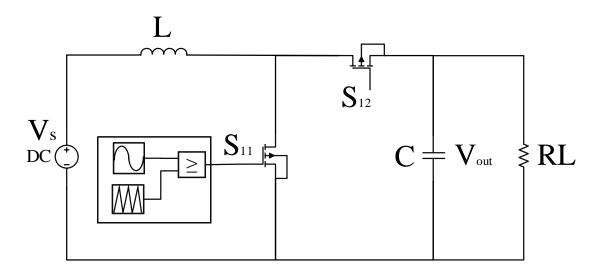


Figure 6.20: DC-DC Boost Converter.

The identification of the boost converter is presented in Fig. 6.21. The boost converter contains an inductor (L), capacitor (C), and resistive load. The input voltage is a constant value. The control signal is combination of a dc voltage added to a sinusoidal a wave at a selected frequency rangr. However, the control signal frequencies are less than the triangular wave frequency. The converter operates in the continuous conduction mode by adjusting the amplitude of the control signal for each application. The results represent the relationship between the control reference signal and the output signal. The input (duty-cycle signal) and the output voltage are displayed on the phasor model of the small signal variation.

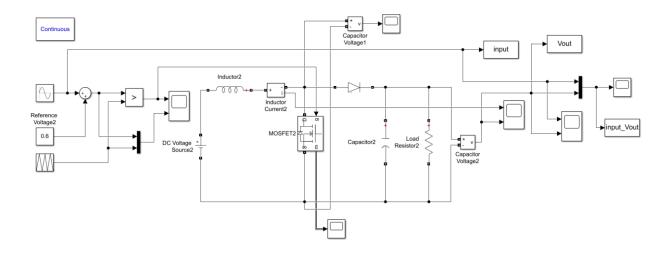


Figure 6.21: DC-DC Boost Converter Matlab Simulink.

6.3.1 Fast Fourier Transform (FFT) Algorithm for Boost Converter

The FFT is applied in the analysis to define the amplitude and phase angle. By using the FFT of the output signal the gain and the phase shift between the input and output signal ws determined as shown in the following figures:

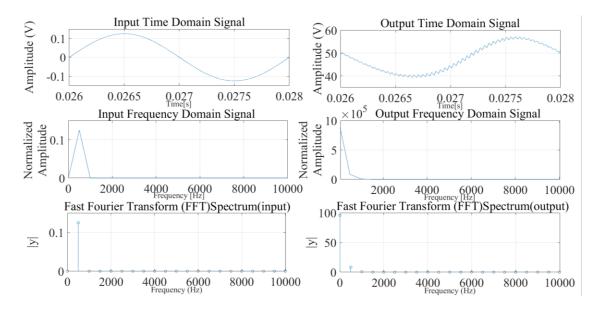


Figure 6.22: Example of the (FFT) analysis for buck converter of 500Hz.

A magnitude and phase shift for each frequency applied was tabulated and is shown in Table 6.2.

Frequency (Hz)	Magnitude (dB)=20*log()	Phase shift (degrees)
5	42.4555	-1.5061
10	42.4648	-3.0156
20	42.5014	-6.0584
100	43.3133	-33.1772
200	45.5471	-73.3662
300	45.0749	-140.7821
400	40.5477	-183.5606
500	36.4659	-202.3726
600	33.597	-213.5911
700	31.2051	-222.7427
800	29.8854	-224.9429
900	28.4123	-229.7827
1000	27.162	-233.7191
1500	22.8994	-246.3085
2000	20.2925	-253.2719

Table 6.2: Collected data bode plot for boost converter.

Frequency (Hz)	Magnitude (dB)=20*log()	Phase shift (degrees)
2500	18.1081	-255.4484
3000	16.7004	-257.3572
3500	15.1509	-258.2636
4000	14.1601	-259.2652
4500	13.6031	-261.0557
4700	13.0189	-263.6907
4800	13.0189	-263.6907
5000	13.0189	-263.6907

Continued Table 6.2: Collected data bode plot for boost converter.

6.3.2 Bode Plot Generation for Boost Converter

Using the data of magnitude and phase shift for the boost converter, the Bode plot drawing has a magnitude (\mp 20) in decibel (dB). The frequencies sine function of the control input is 2π f rad/sec. A frequency range of 5-5K Hz is applied in the Simulink model.

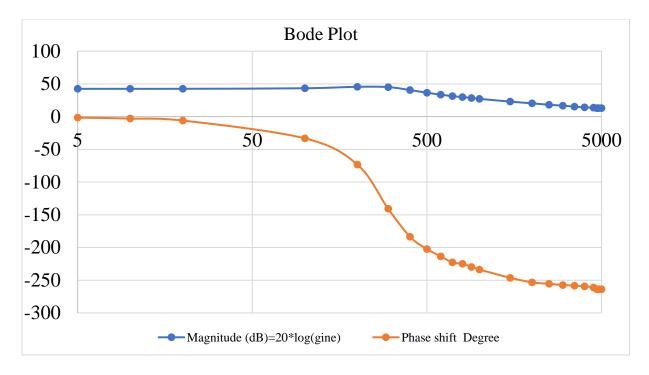


Figure 6.23:Bode Plot of Boost converter Powersim simulation data.

A high-order integer transfer function for the system is derived using the TFEST command on Matlab. A second order system estimated for the system is analyzed for fitting the estimation data by 66.71% of the actual behavior for the system.

$$G(s)_{sose} = \frac{2.786 \times 10^8}{s^2 + 943.4 s + 2.067 \times 10^6}$$

The estimation by using TFEST on Matlab to a third order system that fits the estimation data by 81.21% of the real system as:

$$G(s)_{tose} = \frac{1.116 \times 10^{12}}{s^3 + 3507 \, s^2 + 5.907 \times 10^6 \, s + 7.817 \times 10^9}$$

The third order transfer function estimated for the system is analyzed with estimation data by 95.82% the real system as:

$$G(s)_{tosez} = \frac{-9.513 \times 10^9 \text{ s} + 2.761 \times 10^{13}}{\text{s}^3 + 7.563 \times 10^4 \text{ s}^2 + 9.698 \times 10^7 \text{ s} + 2.142 \times 10^{11}}$$

However the fourth order system is estimated that fits of estimation data by 98.09% of the real behavior of the system.

$$G(s)_{sxose} = \frac{-2.469 \times 10^{11} \, s^2 + 4.4 \times 10^{14} \, s + 7.544 \times 10^{17}}{s^4 + 2.067 \times 10^6 \, s^3 + 4.351 \times 10^9 \, s^2 + 8.503 \times 10^{12} \, s + 5.675 \times 10^{15}}.$$

The comparison between all the estimated systems for simulation data is made to show the high order equivalent to the original system behavior is shown in the Fig. 6.27.

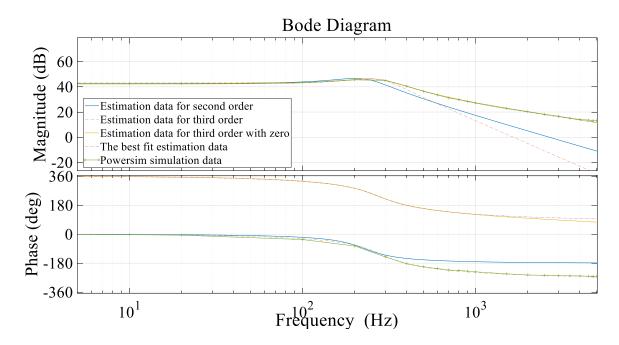


Figure 6.24: Simulated data Bode plot of boost converter and estimation data.

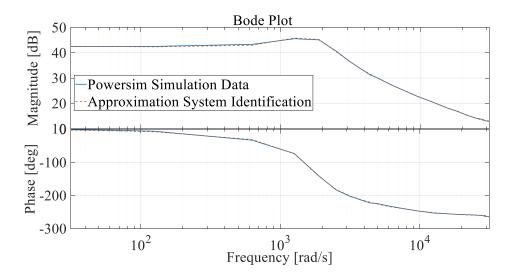


Figure 6.25: Simulation data Bode plot boost converter and approximation data.

The real behavior of the system is represented by the fractional order transfer function by applying Levy's method with resulting estimation error of 4%.

 $G(s)_{FO} =$

 $\frac{2.5708 \times 10^{-13} s^{3.25} - 1.2387 \times 10^{-9} s^{2.6} + 1.1012 \times 10^{-9} s^{1.95} + 0.00035331 s^{1.3} - 0.84513 s^{0.65} + 137.38}{53.669 \times 10^{-12} s^{3.25} + 3.8974 \times 10^{-10} s^{2.6} + 3.1564 \times 10^{-7} s^{1.95} + 3.5464 \times 10^{-5} s^{1.3} + 0.00059335 s^{0.65} + 137.38}$

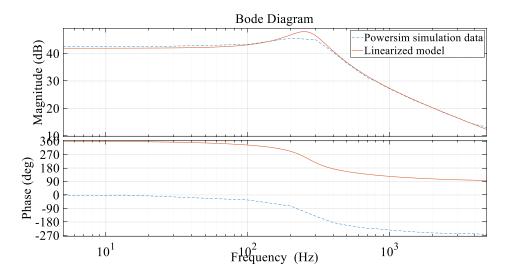


Figure 6.26: Simulation of boost converter actual Bode Plot and FO model.

6.3.3 Controller for the DC-DC Boost Converter

The cascaded PI control of the boost converter is used with an inner and outer loop controller for the converter. Design of the inner-loop for the inductor current is tracking a reference value of the current by define an error variable as:

$$\dot{x}_1 = e(t) = i_{lref} - i_l$$

With an additional state variable defined from the tracking error as:

$$x_1=\int e(t),$$

$$x_2 = i_l$$

The state space equations are represented as:

$$\dot{x}_1 = \dot{i}_{lref} - x_2$$

 $\dot{x}_2 = -\frac{r_l x_2}{L} - \frac{v_o(1-u)}{L}.$

Converting the state-space equations to a standard LQR form is represented as:

$$\dot{z}_1 = -z_2$$
$$\dot{z}_2 = -\frac{r_l z_2}{L} - \frac{u}{L}.$$

Control input *u* is minimized by the cost function of the LQR:

$$J(t) = \int_0^\infty (z^T Q z + v^2) dt,$$

where Q is symmetrical positive semi-definite matrix. The root locus plot for current control of the boost converter as presented in Chapter 5 is shown in Fig. 5.10.

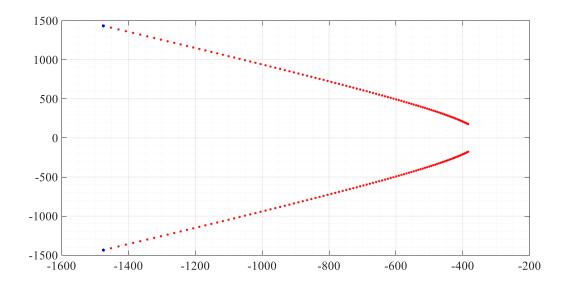


Figure 6.27: Root locus of closed loop poles for boost converter inner-loop cpntroller.

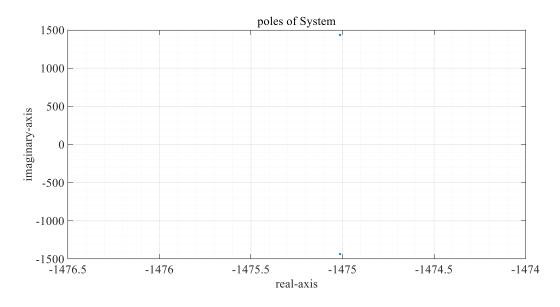


Figure 6.28: Poles of current controller location of the boost converter.

Defining gains for outer-loop controller for the boost converter, the LQR technique is used. The output error function is represented:

$$\dot{x}_1 = e(t) = V_{ref} - V_{out}$$

A tracking error state variable is added as

$$x_{1} = \int e(t),$$

$$x_{2} = V_{out},$$

$$\dot{x}_{1} = V_{ref} - x_{2}$$

$$\dot{x}_{2} = \frac{(1-u)}{C} i_{l} - \frac{1}{C R_{L}} x_{2}.$$

For determining the equation for tracking of the system behavior is

$$\dot{z}_1 = -z_2$$
$$\dot{z}_2 = \frac{v_{in}}{Cv_{ref}}u - \frac{1}{CR_L}z_2.$$

The control input *u* is minimized by the quadratic cost function :

$$J(t) = \int_0^\infty (z^T Q z + v^2) dt,$$

where Q is symmetrical positive semi-definite matrix. The control for boost converter from Chapter 5 is shown in Fig. 5.11. The corresponding root locus and closed loop poles are shown in Figs. 6-30 and 6.31.

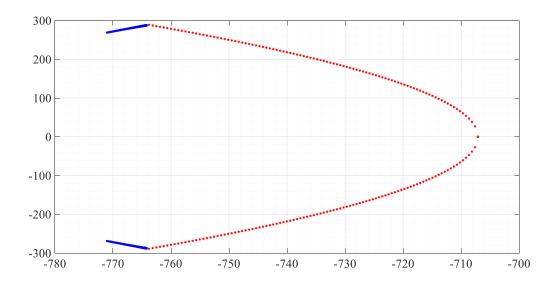


Figure 6.29: Movement of closed loop poles for voltage controller of the boost converter.

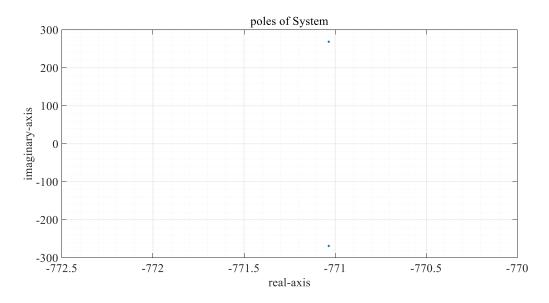
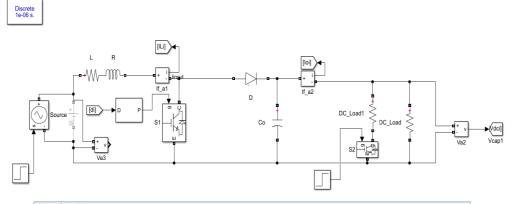
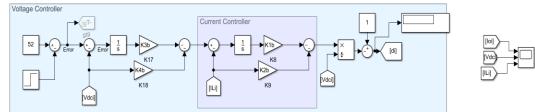


Figure 6.30: Poles of voltage controller location of the boost converter.





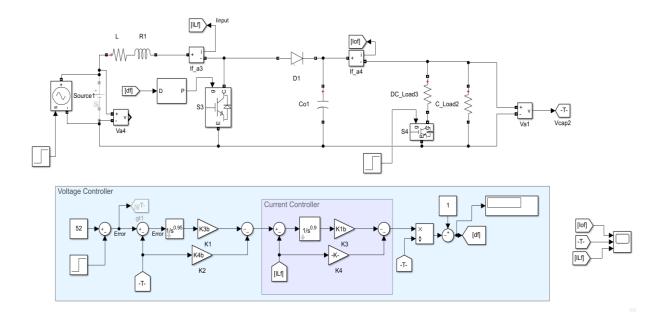


Figure 6.31: Controller configuration for the boost converter.

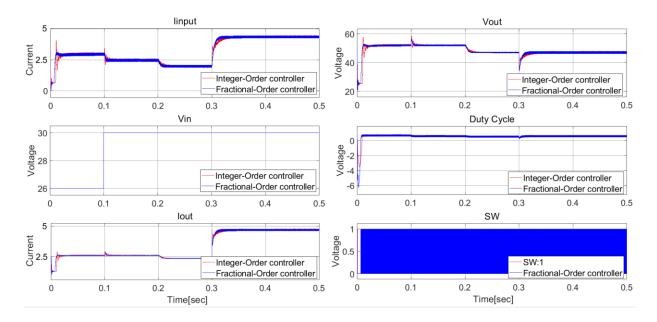


Figure 6.32: Integer-order controller with fractional-order controller for boost converter.

The actual system behavior of the boost converter with a fractional controller has less overshoot and faster response. Disturbances are applied to the system for controlling the output voltage. At 0.1 s the input voltage is increased but the reference output voltage remains at the sa,e value. At 0.2 s, the reference of the output voltage is reduced to have verification of tracking the output value. At 0.3 s the resistive load is decreased for the system to show the performance of the controller shown in Fig. 6.33.

6.3.4 Experimental Reults for Boost Converter

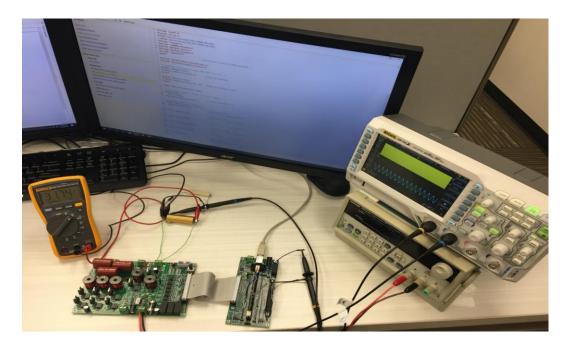


Figure 6.33: Experimental control of the boost converter.

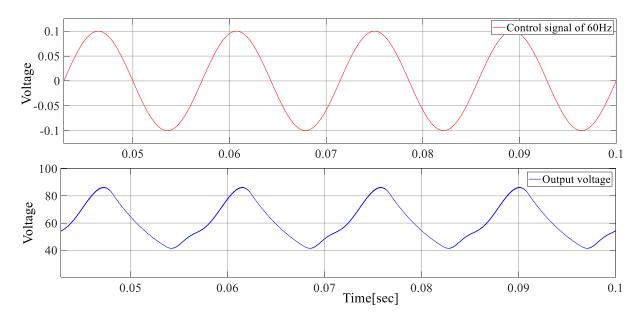


Figure 6.34: Simulation of reference signal at 60Hz and output voltage of the boost converter.

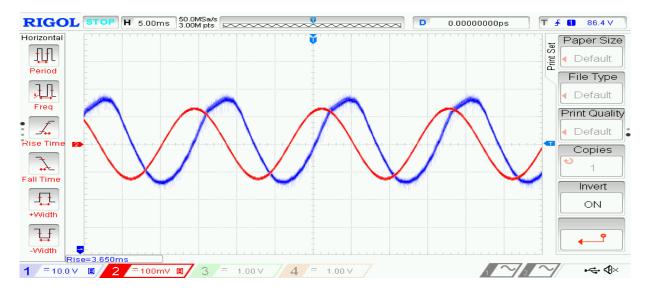


Figure 6.35: Simulation control signal 60Hz and output voltage of the boost converter.

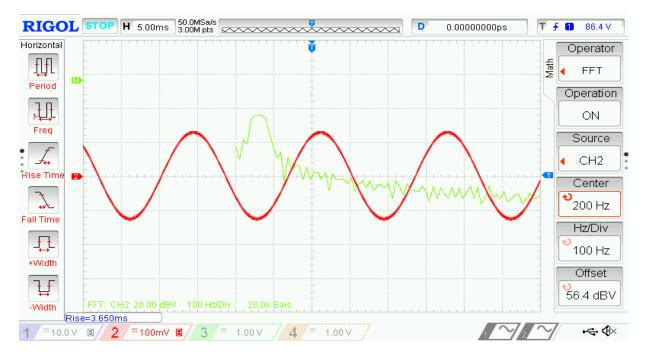


Figure 6.36: Experimental FFT analysis of control signal 60Hz the boost converter.

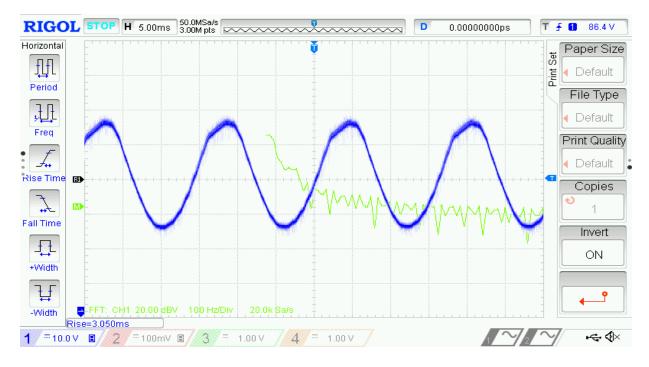


Figure 6.37: Experimental FFT analysis of output voltage boost converter.

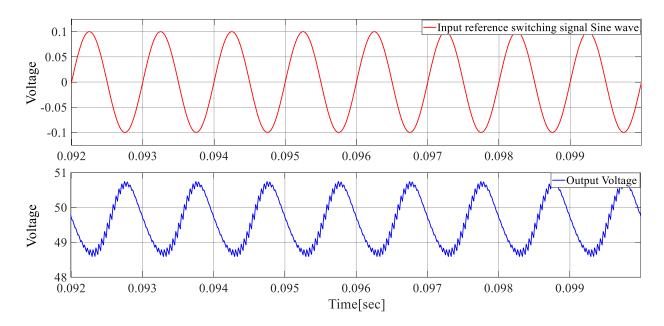


Figure 6.38: Simulation control signal of 100Hz and output voltage boost converter.

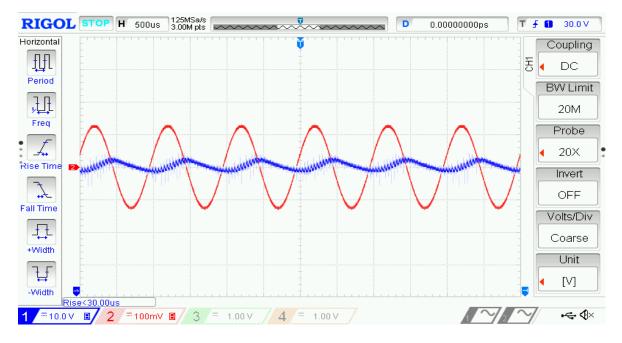


Figure 6.39: Experimental control signal of 100Hz and output voltage boost converter.



Figure 6.40: Experimentalal FFT analysis of control signal of 100Hz boost converter.



Figure 6.41: Experimental analysis FFT of output voltage boost converter.

Experimental output voltages with FFT analysis of the boost converter is shown in Fig. 6.41 and Fig. 6.42. The experimental analysis of the input (control) and output voltage signal by FFT of the boost converter matches the simulation analysis of the system.

6.4 Identification of a DC-DC Boost Converter with Constant Power Load (CPL)

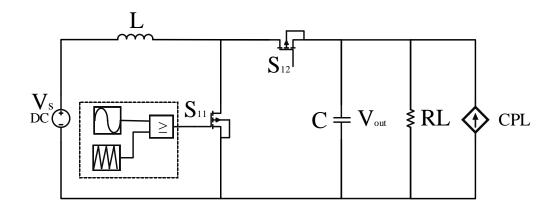


Figure 6.42: DC-DC boost converter with CPL.

The circuit of the boost converter with a constant power load (CPL) is presented that in Figure. 6.43. The system of the boost converter with CPL contains an inductor L, capacitor C,

resistive load and the constant power load. The input voltage on this application is constant value. The control signal is combination of a dc voltage added to a sinusoidal a wave of a selected frequency range. The converter operates in the continuous conduction mode at all time by adjusting the amplitude of the control signal for the application. The results represent the relationship between the control signal and the output voltage.

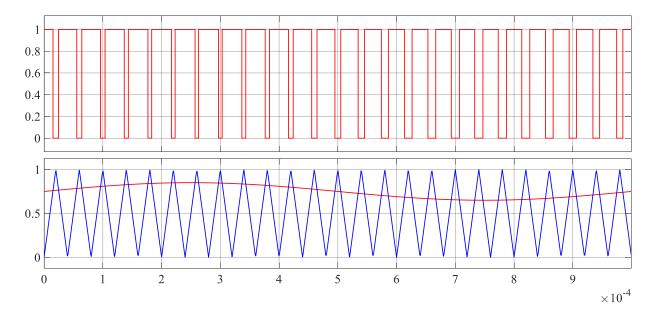


Figure 6.43: Example of control signal for boost converter with CPL at 1000Hz.

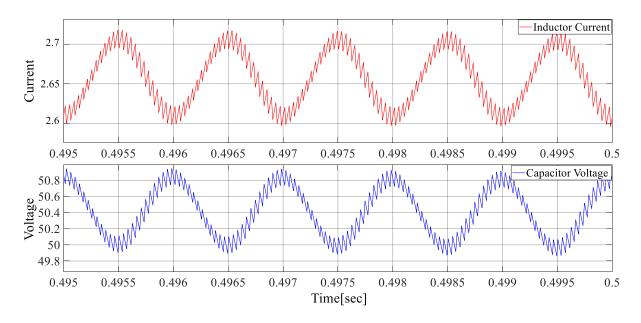


Figure 6.44: Inductor current and capacitor voltage with CPL at 1000Hz.

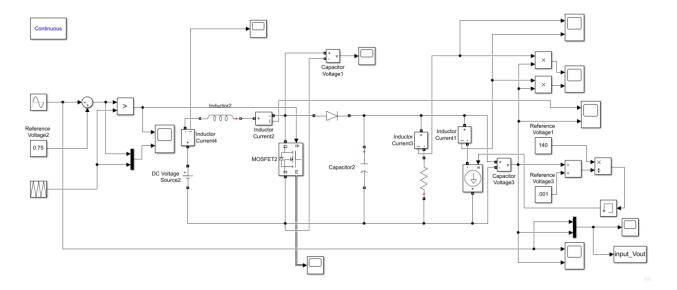


Figure 6.45: DC-DC boost converter with CPL in Matlab-Simulink.

6.4.1 FFT Algorithm for Boost Converter with CPL

The FFT is used to define the amplitude and phase angle for the output signal. A magnitude and phase shift for each frequency is applied to the system that achieved as shown in Table 6.4.

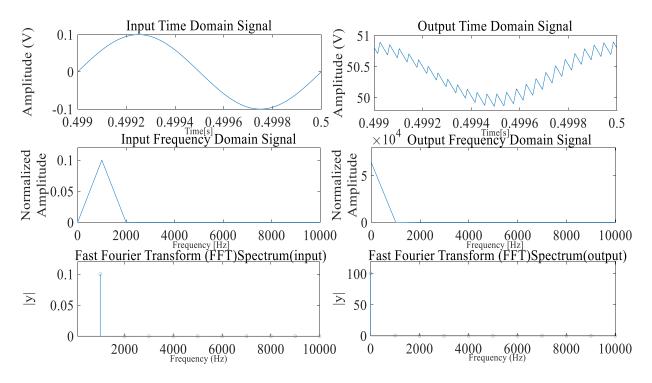


Figure 6.46: Example of FFT analysis for boost converter with CPL of 1000Hz.

Frequency(Hz)	Magnitude (dB)=20*log(gine)	Phase shift Degree
1	47.3921	-10.9952
5	47.0526	-22.0885
10	47.6473	-30.7251
15	51.4103	-35.5453
20	53.6425	-46.8761
25	56.854	-52.6526
30	57.3562	-86.1381

Table 6.3: Collected data bode plot for boost with CPL converter.

Frequency(Hz)	Magnitude (dB)=20*log(gine)	Phase shift Degree
40	52.9534	-212.2466
50	45.4137	-226.6947
60	41.4514	-228.2263
75	37.8449	-235.7627
85	35.9335	-241.4321
100	34.8573	-246.9964
150	30.6334	-247.3601
200	28.9137	-256.3521
250	27.84	-259.6278
300	26.1333	-261.425
350	24.7746	-262.295
400	23.5796	-263.5039
450	22.9723	-263.6524
500	21.8184	-264.5572
550	20.7828	-265.2161
600	20.3112	-265.6837

Continued Table 6.3: Collected data bode plot for boost with CPL converter.

Frequency(Hz)	Magnitude (dB)=20*log(gine)	Phase shift Degree
650	19.3166	-266.1577
700	18.6279	-267.236
750	18.0871	-266.1082
800	17.4952	-266.9808
850	17.0051	-266.6152
900	16.475	-267.103
950	15.9756	-268.3574
1000	15.5359	-267.7728
1250	14.6166	-267.464
1500	13.0996	-267.4748
1750	12.7184	-268.8022
2000	12.1755	-268.6971
2250	10.9842	-269.2033
2500	9.1765	-268.8765
2750	8.7891	-268.3201
3000	8.2396	-269.0522
3250	7.5043	-269.3374

Continued Table 6.3: Collected data bode plot for boost with CPL converter.

Frequency(Hz)	Magnitude (dB)=20*log(gine)	Phase shift Degree
3500	6.7154	-269.0346
3750	6.3799	-269.6419
4000	5.7671	-269.0872
4250	5.0017	-269.5761
4500	4.2365	-269.8106
4750	4.1526	-269.0254
5000	4.0012	-269.4579

Continued Table 6.3: Collected data bode plot for boost with CPL converter.

6.4.2 Bode Plot Generation for Boost Converter with CPL

The open-loop system response to varying frequency inputs (duty cycle) is obtained and data tabulated in Table 6.3. Based on the accuracy of data is obtained of magnitude and phase shift resulting a Bode plot drawing has magnitude (\mp 20) in decibel (dB) and the phase shift in degrees and angular frequency ω . The frequency of the sinusoidal function in the control input of Simulink model is 2π f rad/sec. The frequency range of (1-5K Hz) applied to the model as shown that in Figure . 6.48.

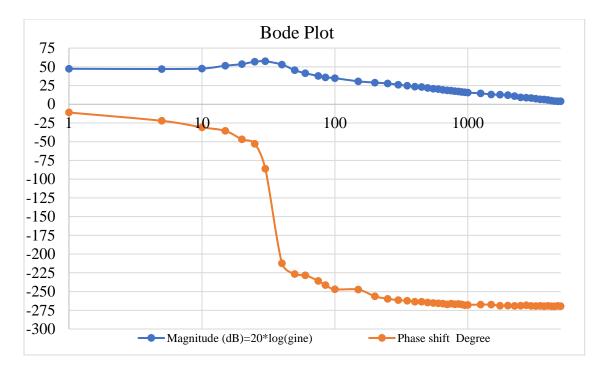


Figure 6.47: Bode Plot of boost with CPL from simulation data.

Comparing the high order transfer function determined with the Matlab command (TFEST) is applied to the frequency response data. A second order system is estimated from the analyzed data that is fitted with estimation accuracy of 58.07% of the actual system behavior as:

$$G(s)_{sose} = \frac{8.65 \times 10^6}{s^2 + 63.87 \, s + 3.413 \times 10^4}.$$

A third order system is estimated from the simulation data that fits to the estimation data by 76.21% of the simulated system,

$$G(s)_{tose} = \frac{2.82 \times 10^9}{s^3 + 244.4 \, s^2 + 5.374 \times 10^4 \, s + 8.981 \times 10^6}.$$

A third order system is estimated for the system with an added zero. It fits the data by 77.96% of the simulated system,

$$G(s)_{tosez} = \frac{-4.669 \times 10^6 \text{ s} + 3.797 \times 10^9}{\text{s}^3 + 346.7 \text{ s}^2 + 5.955 \times 10^4 \text{ s} + 1.357 \times 10^7}.$$

An eleventh order system is estimated for the analyzed system and has the best fits to the data by 99.28% of the actual system as following:

$$G(s)_{fose} = \frac{-3.837 \times 10^4 \, s^{10} + 1.569 \times 10^7 \, s^9 - 1.299 \times 10^9 \, s^8 + 3.145 \times 10^{11} \, s^7}{s^{11} + 185 \, s^{10} + 1.825 \times 10^5 \, s^9 + 5.256 \times 10^7 \, s^8 + 1.374 \times 10^{10} \, s^7}$$

-8.446 × 10¹⁴ s⁶ + 4.109 × 10¹⁶ s⁵ - 3.298 × 10¹⁹ s⁴ + 1.315 × 10²¹ s³ - 1.06 × 10²³ s²
+2.848 × 10¹² s⁶ + 2.668 × 10¹³ s⁵ + 4.657 × 10¹⁶ s⁴ + 7.913 × 10¹⁸ s³ + 6.848 × 10¹⁹ s²
+9.548 × 10²⁴ s + 6.746e25
+4.939 × 10²² s + 2.543 × 10²³.

The comparison between all the estimated transfer functions for simulation data is shown in Figure. 6.49.

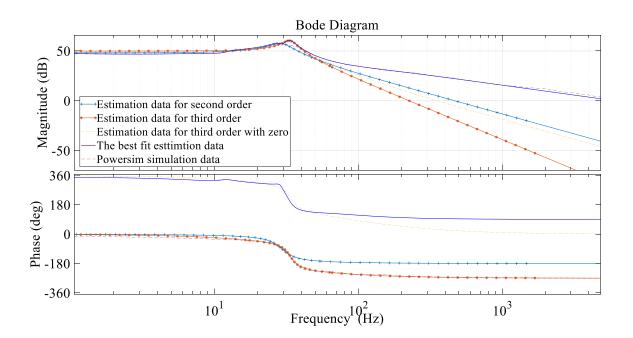


Figure 6.48: Bode plot comparison of the boost converter with CPL data.

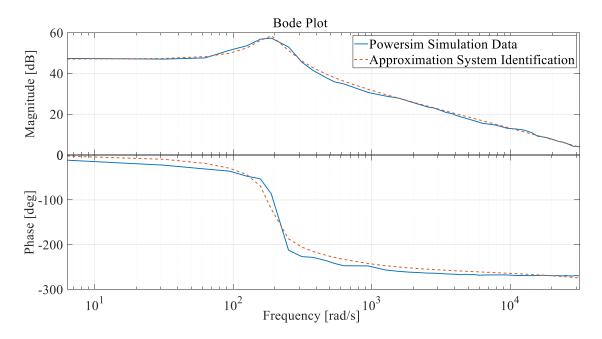


Figure 6.49: Bode plot of boost converter with CPL.

The actual behavior of the system for the fractional order transfer function is derived by applying Levy's method resulting the estimation error is 6% as following:

 $G(s)_{FO} = \frac{6.0463 \times 10^{-5} s^{1.8} - 1.4803 s^{0.9} + 227}{6.425 \times 10^{-14} s^{3.6} + 1.4168 \times 10^{-9} s^{2.7} + 8.0277 \times 10^{-5} s^{1.8} + 7.4832 \times 10^{-5} s^{0.9} + 1}.$

6.4.3 Modeling and Analysis of a DC-DC Boost Converter with CPL

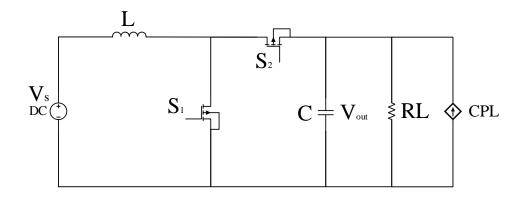


Figure 6.50: Boost converter with CPL.

A dc-dc power converter is constructed by using connection of a boost converter with a constant power load (CPL). The model of the boost converter with CPL is derived to be:

KVL L:

$$V_s = L \frac{d_i}{dt} + V_C (1 - S_1)$$

KCL C:

$$i(1-S_1) = C\frac{d_v}{dt} + \frac{V_C}{RL} + \frac{P}{V_C}$$

Average Normalized Model

$$x_1 = i, x_2 = V_{out}, S_1 = U$$

 $\dot{x}_1 = \frac{V_S}{L} - \frac{x_2(1 - U)}{L}$

$$\dot{x}_{2} = \frac{x_{1}(1-U)}{C} - \frac{x_{2}}{CRL} - \frac{P}{Cx_{2}^{2}}$$

With state-space equations:

$$A = \begin{bmatrix} 0 & \frac{-(1-U)}{L} \\ \frac{(1-U)}{C} & \frac{-1}{CRL} - \frac{P}{Cx_2^2} \end{bmatrix}, \qquad B = \begin{bmatrix} \frac{x_2}{L} \\ \frac{-x_1}{C} \end{bmatrix}, \qquad C = \begin{bmatrix} 0 & 1 \end{bmatrix}, \qquad D = \begin{bmatrix} 0 \end{bmatrix}$$

The conventional linearized transfer function is:

$$G(s)_{Linearized} = \frac{-2.677e04 \, \text{s} + 7.738e06}{s^2 + 25.15 \, \text{s} + 3.72e04}$$

The actual system is compared with the linearized system as a Bode plot and is shown in Figure.

6.51.

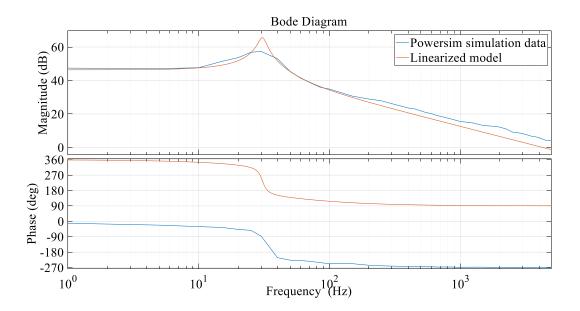


Figure 6.51: Bode plot comparison of boost converter with CPL (actual data and linearized model).

6.4.4 Controller for the DC-DC Boost Converter with CPL

The cascaded controller is used for the boost converter with an inner and outer loop controller for the converter. The error variable is defined as:

$$\dot{x}_1 = e(t) = i_{lref} - i_l$$

and the a new state variable is defined from the tracking error as:

$$x_1 = \int e(t),$$
$$x_2 = i_l.$$

The state-space equations are represented as:

$$\dot{x}_{1} = \dot{i}_{lref} - x_{2}$$
$$\dot{x}_{2} = -\frac{r_{l}x_{2}}{L} - \frac{v_{o}(1-u)}{L}$$

Converting the state-space equations to standard LQR is represented as following:

$$\dot{z}_1 = -z_2$$
$$\dot{z}_2 = -\frac{r_l z_2}{L} - \frac{u}{L}$$

The controller input *u* is minimized by the quadratic cost function

$$J(t) = \int_0^\infty (z^T Q z + v^2) dt,$$

where Q is a symmetric positive semi-definite matrix. The root locus of the boost converter is provided in Fig. 6.53.

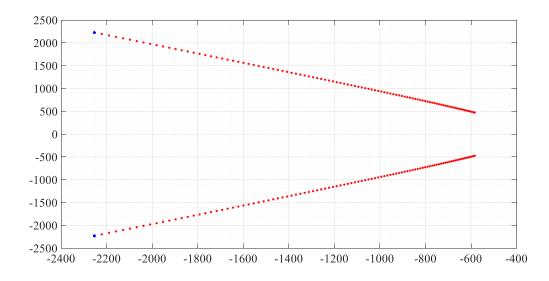


Figure 6.52: Movement of closed loop poles for current control loop of the boost with CPL.

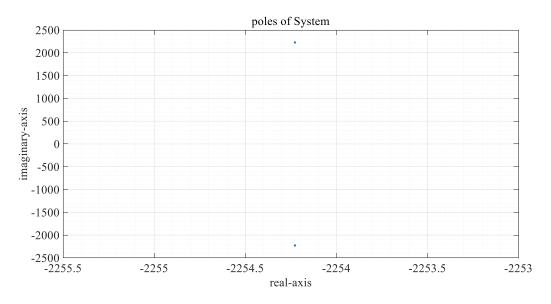


Figure 6.53: Poles of current control loop location of the boost with CPL.

Define the gains for outer loop to control the output voltage of the boost converter by using the LQR technique. The new state variable is represented as:

$$\dot{x}_1 = e(t) = V_{ref} - V_{out}.$$

Consideration the tracking error,

$$x_{1} = \int e(t),$$

$$x_{2} = V_{out},$$

$$\dot{x}_{1} = V_{ref} - x_{2}$$

$$\dot{x}_{2} = \frac{(1-u)}{C}i_{l} - \frac{1}{CR_{L}}x_{2} - \frac{P}{Cx_{2}}$$

Determining the tracking of the system behavior as following:

$$\dot{z}_1 = -z_2$$
$$\dot{z}_2 = \frac{v_{in}}{Cv_{ref}}u - \frac{1}{CR_L}z_2 - \frac{P}{Cz_2}$$

The controller of the input *u* minimizes the quadratic cost function:

$$J(t) = \int_0^\infty (z^T Q z + v^2) dt$$

where Q is a symmetric positive semi-definite matrix. The control for boost converter is shown in Figure. 6.55.

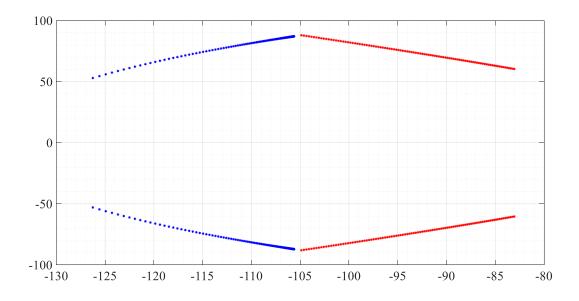


Figure 6.54: Movement of closed loop poles for voltage controller of the boost with CPL.

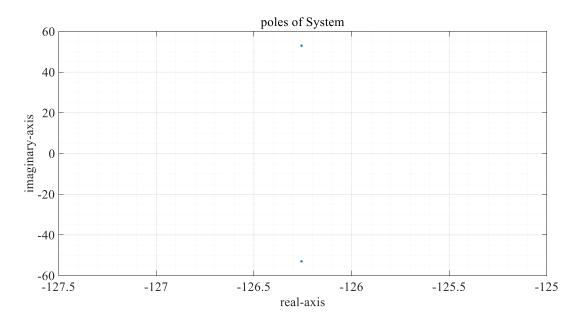


Figure 6.55: Poles of voltage controller location of the boost with CPL.

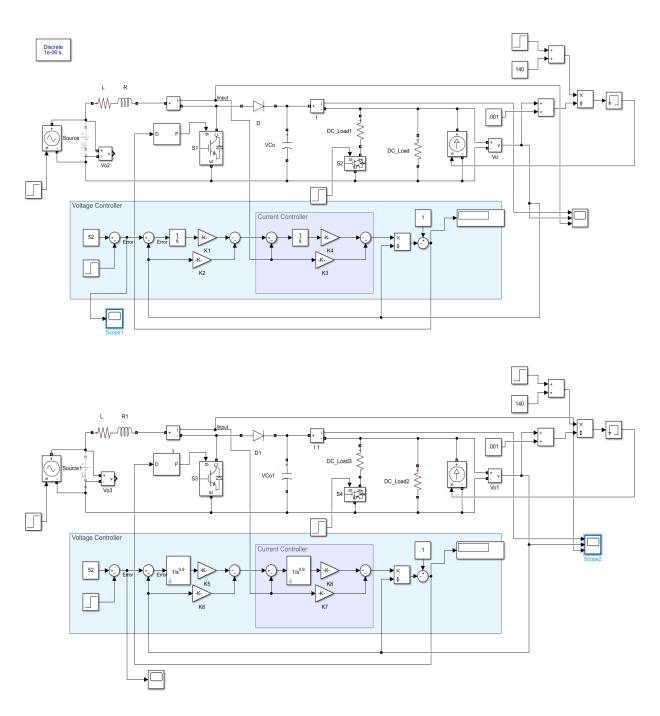


Figure 6.56: Integer-Order controller with fractional-order controller for boost with CPL.

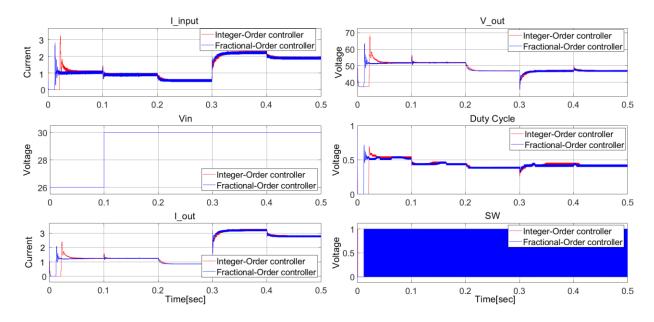


Figure 6.57: Fractional order controller and integer-order controller for boost with CPL.

The actual system behavior of the boost converter with CPL with the fractional controller has less overshoot and faster response. Disturbances are applied for the systems for controlling the output voltage. At 0.1 s, the input voltage is increased but the output voltage remains at the expected value. At 0.2 s, the reference of the output voltage is reduced to have verification of tracking the output value. At 0.3 s, the resistive load is decreased top 0.4 of the value of the constant power load. The system performance with the controller is shown in Fig. 6.58.

6.5 Identification of a Boost-Buck Converter Circuit

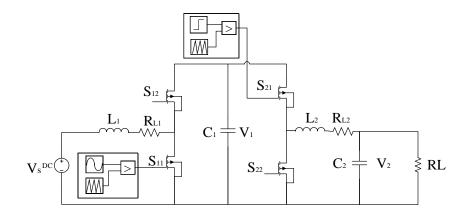


Figure 6.58: Boost-buck frequency response simulation.

The system used in this experimental part is a cascaded boost and buck converter. The objective behind this study is to develop the FO control for a boost converter with a CPL and controlling non-minimum phase fractional-order system. The research additionally shows new methods for modeling and examining converters. The procedure presents the actual system boost converter with CPL boost buck converter. The cascade boost-buck converter has two controller switches. The cascade boost-buck converter is considered as one system. The system is represented by a fractional order transfer function. The boost converter PWM switching is shown in Fig. 6.60. The PWM structure for the buck converter is shown in Fig. 6.61. The output signal taken from the output voltage over the load of the system. The voltage output ripples of frequency is determined between 5-5KHz.

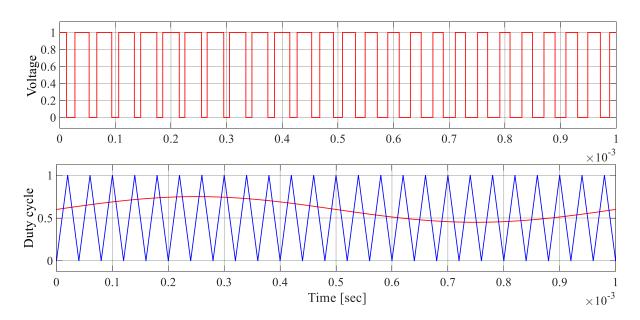


Figure 6.59: Switching input for boost converter.

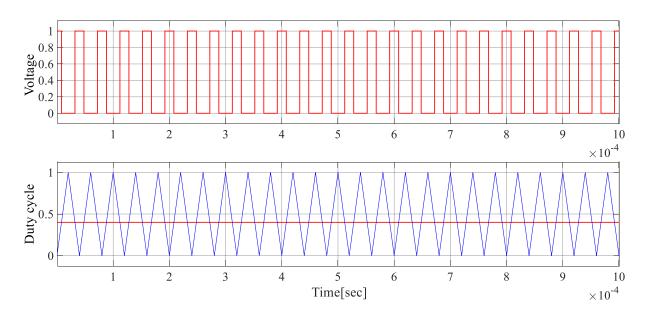


Figure 6.60: Switching input for buck converter.

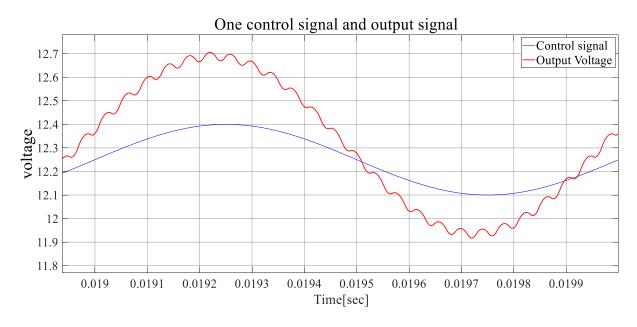


Figure 6.61: Input-Output frequency response of boost-buck converter.

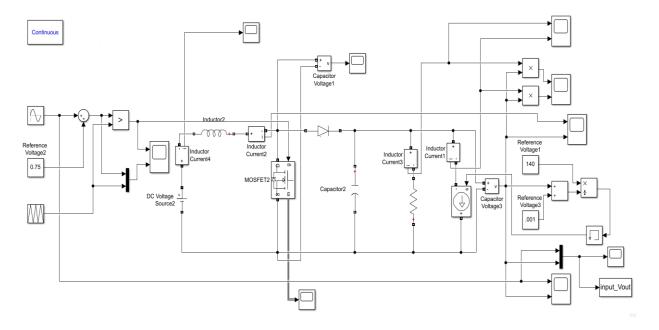


Figure 6.62: Boost-buck Matlab simulation.

The magnitude and phase shift of the system is represented in Table 6.4 by using the previously defined methods.

Frequency (Hz)	Magnitude (dB)=20*log()	Phase shift (degrees)
50	26.6144	-25.9991
100	27.0115	-51.5781
200	28.6012	-100.997
300	30.9423	-159.5464
400	29.4158	-230.1422
500	24.4035	-275.7914
600	19.9116	-301.6907
700	16.2372	-319.0664
800	13.1326	-332.1507
900	10.4446	-342.4006
1000	8.0431	-351.0239
1500	-1.3096	-378.7302
2000	-8.1307	-395.1614
2500	-13.5589	-405.0542
3000	-17.4309	-416.5254
3500	-21.7178	-454.032
4000	-22.9075	-465.3801

Table 6.4: Data of Bode plot for boost-buck converter.

Frequency (Hz)	Magnitude (dB)=20*log()	Phase shift (degrees)
4500	-27.4369	-491.8412
5000	-29.1813	-490.2091

Continued Table 6.4: Data of Bode plot for boost-buck converter.

In Fig. 6. 64 shows the bode plot from the observed data.

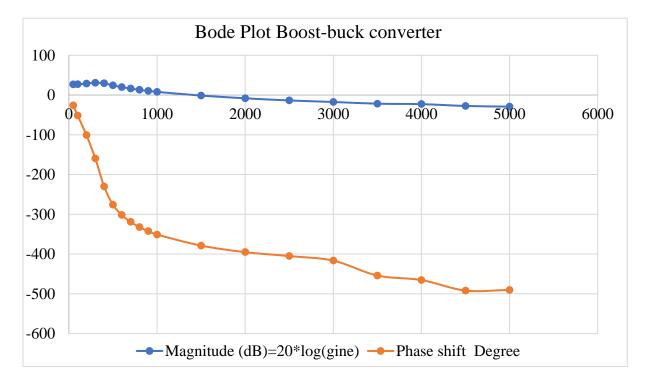


Figure 6.63: Bode plot of boost-buck converter form collected simulation data.

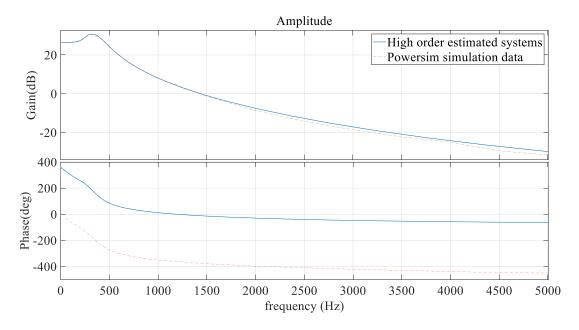


Figure 6.64: Bode plot of boost-buck simulation data and estimated system.

Hence, the transfer function for the estimated system in integer order generated by using Matlab command (TFEST).

$$G(s) = \frac{-8.453e11s^4 - 3.431e16s^3 + 2.806e19s^2 - 2.381e22s + 1.837e26}{s^7 + 5.062e04s^6 + 5.242e08s^5 + 2.028e12s^4 + 5.706e15s^3 + 1.007e19s^2 + 1.148e22s + 8.687e24}$$

Zeros and poles for the high order system show a non-minimum phase system as below with

poles in the left hand side.

 $p = 1.0e+04 * \\ -3.8180 + 0.0000i \\ -0.8097 + 0.0000i \\ -0.1875 + 0.0000i \\ -0.0706 + 0.2185i \\ -0.0706 - 0.2185i \\ -0.0527 + 0.1601i \\ -0.0527 - 0.1601i$

6.5.1 Modeling and Analysis of a DC-DC Boost-Buck Converter

A multi-variable dc-dc power converter is constructed by using a cascaded boost and buck converter. Fig. 6. 69 shows the boost-buck circuit.

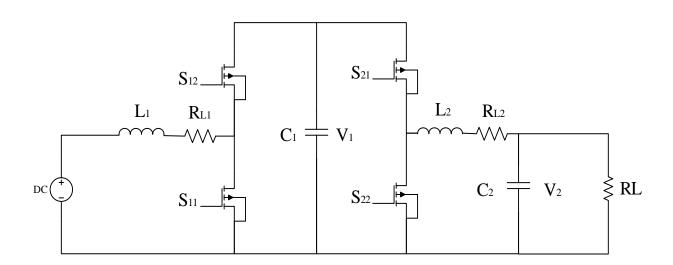


Figure 6.65: Boost-buck circuit.

The model of the converter is derived to be:

KVL L1:

$$V_{s} = L_{1} \frac{d_{i1}}{dt} + R_{L1} i_{1} + V_{1} \left(1 - S_{11} \right)$$

KCL C1:

127

$$i_1(1 - S_{11}) = C_1 \frac{dv_1}{dt} + i_2 S_{21}$$

KVL L2:

$$V_1 S_{21} = L_2 \frac{di_2}{dt} + R_{L2} i_2 + V_2$$

KCL C2:

$$i_2 = C_2 \frac{dv_2}{dt} + \frac{V_2}{RL}$$

Average normalized Model

$$\begin{aligned} x_1 &= i_1, x_2 = V_1, x_3 = i_2, x_4 = V_2, S_{11} = U_1, S_{21} = U_2 \\ \dot{X}_1 &= \frac{V_s}{L_1} - \frac{R_{L1}x_1}{L_1} - \frac{x_2\left(1 - U_1\right)}{L_1} \\ \dot{X}_2 &= \frac{x_2\left(1 - U_1\right)}{C_1} - \frac{x_3U_2}{C_1} \\ \dot{X}_3 &= \frac{x_2U_2}{L_2} - \frac{R_{L2}x_3}{L_2} - \frac{x_4}{L_2} \\ \dot{X}_4 &= \frac{x_3}{C_2} - \frac{x_4}{C_2RL} \end{aligned}$$

A state space model and controller inputs is given by:

$$A = \begin{bmatrix} \frac{-R_{L1}}{L_1} & \frac{-(1-U_1)}{L_1} & 0 & 0\\ \frac{(1-U_1)}{C_1} & 0 & \frac{-U_2}{C_2} & 0\\ 0 & \frac{U_2}{L_2} & \frac{-R_{L2}}{L_2} & \frac{-1}{L_2}\\ 0 & 0 & \frac{1}{C_2} & \frac{-1}{C_2RL} \end{bmatrix}$$

$$B = \begin{bmatrix} \frac{x_2}{L_1} & 0\\ \frac{-x_1}{C_1} & \frac{-x_3}{C_1}\\ 0 & \frac{x_2}{L_2}\\ 0 & 0 \end{bmatrix}$$
$$C = \begin{bmatrix} 0 & 1 & 0 & 0\\ 0 & 0 & 0 & 1 \end{bmatrix}$$
$$D = \begin{bmatrix} 0 & 0\\ 0 & 0 \end{bmatrix}$$

The linearized transfer function is

$$G(s)_{\text{linearized}} = \frac{-9.276e11 \text{ s} + 1.657e15}{s^4 + 1.096e04s^3 + 3.345e07s^2 + 7.083e10s + 7.857e13}$$

The zero and poles are:

p = 1.0e+03 * -0.6882 + 2.1813i -0.6882 - 2.1813i -1.9720 + 0.0000i -7.6159 + 0.0000i

The actual system is compared with the linearized system as shown in Figure 6.68.

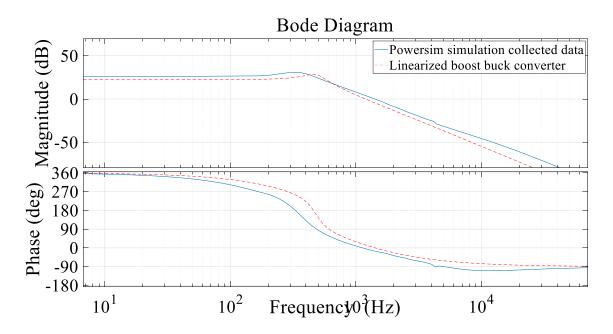


Figure 6.66: Bode plot for boost-buck of actual versus linearized data.

The fractional-order transfer function is compared with the real system characteristic behavior is shown in the Figure 6.72.

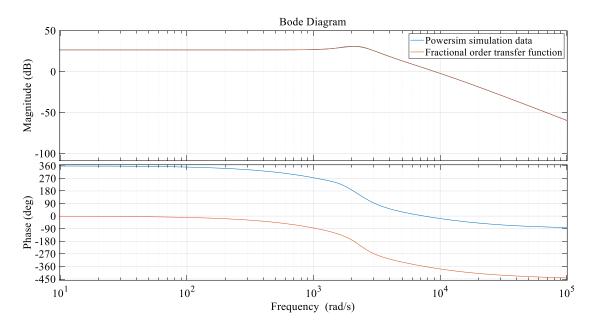


Figure 6.67: Bode plot for boost-buck actual versus fractional order.

The Levy identification method is used to find the fractional order system. It is compared with converter data is shown in Fig. 6.68. The fractional-order transfer function is equivalent to the actual system as:

$$G(s)_{fo} = \frac{-0.0011301s + 0.03882s^{0.8} - 0.54057s^{0.6} + 3.7922s^{0.4} - 13.322s^{0.2} + 18.58}{-3.6463e - 0.5s + 0.0029157s^{0.8} - 0.041271s^{0.6} + 0.25783s^{0.4} - 0.7878s^{0.2} + 18.58}$$

6.5.2 PI Controller for the DC-DC Boost-buck Converter

The main objective of this research applies fractional order system identification method to find the actual behavior of the system and to design controllers for each stage of boost-buck converter to assure a stable system as shown in Figure 6.69. The controller is designed based on the both an integer order and fractional order models for comparison purposes.

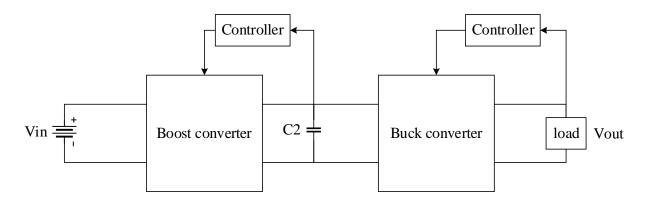


Figure 6.68: System block diagram of boost-buck converter.

The controller of the boost converter stage is used an inner and outer loop. The current state variable is represented as:

$$\dot{x}_1 = e(t) = i_{lboref} - i_{lbo}$$

where the reference inductor current for the boost converter and i_{lbo} is for the boost converter inductor. The state variable is defined from the tracking error as:

$$x_1=\int e(t),$$

 $x_2 = i_{lbo}.$

The state-space equations are represented as:

$$\dot{x}_1 = i_{lboref} - x_2$$
$$\dot{x}_2 = -\frac{r_{lbo}x_2}{L} - \frac{v_o(1-u)}{L}$$

Converting the state-space equations to standard LQR is represented as following:

$$\dot{z}_1 = -z_2$$
$$r_1 z_2 \qquad u$$

$$\dot{z}_2 = -\frac{r_1 z_2}{L} - \frac{\alpha}{L}$$

For controlling input *u* is minimized by the quadratic cost function:

$$J(t) = \int_0^\infty (z^T Q z + v^2) dt$$

where Q is a symmetric positive semi-definite matrix. The root locus for boost converter as shown in Fig. 6.70.

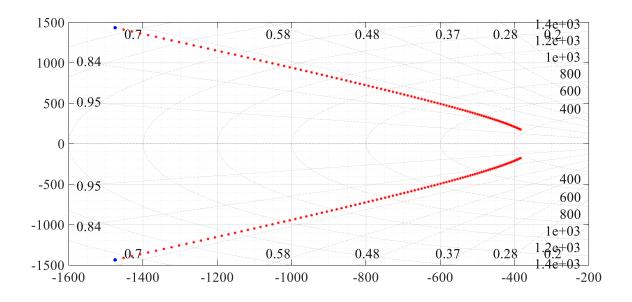


Figure 6.69: Movement of closed loop poles current controller of boost-buck converter.

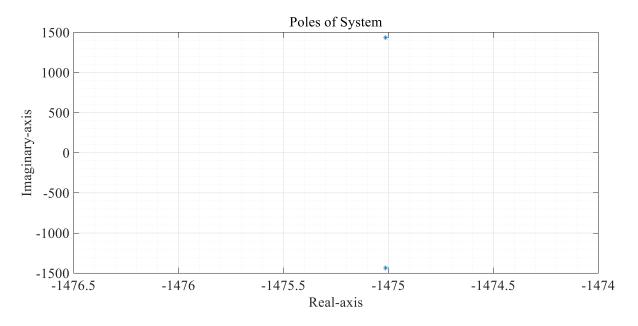


Figure 6.70: Poles of current controller location boost-buck converter.

Define the gains for outer-loop to control the output voltage of the boost converter. An error is defined as:

$$\dot{x}_1 = e(t) = V_{ref} - V_{out}$$

A new tracking error state-variable is added as

$$x_{1} = \int e(t),$$

$$x_{2} = V_{out},$$

$$\dot{x}_{1} = V_{ref} - x_{2}$$

$$\dot{x}_{2} = \frac{(1-u)}{c}i_{l} - \frac{1}{CR_{L}}x_{2}.$$

For determining the tracking of the system behavior:

$$\dot{z}_1 = -z_2$$

$$\dot{z}_2 = \frac{v_{in}}{C v_{ref}} u - \frac{1}{C R_L} z_2$$

The control input u is minimized by quadratic cost function

$$J(t) = \int_0^\infty (z^T Q z + v^2) dt$$

where Q is a symmetric positive semi-definite matrix. The control for boost converter is shown in Fig. 6.72.

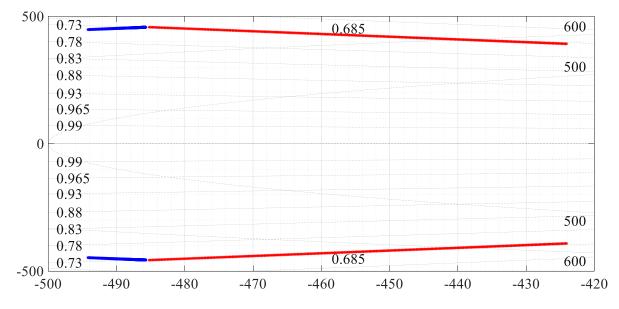


Figure 6.71: Movement of closed loop poles for voltage controller of the boost-buck converter.

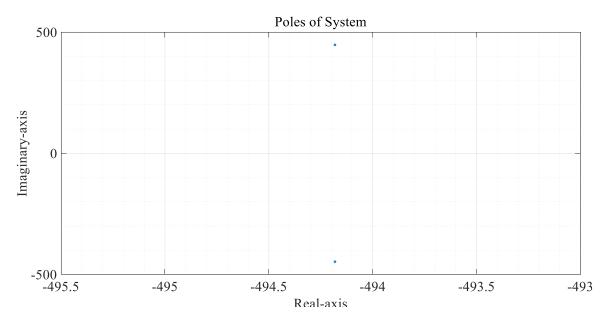


Figure 6.72: Poles of voltage controller location of the boost-buck converter.

The gains for controlling the output voltage of the buck stage uses a new state variable as:

$$\dot{x}_1 = e(t) = V_{ref} - V_{out}.$$

The tracking error has a new state-variable defined as

$$x_{1} = \int e(t),$$

$$x_{2} = i_{l},$$

$$x_{3} = V_{out},$$

$$\dot{x}_{1} = V_{ref} - x_{3}$$

$$\dot{x}_{2} = \frac{1}{C}x_{2} - \frac{1}{CR_{L}}x_{3}$$

$$\dot{x}_{3} = -\frac{rL}{L}x_{2} - \frac{1}{L}x_{3} + \frac{V_{in}}{L}u$$

Tracking of the system behavior is:

$$\dot{z}_1 = -z_3$$
$$\dot{z}_2 = \frac{1}{C} z_2 - \frac{1}{C R_L} z_3$$
$$\dot{z}_3 = -\frac{rL}{L} z_2 - \frac{1}{L} z_3 + \frac{V_{in}}{L} u$$

For control input *u* is minimized by the quadratic cost function:

$$J(t) = \int_0^\infty (z^T Q z + v^2) dt$$

where Q is a symmetric positive semi-definite matrix . The closed-loop control for buck converter is showing in Figure 6.74.

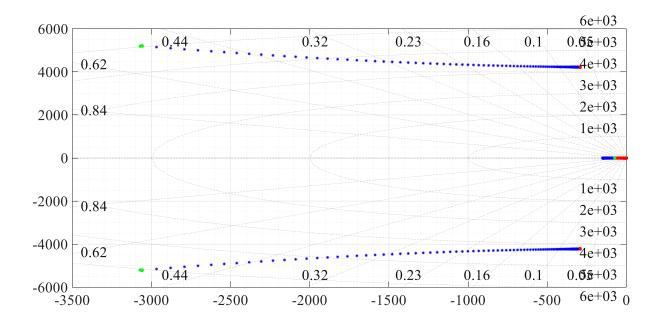


Figure 6.73: Movement of poles in LQR boot-buck converter.

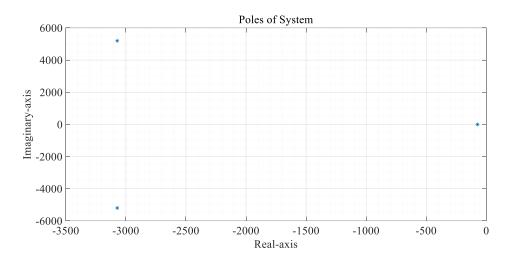


Figure 6.74: Poles of the boost-buck System.

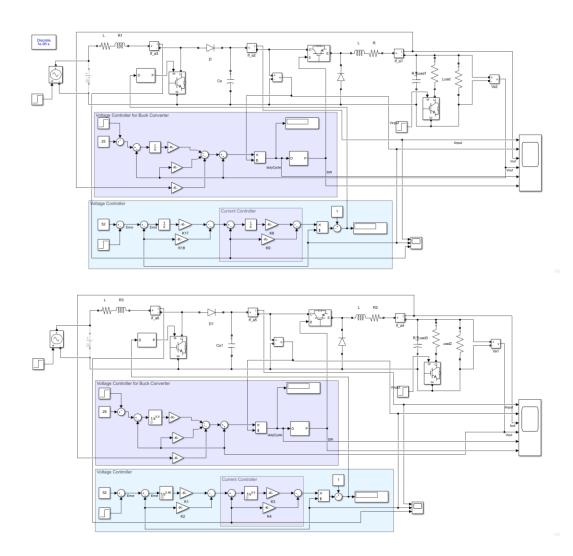


Figure 6.75: Simulation fractional-order, integer-order controller of boost-buck converter.

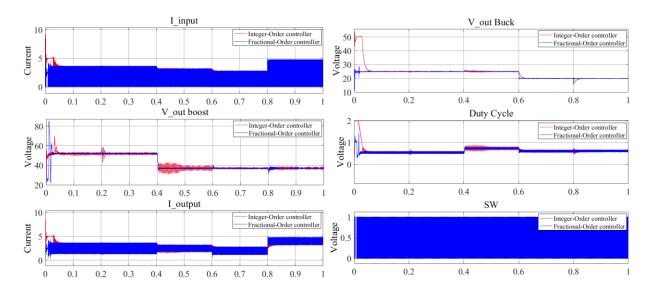


Figure 6.76: Simulation result for fractional and integer order controller of boost-buck converter.

The actual system behavior of the cascaded boost-buck converter with the fractional controller has less overshoot and faster response. Disturbances are applied to the systems for controlling the output voltage. At 0.2 s, the input voltage of the boost sage is reduced but the output voltage remains at the expected value. At 0.4 s, the reference of the output voltage of the boost stage is reduced to have verification of tracking the output value. At 0.6 seconds, the reference of the output voltage of the buck stage is reduced to have verification of tracking the output value. At the 0.6 seconds, the resistive load is decreased and the system performance of the controller is shown in Fig. 6.77.

6.5.3 Experimental Part for the DC-DC Boost-Buck Converter

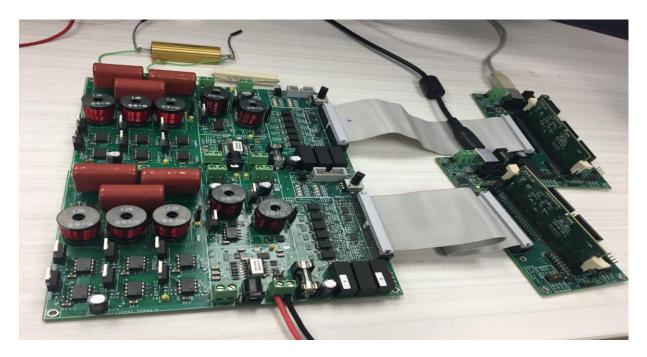


Figure 6.77: Experiment of identify and control boost-buck converter.

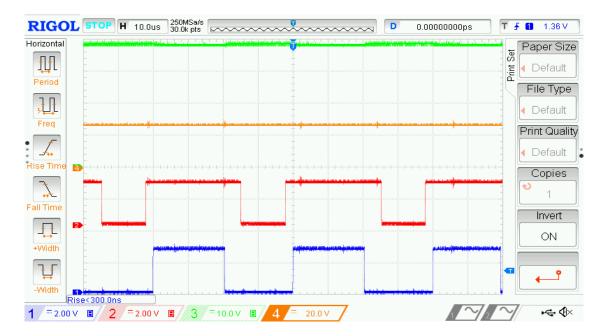


Figure 6.78: Open loop switching and voltages output of boost-buck converter.

The open-loop response for the boost stage switching is around 50% to boost the input voltage for double value. However, the buck switch reduces the output voltage around 70%.

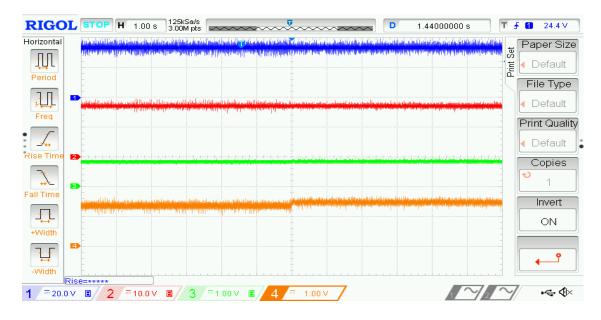


Figure 6.79: Experimental result of output voltages and currents of boost-buck converter.

The result for controlling the system output voltage matches the simulation results. Also applying change for the resistive load the system is stable and the voltage is tracking the reference value for the output voltage. 6.6 Identification of a DC-DC Interleaved Boost Converter with (CPL)

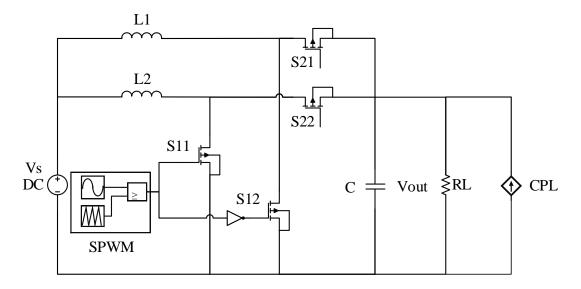


Figure 6.80: DC-DC interleaved boost Converter with CPL.

The interleaved boost Converter with CPL circuit design is presented in Figure 6. 81.

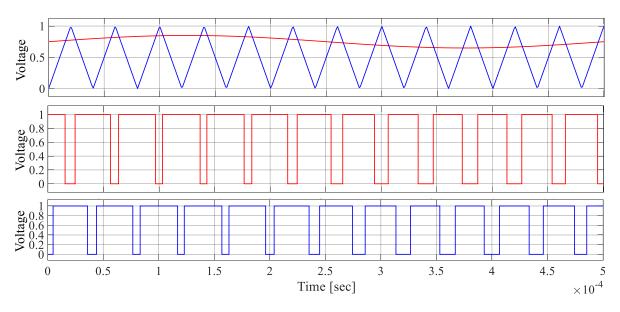


Figure 6.81: Control signals for interleaved boost converter with CPL of 2000Hz.

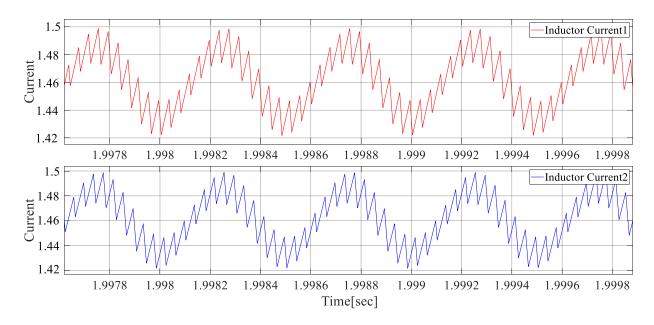


Figure 6.82: Inductor current signal for interleaved boost converter with CPL of 2000Hz.

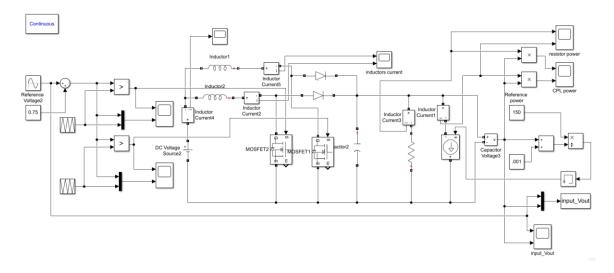


Figure 6.83: DC-DC Interleaved Boost Converter with CPL Matlab Simulink.

6.6.1 Fast Fourier Transform (FFT) Algorithm for Interleaved Boost Converter with (CPL)

The FFT applied to define the amplitude and phase angle for the output signal to determine amplitude and phase angle of input signal.

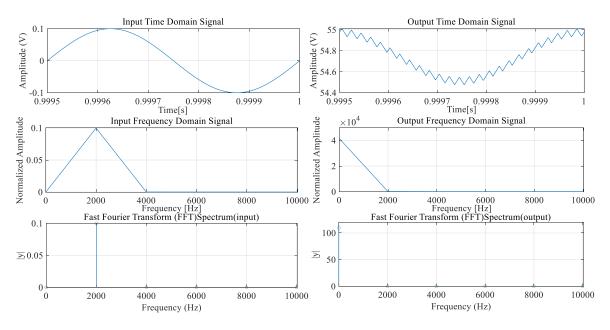


Figure 6.84: FFT analysis of interleaved boost converter with CPL of 2000Hz.

The magnitude and phase shift for each frequency is applied for the system obtained as shown in the Table 6.5:

Frequency(Hz)	Magnitude (dB)=20*log()	Phase shift (degrees)
1	47.9018	-1.1225
5	48.0829	-5.802
10	48.6301	-13.5089
25	48.9811	-28.6078
30	54.6687	-87.3775
35	53.8314	-129.7308
50	49.9931	-176.6309

Table 6.5: Collected data of bode plot for Interleaved Boost Converter with CPL.

75	43.0618	-214.1855
325	23.0879	-261.7982
350	22.3709	-263.8613
400	21.1952	-264.9893
450	20.1376	-266.5506
500	19.1868	-268.1228
550	18.3581	-268.707
600	17.589	-269.6024
650	16.8806	-270.4099
700	16.2368	-270.8058
750	15.6348	-271.4044
800	15.0701	-271.8631
850	14.5405	-272.147
900	14.046	-272.5913
950	13.5701	-272.8576
1000	13.128	-273.0826
1250	11.1963	-274.1456
1500	9.5991	-274.8814

Continued Table 6.5: Collected data of bode plot for Interleaved Boost Converter with CPL.

Frequency(Hz)	Magnitude (dB)=20*log()	Phase shift (degrees)
1750	8.2625	-275.3563
2000	7.1124	-275.6909
2250	6.0837	-275.994
2500	5.1873	-276.2134
2750	4.3514	-276.4246
3000	3.5577	-276.3014
3250	2.8382	-276.853
3500	2.2168	-276.8672
3750	1.6018	-276.9843
4000	1.01	-276.7598
4250	0.5309	-277.0524
4500	0.0831	-277.1601
4750	-0.527	-277.3
5000	-0.819	-277.2365

Continued Table 6.5: Collected data of bode plot for Interleaved Boost Converter with CPL.

6.6.2 Bode plot generation for Interleaved Boost Converter with CPL

Based on the accuracy of data obtained of magnitude and phase shift, the resulting Bode plot has magnitude (\mp 20) in decibels (dB) and the phase shift represented in degrees. The

frequency of the sine waveform in the control input of Simulink model is $2\pi f$ rad/sec. The frequency range of (50-5K Hz) applied to the model as showing in Fig. 6.91.

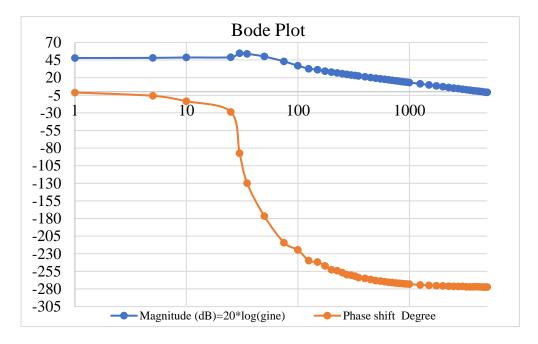


Figure 6.85: Bode plot of interleaved boost converter with CPL form obtained data.

The higher order transfer function of the system is estimated by using TFEST on Matlab to the frequency response collected data of the real system. Third order system is estimated for the actual system is fits the estimation data by 79.34%.

$$G(s)_{tose} = \frac{2.82 \times 10^9}{s^3 + 244.4 \, s^2 + 5.374 \times 10^4 \, s + 8.981 \times 10^6}$$

A fourth order system is estimated by using TFEST command fits the estimation data by 77.75%

$$G(s)_{fose} = \frac{2.6 \times 10^{12}}{s^4 + 928 \, s^3 + 2.884 \times 10^5 \, s^2 + 4.969 \times 10^7 \, s + 9.126 \times 10^9}$$

The fourth order system is estimated with an added zero that fits the data by 79.34% of the actual behavior.

$$G(s)_{fosez} = \frac{3.188 \times 10^9 \,\text{s} - 9.894 \times 10^{10}}{\text{s}^4 + 209.4 \,\text{s}^3 + 4.316 \times 10^4 \,\text{s}^2 + 7.125 \times 10^6 \,\text{s} - 4.493 \times 10^8}$$

A seventeenth order system fits the data by 99.43% the actual system.

$$G(s)_{sose} = \frac{1.984 \times 10^{27} \text{ s}^{11} - 6.144 \times 10^{29} \text{s}^{10} + 6.462 \times 10^{32} \text{s}^9 - 1.056 \times 10^{35} \text{s}^8}{\text{s}^{17} + 1.1557 \times 10^5 \text{ s}^{16} + 1.082 \times 10^9 \text{ s}^{15} + 1.0031 \times 10^{14} \text{ s}^{14} + 2.4 \times 10^{17} \text{s}^{13}}$$
$$\frac{+4.892 \times 10^{37} \text{ s}^7 - 1.586 \times 10^{40} \text{s}^6 + 1.739 \times 10^{42} \text{s}^5 - 4.934 \times 10^{44} \text{s}^4}{5.69 \times 10^{22} \text{ s}^{12} + 1.566 \times 10^{24} \text{ s}^{11} + 1.73 \times 10^{28} \text{ s}^{10} + 2.618 \times 10^{30} \text{ s}^9 + 2.023 \times 10^{33} \text{s}^8}$$
$$\frac{+4.892 \times 10^{37} \text{ s}^7 - 1.586 \times 10^{40} \text{s}^6 + 1.739 \times 10^{42} \text{s}^5 - 4.934 \times 10^{44} \text{s}^4}{1.638 \times 10^{35} \text{ s}^7 + 9.072 \times 10^{37} \text{ s}^6 + 8.476 \times 10^{38} \text{ s}^5 + 1.569 \times 10^{42} \text{ s}^4 + 6.24 \times 10^{43} \text{s}^3}$$
$$\frac{+1.675 \times 10^{46} \text{ s}^3 - 1.965 \times 10^{48} \text{s}^2 + 7.214 \times 10^{49} \text{s} + 8.357 \times 10^{50}}{4.725 \times 10^{45} \text{ s}^2 + 4.204 \times 10^{47} \text{ s} + 3.275 \times 10^{48}}.$$

The comparison between the estimated systems to simulation data shows the high order equivalent to the original system behavior as shown in Fig. 6. 92.

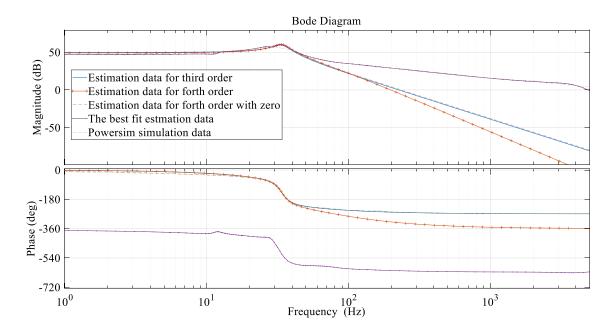


Figure 6.86: Bode plot of the interleaved boost converter with CPLVs estimation data.

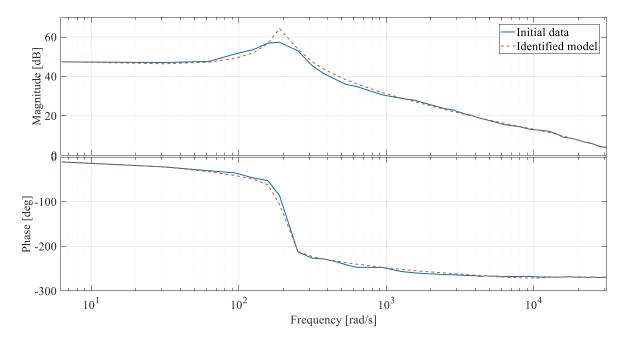


Figure 6.87: Bode Plot of interleaved boost converter with CPL

The real behavior of the system represented by fractional order transfer function using Levy's method with resulting the approximation error of 6%.

6.6.3 Modeling and Analysis of a DC-DC Interleaved Boost Converter with (CPL)

DC-to-DC power converter is constructed by using connection of Boost converters and CPL load. The model of the converter is derived to be:

KVL L:

$$V_{S} = L_{1} \frac{d_{i_{1}}}{dt} + V_{C}(1 - S_{11})$$

$$V_s = L_2 \, \frac{d_{i_2}}{dt} + V_C (1 - S_{12})$$

KCL C:

$$i_1(1 - S_{11}) + i_2(1 - S_{12}) = C \frac{d_v}{dt} + \frac{V_C}{RL} + \frac{P}{V_C}$$

Average Normalized Model

$$x_{1} = i, x_{2} = V_{out}, x_{3} = \frac{V_{s}}{(1 - U)}, S_{11} = S_{12} = U$$
$$\dot{x}_{1} = \frac{V_{s}}{L_{1}} - \frac{x_{3}(1 - U)}{L_{1}}$$
$$\dot{x}_{2} = \frac{V_{s}}{L_{2}} - \frac{x_{3}(1 - U)}{L_{2}}$$
$$\dot{x}_{3} = \frac{x_{1}(1 - U)}{C} + \frac{x_{2}(1 - U)}{C} - \frac{x_{3}}{CRL} - \frac{P}{Cx_{3}^{2}}$$

Equilibrium Point and State space model:

$$A = \begin{bmatrix} 0 & 0 & \frac{-(1-U)}{L_1} \\ 0 & 0 & \frac{-(1-U)}{L_2} \\ \frac{(1-U)}{C} & \frac{(1-U)}{C} & \frac{-1}{CRL} + \frac{P}{Cx_3^2} \end{bmatrix}$$
$$B = \begin{bmatrix} \frac{x_3}{L_1} \\ \frac{x_3}{L_2} \\ \frac{-(x_1+x_2)}{C} \end{bmatrix}, C = \begin{bmatrix} 0 & 0 & 1 \end{bmatrix}, D = \begin{bmatrix} 0 \end{bmatrix}$$

The linearized transfer function is:

$$G(s)_{Linearized} = \frac{-2.204 \times 10^4 s^2 + 9.375 \times 10^6 s + 3.052 \times 10^{-8}}{s^3 + 26.63 s^2 + 4.185 \times 10^4 s + 3.738 \times 10^{-10}}$$

•

The actual system is compared with the linearized system. The resulting Bode plot resonant peak is shown in Fig. 6.89.

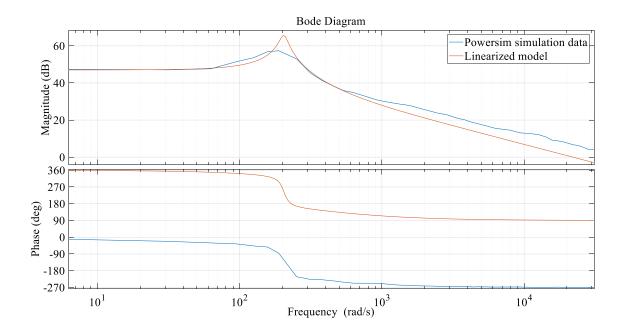


Figure 6.88: Bode plot of interleaved boost converter with CPL

6.6.4 Controller for the DC-DC Interleaved Boost with CPL Converter

The cascaded control is used for controlling the interleaved boost converter with the CPL The error variable is represented as:

$$\dot{x}_1 = e(t) = i_{lref} - (i_{l1} + i_{l2})$$

the new state variable is defined for the tracking error as:

$$x_{1} = \int e(t),$$
$$x_{2} = (i_{l1} + i_{l2}).$$

The state-space equations are represented as:

$$\dot{x}_{1} = i_{lref} - x_{2}$$
$$\dot{x}_{2} = -\frac{r_{l1}x_{2}}{L} - \frac{r_{l2}x_{2}}{L} - \frac{v_{o}(1-u)}{L}$$

Converting the state-space equations are represented as following:

$$\dot{z}_1 = -z_2$$
$$\dot{z}_2 = -\frac{r_{l1}z_2}{L} - \frac{r_{l2}z_2}{L} - \frac{u}{L}$$

For control input *u* is minimized by the cost function:

$$J(t) = \int_0^\infty (z^T Q z + v^2) dt$$

where Q is a symmetric positive semi-definite matrix, The current controller for interleaved boost with CPL converter is showing in Figure. 6.90.

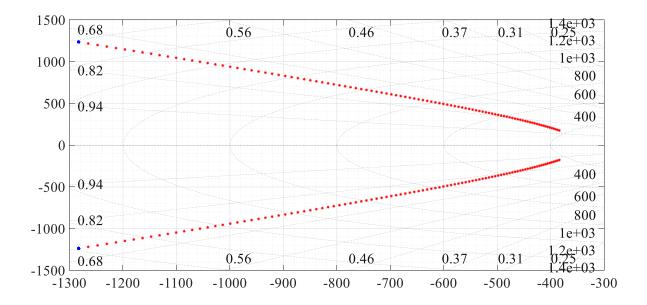


Figure 6.89: Movement of closed loop poles current controller (interleaved boost with CPL).

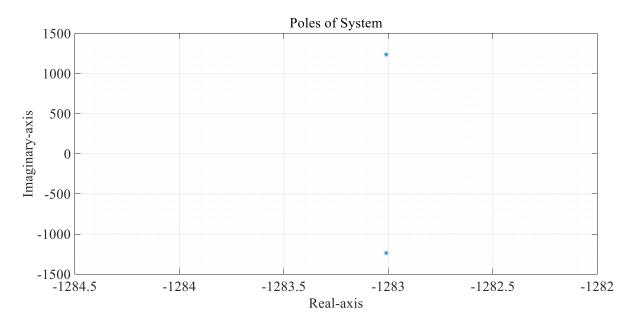


Figure 6.90: Poles of current controller location of interleaved boost converter with CPL.

Defining the gains of outer loop to control the output voltage boost converter, the LQR design technique is used. For a new state variable is defined as:

$$\dot{x}_1 = e(t) = V_{ref} - V_{out}$$

with tracking error,

$$\begin{aligned} x_1 &= \int e(t), \\ x_2 &= V_{out}, \\ \dot{x}_1 &= V_{ref} - x_2 \\ \dot{x}_2 &= \frac{(1-u)}{C} i_{ltotal} - \frac{1}{CR_L} x_2 - \frac{P}{Cx_2}. \end{aligned}$$

The tracking of the system behavior is:

$$\dot{z}_1 = -z_2$$

$$\dot{z}_2 = \frac{v_{in}}{Cv_{ref}}u - \frac{1}{CR_L}z_2 - \frac{P}{Cz_2}$$

For control input u is minimized by quadratic cost function

$$J(t) = \int_0^\infty (z^T Q z + v^2) dt$$

where Q is a symmetric positive semi-definite matrix. The control for boost converter is shown in Figure. 6.92.

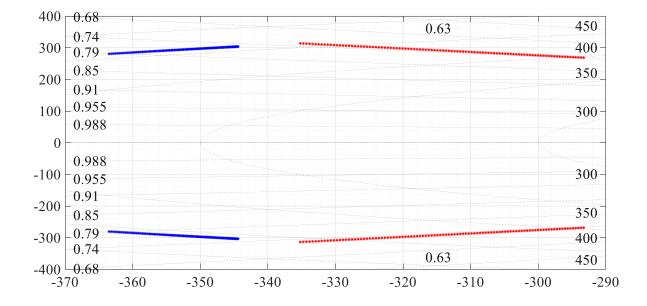


Figure 6.91: Closed loop poles location of voltage controller interleaved boost with CPL.

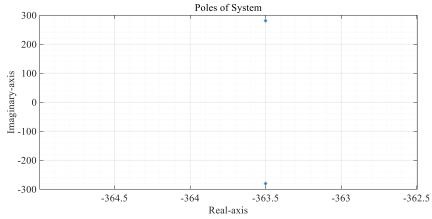


Figure 6.92: Poles of voltage controller location of interleaved boost converter with CPL.

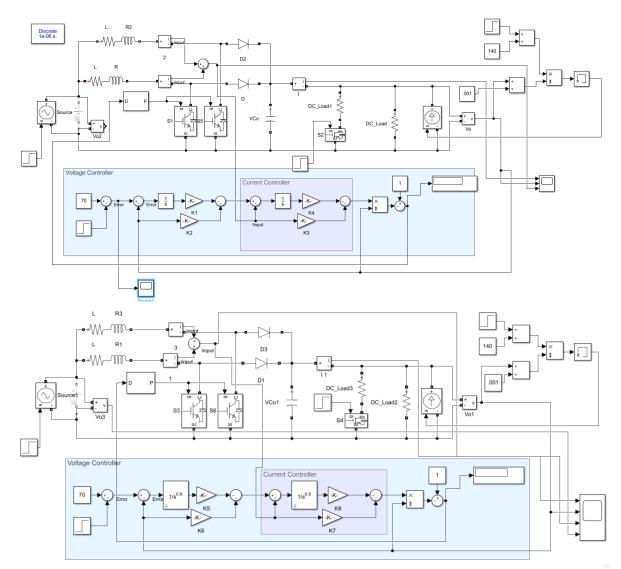


Figure 6.93: Simulation design of fractional-order versus integer-order controller of interleaved boost converter with CPL.

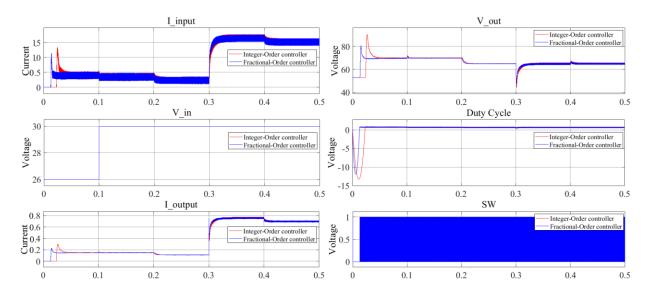


Figure 6.94: Simulation result Fractional Order controller-Integer order controller of interleaved boost converter with CPL.

The actual system behavior of the interleaved boost converter with a CPL using the fractional controller has less overshoot and faster response. Disturbances are applied to the system for controlling the output voltage. At 0.1 s, the input voltage is reduced but the output voltage remains at the expected value. At 0.2 seconds, the reference of the output voltage of the boost stage is reduced to verify the tracking the output value. At 0.3 seconds, the resistive load is decreased such that the tracking performance of the controller is shown in Fig. 6.95.

7 Conclusion

The research developed a new method for modelling switched-mode power electronic dcdc converters, By employing fractional order calculus, a modified describing function (MDF) method was developed that provides a systematic way to determine an accurate representation of the converter dynamics such that an improved feedback control design can be implemented. The method is primarily motivated by the challenges seen with boost converters however the experimental results showed benefits to buck and boost-buck converters. In addition, the nonlinear behavior of constant power loads was also demonstrated with the new fractional order MDF technique. Examples of constant power loads in LED lighting and computer loads such a data centers. Constant power loads exhibit a destabilizing characteristic on the overall dc-dc converter operation because of an incremental negative impedance characteristic. As constant power loads become more prevalent in electric utility operations, the types of loads will tend towards dominating the dynamic response of electric utility operations. Consequently, the importance of this research is demonstrated through the improved capability to stabilize dc-dc converters that have constant power loads.

Future work will focus on improved implementation methods of fractional order controllers. The disadvantage of the methods developed in this research is the increased numerical processing needed for fractional order systems. Improved off-line analysis and real-time fractional order computational methods are needed to support future deployment of advanced dc-dc converter technologies.

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Appendix

Appendix A

Codes for Resamples input-output signal

```
function [yy,tt] = myresample(y,t,dtol,NN)
```

```
%function [yy,tt] = myresample(y,t,dtol,NN)
```

%

% Resamples y = y(t) on a uniform grid tt = min(t) : dt : max(t).

% y may have n columns but should have length(t) rows.

% This simply calls Matlab's interp1 (with 'linear' as METHOD), but preprocess

% t (and y) so that tt is monotone increasing.

%

```
% dtol is a threshold such that t(j+1)-t(j) < dtol, then t(j+1) (and y(j+1,:))
```

% is removed.

%

% NN is the number of new sample points (defaults to length(t)), giving dt above

```
% as dt = (max(t) - min(t))/(NN-1)
```

%

if nargin < 4,

NN = length(t);

if nargin < 3, dtol = 100*max(abs(t))*eps; end

end

[yy,tt,p] = makestrict(y,t,dtol);

[m,n] = size(yy); N = NN;

nt = tt(1):(tt(end)-tt(1))/(N-1):tt(end); nt = nt(:); yy = interp1(tt,yy,nt,'linear'); tt = nt;

Appendix B

Codes for Restore input-output signal

```
function [yy,tt,p] = makestrict(y,t,dtol);
```

%function [yy,tt,p] = makestrict(y,t,dtol);

% Sorts y and t for increasing t and removes duplicate entries in t

% (assuming y = y(t)). Duplicates are defined by t(j+1)-t(j) < dtol.

% p is an permutation into 1:length(t), so that yy = y(p,:) and tt = t(p).

% If y is a (2d) matrix, then one of its dimensions must be length(t).

% If #rows(y) is $\sim = length(t)$, then it is transposed. So on output

% #rows(yy) = length(tt), and tt is a column vector.

n = length(t); t = t(:);

[ry,cy] = size(y);

if (ry ~= n & cy ~= n),

error('myresample: t and y vectors must be same size');

end

```
if ry \sim = n, y = y'; end
dt = diff(t); s = find(dt < 0);
```

```
at = ann(t); s = nna(at < 0);
```

if length(s),

disp('myresample: must sort')

```
[t,p] = sort(t); y = y(p,:);
```

else,

```
p = 1:n; p = p(:);
```

end

dt = diff(t); s = find(dt < dtol); m = length(s);

<mark>if</mark> m,

```
nots = find(dt >= dtol);
yy = [y(nots,:);y(end,:)]; tt = [t(nots);t(end)]; p = [p(nots);p(end)];
%[yy,tt] = makestrict(yy,tt,dtol);
```

else,

```
yy = y; tt = t;end
```