

Facile and Ultraclean Graphene-on-Glass Nanopores by Controlled Electrochemical Etching

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ABSTRACT: A wide range of approaches have been explored to meet the challenges of graphene nanostructure fabrication, all requiring complex and high-end nanofabrication platform and suffering from surface contaminations, potentially giving electrical noise and increasing the thickness of the atomically thin graphene membrane. Here, with the use of an electrical pulse on a low-capacitance graphene-on-glass (GOG) membrane, we fabricated clean graphene nanopores on commercially available glass substrates with exceptionally low electrical noise. *In situ* liquid AFM studies and electrochemical measurements revealed that both graphene nanopore nucleation and growth stem from the electrochemical attack on carbon atoms at defect sites, ensuring the creation of a graphene nanopore. Strikingly, compared to conventional TEM drilled graphene nanopores on SiN supporting membranes, GOG nanopores featured an order-of-magnitude reduced broadband noise, which we ascribed to the electrochemical refreshing of graphene nanopore on mechanically stable glass chips with negligible parasitic capacitance (~1 pF). Further experiments on double-stranded DNA translocations demonstrated a greatly reduced current noise, and also confirmed the activation of single nanopores. Therefore, the exceptionally low noise and ease of fabrication will facilitate the understanding of the fundamental property and the application of such atomically thin nanopore sensors.

KEYWORDS: graphene nanopores, ultraclean, low noise, electrochemical etching, glass substrate

wing to the atomically thin and ion-impermeable structure of graphene, nanopores¹⁻³ and nanogaps⁴ in graphene provide the opportunity for reaching single building block resolution in biomolecule sequencing.⁵ In nanopore sequencing, a strand of DNA traverses a nanopore submerged in a saline recording solution. The ionic current through the pore is monitored as the DNA strand passes, providing information on the local diameter of the strand. From the standpoint of device fabrication, the challenge lies in the reliable formation of atomic clean and stable pores or gaps in graphene at the nanometer scale.⁶ Among the current methodologies, transmission electron microscopy (TEM) sculpting with a highly focused electron beam is the most controllable.⁷ Nevertheless, the high cost and potential carbon contamination of the TEM technique make it unsuitable for scaling up the production of nanopore or nanogap devices. In addition, the TEM processing step limits the choice of substrates, as TEM substrates need to be compatible with a TEM holder and electrically conductive-to prevent charge accumulation during exposure to the electron beam. Freestanding thin silicon-nitride (SiN) membranes (several tens of nanometers in thickness supported on Si substrates) with a prefabricated nanopore (several hundred of nanometers in diameter) are commonly used as supporting substrates for fabricating graphene nanopores by TEM.^{2,3} The presence of a Si chip, however, raises a drawback during the nanopore measurement when submerged in the saline solution: the large parasitic capacitance (\gg 10–100 pF) resulting from the relatively thin supporting SiN membrane between the semiconducting Si and the saline solution induces high current noise.^{8,9} Recently, electrical pulse fabrication was used as an alternative route to efficiently and reproducibly fabricate graphene nanopores.¹⁰ However, so far such graphene

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Figure 1. Preparation and characterization of the graphene-on-glass (GOG) substrate. (a) Upper panel: an optical image shows a well-defined aperture at the center of a glass chip; lower panel: experimental scheme consisting of "cis" and "trans" fluidic chambers with a glass pore between the two reservoirs. The Ag/AgCl reference electrode residing in the cis fluidic chamber was used to introduce transmembrane voltage pulses for the fabrication of GOG nanopores. (b) AFM image of a clean, smooth-rimmed glass pore over a 1 μ m-diameter circular aperture. (c) AFM image of a glass pore completely covered by a free-standing CVD graphene, forming the GOG substrate. (d) Current-voltage curves of a bare glass pore (gray line) and a GOG substrate with a stable resistance of 2.7 G Ω (black rectangular and green circles) in 1 M KCl, 10 mM Tris, and 1 mM EDTA, pH 8.1. The corresponding increase in current (reduction in resistance, 1.7 G Ω , red triangle) suggests nanopore nucleation after applying short-term (50 ms), high-voltage (7 V) pulses.

nanopores were fabricated on silicon-based semiconducting substrates with relatively high current noise and low bandwidth. Particularly, to a large extent, the mechanism of nanopore formation in graphene with the electrical pulse method, as well as the ultraclean nature of the nanopores, are unclear and therefore limiting the broad application of the electrical pulse approach.¹⁰

In this paper, we use low-capacitance, commercially available glass chips featuring a micrometer-sized aperture at the center and use it as a support for graphene. This graphene-on-glass (GOG) platform, as we called it, is then subjected to electrically pulsed voltages yielding the formation of ultraclean graphene nanopores. In contrast to the dielectric breakdown fabrication on silicon substrates, we could probe into electrochemical mechanism using in situ liquid AFM, where a graphene nanostructure could be enlarged by removing carbon atoms at its freestanding edges during the breakdown process. Further experiments on double-stranded DNA translocations not only confirmed the formation of a single GOG nanopore, but also demonstrated greatly reduced current noise which we ascribed to the electrochemical refreshing of graphene nanopore on mechanically stable glass chips with minimized parasitic capacitance.

EXPERIMENTAL METHOD

CVD Graphene Transfer and Liquid Handling. Polycrystalline CVD graphene was obtained from Graphenea with good monolayer homogeneity (>98%) and micrometer-sized grains. Raman spectroscopy and imaging were performed on a WITec confocal spectrometer with a 100× objective (lateral resolution of ~300 nm) and blue laser (488 nm, 1 mW), confirming the relatively high quality of graphene on copper foils (not shown). Prior to CVD graphene transfer, the Nano-Patch-Clamp chips (Nanion) were subjected to sonication in deionized (DI) water and ethanol, followed by blow drying with nitrogen. The glass chips were then treated in oxygen plasma for 2 min. A capacitively coupled plasma system with a radiofrequency

(RF) of 40 kHz and 200 W power from Diener electronic (Femto) was employed at room temperature with a base pressure below 0.02 mbar and a power of 50 W at a pressure of 2.4 mbar for oxygen plasma. A PMMA supported CVD graphene layer floating on DI water (obtained by spin coating PMMA, copper etching, and rinsing with DI water) was then transferred onto those glass chips. The PMMA layer was then dissolved in acetone in the last step of transfer, leaving freestanding CVD graphene on the glass chips with micrometer-sized conical-shaped aperture at the center (Figure 1). We adopted a protocol to use 50% ethanol (in DI water), DI water, and 1 M KCl/10 mM Tris solution (~11 S/m at room temperature) at pH 8.1. Such protocol allows complete wetting and electrical contact via Ag/AgCl electrodes to both sides of the graphene membrane, as any air bubbles will be repelled because of the low surface tension of the 50% ethanol solution. The leakage conductance (0.5-1 nS) can be attributed to ion transport through defect structures in the free-standing graphene and the gaps between graphene and glass substrate,¹ which is negligible compared to pore conductance for pores larger than 1 nm (>6 nS).¹⁰ The whole device is very stable: control experiment without electrical pulse shows almost no change of leakage current during the measurements.

AFM Microscopy. AFM images were performed on a JPK NanoWizard Ultra Speed machine. A silicon probe (AC240TS, Asylum Research) with ~70 kHz nominal resonance frequency in air and ~30 kHz resonance frequency in liquid was used. The images were scanned in intermittent contact mode, which allows one to work with soft and easily damaged material, in liquid and at room temperature with 512×512 pixels. Such collected images were then processed using a JPK SPM Data Processing software. The *in situ* liquid AFM images in the inside were performed both before and after the application of electrical pulses without drying the sample. Detailed pictures and illustrations of the *in situ* liquid AFM setup can be found in Figure S1.

Electrical Measurements and Pulse Generator. All the nanopore measurements were performed with an ultralow-noise patch clamp amplifier (Axopatch 200B, Molecular Devices) with resistive-feedback circuitry in whole-cell mode. Ag/AgCl electrodes are used to detect ionic currents and to apply electric fields. An Agilent pulse generator (81101A) was used to apply 50-ms-long

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Figure 2. Mechanisms of GOG nanopore nucleation and formation by electrochemical etching. (a) Evolution of experimental GOG resistance *R* vs number of 7 V/50 ms pulses during electrical fabrication. Modest voltage pulses (5 V/50 ms for 50 times) stabilize the *R* from 88 MΩ (blue dot) to its final value at 54 MΩ (gray dot). Inset: average relative resistance R/R_0 vs number of pulses for three GOG nanopores, which exhibit similar resistance of ~90 MΩ after experiencing 60 times of 7 V/50 ms pulses. (b) Circuit diagram of the GOG system during nucleation. Any applied potential will mainly drop at the graphene/liquid interface inside the glass pore with the smallest capacitance (C_{lower}) in the system. (c) Circuit diagram of the GOG system during nanopore formation. The nucleation of the graphene nanopore introduces a possible pathway for the current. As a result, (1) the potential of the electrolyte close to the graphene edge plane will be half of the applied potential: 3.5 V; (2) a potential drop of ~1.5 V = 5 V - 3.5 V will be developed across the graphene edges. (d) Scheme of the GOG system when both the "cis" and the "trans" chambers are grounded. (e) and (f) are the schemes of the GOG platform during nanopore nucleation and growth under voltage pulses of 7 V, respectively.

voltage pulses. To protect the amplifier from large input voltages, a mechanical switcher was used to disconnect the amplifier before each pulse. The Axopatch 200B amplifier was reconnected 2 s after each pulse such that the GOG system gets stabilized. We empirically adopted a 7 V/5 V protocol for electrochemical etching as previous electrical pulse fabrication method also reported the lowest voltage to reliably nucleate pores (\sim 7 V) and the lowest voltage to reliably enlarge nucleated pores (\sim 5 V): (1) 7 V pulses with 50 ms period were applied repeatedly for both nucleation and formation, and (2) 5 V pulses with 50 ms were used for enlarging and stabilizing the GOG nanopores.

DNA Translocation and Noise Measurement. λ -DNA (48,502 base pairs, Lot: 1651504, from New England Biolabs Tech. N3011S) solution was prepared at a concentration of 15 μ g mL⁻¹ in 1 M KCl with 10 mM Tris and 1 mM EDTA, pH 8.1. DNA translocation events were recorded under 100 and 200 mV bias voltage using a band-pass filter of 100 kHz and a sampling rate of 500 kHz. The current traces were filtered using an 8-pole Bessel filter at 10 kHz in Clampfit. A low noise data acquisition system (Axon Digidata 1550A, Molecular Devices) was adopted to further eliminate local noise and to record the translocation event signals. Power spectral densities were calculated from the Fourier transform of the autocorrelation function and dividing it by the sampling frequency and the sample length. For normalization, the power spectral densities were divided by the mean current of the corresponding traces. All of the analyses and fitting were done with a Matlab GUI-based package named Transalyzer.29

RESULTS AND DISCUSSION

As the starting substrate, commercially available glass chips (insulating borosilicate Nano-Patch-Clamp chips purchased from Nanion) were used, containing a micrometer-sized conical aperture at the center (Figure 1a, upper panel). A monolayer of chemical vapor deposition (CVD) graphene was transferred onto the conical glass pore using the standard poly(methyl methacrylate) (PMMA) transfer method¹¹ to obtain a so-called GOG membrane. This platform was submerged in a saline solution (1 M KCl with 10 mM Tris and 1 mM EDTA, pH 8.1) and connected on both side of the pore to an amplifier through Ag/AgCl electrodes (Figure 1a, lower panel). We measured the pore resistance, capacitance, and current noise spectra. Before the transfer of graphene, the 100- μ m-thick insulating glass substrate contributes 1.8 pF $(C_{\text{glass}} = 1.8 \text{ pF}, \text{Figure S2})$ to the total input capacitance with an aperture resistance of 2.1 M Ω (Figure 1d, gray line). Such small parasitic capacitance benefits low noise/large bandwidth sensing applications as discussed later. The as-prepared GOG ensemble exhibited a stable transmembrane resistance of 2.7 $G\Omega$ (black rectangles and green circles, Figure 1d), indicating a complete graphene coverage of the glass pore. AFM images (Figure 1b and c) show the morphology of the micrometersized glass pore with a clean, smooth-rimmed opening, before and after graphene transfer. In the 90 GOG substrates fabricated and tested, about 30 devices revealed finite transmembrane resistance in the order of 100 M Ω after transfer, attributed to inherent defects in CVD-grown graphene or electrostatic discharge effects¹² (which we will discuss later). Frequently, significant capillary force leads to damaged graphene membranes with resistance <10 M Ω . After excluding samples with damaged graphene, the yield of GOG samples for subsequent processing was 31 devices (34%) exhibiting transmembrane resistance of >1 G Ω .

In situ, a voltage was applied to open up a graphene nanopore. We applied short, high-voltage pulses (50 ms, 7 V) across the graphene membrane. Such a millisecond pulse is easier to achieve than the previously reported 250 ns pulses applied for graphene nanopore fabrication.¹⁰ In Figure 1d, repeated application of high voltage pulses (50 ms, 7 V, 10 repetitions with 2 s separation) lead to a decrease in the resistance to 1.7 G Ω (red triangles). In the following, we always use 50 ms pulses with 2 s separation time, if not otherwise specified.

We formed a GOG nanopore with a resistance of 88 M Ω when continuously repeating the 50 ms pulses for another 50 times after the first opening of the pore (Figure 2a, blue dot). To evaluate the controllability of the electrical fabrication, we averaged the relative resistance R/R_0 of three different GOG nanopores, which exhibited a similar resistance of around 90 M Ω after experiencing 60 times 7 V/50 ms pulses. As shown in the inset of Figure 2a, after applying 7 V/50 ms pulses for 10, 20, and 60 times, the standard deviations of R/R_0 : $\left(\frac{\text{error bar}}{R/R_0}\right)$ are

22%, 27%, and 60%, respectively, suggesting a reasonable reproducibility of our electrical fabrication method. In the final step, we kept enlarging and stabilizing the nanopore by using modest voltage pulses until a resistance of 54 M Ω was reached (Figure 2a, gray dot). This resistance value of 54 M Ω corresponds to a graphene nanopore with an equivalent 2.3 nm pore diameter assuming a regular round shape.^{1,2} The fact that this resistance value is kept unchanged over a time period of at least 1 h (Figure S3) suggests that the GOG nanopore is stable.

In the case of dielectric breakdown fabrication in SiN membranes,¹³ the applied electric fields (up to $\sim 1 \text{ V nm}^{-1}$) are close to the dielectric breakdown strength of low-stress SiN films, and are able to induce accumulation of charge traps forming a highly localized conductive path. This results in the physical damage of the conductive path because of substantial power dissipation and heat generated, yielding a nanopore in the bulky SiN membrane.¹³ Unlike 10-30-nm-thick SiN membranes, graphene is a conducting membrane with atomic thickness. Figure 2b and Figure S4 represent the circuit diagram of the GOG and graphene on SiN systems, respectively. Instead of crossing the membrane, the potential drop $V_{\rm appl}$ occurs mainly at the graphene/liquid interface in the glass pore where the charging capacitance is smallest: $C_{lower} =$ 16 fF $\ll C_{\text{glass}} = 1-2$ pF, $C_{\text{Q}} = 300$ pF, $C_{\text{upper}} = 60$ pF, where C_{Q} and C_{upper} represent the quantum capacitance and the interface capacitance at the upper side of the graphene membrane, respectively. To date, little is known about the mechanism that is responsible for graphene nanopore nucleation and growth.

As a rough estimation, we calculated the negatively charged surface group density at the graphene/glass interface Q_{int} to be 5×10^{17} m⁻² in a solution of 10 mM Tris (pH = 8.1, see also the Supporting Information).¹⁴ Therefore, a potential drop $V_{ch} = V_{vari} \sim -4$ V would be obtained upon submerging the GOG

substrate in the saline solution (grounded, $V_{appl} = 0$ V). On the other hand, a potential drop V_{ch} at the graphene-electrolyte interface (inside the glass pore) can, in principle, cause various electrochemical processes with electron transfer from graphene to electrolyte. It is most likely that such electron transfer tends to stabilize V_{ch} at -2 V (Figure 2d), the potential barrier of H⁺ reduction at graphene cathode (considering the overvoltage). Compared to the fast electrostatic charging of the electric double layer at C_{lower} (100 ns $-1 \ \mu s$),^{15,16} this discharging process (discharging time constant of $\tau = rC_{upper} = 150 \text{ ms}$) is relatively slow and is therefore presumably due to electron transfer from graphene to the electrolyte. The detailed mechanism on ultraclean graphene nanopore/nanogap formation via electrochemical etching is discussed in Supporting Information. In principle, the obtained graphene nanopore has a sharp nanopore edge without possible amorphous carbon contaminations, which holds the potential of ultimate atomic spatial resolution. In addition, our previous experiments¹⁷ have demonstrated greatly improved electronic performance of electrochemically cleaned CVD graphene biosensors compared to their as-fabricated counterparts, including suppressed lowfrequency 1/f noise, yielding an increased signal-to-noise ratio (SNR).

Figure 2e illustrates the charge distribution across the graphene membrane during a voltage pulse. During the first 0.1–1 μ s from the start of the 7 V voltage pulse, a voltage drop of $V_{\rm ch}$ = 5 V (instead of 7 V, due to the offset voltage -2 V in Figure 2d) develops across C_{lower} (with the smallest capacitance of 16 fF among all the capacitors in the system). The nucleation of the graphene nanopore is taking place under this $V_{ch} = 5$ V during the following 50 ms time period. Here, the voltage drop at $C_{\text{lower}} = 5$ V has significantly passed the potential barrier of Cl⁻/Cl₂ and OH⁻/O₂ oxidization reactions at graphene anode, which occur around 2 V. Consequently, we expect the electrochemical formation of Cl₂, O₂, and more aggressively, Cl-, O-, and O_3- radicals, 18 which can attack carbon atoms in graphene-especially the ones at defect sites in the graphene crystal lattice-and are responsible for atomically sharp nanopore nucleation. Notably, the separation time of 2 s between pulses seems to be sufficient for dissolving any gases formed during the pulse period of 50 ms and stabilizing the system before subsequent pulses, as we did not observe any gas bubble forming even after up to 200 pulses. 7 V/500 ms pulses, on the other hand, lead to immediate gas bubble formation. As soon as the applied voltage V_{appl} drops from 7 to 0 V, the actual potential $V_{\rm ch}$ drops to and stabilizes at -2 V again (Figure 2d). Repeating this process up to 10-20times normally led to nanopore nucleation with noticeable reduction in the GOG resistance, signaling pore formation, as mentioned above (red triangles, Figure 1d).

The successful nucleation of a graphene nanopore (7 V, 50 ms, 10 times) introduces a possible pathway of the current. In Figure 2c, given that the resistance of the graphene nanopore $(R > 1 \text{ G}\Omega)$ is dominantly larger than the glass pore resistance $(r = 2-3 \text{ M}\Omega)$, we expect that half of the applied voltage is distributed at the nanopore: $V_{\text{pore}} = 3.5 \text{ V}$. During the GOG nanopore growth, a voltage drop up to 1.5 V occurs across the edge of the graphene pore ($V_{\text{edge}} = V_{\text{ch}} - V_{\text{pore}} = 5 \text{ V} - 3.5 \text{ V}$, Figure 2f), which is sufficient to initiate the electrochemical etching and refreshing effect, as below.

CVD graphene (or even exfoliated graphene) contains defects, including vacancies and grain boundaries. These defective carbon atoms, as well as possible amorphous carbon



Figure 3. GOG nanopore formation via electrochemical etching. (a) *In situ* liquid AFM images scanned at the same position during graphene nanostructure enlargement under electric pulses, as indicated by the white arrows (upper panel: before the pulses; lower panel: after the pulses). Scale bar: 50 nm. (b) Scheme of electrochemical etching of CVD graphene transferred on $Al_2O_3(20 \text{ nm})/SiO_2(285 \text{ nm})/Si$ substrate and the corresponding circuit diagram. (c) SEM image of as-transferred CVD graphene on the $Al_2O_3/SiO_2/Si$ substrate. (d) SEM images of the CVD graphene after 30 min of 1.6 V electrochemical etching in 1 M KCl solution. The exposed hexagonal edges of the CVD graphene (on the insulating $Al_2O_3/SiO_2/Si$ substrate in white color) suggest that the electrochemical etching process was most likely initiated from its grain boundaries contains active defects.

introduced during chip preparation and storage, are more inclined to be oxidized and attacked in an electrochemical reaction induced by the voltage drops at the graphene/liquid interface. Regrettably, given the extremely small amount of carbon atoms being removed from the graphene membrane during nucleation, it was impossible to directly study the reaction products to resolve the exact mechanisms of nanopore nucleation. However, using liquid AFM we were able to monitor the in situ electrochemical etching of graphene (Figure S4). We were able to trace the electrochemical etching of the graphene along its edge, after applying electrical pulses as in Figure 2a. Figure 3a (upper panel) shows the AFM image of an as-transferred GOG. This graphene area features exposed edges, represented by the darker yellow areas in Figure 3a, which can be ascribed to the mechanical damage and/or electrostatic discharge during the assembly of the GOG sample. As shown in the lower panel of Figure 3a, the electrical pulses attack the graphene edges (white arrows), evident from the widening of the darker regions. That is, defective carbon atoms at the exposed graphene edge are attacked, presumably due to the effective oxidation of carbon into reactive products, which dissolve into the electrolyte solution during the electrochemical etching process.^{19,20} Naturally, such a solution based in situ electrochemical process produces clean graphene

nanopores due to the progressive etching compared to TEM sculpting approaches.

To further shed light on the mechanism of pore formation, we transferred CVD graphene on an Al₂O₃(20 nm)/SiO₂(285 nm)/Si substrate (Figure 3c) and examined electrochemical etching behaviors under similar conditions during nanopore formation. For that, we used a semiconductive silicon substrate instead of an insulating glass substrate to facilitate SEM imaging. We also applied an additional 20-nm-thick atomic layer deposition (ALD) Al₂O₃ layer to block any possible leakage of current through the substrate.²¹ As shown in Figure S5 in the Supporting Information, on polycrystalline CVD graphene, the growth started from high-density seeds with hexagonal features (see Figure S5a). As these hexagonal graphene flakes grew larger laterally and meet each other at the edges, grain boundaries containing vacancy or topological defects formed. In Figure 3b, we applied direct current (DC) voltage to the graphene using evaporated gold electrodes or silver paste, to apply a mild voltage of 1.6 V at the graphene/ liquid interface via a droplet of 1 M KCl solution (grounded via a reference electrode). This electrochemical condition is similar to the electrical condition during the growth of the graphene nanopore in the previous section of the paper (1.5 V)across the edges of the graphene pore, see Figure 2c and f). After 30 min, we were able to etch the CVD graphene as



Figure 4. DNA translocations through a GOG nanopore. (a) Baseline conductance (left) and blockade events (right) upon addition of 48.5 kb double-stranded λ -DNA across a 16.5 nm (6 M Ω) GOG nanopore. (b) Examples of translocation events of folded (F, blue), partially folded (F+N, red), and nonfolded (N, black) DNA molecules recorded at 200 mV. (c) Scatter diagram of the amplitude of the conductance blockade versus translocation time for λ -DNA translocate through the GOG nanopores. Inset is a schematic of a DNA molecule translocating through a GOG nanopore. The accompanying histograms for the DNA blockade time is included at the top of the Figure 4c, and the histograms for DNA blockade amplitude data is included on the right of Figure 4c, respectively. Regions of DNA translocation data for nonfolded and folded DNA translocation events are highlighted inside the black and blue circled areas, respectively. Each point in this scatter diagram corresponds to a single translocation event.

shown in Figure 3d. The exposed white-colored insulating $Al_2O_3/SiO_2/Si$ substrate reveals the hexagonal edges of the etched CVD graphene. The fact that the size as well as the hexagonal shape of the etched CVD graphene closely resemble those of its grain boundaries (see Figure S5) suggests that the electrochemical etching process was most likely initialized from and progressed along its grain boundaries, which contain active defects. It is at these defect sites that a graphene nanopore preferentially nucleats and grows (50 ms pulses of 5–7 V). Under electrical pulse condition, this electrochemical etching/refreshing process forms an atomic sharp pore edge without contamination, which not only reduces the spatial resolution, but also was supposed as a source for high nanopore noise.²²

To demonstrate the suitability of these GOG nanopores for single molecule measurements, we have performed DNA translocation experiments (Figure 4) after the electrical pulse fabrication of the GOG nanopore. DNA is negatively charged. Under an electric field, the molecules therefore move from anode (-) to cathode (+). When a graphene membrane with nanopore is placed between the two electrodes, the electric field concentrates across the nanopore and drags the DNA molecules from head-to-tail (including folded conformations) through the pore. Each nucleotide within the DNA strand could be read off when it is located at the narrowest constriction of the pore by detecting the ion current passing through the pore to probe the sequence of nucleotides constituting the DNA. We injected λ -DNA on the grounded side of the flow cell and applied a negative bias voltage to drive the negatively charged λ -DNA through the GOG nanopore with a 100 mV or 200 mV DC voltage bias via Ag/AgCl reference electrodes (Figure S6). Threading of the doublestranded λ -DNA molecule through the pore causes a transient reduction in the ionic current (conductance) measured with a low noise Axopatch amplifier (Figure 4a).

These translocation events can be distinguished based on the magnitude of the conductance blockade (ΔG). In Figure 4b, we observed two characteristic reductions in the ionic conductance (ΔG): 1.5 nS and 3 nS, representing the nonfolded (N) and folded (F) translocation events, respectively. These ΔG values for λ -DNA in GOG pores are quite similar in magnitude to that previously measured for graphene pores with diameter of around 20 nm for the same conditions.² We measured DNA translocations on 9 graphene nanopores with pore diameters ranging from 10 to 25 nm, and



Figure 5. 1/f noise of GOG nanopore. (a) Typical current trace of a GOG nanopore ($R = 23 \text{ M}\Omega$, d = 4.8 nm in red) and of a graphene nanopore on SiN pore ($R = 30 \text{ M}\Omega$, d = 3.8 nm in black) under 200 mV. (b) Spectral densities of the GOG nanopore and of a graphene nanopore on SiN pore. (c) Spectral densities (S_1) at various bias voltages for the GOG nanopore over the 1/f regimes (for graphene pores this band is 1-1000 Hz). (d) Normalized spectral densities (S_1/I^2) of the curves in c. A linear fit of these curves yields the low-frequency noise coefficient C_{LF} that represents the magnitude of the 1/f noise. (e) Probability distributions of low-frequency noise coefficients C_{LF} of 19 GOG nanopores. (f) C_{LF} versus pore diameter was plotted logarithmically. The red line represents a linear fit of the data.

collected good statistics on 4 devices with pore diameters around 20 nm. Here we present a typical experiment of DNA translocation, confirming the translocation through a single GOG nanopore, since DNA translocation in multiple pores will affect the conductance much more substantially (larger reductions than 3 nS in the ionic conductance when more than one DNA translocates the nanopores at the same time). The formation of such GOG nanopores during the electrochemical etching process is likely initiated via a fluctuation that randomly selects an active site for the nanopore nucleation and is then limited to that site during the GOG nanopore formation and enlargement. The average translocation time is 2.3 \pm 0.9 ms for the 48.5 kb double-stranded λ -DNA (Figure 4c), corresponding to 0.05 μ s/base. This translocation speed is comparable to the previously reported translocation time on λ -DNA in graphene nanopores.²

Minimizing noise is of key importance to improve the minimum change in current that can be reliably detected at a given bandwidth. Generally, the noise spectrum in nanopore systems can be divided into a low-frequency regime ($f < \sim 1$ kHz) and a high-frequency regime ($f > \sim 1$ kHz). Whereas the low-frequency noise is dominated by pink noise with a 1/f dependence,²³ the high-frequency noise originates from the

parasitic capacitance²⁴ and prevents sampling at high-frequency bandwidths.

Figure 5a provides the typical ionic current traces for a GOG nanopore in red and a SiN membrane supported graphene nanopore in black, respectively. Both graphene nanopores were suspended over a pore of 1 μ m diameter. Both traces presented in Figure 5a were recorded at 200 mV bias voltage and processed following the same protocol (low-pass filtered with an 8-pole Besssel filter at 100 kHz). Strikingly, the GOG nanopore exhibits much lower current noise than its counterparts on the SiN membrane window. In Figure 5b, the corresponding noise spectra reveals one order of magnitude lower noise for the GOG nanopore than the SiNsupported graphene nanopore. At the high-frequency region, the reduction of noise in the GOG nanopores can be attributed to the fact that a SiN membrane of tens of nanometers in thickness is replaced by a thick glass substrate (thickness of $\sim 100 \ \mu m$) with low parasitic capacitance, which significantly reduces the capacitance of the whole support and resulting in consistently reduced electrical noise.⁴

Similarly, in the low-frequency regime, the amplitude of 1/f noise for the GOG nanopore is also around one order of magnitude lower than that for graphene nanopores on SiN (Figure 5b). It is well-known that the 1/f noise of graphene nanopores fabricated on the Si supported SiN membrane is relatively large and is typically two orders of magnitude higher than for silicon nitride nanopores.²⁶ This large low-frequency 1/f noise is most probably due to either surface contamination introduced during preparation/storage or mechanical fluctuations of the free-standing graphene membrane in aqueous solution.²⁶

Power spectral density S_{I} at varying voltage levels for a given graphene nanopore shows that S_{I} curves are bias-voltage dependent (Figure 5c). Indeed, the normalized power spectral densities-found by dividing the current power spectral density S_{I} by the squared current amplitude, $(S_{I})/I^{2}$ —exhibit the same low-frequency noise magnitude and show no biasvoltage dependency (Figure 5d). In order to compare the noise levels for different samples under various bias conditions, we adopted $\frac{S_1}{I^2} = \frac{C_{LF}}{f}$, where C_{LF} represents the low-frequency noise amplitude. Here, based on the normalized power spectral density measurement (Figure 5d) on a total of 19 GOG chips, the histogram of C_{LF} (Figure 5e) indicates that the low frequency 1/f noise of the GOG nanopore is statistically one order of magnitude lower than that of electrically fabricated or TEM drilled graphene nanopores on SiN membrane in other studies. That is, the red curves depict log-normal distributions exhibiting an average value of $\langle C_{\rm LF} \rangle$ = 5.8 \times 10⁻⁷ for GOG nanopores, compared to $\langle C_{\rm LF} \rangle$ = 6.3 \times 10⁻⁶ for previously reported graphene nanopores in SiN membranes.²⁶ We ascribe such exceptionally low 1/f noise to the ultraclean nature of the electrochemically formed nanopores, as well as to the removal of thermal vibration from the solid support of the glass substrate compared to the free-standing SiN membrane. It was reported that as a nanoscale-microscale electromechanical mixing system, the graphene resonator was able to transduce the motion of the coupled free-standing metal electrode with minimal damping.^{27,28} We also investigated the dependence of the 1/f noise on the size of the graphene pores. Figure 5f plots the 1/f noise as a function of pore diameter. The pore diameters are determined from conductance calculation based on the assumption that the resistance of graphene nanopore is

dominated by the access resistance $R = \sigma^{-1\frac{1}{d}}$, where σ represents the bulk conductivity and *d* is the pore diameter.²⁶ Although there is appreciable scatter in the data, we evidenced a correlation between the size and the amplitude of the 1/f noise where large pores have lower noise, in accordance with previous studies on SiN membrane supported graphene nanopores.²⁶ Thus, our results clearly demonstrate order-of-magnitude reduced current noise in electrochemically formed GOG nanopores, which yields ultraclean nanopores with a favorable reduction in the parasitic capacitive coupling and in the mechanical vibration coupling, making our approach highly promising for potential practical application of graphene nanopores for single molecule detection.

CONCLUSION

We unraveled the mechanism of ultraclean graphene nanopore/nanogap formation via electrochemical etching based on a graphene-on-glass platform. We conducted in situ liquid AFM studies and demonstrated the enlargement of a graphene nanostructure taking place at its freestanding edges during electrical fabrication. This electrochemical mechanism is strongly supported by our observed electrical etching of CVD graphene on silicon substrate under similar electrochemical conditions. One step further, we confirmed the electrochemical formation of graphene nanopores by doublestranded DNA translocation experiments and envision that this electrochemical etching mechanism offers a new strategy to meet the challenges of ultraclean graphene device fabrication at nanometer scale, including nanogaps. Particularly, the ultraclean GOG nanopores with exceptionally low noise level and ease of fabrication under voltage pulse make this a versatile platform to be used for improved nanopore measurement. Furthermore, such a platform also featured as a relatively cheap substrate to obtain nanopores in various 2D materials for various nanoelectronics and biomolecule detection applications.

ASSOCIATED CONTENT

Supporting Information

The Supporting Information is available free of charge at https://pubs.acs.org/doi/10.1021/acssensors.0c00883.

In situ liquid AFM system; Experimentally measured parasitic capacitance; Stability of GOG nanopores; Circuit diagram of a suspended graphene membrane on a SiN membrane; Polycrystalline CVD graphene with micrometer-sized grains; DNA translocation under different applied voltage (PDF)

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Notes

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