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High-mobility pentacene thin-film transistor by using $\text{La}_x\text{Ta}_{(1-x)}\text{O}_y$ as gate dielectric

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Abstract

Pentacene organic thin-film transistors (OTFTs) using $\text{La}_x\text{Ta}_{(1-x)}\text{O}_y$ as gate dielectric with different La contents ($x = 0.227, 0.562, 0.764, 0.883$) have been fabricated and compared with those using Ta oxide or La oxide. The OTFT with $\text{La}_{0.764}\text{Ta}_{0.236}\text{O}_y$ can achieve a carrier mobility of $1.21 \text{ cm}^2/\text{V}\cdot\text{s}$, which is about 40 times and two times higher than those of the devices using Ta oxide and La oxide, respectively. As supported by XPS, AFM and noise measurement, the reasons lie in that La incorporation can suppress the formation of oxygen vacancies in Ta oxide, and Ta content can alleviate the hygroscopicity of La oxide, resulting in more passivated and smoother dielectric surface and thus larger pentacene grains, which lead to higher carrier mobility.

Keywords Organic thin-film transistor, high- κ dielectric, La incorporation, TaLaO, oxygen vacancy

1 Introduction

Pentacene organic thin-film transistors (OTFTs) have recently attracted considerable attention because of their promising potential for radio-frequency identification tags (RFID)[1], sensors [2] and large-area flexible displays [3]. Although some OTFTs have shown good device performance comparable to amorphous silicon devices in terms of carrier mobility[1, 4, 5], the operating voltage of the OTFTs is still needed to be further reduced as many electronic devices are battery-powered at just a few volts. However, OTFTs fabricated with SiO₂ require a high operating voltage due to high threshold voltage (> 5 V), and thinning the dielectric film to reduce the operating voltage inevitably enlarges the leakage current. Therefore, a dielectric material with high dielectric constant (i.e. high- κ) is necessary to be employed in order to lower the threshold voltage. Various high- κ materials have been reported for pentacene OTFT and its carrier mobility varies from 2×10^{-2} to $1.4 \text{ cm}^2/\text{V}\cdot\text{s}$ [6-8]. Among them, the mobility of pentacene OTFT on pure Ta₂O₅ varied from 0.03 to $0.32 \text{ cm}^2/\text{V}\cdot\text{s}$, depending on the growth method, surface modification and thickness of the Ta₂O₅ film [9-11], while that on La₂O₃ was reported to reach $0.02 \text{ cm}^2/\text{V}\cdot\text{s}$ [12]. The performance of the OTFT highly depends on the quality of its high- κ gate dielectric, such as surface roughness and trap density. Recently, it has been reported that the addition of La could suppress the oxygen vacancy of the Hf-based high- κ materials[13, 14], and OTFTs using HfLaO as gate dielectric have achieved high performance, such as high carrier mobility, low threshold voltage and small sub-threshold swing (SS) [15].

Ta₂O₅ has high κ value (~ 26), but it has small band gap ($\sim 4.4 \text{ eV}$) and suffers from a high concentration of oxygen vacancies, whose gap states lead to charge trapping[16, 17]. On the other hand, La₂O₃ with larger band gap ($\sim 6 \text{ eV}$) is very hygroscopic and unstable in air, forming hydroxide[18]. In this study, La_xTa_(1-x)O_y films with different La contents are prepared by RF sputtering and proposed as the gate dielectric of pentacene OTFT in an attempt to combine the advantages of La₂O₃ and Ta₂O₅ while suppressing their undesirable properties. By using La_xTa_(1-x)O_y with proper La content (76.4% La) as gate dielectric, high-performance OTFT with high carrier mobility and low threshold voltage can be obtained.

2 Experimental details

Initially, silicon wafers (n-type, <100>, resistivity of $0.5 \sim 1 \Omega\cdot\text{cm}$) were cleaned according to the standard RCA method and dipped in 2 % hydrofluoric acid to remove the native oxide. Then, La_xTa_(1-x)O_y (with $x = 0, 0.227, 0.562, 0.764, 0.883, \text{ and } 1$) films were deposited by sputtering in Ar/O₂ (24:6 sccm) ambient. A radio-frequency sputterer (Denton Vacuum LLC Discovery 635) as well as Ta and La₂O₃ targets were employed to prepare the La_xTa_(1-x)O_y films at different sputtering powers. After that, the samples were annealed in O₂ at 400 °C for 30 min, with a flow rate of 1000 sccm. Next, 30-nm pentacene (99 %, purchased from Sigma-Aldrich without purification) was evaporated on the dielectrics by an evaporator (Edwards Auto 306) in high vacuum (4×10^{-6} torr) at a deposition rate of 1.3 nm/min, monitored by a quartz-crystal oscillator. Lastly, gold was deposited on the pentacene film by evaporation through a shadow mask to form the drain and source electrodes of the OTFTs. The width and length of the channel on the shadow mask were 200 μm and 30 μm , respectively.

The electrical characteristics and low-frequency noise (LFN) of the transistors were measured by HP 4145B semiconductor parameter analyzer, Berkeley Technology Associates Model 9603 FET noise analyzer and HP 35665A dynamic signal analyzer. Al/high- κ dielectric/heavily-doped Si MOS structure was also fabricated by lithography for measuring the dielectric capacitance with HP 4284A precision LCR meter. All the measurements were conducted at room temperature, in air, and under shielded environment. The thickness of the dielectric films was measured by a Wvase 32 ellipsometer. A Veeco multi-mode scanning probe microscope was employed to record the surface morphologies of the dielectrics and pentacene films. The physical characteristics of the high- κ dielectric films were characterized by X-ray photoelectron spectroscopy (XPS). The binding energies were corrected for the sample charging effect with reference to the C 1s peak at 284.5 eV.

3 Results and discussions

3.1 XPS of the gate dielectrics

Fig. 1 shows the XPS spectrum of O 1s for La_xTa_(1-x)O_y films with different La contents. It is clear that when the La content increases, more La-O-H bonds are formed in the La_xTa_(1-x)O_y film due to the hygroscopicity of the La oxide. Fig. 2(a) shows that the binding energy of Ta 4f is shifted to lower value with the increase of the La content, which should be caused by the La incorporation suppressing the oxygen vacancy in the Ta oxide. It is widely accepted that the oxygen vacancy is positively charged and generates an electric field [19-21], thus leading to binding-energy shift [22]. The binding energy increases when an atom is placed near a positive charge [22]. Hence, reducing oxygen vacancies in the Ta oxide and thus the positive charges around the Ta atoms via the addition of La can result in a shift of Ta 4f peak toward lower binding energy. On the other hand, incorporating Ta into La oxide can generate more positive oxygen vacancies, resulting in higher binding energy for La 3d. In Fig. 2(b), the XPS spectra of La 3d in La_xTa_(1-x)O_y with $1-x = 0.236, 0.438 \text{ and } 0.773$ are shifted to higher binding energy with the increase of Ta content. However, by comparing the spectra of La 3d for the La_xTa_(1-x)O_y oxides with $1-x = 0.116 \text{ and } 0.236$, it is found that the binding-energy shift of La 3d behaves differently at low Ta content. With increasing Ta content and decreasing the amount of La,

the formation of La-O-H bond due to water absorption is suppressed, causing a shift of the La 3d peak to lower binding energy as the La-O-H bond has higher binding energy than the La-O bond [23]. In addition, it has been reported that La incorporation into Hf-based high- κ dielectrics can suppress the formation of oxygen vacancy due to the decrease of local dielectric constant around the La atom[13]. It is highly possible that the same mechanism can be applied to the La-doped Ta oxide because La₂O₃ also has lower κ value than Ta₂O₅.

3.2 AFM images of the pentacene films

Fig. 3 shows the AFM image of 30-nm pentacene film deposited on La_xTa_(1-x)O_y film with different La contents. The pentacene grain size first increases with the La content, and then decreases slightly after the La content reaches $x = 0.764$. Sample D has the most uniform-sized and largest grains among the samples. The pentacene grain size on the La-doped Ta oxide or La oxide (see Fig.3(b)-(f)) is much larger than that on the Ta oxide (see Fig. 3 (a)). The main reason should be that adding La into Ta oxide reduces its traps caused by the oxygen vacancies, which can act as nucleation sites for the growth of pentacene grains on the oxide surface [24-27]. With fewer nucleation sites on the surfaces of the La-doped Ta oxide and La₂O₃, larger pentacene grains can be formed on these oxide films.

3.3 Electrical properties of the OTFTs

The sub-threshold swing (SS) is defined as

$$SS = \frac{dV_G}{d(\log I_D)}, \quad (1)$$

where I_D is the drain current, and V_G the gate voltage.

Assuming that both the density of deep bulk trap and density of deep interface trap are independent of trap energy, the sub-threshold swing can be written as [28]

$$SS = \frac{kT \ln 10}{e} \left[1 + \frac{e}{C_{ox}} N_t \right]. \quad (2)$$

where N_t is the density of deep bulk trap and interface trap per unit area and unit energy, C_{ox} the insulator capacitance per unit area, k the Boltzmann's constant, e the electron charge and T the temperature in Kelvin. Therefore, the trap density can be extracted from measured SS value using equation (2). The transfer characteristics of the OTFTs are shown in Fig. 4. The key parameters of the OTFTs are extracted and summarized in Table 1.

The sample D exhibits the highest carrier mobility of 1.21 cm²/V·s, which is about forty times and two times higher than the devices using Ta oxide and La oxide respectively. The carrier mobility first increases with the La content and then decreases when the La content reaches $x = 0.764$. The highest carrier mobility of the sample D (with $x = 0.764$) should be attributed to its most uniform-sized and largest pentacene grains (see Fig. 3), resulted from its smoother dielectric surface (smaller RMS roughness in Table 1) and best suppression of oxygen vacancy in the dielectric (smallest SS in Table 1) induced by the La incorporation. However, La easily absorbs water moisture and then roughens the surface of the gate dielectric. While the samples B, C and D basically have the same RMS roughness, the samples E and F have much rougher oxide surface and thus stronger carrier scattering at the pentacene/gate-oxide interface, thereby lower carrier mobility.

It is reported that the carrier mobility is dependent on the thickness of the gate dielectric[29]. Therefore, in order to better compare the performance of the devices with different gate-dielectric thicknesses, μC_{ox} which is directly related to $2I_D L / W(V_G - V_T)^2$, is employed to characterize the devices with threshold voltage of V_T . As listed in Table 1, μC_{ox} of the samples D and E is much better than those of the samples A and F. This reveals that La_xTa_(1-x)O_y with $x = 0.764$ or 0.883 can greatly improve the gate control property of the device.

All the OTFTs show good field-effect property and saturation behavior, and can operate at a very low voltage of less than 5 V as shown in Fig. 5. Among all the studied devices, sample D has the largest drain current at $V_G = -5$ V because of its highest carrier mobility.

3.4 LFN of the OTFTs

The LFN of OTFT is very sensitive to carrier-number fluctuation in the conduction channel and therefore is used to study the traps located at the interface between pentacene and gate dielectric [30]. The LFN spectrum of the devices in Fig. 6 reveals that La incorporation in Ta oxide can reduce the LFN and thus the traps. Moreover, with optimal La incorporation, the sample D exhibits the lowest LFN, further supporting its lowest trap density among the devices.

4 Conclusion

In sum, pentacene OTFTs using $\text{La}_x\text{Ta}_{(1-x)}\text{O}_y$ as gate dielectric with different La contents ($x = 0.227, 0.562, 0.764, 0.883$) have been fabricated and compared with their counterparts based on Ta oxide or La oxide. The dielectric films were deposited using co-sputtering of Ta and La_2O_3 targets in Ar/O_2 ambient by varying the sputtering powers of the targets. The binding-energy shift of the XPS spectra reveals that the La incorporation can suppress the formation of oxygen vacancies in Ta oxide, thus reducing the nucleation sites at the oxide surface to obtain larger pentacene grains and hence less grain-boundary-induced carrier scattering in the conduction channel. On the other hand, the incorporation of Ta into La oxide can alleviate the hygroscopicity of La oxide, resulting in a smoother dielectric surface, which can also give rise to larger pentacene grains and less surface-roughness scattering. Among the devices, the OTFT with $\text{La}_{0.764}\text{Ta}_{0.236}\text{O}_y$ as gate dielectric can achieve the highest carrier mobility of $1.21 \text{ cm}^2/\text{V}\cdot\text{s}$, which is about 40 times and two times higher than the devices using Ta oxide and La oxide respectively. AFM confirms that largest grains are formed on this oxide film with a smooth surface. The smallest SS and lowest LFN achieved by this OTFT further support that this device has the best interface quality between the pentacene film and the oxide film.

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Figure Caption

Fig. 1 XPS spectrum of O 1s for $\text{La}_x\text{Ta}_{(1-x)}\text{O}_y$ with $x = 0.227$ (a), 0.562 (b), 0.764 (c), and 0.883 (d).

Fig. 2 XPS spectra of Ta 4f (a) and La 3d (b).

Fig. 3 AFM image ($2 \times 2 \mu\text{m}^2$) of pentacene film (30 nm) on $\text{La}_x\text{Ta}_{(1-x)}\text{O}_y$ with $x =$ (a) 0 for sample A, (b) 0.227 for sample B, (c) 0.562 for sample C, (d) 0.764 for sample D, (e) 0.883 for sample E and (f) 1 for sample F. The average grain size for samples A, B, C, D, E and F is $1.14 \times 10^4 \text{ nm}^2$, $2.14 \times 10^4 \text{ nm}^2$, $4.21 \times 10^4 \text{ nm}^2$, $5.60 \times 10^4 \text{ nm}^2$, $5.01 \times 10^4 \text{ nm}^2$, $4.34 \times 10^4 \text{ nm}^2$, respectively.

Fig. 4 Transfer characteristics of the OTFTs with different La contents.

Fig. 5 Output characteristics of the OTFTs with different La contents.

Fig. 6 Low-frequency noise spectrum of the OTFTs with different La contents.

Fig. 1

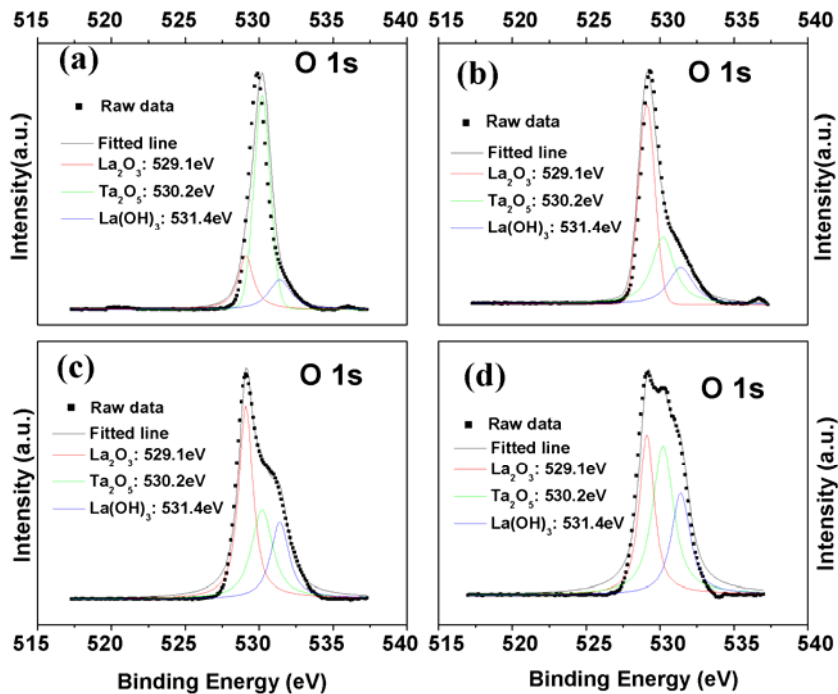


Fig. 2

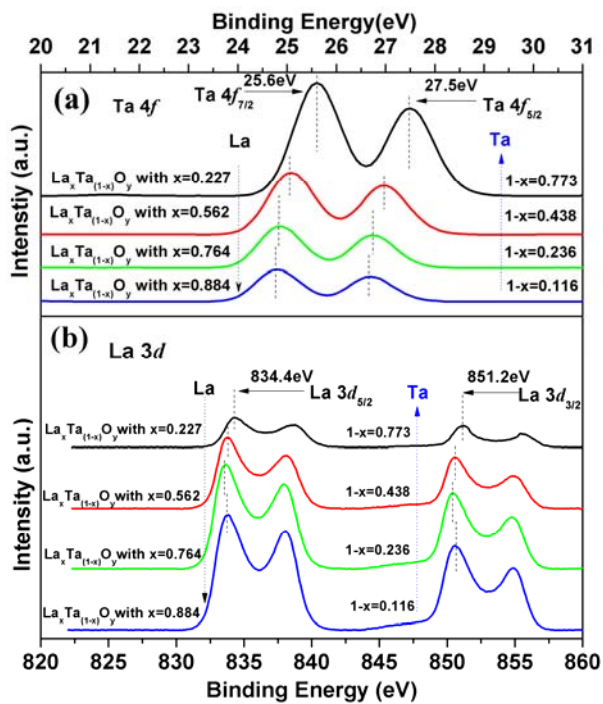


Fig. 3

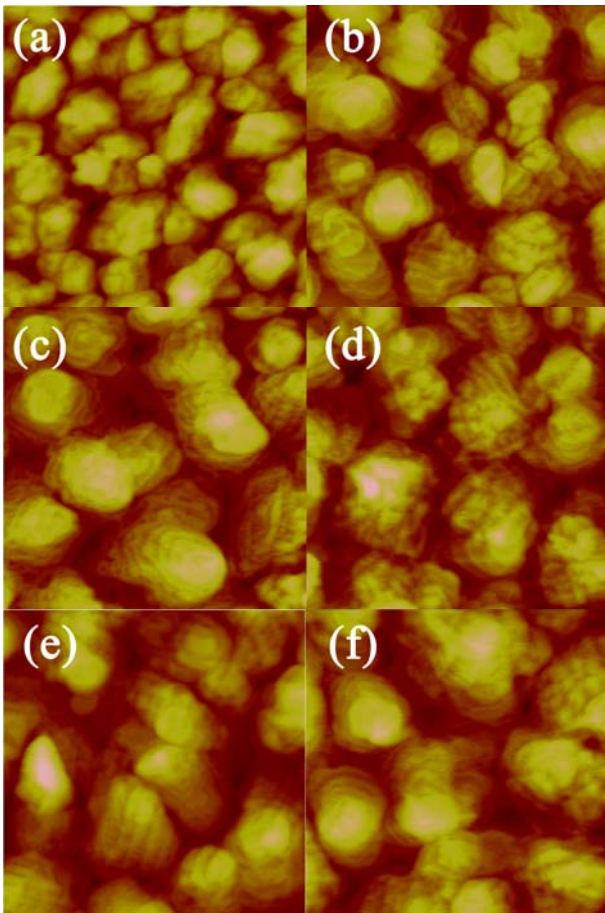


Fig. 4

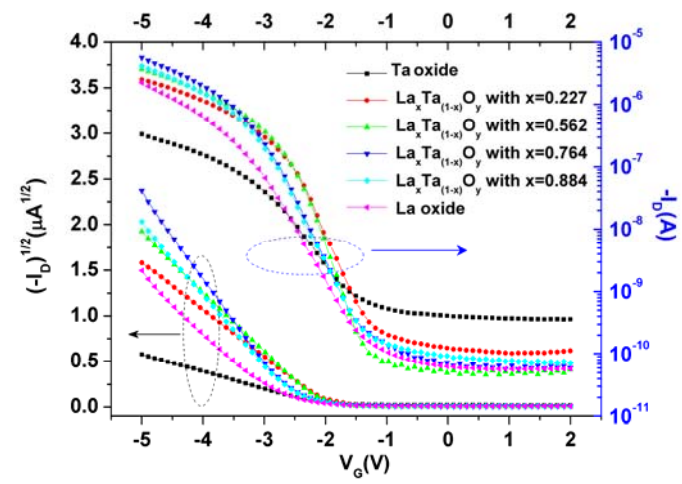


Fig. 5

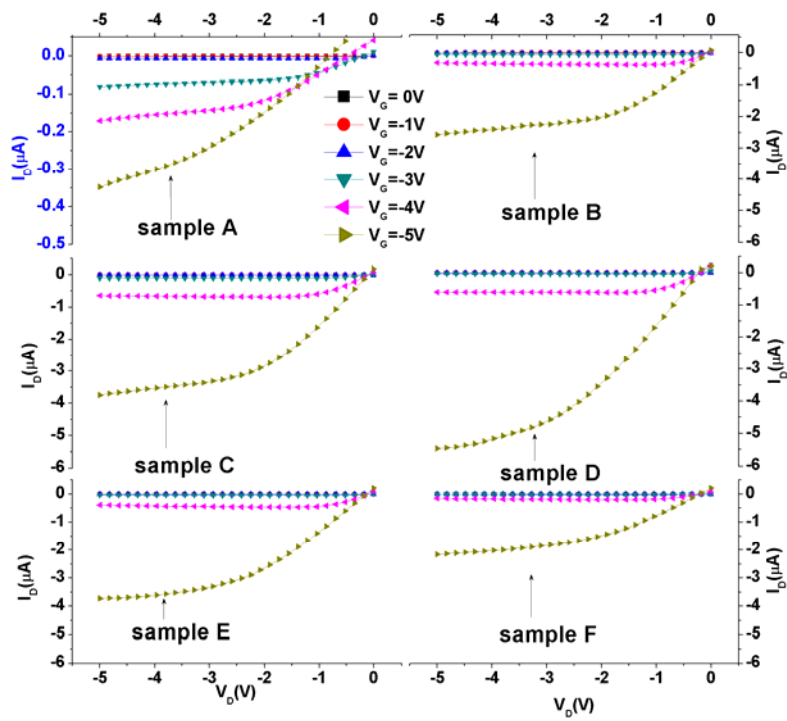


Fig. 6

