



Title	Estimation of optical power and heat-dissipation coefficient for the photo-electro-thermal theory for LED systems
Author(s)	Chen, HT; Tao, XH; Hui, SYR
Citation	IEEE Transactions on Power Electronics, 2012, v. 27 n. 4, p. 2176-2183
Issued Date	2012
URL	http://hdl.handle.net/10722/164085
Rights	IEEE Transactions on Power Electronics. Copyright © IEEE.

Estimation of Optical Power and Heat-Dissipation Coefficient for the Photo-Electro-Thermal Theory for LED Systems

Huanting T. Chen, Xuehui H. Tao, and S. Y. Ron Hui, *Fellow, IEEE*

Abstract—With the use of wall-plug efficiency, estimation techniques for the optical power and heat-dissipation coefficient of LEDs are introduced in this paper to enrich the photo-electro-thermal theory, which has provided a framework for analyzing LED systems. The estimation methods consist of simple procedures for optical and electrical power measurements, which are easy for LED device manufacturers and system designers to follow. The extended theory has been tested with several types of LED devices, with reasonably good agreements between theoretical and practical results.

Index Terms—Light-emitting diodes, optical characteristics, thermal design.

I. INTRODUCTION

IN response to demands for high brightness, the driving power of LED packages has been continuously increasing. In high-power GaN-based LEDs, the current density can be as high as 200 A/cm². The light output of an LED package reaches a maximum level, at a certain current density, and then begins to decrease as current density increases. The effect of luminous flux reduction with increasing current has been largely studied and linked to several mechanisms, such as current leakage, by tunneling of electrons to the states of InGaN/GaN interfaces [1], the effects of auger recombination [2] and of built-in piezoelectric fields [3]. The interactions of photometric, electrical, and thermal aspects have been described mathematically in the photo-electro-thermal (PET) theory [4]–[6] for LED systems. The PET theory can be used to optimize the design of an LED system and determine the operating point of maximum luminous flux per watt. It can also be used to set criteria of the optimal thermal design of the appropriate heatsink for a given application. In addition, junction temperature is a critical parameter

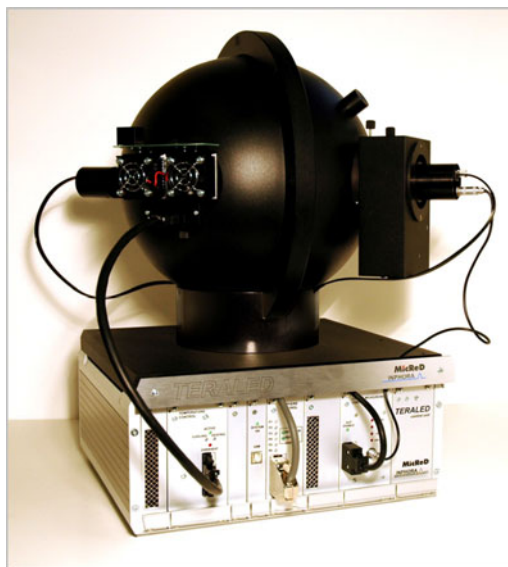


Fig. 1. Photograph of a TeraLED transient thermal tester.

and affects luminous efficacy, maximum light output, and reliability [7]–[11]. However, it is impossible to directly measure the junction temperature due to encapsulation. Many research teams have reported measurements of the junction temperature of the LED by voltage–temperature dependence [12], micro-Raman spectroscopy [13], electroluminescence [14], thermography [15], and a noncontact method [16]. However, most measurement methods require complex equipment setups for precise junction temperature. In [5], the heat-dissipation coefficient of the LED is measured by submerging the LED into silicon oil. Such a measurement method provides accurate thermal measurements, but at the expense of a long experiment time period, because each steady-state measurement may take almost 3 h to obtain (i.e., thermal measurements can only be taken after the whole tank of silicon oil reaches its steady-state temperature).

In this paper, the PET theory is extended with the use of wall-plug efficiency as a function of current and temperature to determine the optical power and the heat-dissipation coefficient that are required in the theory. A fast measurement procedure consisting of a simple optical and electrical power measurement based on the use of the TeraLED transient thermal tester (T3ster system) (see Fig. 1) is illustrated. Based on this procedure, the optical power and the heat-dissipation coefficient of the LED can be estimated. The parameters obtained in this fast procedure are applied to the original PET theory to predict the optical power,

Manuscript received April 24, 2011; revised July 12, 2011; accepted August 11, 2011. Date of current version February 20, 2012. This work was supported by the Hong Kong Research Grant Council under Research Grant HKU-114411. Recommended for publication by Associate Editor J. M. Alonso.

H. T. Chen is with The University of Hong Kong, Hong Kong, and also with the Department of Physics and Electronic Information Engineering, Zhangzhou Normal University, Zhangzhou 363000, China (e-mail: htchen23@yahoo.cn).

X. H. Tao is with the Department of Electronic Engineering, City University of Hong Kong, Kowloon, Hong Kong (e-mail: xuehuitao2@student.cityu.edu.hk).

S. Y. R. Hui is with The University of Hong Kong, Kowloon, Hong Kong, and also with Imperial College London, London SW7 2AZ, U.K. (e-mail: ronhui@eee.hku.hk).

Color versions of one or more of the figures in this paper are available online at <http://ieeexplore.ieee.org>.

Digital Object Identifier 10.1109/TPEL.2011.2165736

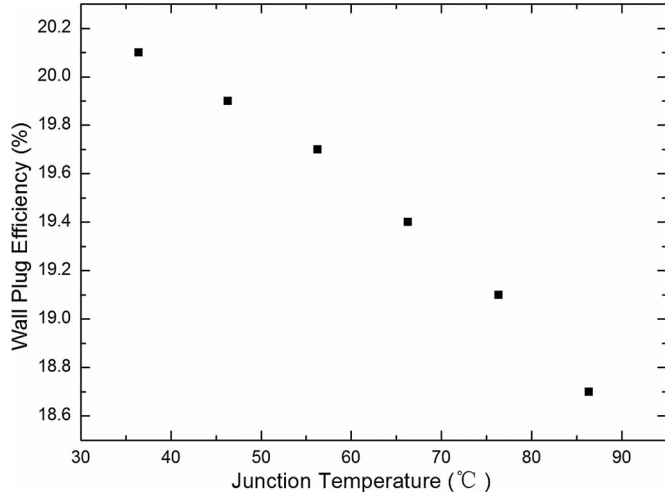


Fig. 2. Relation of wall-plug efficiency versus junction temperature under “constant electrical power” operation.

heat-dissipation coefficients, and internal junction temperature that cannot be easily accessed in practice.

II. WALL-PLUG EFFICIENCY, OPTICAL POWER, AND HEAT-DISSIPATION COEFFICIENT FOR THE PET THEORY

A. Procedure for Extracting the Parameters for Wall-Plug Efficiency and Optical Power

Let P_{opt} be the optical power of an LED device

$$P_{\text{opt}} = \eta_W P_d \quad (1)$$

where η_W is the wall-plug efficiency and P_d is the electrical power of the LED.

Normally, the wall-plug efficiency depends on the junction temperature and the injection current [3]. The relationship is reflected in the junction temperature T_j and wall-plug efficiency η_W of the LED (model number CREE XREWHT-L1-0000-007F5) with the constant electrical power P_{d0} as shown in Fig. 2. These practical measurements in Fig. 2 are obtained with the use of a TeraLED T3ster system that provides practical measurements of the internal junction temperature of the LED device. The TeraLED T3ster system enables the temperature of the mounting plate (on which the LED device is placed) to be controlled. In the practical operating range, the relationship of the wall-plug efficiency η_W as a function of the junction temperature T_j for constant LED power P_{d0} operation is fairly linear and can, therefore, be approximated as a linear relationship

$$\eta_W(T_j, P_{d0}) = \alpha T_j + \beta \quad (2)$$

where α is a constant representing the slope and β is another constant. Both α and β can be obtained from the measurements in Fig. 2.

To establish the dependence of η_W on the LED current, the LED is operated in the pulse width of 300 μs and duty cycle of 0.03% in order to eliminate joule heating dependence on the efficiency. Using the T3ster system, the practical measurements

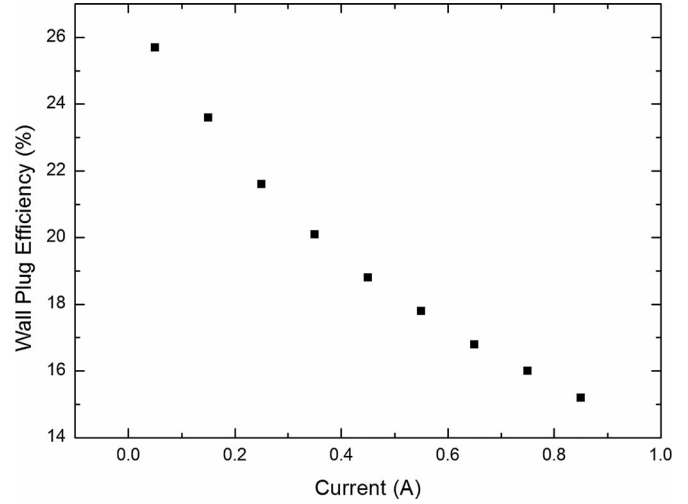


Fig. 3. Relation of wall-plug efficiency versus current under “constant junction temperature” operation.

of the η_W as a function of the LED current are obtained and shown in Fig. 3. It can be seen that η_W decreases as the injection current rises. Normally, the optical power–current curve can be divided into a nonlinear and a linear region [17]. In the nonlinear part, the optical power increases approximately quadratically with the current due to domination of nonradiative recombination. As the current increases, radiative recombination starts to dominate, and the optical power becomes linear with current. In practice, the electrical power P_d is approximately linearly proportional to the injection current at constant junction temperature T_{j0} , and therefore, η_W can be obtained as a quadratic polynomial function of P_d

$$\eta_W(T_{j0}, P_d) = \chi P_d^2 + \delta P_d + \gamma \quad (3)$$

where χ , δ , and γ are the constants that can be extracted from Fig. 3 with constant electrical power.

Based on the aforementioned analysis, η_W can be expressed in terms of P_d and T_j using a 2-D mathematical function. Similar modeling method based on the 2-D linear function for the forward voltage, which relates the junction temperature to the injection current, has been proposed in [18]. In this paper, the function of η_W is constructed as the following equation:

$$\eta_W(T_j, P_d) = \frac{(\alpha T_j + \beta)(\chi P_d^2 + \delta P_d + \gamma)}{\mu} \quad (4)$$

where μ is the intersection value of functions of (2) and (3), and is the value of η_W at point (T_{j0}, P_{d0}) [19], [20].

Based on (1) and (4), the optical power can be expressed as

$$P_{\text{opt}}(T_j, P_d) = \eta_W P_d = \frac{(\alpha T_j + \beta)(\chi P_d^3 + \delta P_d^2 + \gamma P_d)}{\mu} \quad (5)$$

It should be noted that this extended theory can estimate the optical power of the LED at any junction temperature and electrical power. Equation (5) links the optical power to electrical power and the junction temperature together.

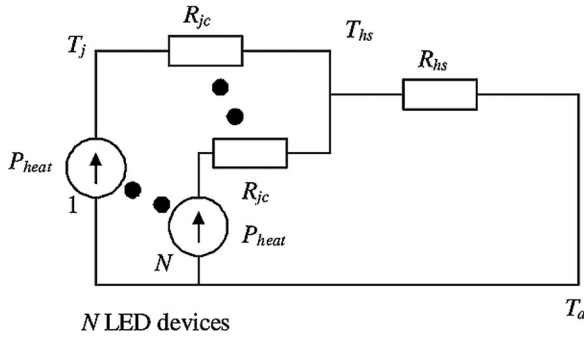


Fig. 4. Simplified dynamic thermal equivalent circuit of N LEDs mounted on the same heatsink.

B. Relationship of the Heat-Dissipation Coefficient and the Wall-Plug Efficiency

The heat-dissipation coefficient k_h in the original PET theory is an indication of the portion of the total input LED power that will be dissipated as heat. It can be expressed as

$$k_h = \frac{P_{\text{heat}}}{P_d} = \frac{P_d - P_{\text{opt}}}{P_d} = 1 - \eta_W. \quad (6)$$

Since η_W can be obtained from (4) based on the parameters extracted from the two-test procedures (i.e., constant-power and constant-junction-temperature tests) described previously, k_h can be calculated with the knowledge of η_W using (6). Therefore, this two-test procedure provides a fast way to determine k_h , provided that sophisticated equipment such as TeraLED T3ster system is available.

III. EXTENSION TO THE ORIGINAL PET THEORY

A. Conventional Calculation Method

Under steady-state conditions, the thermal model of an LED system with N number of LED devices mounted on the same heatsink is shown in Fig. 4. Based on (6), the steady-state heatsink temperature T_{hs} and the internal junction temperature T_j can be expressed as

$$\begin{aligned} T_{\text{hs}} &= T_a + R_{\text{hs}}(NP_{\text{heat}}) = T_a + NR_{\text{hs}}P_d k_h \\ &= T_a + NR_{\text{hs}}P_d(1 - \eta_W) \end{aligned} \quad (7)$$

$$\begin{aligned} T_j &= T_{\text{hs}} + R_{\text{jc}}P_{\text{heat}} = T_a + (R_{\text{jc}} + NR_{\text{hs}})P_d k_h \\ &= T_{\text{hs}} + R_{\text{jc}}(P_d - P_{\text{opt}}) \\ &= T_a + (R_{\text{jc}} + NR_{\text{hs}})P_d(1 - \eta_W) \end{aligned} \quad (8)$$

where T_{hs} is the heatsink temperature, T_a is the ambient temperature, R_{hs} is the thermal resistance of the heatsink, R_{jc} is the thermal resistance of the LED, and P_{heat} is the heat dissipation of the LED.

The total luminous flux ϕ_v of an LED system consisting of N LED devices can be expressed as

$$\phi_v = N \times E \times P_d. \quad (9)$$

The luminous efficacy E can be approximated as

$$\begin{aligned} E &= E_0 [1 + k_e(T_j - T_0)] \\ &= E_0 [1 + k_e(T_a - T_0) + k_e k_h (R_{\text{jc}} + NR_{\text{hs}})P_d] \\ &= E_0 [1 + k_e(T_a - T_0) + k_e(1 - \eta_w)(R_{\text{jc}} + NR_{\text{hs}})P_d] \\ &= E_0 \left\{ 1 + k_e(T_a - T_0) \right. \\ &\quad \left. + k_e \left[1 - \frac{(\alpha T_j + \beta)(\chi P_d^2 + \delta P_d + \gamma)}{\mu} \right] (R_{\text{jc}} + NR_{\text{hs}})P_d \right\} \end{aligned} \quad (10)$$

where E_0 is the rated efficacy at the rated temperature T_0 (typically 25 °C in some LED data sheets) and k_e is the relative rate of reduction of luminous efficacy with increasing junction temperature and can be found in the data sheet.

For $\phi_v > 0$, the total luminous flux ϕ_v is

$$\begin{aligned} \phi_v &= NE P_d \\ &= NE_0 \{ [1 + k_e(T_a - T_0)]P_d + k_e k_h (R_{\text{jc}} + NR_{\text{hs}})P_d^2 \} \\ &= NE_0 \left\{ [1 + k_e(T_a - T_0)]P_d \right. \\ &\quad \left. + k_e \left[1 - \frac{(\alpha T_j + \beta)(\chi P_d^2 + \delta P_d + \gamma)}{\mu} \right] (R_{\text{jc}} + NR_{\text{hs}})P_d^2 \right\}. \end{aligned} \quad (11)$$

Because k_e is negative and less than 1, (11) is in the form of $\phi_v = \alpha_1 P_d - \alpha_2 P_d^2$ where α_1 and α_2 are two positive coefficients. η_w decreases with an increasing P_d ; k_h increases as P_d rises. As P_d is increased from zero, ϕ_v increases almost linearly because the second term is negligible when P_d is small. As P_d increases, the second negative term, which is proportional to the square of P_d , will become increasingly dominant and will reduce ϕ_v significantly.

Combining (4) and (7), T_j can be determined as

$$T_j = \frac{(R_{\text{jc}} + NR_{\text{hs}})[P_d - \beta'(\chi P_d^3 + \delta P_d^2 + \gamma P_d)] + T_a}{1 + \alpha'(R_{\text{jc}} + NR_{\text{hs}})(\chi P_d^3 + \delta P_d^2 + \gamma P_d)} \quad (12)$$

where $\alpha' = \alpha/\mu$ and $\beta' = \beta/\mu$. The coefficients in (12) can be determined through the optical and electrical power measurement procedures explained previously, and R_{jc} and R_{hs} can be obtained from the data sheets. With the coefficients and device parameters obtained, LED system designers can predict the internal junction temperature of the LED at any electrical power using (12). However, it should be noted that the junction temperature has to be measured during the calibration for α and β .

B. Fast Calculation Method

In order to simplify the calculation method, one can take advantage of the special conditions adopted in the two-test procedure described in Section 2. Combining (8) with (2), the wall-plug efficiency η_W at constant electrical power can be expressed

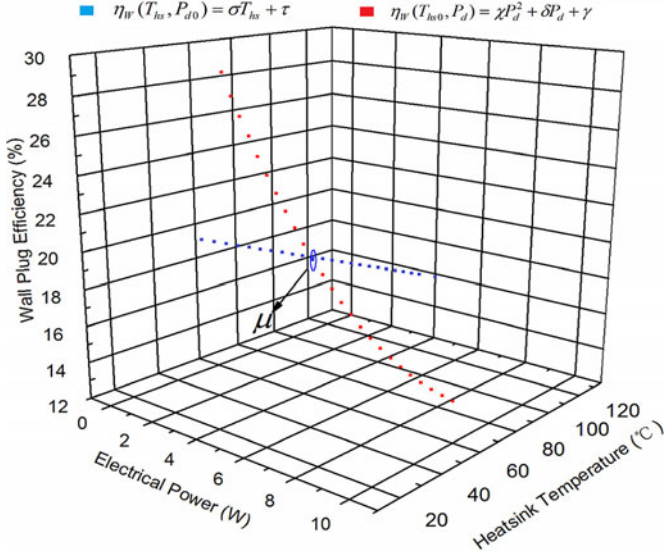


Fig. 5. Wall-plug efficiency of LED as a 2-D function of electrical power and heatsink temperature.

as

$$\eta_W(T_j, P_{d0}) = \alpha T_j + \beta = \alpha[T_{hs} + R_{jc}P_{d0}(1 - \eta_W)] + \beta$$

$$\eta_W(T_j, P_{d0}) = \frac{\alpha(T_{hs} + R_{jc}P_{d0}) + \beta}{1 + \alpha P_{d0}R_{jc}}. \quad (13)$$

With constant electrical power P_{d0} , the only variable parameter of (13) is T_{hs} and other items can be assumed constant coefficients. So the specific form of $\eta_W(T_j, P_{d0})$ at constant power can be rearranged in the following form

$$\eta_W(T_{hs}, P_{d0}) = \sigma T_{hs} + \tau. \quad (14)$$

On the other hand, the heatsink temperature remains constant if the LED is placed on a mounting plate with a fixed heatsink temperature T_{hs0} . So (3) can be expressed as

$$\eta_W(T_{hs0}, P_d) = \chi P_d^2 + \delta P_d + \gamma. \quad (15)$$

Now, the general form of the wall-plug efficiency is

$$\eta_W(T_{hs}, P_d) = \frac{(\sigma T_{hs} + \tau)(\chi P_d^2 + \delta P_d + \gamma)}{\mu}. \quad (16)$$

It is important to note that while (16) has the same mathematical form as (4), (16) is expressed as a function of T_{hs} that is easy to measure and (4) as a function of T_j that is not easy to obtain. As shown in Fig. 5, μ is the intersection value of function for (14) and (15), meaning that μ corresponds to the value of η_W at point (T_{hs0}, P_{d0}) .

The optical power equation remains the same as (5)

$$P_{opt}(T_{hs}, P_d) = (\sigma' T_{hs} + \tau')(\chi P_d^3 + \delta P_d^2 + \gamma P_d) \quad (17)$$

where $\sigma' = \sigma/\mu$ and $\tau' = \tau/\mu$.

The luminous flux is now expressed as

$$\phi_v = NE P_d$$

$$= NE_0 \{ [1 + k_e(T_a - T_0)] P_d + k_e k_h (R_{jc} + NR_{hs}) P_d^2 \}$$

$$= NE_0 \{ [1 + k_e(T_a - T_0)] P_d + k_e [1 - (\sigma' T_{hs} + \tau')] (\chi P_d^2 + \delta P_d + \gamma) (R_{jc} + NR_{hs}) P_d^2 \}. \quad (18)$$

The junction temperature equation is now expressed as

$$T_j = R_{jc} [P_d - (\sigma' T_{hs} + \tau') (\chi P_d^3 + \delta P_d^2 + \gamma P_d)] + T_{hs}. \quad (19)$$

Equations (16)–(19) provide the general equations for the wall-plug efficiency, optical power, luminous flux, and junction temperature, respectively, for an LED system. All the parameters and variables on the right-hand side of these equations are either known or measurable. These equations form the tool for designing and optimizing LED systems with measurable parameters and variables.

It should be noted that the aforementioned analysis assumes that all LEDs are identical in the LED system. In general, this is a common practice in public lighting (i.e., not decorative applications that require color changes) to use the same type of white LEDs in one system in order to avoid differences in color temperature and the binning systems among LED manufacturers. However, if the LED devices are not identical, the aforementioned theory can be extended to incorporate the use of nonidentical LEDs in an LED system by modifying (18) into

$$\sum_{m=1}^N \phi_{v-m} = \sum_{m=1}^N E_m P_{d-m} \quad (20)$$

where ϕ_{v-m} is the luminous flux, E_m is the luminous efficacy, and P_{d-m} is the power of the m th LED; N is the total number of LEDs in the system. A full analysis of an LED system based on nonidentical LEDs will be presented in the future.

IV. EXPERIMENT VERIFICATIONS FOR THE EXTENDED PET THEORY

The samples under test are mounted to a Peltier-cooled fixture that is attached to an integrating sphere in accordance with the recommendations of CIE. The Peltier-cooled fixture is used to stabilize the LED temperature for the optical measurements and it also serves as an actively temperature-controlled cold plate for thermal measurements [21]. Optical measurements of LED samples are made under thermal and electrical steady-state conditions with the TeraLED system. Once all optical measurements have been performed, the LED is switched OFF and the cooling transient of LED package is monitored with the use of the transient thermal tester (T3Ster). Besides the combined thermal and optical measurements, the temperature dependence of all parameters of the LED (such as the temperature dependence of the optical power, luminous flux, chromaticity coordinates, and wall-plug efficiency) can also be measured and recorded. The theoretical framework of evaluation by the T3Ster system is based on the distribution RC networks [22]. The T3Ster system captures the thermal transient response in real time, records the cooling/heating curve and then evaluates the cooling/heating curve so as to derive the thermal characteristics [23]. For the calibration of temperature sensitive parameters, a small current of 5 mA is applied in the temperature range of 25–55 °C with

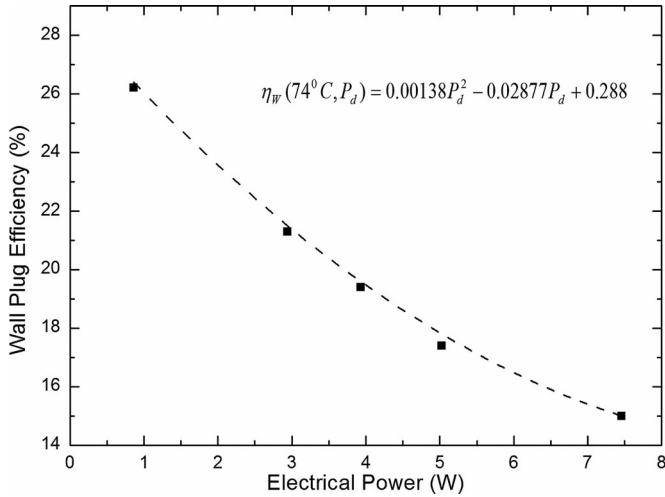


Fig. 6. Measured wall-plug efficiency versus electrical power of Sharp 4.4 W LED at constant heatsink temperature.

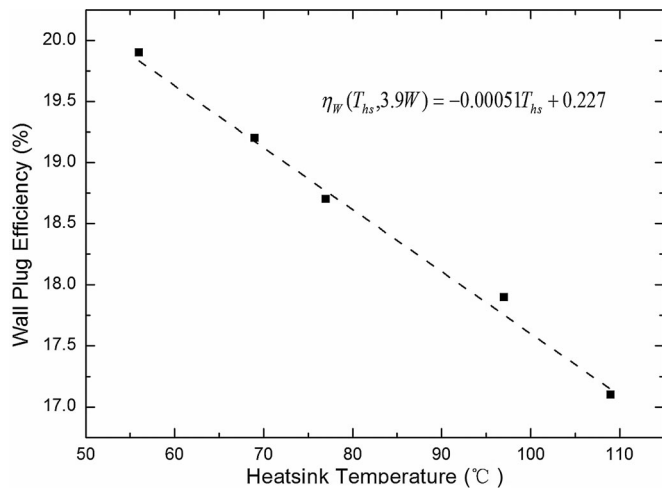


Fig. 7. Measured wall-plug efficiency versus heatsink temperature of Sharp 4.4 W LED at constant electrical power.

an increment of 10 °C. The light output and transient thermal curve are measured after driving the LED with the current for 20 min with the heatsink temperature kept constant.

A. Test on Sharp 4.4 W LED (Model Number: GW5BNC15L02)

One Sharp 4.4 W LED is mounted on the heatsink with a thermal resistance of 7.8 °C/W. The optical power and wall-plug efficiency are measured at different electrical power levels. The parameters required for (16) can be determined using curve-fitting technique in Figs. 6 and 7 as $\sigma = -0.00051$, $\tau = 0.227$, $\chi = 0.00138$, $\delta = -0.02877$, $\gamma = 0.288$, and $\mu = 0.187$. The wall-plug efficiency and optical power follow the forms of (16) and (17), respectively. It should be noted that δ is a negative coefficient, and χ and γ are the positive coefficients. γ is roughly a factor 200 larger than χ , meaning that the χP_d^3 term is relatively insignificant with low value of P_d . Based on these parameters, the theoretical and measured optical power curves are recorded and shown in Fig. 8. As P_d is increased from zero, P_{opt} in-

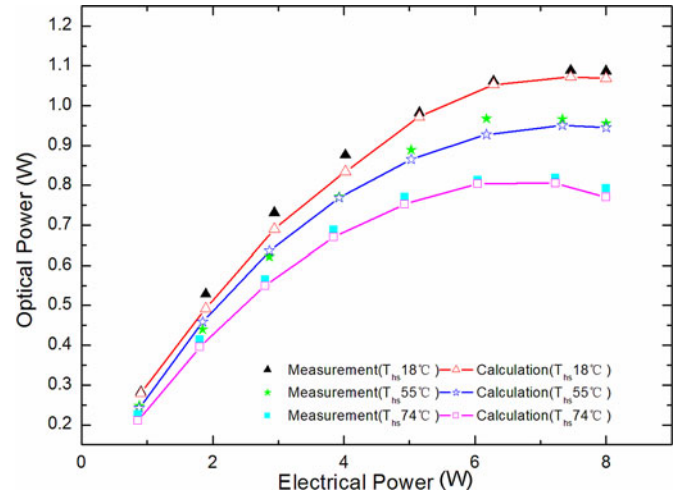


Fig. 8. Calculated and measured optical power versus electrical power for Sharp 4.4 W LED with different heatsink temperature.

creases almost linearly when P_d is small. As P_d continues to increase, the negative item δP_d^2 will reduce P_{opt} significantly. After reaching the maximum optical power, P_{opt} will drop with an increasing P_d . The optical power function is approximately a parabola and, therefore, has a maximum optical power P_d^* . This P_d^* will shift to lower value with an increasing heatsink temperature or heat dissipation, which indicates the dependence of the operating point P_d^* of the LED array systems on the junction temperature. The theoretical and measured optical power curves in Fig. 8 agree reasonably well for a set of heatsink temperature values.

The measured and calculated heat-dissipation coefficient k_h values are shown in Fig. 9. The theoretical curves of k_h are in good agreement with the measured ones. It is important to note that at a controlled heatsink temperature of 18 °C, k_h is about 0.76. When the heatsink temperature is 74 °C, k_h increases to 0.86. This practical result highlights the important fact that, even with the same LED power, k_h increases with increasing operating temperature. The thermal designs of LED systems are, therefore, critical to the photometric performance.

By substituting the calculated k_h and the parameters of $k_e = -0.0027$, $E_0 = 80$ lm/W, $R_{jc} = 6.5$ °C/W, $T_0 = 25$ °C, $T_a = 18$ °C, and $N = 1$ into (18), the theoretical luminous flux can be determined, plotted, and compared with practical measurements as shown in Fig. 10 for a range of heatsink temperature. The theoretical curves of optical power (see Fig. 8) and luminous flux (see Fig. 10) are generally in good agreement with the measured ones particularly within the rated power range. The general shapes of these curves have been explained with the PET theory [4].

Measured junction temperatures are recorded and compared with theoretical values calculated with the use of (19) for an LED device with a thermal resistance R_{jc} between the junction and the case of 6.5 °C/W, which is considered as a constant value with electrical power and heatsink temperature. As shown in Fig. 11, the agreement between measured and calculated results is reasonably good. It is noted that junction temperature

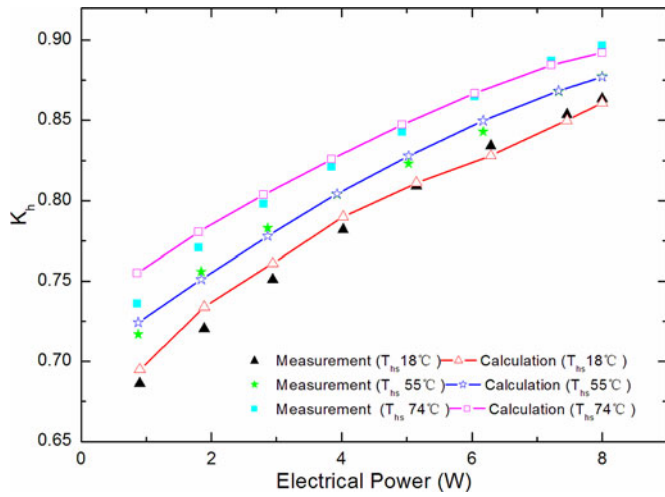


Fig. 9. Calculated and measured k_h versus electrical power for a Sharp 4.4 W LED with different heatsink temperature.

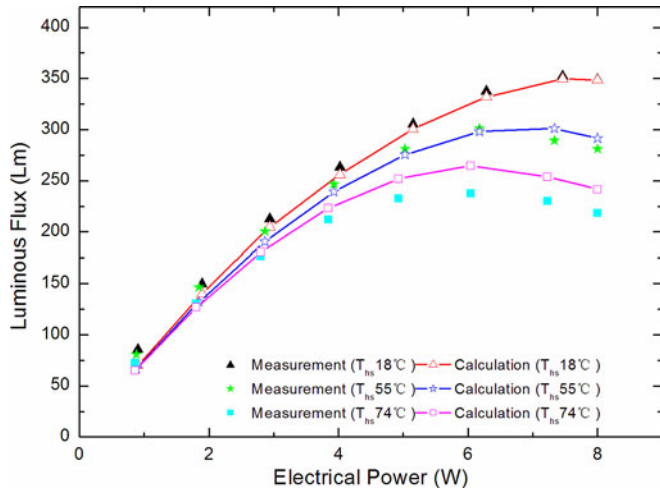


Fig. 10. Calculated and measured luminous flux versus electrical power for Sharp 4.4 W LED with different heatsink temperature.

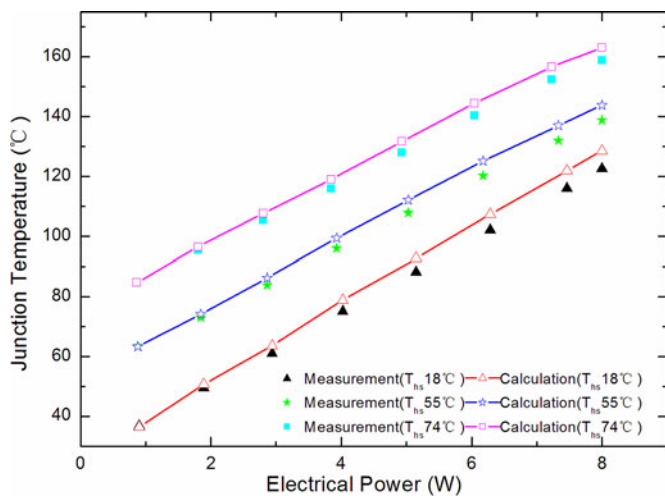


Fig. 11. Calculated and measured junction temperature versus electrical power for Sharp 4.4 W LED with different heatsink temperature.

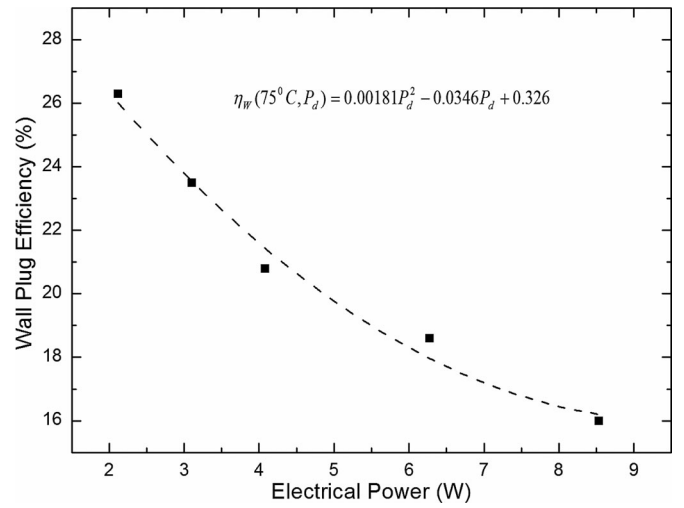


Fig. 12. Measured wall-plug efficiency versus electrical power of Sharp 8 W LED at constant heatsink temperature.

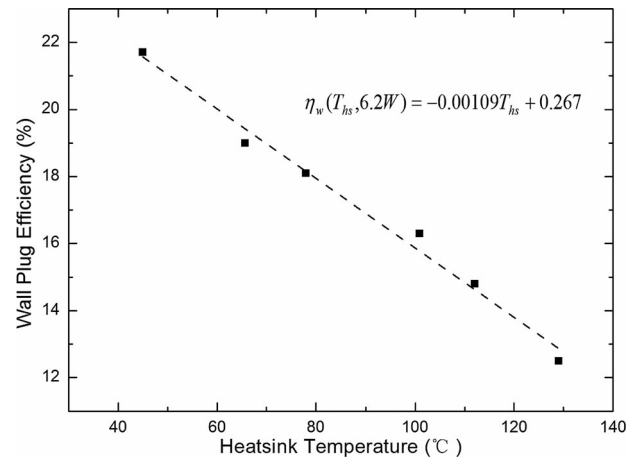


Fig. 13. Measured wall-plug efficiency versus heatsink temperature of Sharp 8 W LED at constant electrical power.

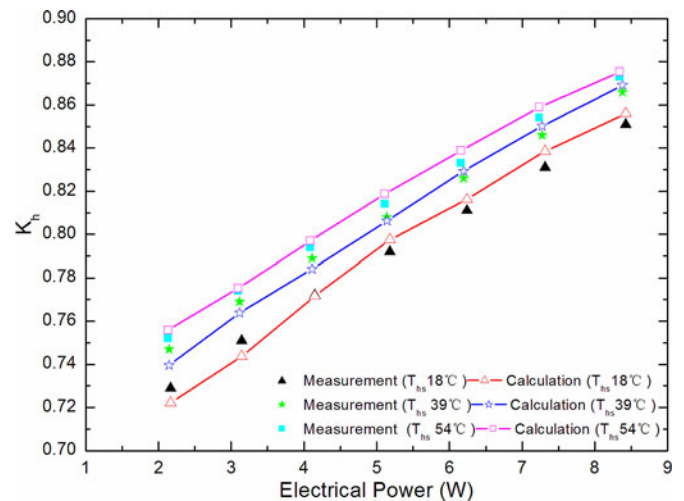


Fig. 14. Calculated and measured k_h versus electrical power for Sharp 8 W LED with different heatsink temperature.

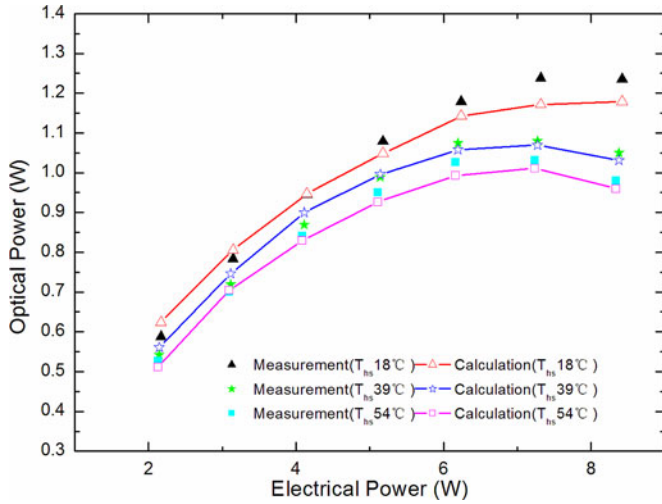


Fig. 15. Calculated and measured optical power versus electrical power for Sharp 8 W LED with different heatsink temperature.

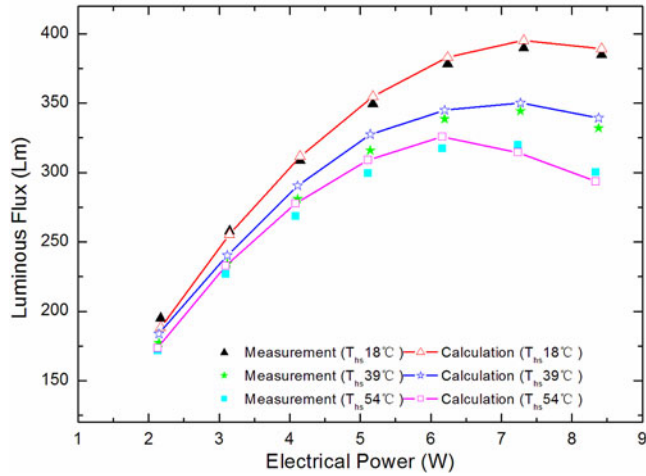


Fig. 16. Calculated and measured luminous flux versus electrical power for Sharp 8 W LED with different heatsink temperature.

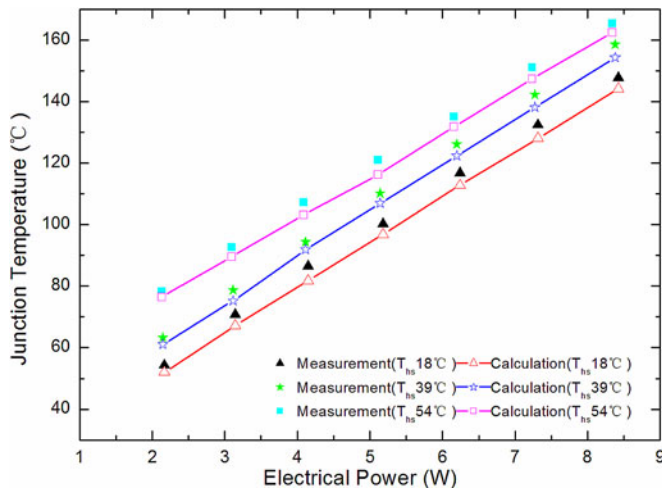


Fig. 17. Calculated and measured junction temperature versus electrical power for Sharp 8 W LED mounted on a heatsink.

prediction by application designer would be largely simplified if the device coefficients and thermal resistance were known in advance. Some device manufacturers provide typical experimental relationships of the wall-plug efficiency with electrical power and heatsink temperature. Inclusion of the coefficients for these functions (2) and (3) in the data sheets by LED device manufacturers could, therefore, be helpful device information for application designers.

B. Test on Sharp 8 W LED (Model number: GW5BWF15L00)

The extended PET theory is also tested with the use of Sharp LED devices. One Sharp 8 W LED device is mounted on the heatsink with a thermal resistance of $9.3 \text{ }^\circ\text{C/W}$ for practical evaluation. The optical power and wall-plug efficiency are measured at different electrical power. The parameters required for (16) can be determined using fitting measurement data with Figs. 12 and 13. Here, $\sigma = -0.00109$, $\tau = 0.267$, $\chi = 0.0018$, $\delta = -0.0346$, $\gamma = 0.326$, and $\mu = 0.180$. The equations for the wall-plug efficiency and optical power follow the form of (16) and (17). The measured and calculated k_h parameters are shown in Fig. 14. The theoretical curves of k_h are in good agreement with the measurements. Substituting the calculated k_h with the parameters of $k_e = -0.0039$, $E_0 = 96 \text{ lm/W}$, $R_{jc} = 6 \text{ }^\circ\text{C/W}$, $T_0 = 25 \text{ }^\circ\text{C}$, $T_a = 18 \text{ }^\circ\text{C}$, $N = 1$ into (18), the luminous flux can be determined.

The measured optical power and luminous flux for LED are shown with their respective calculated values in Figs. 15 and 16. The measured junction temperature and their theoretical values calculated with (19) for this LED with thermal resistance R_{jc} of $6 \text{ }^\circ\text{C/W}$ are shown in Fig. 17. The reasonably good agreements between these measured and calculated values confirm the validity of the proposed estimation method for the optical power and heat-dissipation coefficient.

V. CONCLUSION

An estimation method for the optical power and heat-dissipation coefficient of LED devices based on the wall-plug efficiency is proposed in this paper. The proposal consists of a practical procedure for the required optical and electrical power measurements. The parameters obtained in this fast procedure are applied to the original PET theory to predict the optical power, heat-dissipation coefficients, and internal junction temperature that cannot be easily accessed in practice. It is found that the heat-dissipation coefficient increases with junction temperature even when the LED power consumption remains constant. The estimation method presented in this paper extends the original PET theory to cover optical power, wall-plug efficiency, and the determination of the heat-dissipation coefficient. It is envisaged that the extended theory can be used as a design tool for LED system designs. LED manufacturers are encouraged to include more information such as heat-dissipation coefficient as a function of operating temperature in the data sheets as basic parameters for LED system designs.

REFERENCES

- [1] N. I. Bochkareva, E. A. Zhirnov, A. A. Efremov, Y. T. Rebane, R. I. Gorbunov, and Y. G. Shreter, "Tunnel-recombination currents and electroluminescence efficiency in InGaN/GaN LEDs," *Semiconductors*, vol. 39, no. 5, pp. 594–599, May 2005.
- [2] Y. C. Shen, G. O. Mueller, S. Watanabe, N. F. Gardner, A. Munkholm, and M. R. Krames, "Auger recombination in InGaN measured by photoluminescence," *Appl. Phys. Lett.*, vol. 91, no. 14, pp. 141101-1–141101-3, Oct. 2007.
- [3] M. H. Kim, M. F. Schubert, Q. Dai, J. K. Kim, E. F. Schubert, J. Piprek, and Y. Park, "Origin of efficiency droop in GaN-based light-emitting diodes," *Appl. Phys. Lett.*, vol. 91, no. 18, pp. 183507-1–183507-3, Oct. 2007.
- [4] S. Y. R. Hui and Y. X. Qin, "A general photo-electro-thermal theory for light-emitting-diode (LED) systems," *IEEE Trans. Power Electron.*, vol. 24, no. 8, pp. 1967–1976, Aug. 2009.
- [5] Y. X. Qin, D. Y. Lin, and S. Y. R. Hui, "A simple method for comparative study on the thermal performance of light emitting diodes (LED) and fluorescent lamps," *IEEE Trans. Power Electron.*, vol. 24, no. 7, pp. 1811–1818, Jul. 2009.
- [6] Y. X. Qin and S. Y. R. Hui, "Comparative study on the structural designs of LED devices and systems based on the general photo-electro-thermal theory," *IEEE Trans. Power Electron.*, vol. 25, no. 2, pp. 507–513, Feb. 2010.
- [7] H. T. Chen, Y. J. Lu, Y. L. Gao, H. B. Zhang, and Z. Chen, "The performance of compact thermal models for LED package," *Thermochim. Acta.*, vol. 488, no. 1, pp. 33–38, May 2009.
- [8] S. Y. R. Hui, S. N. Li, X. H. Tao, W. Chen, and W. M. Ng, "A novel passive offline LED driver with long lifetime," *IEEE Trans. Power Electron.*, vol. 25, no. 10, pp. 2665–2672, Oct. 2010.
- [9] Q. C. Hu and R. Zane, "LED driver circuit with series-input-connected converter cells operating in continuous conduction mode," *IEEE Trans. Power Electron.*, vol. 25, no. 3, pp. 574–582, Mar. 2010.
- [10] N. Chen and H. S. H. Chung, "A driving technology for retrofit LED lamp for fluorescent lighting fixtures with electronic ballasts," *IEEE Trans. Power Electron.*, vol. 26, no. 2, pp. 588–601, Feb. 2011.
- [11] X. H. Qu, S. C. Wong, and C. K. Tse, "Resonance-assisted buck converter for offline driving of power LED replacement lamps," *IEEE Trans. Power Electron.*, vol. 26, no. 2, pp. 532–540, Feb. 2011.
- [12] A. Keppens, W. R. Ryckaert, G. Deconinck, and P. Hanselaer, "High power light-emitting diode junction temperature determination from current-voltage characteristics," *J. Appl. Phys.*, vol. 104, no. 9, pp. 093104-1–093104-8, Nov. 2008.
- [13] S. Todoroki, M. Sawai, and K. Aiki, "Temperature distribution along the striped active region in high-power GaAlAs visible lasers," *J. Appl. Phys.*, vol. 58, no. 3, pp. 1124–1128, Aug. 1985.
- [14] P. W. Epperlein and G. L. Bona, "Influence of the vertical structure on the mirror facet temperatures of visible GaInP quantum well lasers," *Appl. Phys. Lett.*, vol. 62, no. 24, pp. 3074–3076, Jun. 1993.
- [15] C. C. Lee and J. Park, "Temperature measurement of visible light-emitting diodes using nematic liquid crystal thermography with laser illumination," *IEEE Photon. Technol. Lett.*, vol. 16, no. 7, pp. 1706–1708, Jul. 2004.
- [16] Y. Gu and N. Narendran, "A non-contact method for determining junction temperature of phosphor-converted white LEDs," in *Proc. SPIE*, 2004, vol. 5187, pp. 107–114.
- [17] Y. Deshayes, L. Bechou, F. Verdier, and Y. Danto, "Long-term reliability prediction of 935 nm LEDs using failure laws and low acceleration factor ageing tests," *Quality Rel. Eng. Int.*, vol. 21, no. 6, pp. 571–594, Oct. 2005.
- [18] Y. J. Lee, C. J. Lee, and C. H. Chen, "Determination of junction temperature in InGaN and AlGaInP light-emitting diodes," *IEEE J. Quantum Electron.*, vol. 46, no. 10, pp. 1450–1455, Oct. 2010.
- [19] F. W. Carroll, "A polynomial in each variable separately is a polynomial," *Amer. Math. Monthly*, vol. 68, no. 1, pp. 42–43, Jan. 1961.
- [20] A. Keppens, *Modelling and Evaluation of High-Power Light-Emitting Diodes for General Lighting*. Belgium: Katholieke Universiteit Leuven, 2010.
- [21] A. Poppe, G. Farkas, V. Székely, G. Horváth, and M. Rencz, "Multi-domain simulation and measurement of power LED-s and power LED assemblies," in *Proc. 22nd Annu. IEEE Semicond. Thermal Meas. Manag. Symp.*, Dallas, TX, Mar. 2006, pp. 191–198.
- [22] G. Farkas, Q. V. V. Vader, A. Poppe, and G. Bognár, "Thermal investigation of high power optical devices by transient testing," *IEEE Trans. Compon., Packag., Technol.*, vol. 28, no. 1, pp. 45–50, Mar. 2005.
- [23] V. Székely, A. Poppe, M. Rencz, M. Rosental, and T. Teszéri, "Therman: A thermal simulation tool for IC chips, microstructures and PW boards," *Microelectron. Rel.*, vol. 40, no. 3, pp. 517–524, Mar. 2000.



Huangting T. Chen was born in Zhangzhou, China, in 1982. He received the B.S. degree in physics from Zhangzhou Normal University, Zhangzhou, China, in 2005, and the Ph.D. degree in radio physics from Xiamen University, Xiamen, China, in 2010. As a joint Ph.D student, he studied in the Light & Lighting Laboratory, Catholic University College Ghent, Ghent, Belgium, during November 2009–May 2010.

Since September 2010, he has been a Lecturer in the Department of Physics and Electronic Information Engineering, Zhangzhou Normal University.

From January 2011 to January 2012, he was a Senior Research Associate in the Department of Electronic Engineering, City University of Hong Kong, Kowloon, Hong Kong. He is currently a Research Associate at the Department of Electrical & Electronic Engineering at HKU. His research interests include solid-state lighting technology and application.



Xuehui H. Tao was born in China. She received the B.S. degree in electronic science and technology and the M.S. degree in electronic engineering from Southwest Jiaotong University, Chengdu, China. She is currently working toward the Ph.D. degree from the Department of Electronic Engineering, City University of Hong Kong, Kowloon, Hong Kong.

Her current research interests include the design and development of switching-mode power supplies, LED driving circuits, electronic ballasts, and thermal

management of LED and electronic components.



S. Y. Ron Hui (F'03) received the B.Sc.Eng (Hons.) from the University of Birmingham, Birmingham, U.K., in 1984, and the D.I.C. and Ph.D. degrees from Imperial College London, London, U.K., in 1987.

From 1987 to 1990, he was a Lecturer at the University of Nottingham, Nottingham, U.K. In 1990, he joined the University of Technology, Sydney, N.S.W., and was a Senior Lecturer at the University of Sydney, Sydney, in 1992, where he became a Reader in 1995. He joined the City University of Hong Kong (CityU) as a Professor in 1996 where he was promoted to

Chair Professor in 1998. From 2001 to 2004, he served as an Associate Dean of the Faculty of Science and Engineering at CityU. Since July 2011, he has been the Chair Professor at the University of Hong Kong, Kowloon, Hong Kong, and Imperial College London. He has published more than 200 technical papers, including more than 150 refereed journal publications and book chapters. More than 50 of his patents have been adopted by industry.

Dr. Hui is a Fellow of the Institution of Engineering Technology (IET). He has been an Associate Editor of the IEEE TRANSACTIONS ON POWER ELECTRONICS since 1997 and an Associate Editor of the IEEE TRANSACTIONS ON INDUSTRIAL ELECTRONICS since 2007. He was the IEEE Distinguished Lecturer by the IEEE Power Electronics Society in 2004 and 2006. From 2002 to 2010, he served as one of the 18 Administrative Committee members of the IEEE Power Electronics Society and was Chairman of its Constitution and Bylaws Committee. He received the Excellent Teaching Award at CityU in 1998 and the Earth Champion Award in 2008. He won the IEEE Best Paper Award from the IEEE Industry Applications Society Committee on Production and Applications of Light in 2002, and two IEEE Power Electronics Transactions Prize Paper Awards for his publications on Wireless Battery Charging Platform Technology in 2009 and on LED system theory in 2010. His inventions on wireless charging platform technology underpin key dimensions of Qi, the world's first wireless power standard, with freedom of positioning and localized charging features for wireless charging of consumer electronics. In November 2010, he received the IEEE Rudolf Chope R&D Award from the IEEE Industrial Electronics Society, the IET Achievement Medal (The Crompton Medal), and was elected to the Fellowship of the Australian Academy of Technological Sciences and Engineering.