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Author(s)	Zhang, J; Cheung, SW; Yuk, TI
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A Rectangular-shaped Time-delay Line Inspired by CRLH TL Unit Cell for UWB Operation

J. Zhang, S.W. Cheung and T.I. Yuk

Department of Electrical and Electronic Engineering

The University of Hong Kong

Hong Kong, China

zhangjun@eee.hku.hk, swcheung@eee.hku.hk, tiyuk@eee.hku.hk

Abstract—This paper presents the design of a rectangular-shaped ultra-wideband (UWB) time-delay line inspired by the use of composite right/left-handed transmission line (CRLH TL) unit cells. A rotated version of a conventional CRLH TL unit cell is used as the basic elements to achieve UWB operation. For comparison, time-delay lines using the right-handed transmission line (RH TL) and CRLH TL unit cells are also studied, fabricated and tested. Simulation and measurement results show that our proposed time-delay lines have high return loss, low insertion loss, UWB operation and much longer time delays than that of the time-delay line based on RH TL.

Keywords-compact; rectangular-shaped time-delay line; UWB; CRLH TL

I. INTRODUCTION

Microwave time-delay lines find many applications in different areas such as radar and phased array systems and other telecommunication systems. In the design of microwave time-delay lines, the size, return loss and insertion loss are all important design parameters to be concerned.

Metamaterials, also known as left-handed materials (LHMs), have negative permittivity and permeability which are not commonly found in nature. Its concept was first investigated by Veselago in 1968 [1]. Although the properties of LHMs promised for a large diversity of novel applications and devices, LHMs did not attract much attention until it was found that the materials could be realized using a general transmission line (TL) approach [2]. Practical LH TLs also have the right-handed (RH) effects, so LHMs realized using TLs are called composite right/left handed transmission lines (CRLH TLs). CRLH TL can be used to design many different microwave components such as phase shifters, antennas and bandpass filters, etc. Recently, time-delay lines based on CRLH TL have been proposed and studied [3-6]. However, these designs have relatively narrow bandwidths.

In this paper, we study using CRLH TL unit cell [3] as the basic elements to design time-delay lines and the rotated version of the unit cell to design time-delay lines for ultra-wide band (UWB) applications. Long and narrow time-delay lines can not be easily fitted to practical circuits which normally have relatively even widths and lengths. In this paper, we propose to bend the wideband time-delay line to form a rectangular shape so that it can be placed on a rectangular area

of the PCB or used to enclose large circuits on a PCB for easy fitting to practical circuits. Simulation results using CST MWS 2009 and measurement results show that our proposed time-delay lines have UWB operation bandwidths from 0.1–10 GHz, high return loss of more than 15 dB and low insertion loss of less than 1 dB.

II. TIME-DELAY LINE USING TRANSMISSION LINE

For a TL with a length of L , the time for a signal to propagate through is given by:

$$t_{\text{delay}} = \frac{L}{\lambda f} = \frac{L \times \beta}{2\pi f} \quad (1)$$

where f and β are the operating frequency and phase constant, respectively. Thus if a TL is used as a time-delay line, both the length of the transmission line and the phase constant β can be used to adjust the amount of time delay for signals to propagate through. For a fixed time delay, a larger β implies a shorter transmission line is required. Traditional TL, also known as RH TL, has a small phase constant β and so a very long length is needed to produce a large time delay, which leads to a large insertion loss.

III. MODEL OF CRLH TL UNIT CELL

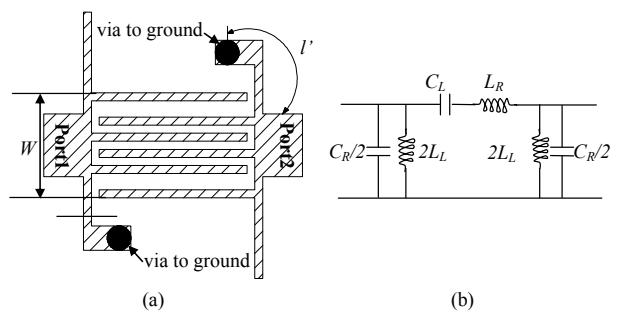


Figure 1 (a) Structure and (b) equivalent circuit of conventional CRLH TL unit cell

The general schematic diagram and the equivalent circuit of a CRLH TL unit cell [3] with six fingers are shown in Fig. 1. In

Fig. 1(b), the series inductance L_R and capacitance C_L are the inductance along the fingers and the coupling capacitance between the fingers, respectively, the shunt inductance L_L is realized by the two fingers each having a via at the ends to the ground, and the shunt capacitance C_R is the stray capacitance of the fingers. The expressions for L_R , C_R , L_L and C_L are, respectively, given by [7,8]:

$$L_R = \frac{Z_0 \sqrt{\epsilon_{re}}}{c} l \quad (2)$$

$$C_R = \frac{\sqrt{\epsilon_{re}}}{Z_0 c} l \quad (3)$$

$$C_L = (\epsilon_r + 1)l \left\{ 4.409(N - 3) \tanh \left[0.55 \left(\frac{h}{W} \right)^{0.45} \right] + 9.92 \tanh \left[0.52 \left(\frac{h}{W} \right)^{0.5} \right] \right\} \times 10^{-12} \quad (4)$$

$$L_L = \frac{\mu_0}{2\pi} \left[h \cdot \ln \left(\frac{h + \sqrt{r^2 + h^2}}{r} \right) + \frac{3}{2} (r - \sqrt{r^2 + h^2}) \right] + \frac{Z_0 \sqrt{\epsilon_{re}}}{c} l' \quad (5)$$

where h is the thickness of the substrate, r is the radius of the ground via, ϵ_r is the relative dielectric constant, ϵ_{re} is the effective dielectric constant, l' is the distance from the ground via to the port, l is the length of the finger, N is the number of the fingers, W is the total width of all the fingers together and Z_0 is the characteristic impedance of each of the fingers. The phase constant β of such CRLH TL unit cell is [9]:

$$\beta = \omega \sqrt{L_R C_R} - \frac{1}{\omega \sqrt{L_L C_L}} \quad (6)$$

(2) - (6) show that the structural parameters L_R , C_R , L_L and C_L determine β and hence can be used to adjust the time delay of the unit cell given in (1).

IV. DESIGN OF TIME-DELAY LINES INSPIRED BY CRLH TL UNIT CELL

A. Narrowband time-delay line

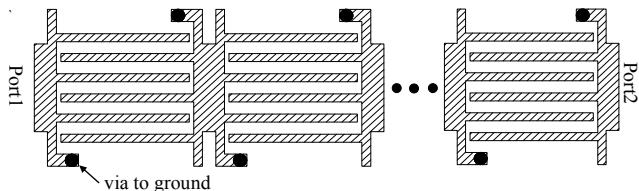


Figure 2 Narrowband time-delay line by cascading conventional CRLH TL unit cells

Since the structural parameters L_R , C_R , L_L and C_L of the CRLH TL unit cell can be used to determine the value of β , we can design a unit cell to maximize the time delay through (1) and use it as the basic elements to design time-delay lines, i.e., cascading a number of the unit cells together, as shown in Fig. 2, to produce a long delay time. However, computer simulation results have shown such time-delay lines have narrow bandwidths.

B. Wideband time-delay line

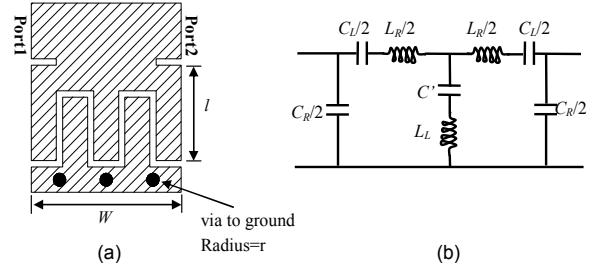


Figure 3 Rotated structure and (b) equivalent circuit of the unit cell

To increase the operation bandwidth of the time-delay line, we propose to rotate the CRLH TL cell of Fig. 1(a) by 90-degree, as shown in Fig. 3(a), with the equivalent circuit shown in Fig. 3(b). Simulation studies have shown that such unit cell has a much wider bandwidth. When this unit cell is used as the basic periodic elements to design time-delay lines with long time delays, we also propose to reduce the number of fingers to five (an odd number) to make the unit cell a symmetrical structure, so that the two S-parameters, S_{11} and S_{21} , of the unit cell are equal. Then when identical unit cells are used in cascade to provide a long time delay, there is no need to perform any matching between the unit cells. The time-delay lines constructed by cascading 16 conventional CRLH TL unit cells and 16 wideband unit cells are shown in Figs. 4(b) and 4(c), respectively.

C. Rectangular-shaped time-delay line

Practical circuits normally occupy areas with relatively even widths and lengths on the PCBs which may not have the space to accommodate a long and narrow time-delay line. Here we propose to bend the wideband time-delay line into a rectangular shape, so that it occupies a rectangular area on a PCB. For a long time-delay line, circuits may also be fabricated inside the rectangular time-delay line. A rectangular-shaped time-delay line using 16 wideband unit cells is shown in Fig. 4(d).

V. RESULTS AND DISCUSSIONS

For the purpose of comparison, we have designed time-delay lines using RH TL, 16 conventional CRLH TL unit cells, 16 wideband unit cells, and 16 wideband unit cells in a rectangular shape, with the layouts shown in Fig. 4. They all have a length of about 60 mm. The designs have been optimized using the EM simulation tool CST MWS 2009. In the optimization process, the width of each finger was set to 0.2 mm which is the smallest dimension we could make in our lab. Results have shown that the length of the fingers in the rotated

unit cells has significant effects on the return loss, so we mainly optimized the length of the fingers. The dimensions of the unit cells were optimized based on the criteria of having the return loss of more than 15 dB and the largest phase constant β . The final optimization results of these designs were used for fabrication of the time-delay lines on a F4B-2 PCB with a thickness of 0.8 mm and permittivity of 2.65, as shown in Fig. 5. The return loss, insertion loss and time delay of all these time-delay lines have been simulated and measured.

Figure 6 shows the simulated and measured return loss and insertion loss of the time-delay lines using the RH TL, the conventional CRLH TL unit cells and the wideband unit cells. The time delays of these time-delay lines are shown in Fig. 7. It can be seen that the simulated and measured results show good agreements. The time-delay line using the RH TL has quite a high return loss as can be seen in Fig. 6(a) and a low insertion loss of less than 1 dB, but the time delay achieved is only 300 ps. The narrowband time-delay line achieves a slightly longer time delay of 1800 ps, about 60 ps (3.5%) longer than that of the wideband time-delay line, but it has a narrower bandwidth of only 4-6 GHz and a larger insertion loss of about 2 dB within the 4-6 GHz pass band as shown in Fig. 6(b).

The return loss and insertion loss for the wideband time-delay line are shown in Fig. 6(c). The measured return loss is more than 15 dB and the insertion loss is less than 1 dB across the whole frequency band from 0.1 – 10 GHz. Figure 7 shows that it has a time delay of 1740 ps across the whole frequency band tested, nearly 6 times longer than that of the RH TL.

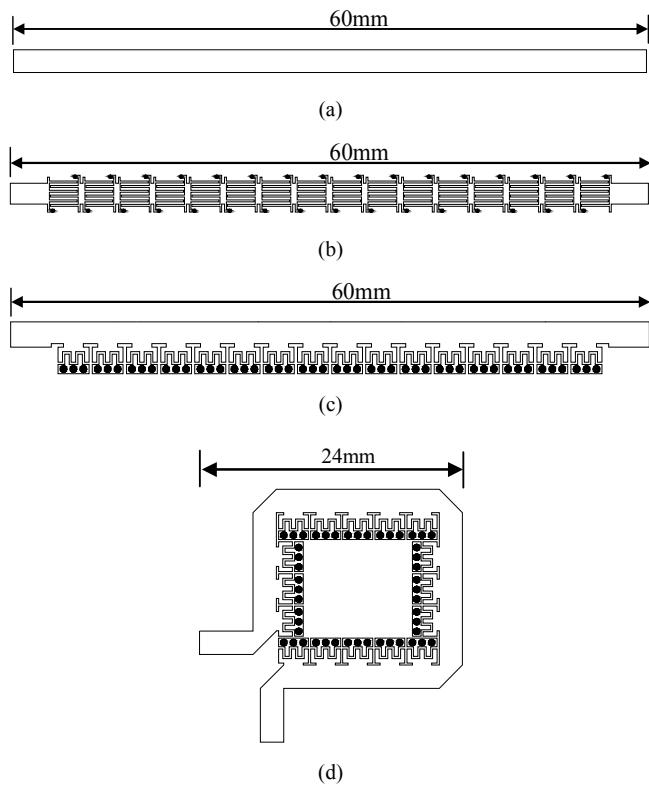
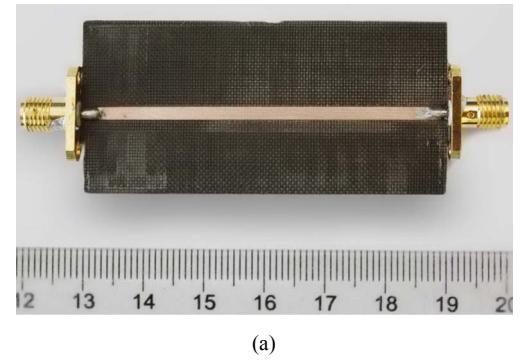
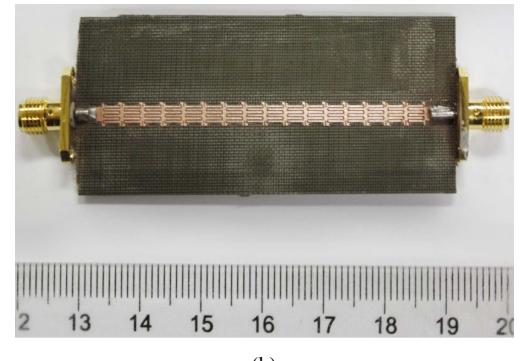


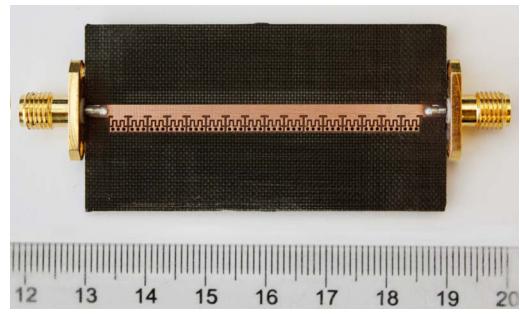
Figure 4 Layouts of time-delay lines using
 (a) RH TL, (b) conventional CRLH TL unit cells, (c) wideband unit cells and
 (d) wideband unit cells in rectangular shape



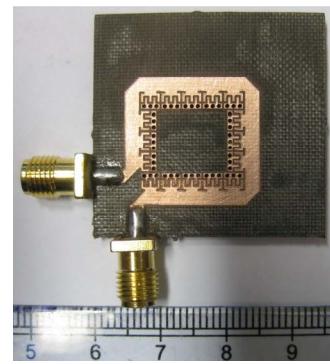
(a)



(b)

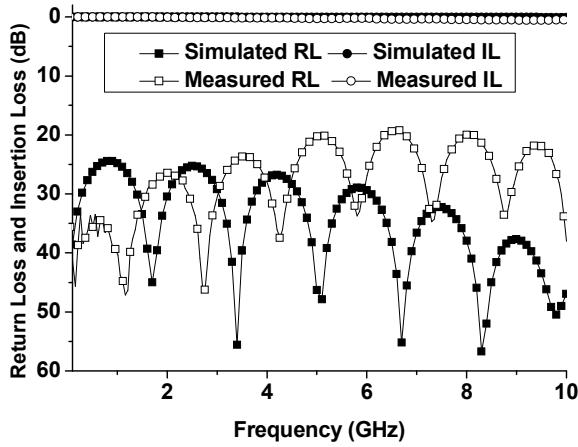


(c)

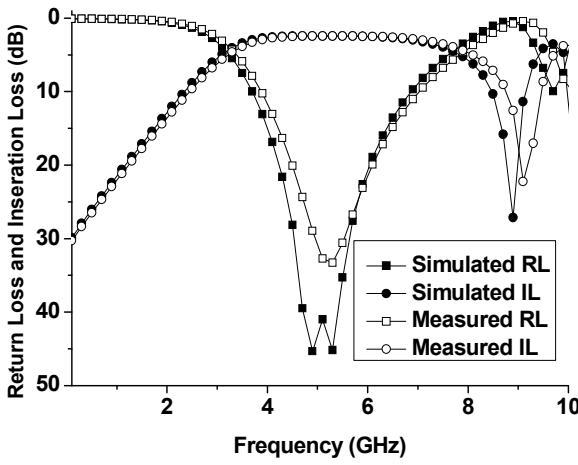


(d)

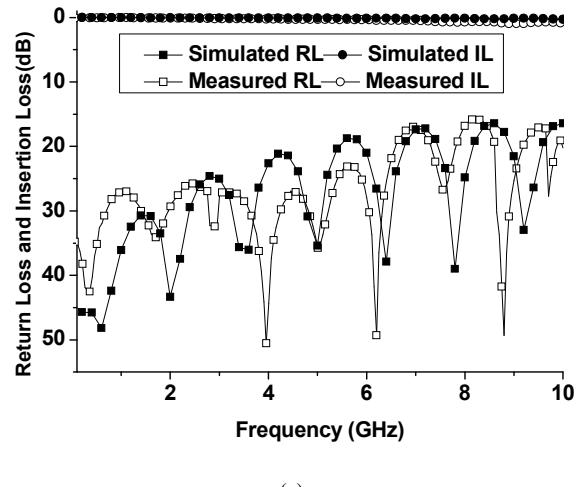
Figure 5 Prototyped time-delay lines using
 (a) RH TL, (b) conventional CRLH TL unit cells, (c) wideband unit cells and
 (d) wideband unit cells in rectangular shape



(a)



(b)



(c)

Figure 6 Simulated and measured return loss and insertion loss of time-delay lines using (a) RH TL, (b) conventional CRLH TL unit cells and (c) wideband unit cells

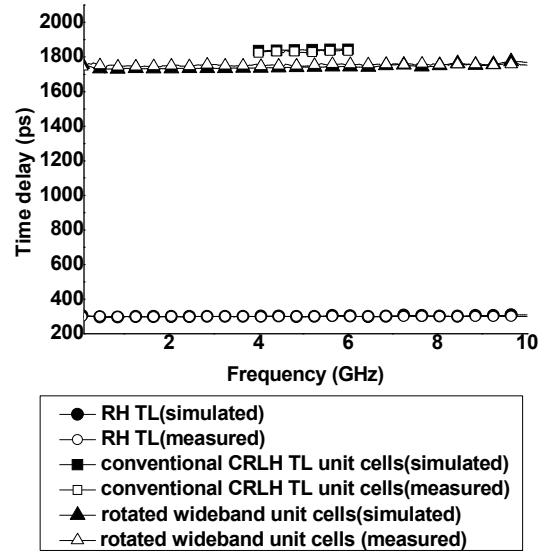


Figure 7 Simulated and measured time delays of different time-delay lines

For the rectangular-shaped wideband time-delay line of Fig. 4(d), the simulated and measured return loss and insertion loss are shown in Figs. 8 and 9, respectively. Again, the simulated and measured results show good agreements. Figure 8 shows that the measured return loss is more than 15 dB and the insertion loss is less than 1 dB across the whole tested frequency band. The time delay is about 1780 ps across the whole frequency band of 0.1 – 10 GHz, about 6 times longer than that of the time-delay line for the RH TL. The time delay is slightly longer than that of the wideband time-delay line. This is because the bending slightly increases the overall length. The results show that the rectangular shape does not change the properties much, but makes the fitting to practical circuits on the PCBs much easier.

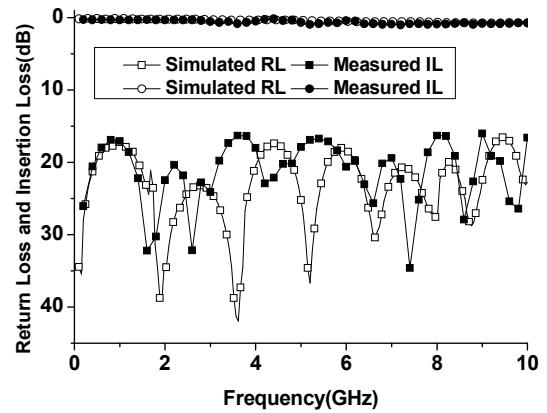


Figure 8 Simulated and measured return loss and insertion loss of time-delay line using rotated wideband unit cells in rectangular shape

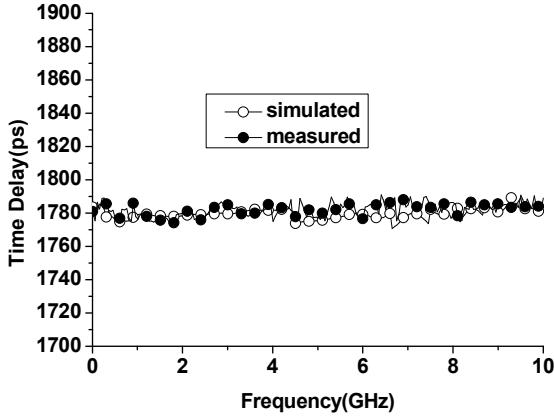


Figure 9 Simulated and measured time delay of time-delay line using rotated wideband unit cells in rectangular shape

VI. CONCLUSIONS

A rectangular-shaped UWB time-delay line constructed by cascading a number of rotated versions of the conventional CRLH TL unit cells has been proposed, studied and fabricated. Three other time-delay lines using the conventional CRLH TL unit cells, wideband unit cells and RH TL have also been designed, optimized and fabricated for comparison. Simulation and measurement results have demonstrated that, for the length of 60 mm, our proposed rectangular-shaped wideband time-delay line has a much longer time delay of 1780 ps, about 6 times longer than that of using the RH TL,

yet having a much wider operating bandwidth of 0.1–10 GHz, a high return loss and a low insertion loss. The time-delay line has a rectangular shape and so can be easily integrated with practical circuits on a PCB.

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