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Modeling of Subharmonics and Chaos in DC Motor Drives

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Abstract—In this paper, the nonlinear dynamics of both voltage-mode and current-mode controlled dc motor drive systems are presented. The investigation is based on the derivation of the discrete mappings that describe their system subharmonics and chaos in the continuous conduction mode of operation. It illustrates that different bifurcation diagrams can be obtained by using different modes of control while varying the same system parameters. A unified modeling approach for the period-1 and hence the period-*p* orbits as well as their stability analysis during both voltage-mode and current-mode of control is proposed and verified.

I. INTRODUCTION

The belief in our common sense that simple systems exhibiting simple behavior is not always true. In fact, the investigation of some simple switching dc-dc converters has revealed their remarkable complex and chaotic behaviors [1]-[5].

The investigation onto chaotic behavior in power electronics circuits was launched in late 1980's. Since linear system theory is ill-suited to investigate the subharmonics and chaotic phenomena occurred in power electronics circuits, the most attractive approach has been the iterative nonlinear mapping. Recently, the two-dimensional mapping has been manipulated to investigate the chaotic behavior of voltage-mode or current-mode controlled dc-dc converters [4],[5]. However, these studies have been focused on the use of linearization such as the constant or compensating ramp current reference in current-mode control. Notice that this current reference is practically variable, depending on the output voltage variation.

On the other hand, the investigation onto chaotic behavior of industrial motor drives is becoming more and more attractive [6]-[9]. The chaotic behavior of PWM inverter-fed induction motor drives has been investigated by using numerical analysis [6]. It has also been attempted to investigate the chaotic behavior of brushless dc motor drives by ignoring the switching effect and approximately transforming into the Lorenz system [7]. Recently, the nonlinear dynamics and chaotic behavior of industrial dc motor drives have been investigated numerically and analytically, without ignoring the switching effect or accepting rough assumptions [8],[9].

It is the purpose of this paper to systematically investigate the nonlinear dynamics of both voltage-mode and currentmode controlled dc motor drives. Based on a dc chopper-fed permanent-magnet (PM) dc motor drive, a unified analytical modeling approach for investigating their subharmonics and chaos as well as their stability analysis will been presented. Computer simulations will be carried out to study these subharmonics and chaotic behavior. Finally, theoretical results will be verified by comparing with those obtained by PSpice simulation.



Fig. 1. Voltage-mode controlled dc motor drive.



Fig. 2. Current-mode controlled dc motor drive.



Fig. 3. Equivalent circuit of current-mode controlled dc motor drive.

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II. MODELING OF DRIVE SYSTEMS

As shown in Figs. 1 and 2, the voltage-mode and currentmode buck-type dc chopper-fed PM dc motor drives operating in the continuous conduction mode are used for exemplification. The equivalent circuit of the current-mode controlled dc motor drive is shown in Fig. 3.

Whatever the dc drive system is controlled by voltagemode or current-mode, it always operates with two topological stages during the continuous conduction mode. Stage 1 is that power switch S is on and diode D is off, and stage 2 is that S is off and D is on. Therefore, the system equation can be divided into two linear differential equations as given by:

• Stage 1 (S is on and D is off):

$$\frac{d}{dt} \begin{pmatrix} \omega(t) \\ i(t) \end{pmatrix} = \begin{pmatrix} -B/J & K_T/J \\ -K_E/L & -R/L \end{pmatrix} \begin{pmatrix} \omega(t) \\ i(t) \end{pmatrix} + \begin{pmatrix} -T_I/J \\ V_{in}/L \end{pmatrix}$$
(1)

• Stage 2 (S is off and D is on):

$$\frac{d}{dt} \begin{pmatrix} \omega(t) \\ i(t) \end{pmatrix} = \begin{pmatrix} -B/J & K_T/J \\ -K_E/L & -R/L \end{pmatrix} \begin{pmatrix} \omega(t) \\ i(t) \end{pmatrix} + \begin{pmatrix} -T_I/J \\ 0 \end{pmatrix} \quad (2)$$

where $\omega(t)$ is the instantaneous speed, i(t) instantaneous armature current, R armature resistance, L armature inductance, V_{in} dc supply voltage, K_E back-EMF constant, K_T torque constant, B viscous damping, J load inertia, and T_l load torque.

By defining the state vector $\mathbf{X}(t)$ and the following matrices \mathbf{A} , \mathbf{E}_{on} , \mathbf{E}_{off} as:

$$\mathbf{A} = \begin{pmatrix} -B/J & K_T/J \\ -K_E/L & -R/L \end{pmatrix}, \quad \mathbf{X}(t) = \begin{pmatrix} \omega(t) \\ i(t) \end{pmatrix}$$
(3)

$$\mathbf{E}_{on} = \begin{pmatrix} -T_l / J \\ V_m / L \end{pmatrix}, \quad \mathbf{E}_{off} = \begin{pmatrix} -T_l / J \\ 0 \end{pmatrix}$$
(4)

the system equation given by (1) and (2) can then be rewritten as:

$$\mathbf{X}(t) = \mathbf{A} \mathbf{X}(t) + \mathbf{E}_{k} \qquad (k = \text{on, off})$$
(5)

Since k is topology dependent, hence time dependent, the system equation given by (5) is in fact a time-varying state equation. Thus, this closed-loop drive system is a second-order non-autonomous dynamical system. The drive system switches from one stage to the other one whenever the control signal $v_c(t)$ is equal to the carrier signal $v_r(t)$.

For a voltage-mode drive system, the control signal $v_c(t)$ can be expressed as:

$$v_c(t) = g(\omega(t) - \omega_{ref})$$
(6)

where ω_{ref} is the reference speed, and g feedback gain. The carrier signal ramp voltage $v_r(t)$ is represented by:

$$v_r(t) = v_1 + (v_u - v_1)t / T$$
(7)

where v_l and v_u are respectively the lower and upper voltages of the ramp signal, and T is its period.

For a current-mode drive system, the control signal $v_c(t)$ can be expressed as:

$$v_c(t) = g_i i(t) - g_\omega (\omega_{ref} - \omega(t))$$
(8)

where g_i and g_{ω} are armature current and voltage feedback gains, respectively. The carrier signal in the interval of clock pulses is zero.

Therefore, the unified condition of both voltage-mode and current-mode drive systems can be represented as:

 $\mathbf{E}_{3}(\mathbf{X}(t) - \mathbf{E}_{4}) = v_{r}(t)$ (9) where $\mathbf{E}_{3} = (g, 0)$ and $v_{r}(t) = v_{l} + (v_{u} - v_{l})t/T$ for the voltage-mode, $\mathbf{E}_{3} = (g, 1)$ with $g = g_{\omega} / g_{i}$ and $v_{r}(t) = 0$ for the current-mode, and $\mathbf{E}_{4} = (\omega_{ref}, 0)$.

III. MODELING OF SUBHARMONICS AND CHAOS

Given the initial conditions $X(t_0)$, the analytical solution of the system equation given by (5) can be expressed as:

$$\mathbf{X}(t) = \Phi(t - t_0) \mathbf{X}(t_0) + \int_{t_0}^{t} \Phi(t - \tau) \mathbf{E}_k d\tau$$

= $-\mathbf{A}^{-1} \mathbf{E}_k + \Phi(t - t_0) (\mathbf{X}(t_0) + \mathbf{A}^{-1} \mathbf{E}_k)$ (10)

where $\Phi(t) = e^{At}$ is so-called the state transition matrix. \mathbf{E}_k is \mathbf{E}_{on} or \mathbf{E}_{off} . The corresponding discrete state vector is denoted as $\mathbf{X}_n = \mathbf{X}(nT)$. The iterative function that maps this vector to its successive one $\mathbf{X}_{n+1} = \mathbf{X}((n+1)T)$ is called the Poincaré map $P: \mathfrak{R}^2 \to \mathfrak{R}^2$ which is defined as:

$$\mathbf{X}_{n+1} = P(\mathbf{X}_n) \tag{11}$$

A. The generalized Poincaré Map for Voltage-Mode Drives

Since the voltage-mode dc drive system operates periodically with T, $\mathbf{X}(t)$ is generally sampled at the beginning of every ramp cycle. Within each T, there are two possible situations – a skipped cycle because of the absence of intersection between v_c and v_r , and an intersected cycle in which there is at least one intersection between v_c and v_r .

For the skipped cycle, S does not change its state, remaining either on or off. The corresponding Poincaré map P can be easily derived from (10) as given by:

$$\mathbf{X}_{n+1} = -\mathbf{A}^{-1}\mathbf{E}_k + \Phi(T)(\mathbf{X}_n + \mathbf{A}^{-1}\mathbf{E}_k) \quad (k = \text{on, off})$$
(12)

For the intersected cycle, S may change *m* times when v_c crosses v_r by m_v times within the same *T*, so-called multiple pulsing. The corresponding intersections occur at:

 $t = nT + \delta_i T$, $0 = \delta_0 < \delta_1 <, \dots, < \delta_m < 1$ $(i = 1, \dots, m_v)$ (13) Hence, the generalized Poincaré map can be expressed as the following iterative form:

$$\begin{cases} \mathbf{X}(nT+\delta_{i}T) = -\mathbf{A}^{-1}\mathbf{E}_{k} + \Phi(\delta_{i}T-\delta_{i-1}T)(\mathbf{X}(nT+\delta_{i-1}T) + \mathbf{A}^{-1}\mathbf{E}_{k}) \\ h(\delta_{i}) = \mathbf{E}_{3}(\mathbf{X}(nT+\delta_{i}T) - \mathbf{E}_{4}) - v_{r}(\delta_{i}T) = 0 \\ i = 1, 2, \cdots, m_{v} \end{cases}$$
(14)

 $\mathbf{X}_{n+1} = -\mathbf{A}^{-1}\mathbf{E}_{k} + \Phi(T - \delta_{m}T)(\mathbf{X}(nT + \delta_{m}T) + \mathbf{A}^{-1}\mathbf{E}_{k}) \quad (15)$ where k is off or on, depending on whether v_{c} is over or under v_{r} .

B. The generalized Poincaré Map for Current-Mode Drives

As shown in Fig. 3, the interval of closing or opening a switch may be longer than a clock cycle under certain conditions. Thus, $\mathbf{X}(t)$ is generally sampled at the beginning of the clock pulse that makes S changing from off to on, instead of every clock cycle. Hence, the drive system always operates in stage 1 first and then in stage 2 for each sampling interval. By defining the intervals of stages 1 and 2 as $\delta_{on}T$ and $\delta_{off}T$, respectively, the interval of the Poincaré map becomes $m_iT=(\delta_{on}+\delta_{off})T$. Thus, $\mathbf{X}(\delta_{on}T)$ and \mathbf{X}_{n+1} can be directly derived from (10) as given by:

$$\begin{cases} \mathbf{X}(\delta_{on}T) = -\mathbf{A}^{-1}\mathbf{E}_{on} + \Phi(\delta_{on}T)(\mathbf{X}_{n} + \mathbf{A}^{-1}\mathbf{E}_{on}) \\ h(\delta_{on}) = \mathbf{E}_{3}(\mathbf{X}(\delta_{on}T) - \mathbf{E}_{4}) = 0 \end{cases}$$
(16)

$$\mathbf{X}_{n+1} = -\mathbf{A}^{-1}\mathbf{E}_{off} + \Phi(m_i T - \delta_{on} T)(\mathbf{X}(\delta_{on} T) + \mathbf{A}^{-1}\mathbf{E}_{off}) \quad (17)$$

On the other hand, m_i can be obtained as the minimum integer which is larger than δ_{on} and fulfills $h(m_i) < 0$. Hence, the Poincaré map can be written as:

$$\mathbf{X}_{n+1} = -\mathbf{A}^{-1}\mathbf{E}_{on} + \Phi(m_i T)(\mathbf{X}_n + \mathbf{A}^{-1}\mathbf{E}_{on}) + (\mathbf{1} - \Phi(m_i T - \delta_{on} T))\mathbf{A}^{-1}(\mathbf{E}_{on} - \mathbf{E}_{off})$$
(18)



Fig. 4. Chaotic waveforms of v_{ω} and v_i against clock pulse.

C. Modeling of Periodic Orbits

The above generalized Poincaré map of the voltage-mode dc drive system is so general that it includes cycle skipping and multiple pulsing. Due to their presence, it is inconvenient to analyze the steady-state periodic orbits and their stability. Also, the presence of multiple pulsing can greatly increase the switching losses, which should be avoided by using a latch or sample-and-hold. Therefore, instead of using the generalized one, the detailed analysis of periodic orbits will adopt a specific Poincaré map that the orbits cross the ramp signal once per cycle – mathematically, $m_v=1$.

The steady-state periodic solution of the dc drive system can be a fixed point, so-called the period-1 orbit \mathbf{X}^* , or a cycle point, so-called the period-*p* orbit $\{\mathbf{X}_1^*, \dots, \mathbf{X}_p^*\}$ (*p*>1). The corresponding specific Poincaré maps are described as:

$$\mathbf{X}^* = P(\mathbf{X}^*) \tag{19}$$

$$\mathbf{X}_{k+1}^{*} = P(\mathbf{X}_{k}^{*}) \quad (k = 1, \cdots, p-1), \quad \mathbf{X}_{1}^{*} = P(\mathbf{X}_{p}^{*})$$
(20)

Firstly, the period-1 orbit is analyzed. Since m=1 and the orbit must start with $v_I < v_C < v_u$, the drive system operates in stage 2 whose interval is $\delta_{off}T$ and then in stage 1 whose interval is $\delta_{on}T$. By defining the duty cycle $\delta = \delta_{off}$, the corresponding Poincaré map can be obtained as:

$$\mathbf{X}_{n+1} = -\mathbf{A}^{-1}\mathbf{E}_{off} + \Phi(T)(\mathbf{X}_n + \mathbf{A}^{-1}\mathbf{E}_{off}) + (\mathbf{1} - \Phi(T - \delta T))\mathbf{A}^{-1}(\mathbf{E}_{off} - \mathbf{E}_{on})$$
(21)

Substituting (21) into the map given by (19), the period-1 orbit can be obtained as:

$$\begin{cases} \mathbf{X}^{*} = -\mathbf{A}^{-1}\mathbf{E}_{off} + (\mathbf{1} - \Phi(T))^{-1}(\mathbf{1} - \Phi(T - \delta T))\mathbf{A}^{-1} \\ \times (\mathbf{E}_{off} - \mathbf{E}_{on}) \\ h(\delta) = \mathbf{E}_{3}[-\mathbf{A}^{-1}\mathbf{E}_{off} + \Phi(\delta T)(\mathbf{X}^{*} + \mathbf{A}^{-1}\mathbf{E}_{off}) - \mathbf{E}_{4}] - v_{r}(\delta T) \\ = 0 \end{cases}$$
(22)

Hence, provided that $\delta \in (0,1)$, \mathbf{X}^* can be obtained from (22). Notice that \mathbf{X}^* is the period-1 orbit if and only if v_c always lies between v_l and v_u . Its characteristic multipliers (λ_1, λ_2) are the eigenvalues of the Jacobian matrix of that mapping (21), which is given by:

$$DP(\mathbf{X}^{*}) = \Phi(T) + \Phi(T - \delta T)(\mathbf{E}_{off} - \mathbf{E}_{on})T\frac{\partial\delta}{\partial \mathbf{X}^{*}}$$
(23)

where $\frac{\partial \delta}{\partial \mathbf{X}^*} = \left(\frac{\partial \delta}{\partial \omega}, \frac{\partial \delta}{\partial i}\right)$. According to the implicit-function

theorems, $\frac{\partial \delta}{\partial \mathbf{X}^*}$ can be deduced from (22) as:

$$\frac{\partial \delta}{\partial \mathbf{X}^{*}} = -\left(\frac{\partial h}{\partial \delta}\right)^{-1} \frac{\partial h}{\partial \mathbf{X}^{*}}$$
$$= -[\mathbf{E}_{3} \Phi(\delta T) (\mathbf{A} \mathbf{X}^{*} + \mathbf{E}_{off}) T - (v_{u} - v_{l})]^{-1} \mathbf{E}_{3} \Phi(\delta T)$$
(24)

It is known that the fixed point of the mapping is stable if and only if its characteristic multipliers all lie within the unit circle in the complex plane. If one of their magnitudes equals unity, this fixed point can be a bifurcation point.

Secondly, the period-*p* orbit $\{\mathbf{X}_1^*, \dots, \mathbf{X}_p^*\}$ is analyzed. By defining $\delta_1, \dots, \delta_p$ as the duty cycles of *p* periods with p>1, it indicates that $v_c(t)$ crosses the voltage ramp *p* times at $(nT+\delta_1T), \dots, ((n+p)T+\delta_pT)$, respectively. Based on the specific Poincaré map derived in (21), the *p*-fold iterative Poincaré map can be formulated as:

$$\mathbf{X}_{n+p} = P^{(p)}(\mathbf{X}_n)$$

= $-\mathbf{A}^{-1}\mathbf{E}_{off} + \Phi(pT)(\mathbf{X}_n + \mathbf{A}^{-1}\mathbf{E}_{off})$ (25)
+ $\sum_{j=1}^{p} [\Phi((p-j)T)(1 - \Phi(T - \delta_j T))]\mathbf{A}^{-1}(\mathbf{E}_{off} - \mathbf{E}_{on})$

By using (20), the period-*p* orbit can then be derived as:

$$\mathbf{X}_{1}^{*} = -\mathbf{A}^{-1}\mathbf{E}_{off} + (1 - \Phi(pT))^{-1}\sum_{j=1}^{p} [\Phi((p-j)T) + (26)] \times (1 - \Phi(T - \delta_{j}T))]\mathbf{A}^{-1}(\mathbf{E}_{off} - \mathbf{E}_{on})$$

$$\mathbf{X}_{i}^{*} = -\mathbf{A}^{-1}\mathbf{E}_{off} + \Phi((i-1)T)(\mathbf{X}_{1}^{*} + \mathbf{A}^{-1}\mathbf{E}_{off}) - \sum_{j=1}^{i-1} [\Phi((i-j-1)T)(1 - \Phi(T - \delta_{j}T))] \times \mathbf{A}^{-1}(\mathbf{E}_{off} - \mathbf{E}_{on}) + (i = 2, \cdots, p)$$
(26)

$$h_i(\mathbf{d}) = \mathbf{E}_3 [-\mathbf{A}^{-1} \mathbf{E}_{off} + \Phi(\delta_i T) (\mathbf{X}_i^* + \mathbf{A}^{-1} \mathbf{E}_{off}) - \mathbf{E}_4] - v_r (\delta_i T) = 0 \qquad (i = 1, \cdots, p)$$
(28)

where $\Phi(0) = 1$ and $\mathbf{d} = (\delta_1, \dots, \delta_p)$. By substituting (26) and (27) into (28), \mathbf{d} can be determined. Provided that $\delta_i \in (0,1)$ for $i=1,\dots,p$, $\mathbf{X}_1^*,\dots,\mathbf{X}_p^*$ can then be obtained from (26) and (27). If the condition $v_l < v_c < v_u$ is guaranteed in each period, { $\mathbf{X}_1^*,\dots,\mathbf{X}_p^*$ } is a period-*p* orbit.

Due to the cyclic property of a cycle point, $\{\mathbf{X}_{2}^{*}, \dots, \mathbf{X}_{p}^{*}, \mathbf{X}_{1}^{*}\}, \dots, \{\mathbf{X}_{p}^{*}, \mathbf{X}_{1}^{*}, \dots, \mathbf{X}_{p-1}^{*}\}\)$ are also period-*p* orbits which correspond to the same subharmonic frequency. If $\mathbf{X}_{1}^{*} = \dots = \mathbf{X}_{p}^{*}$, the period-*p* orbit becomes the period-1 orbit, which shows that period-1 is its subset.

For the period-*p* orbits $\{\mathbf{X}_1^*, \dots, \mathbf{X}_p^*\}$, \mathbf{X}_1^* is a fixed point of the *p*-fold iterative Poincaré map. Therefore, its characteristic multipliers (λ_1, λ_2) are the eigenvalues of the Jacobian matrix of that mapping, which is given by:

$$DP^{(p)}(\mathbf{X}_{1}^{*}) = \Phi(pT) + \sum_{j=1}^{p} [\Phi((p-j)T) \\ \times \Phi(T-\delta_{j}T)(\mathbf{E}_{off} - \mathbf{E}_{on})T \frac{\partial \delta_{j}}{\partial \mathbf{X}_{1}^{*}}]$$
(29)

By substituting (28) into (29), it results:

$$\mathbf{H}(\mathbf{d}, \mathbf{X}_{1}^{*}) = [h_{1}(\mathbf{d}, \mathbf{X}_{1}^{*}), \cdots, h_{p}(\mathbf{d}, \mathbf{X}_{1}^{*})]^{T} = \mathbf{0}$$
(30)

Hence, according to the implicit-function theorems, the partial derivative in (32) can be expressed as:

$$\frac{\partial \mathbf{u}}{\partial \mathbf{X}_{1}^{*}} = \left(\frac{\partial \delta_{1}}{\partial \mathbf{X}_{1}^{*}}, \cdots, \frac{\partial \delta_{p}}{\partial \mathbf{X}_{1}^{*}}\right)^{T} = -\left[\frac{\partial \mathbf{H}}{\partial \mathbf{u}}\right]^{-1} \left[\frac{\partial \mathbf{H}}{\partial \mathbf{X}_{1}^{*}}\right]$$
(31)
$$\frac{\partial \mathbf{H}}{\partial \mathbf{H}} \left[\frac{\partial h_{i}}{\partial \mathbf{x}_{1}^{*}}\right]$$

$$\overline{\partial \mathbf{u}} = \left[\overline{\partial \delta_j} \right]_{p \times p}$$
$$= diag[\mathbf{E}_3 \Phi(\delta_i T) (\mathbf{A} \mathbf{X}_i^* + \mathbf{E}_{off}) T - (v_u - v_l)]$$
(32)

$$+ \left[\mathbf{E}_{3} \Phi(\delta_{i}T) \Phi((i-j)T - \delta_{j}T) (\mathbf{E}_{off} - \mathbf{E}_{on})T \right]_{p \times p}$$
$$\frac{\partial \mathbf{H}}{\partial \mathbf{X}_{1}^{*}} = \left[\frac{\partial h_{1}}{\partial \mathbf{X}_{1}^{*}}, \dots, \frac{\partial h_{p}}{\partial \mathbf{X}_{1}^{*}} \right]^{T}$$
(33)

$$= [\mathbf{E}_{3} \Phi(\delta_{1}T), \cdots, \mathbf{E}_{3} \Phi((p-1)T + \delta_{p}T)]^{T}$$

Notice that $\Phi((i-j)T - \delta_j T)$ in (32) becomes a zero matrix when $j \ge i$.

Similarly, the analytical solution of the Poincaré map with $m_v=1$ for the current-mode dc drive system can be derived. Since the system always operates from stage 1 to stage 2, the duty cycle $\delta = \delta_{on}$ is defined. By comparing (18) and (21), it shows that the differences are the replacement of all \mathbf{E}_{on} by \mathbf{E}_{off} and vice versa. Thus, the analytical modeling of the subharmonics derived previously is in fact a unified one for both voltage-mode and current-mode control. By setting $v_r(t)=0$ ($v_u=v_l=0$) and exchanging \mathbf{E}_{off} and \mathbf{E}_{on} in the modeling of the voltage-mode controlled dc drive system, the analytical modeling of the current-mode controlled one can be obtained.

IV. COMPUTER SIMULATION

To illustrate the derived modeling, computer simulation is carried out. The component and parameter values of the voltage-mode dc drive system are as follows: $v_I=0V$, $v_u=2.2V$, T=4ms, g=2, $V_{in}=100V$, $R=3.5\Omega$, L=36mH, $K_E=0.1356V/rads^{-1}$, $K_T=0.1324Nm/A$, $B=0.000564Nm/rads^{-1}$, $J=0.000971Nm/rads^{-2}$, $T_I=0.39Nm$, $\omega_{ref}=100rads^{-1}$. Those of the current-mode dc drive system are as follows: T=4ms, $V_{in}=100V$, $g_i=1$, $g_{\omega}=2$, g=2, $R=3.5\Omega$, L=66mH, $K_E=0.1356V/rads^{-1}$, $K_T=0.1324Nm/A$, $B=0.000164Nm/rads^{-1}$, $J=0.000571Nm/rads^{-2}$, $T_I=0.39Nm$, $\omega_{ref}=100rads^{-1}$.

A. Bifurcation diagrams Using Numerical Computation

By employing standard numerical techniques such as the brute-force algorithm [10] to compute the Poincaré map given by (12)-(15) for the voltage-mode dc drive system, the bifurcation diagrams of motor speed ω versus input voltage V_{in} and gain g can be resulted as shown in Figs. 5 and 6, respectively. The corresponding bifurcation diagrams ω versus V_{in} and g by computing the Poincaré map given by (16) and (17) for the current-mode dc drive system are also shown in Figs. 7 and 8.



Fig. 5. Bifurcation diagram of ω vs V_{in} using voltage-mode control.



Fig. 6. Bifurcation diagram of ω versus g using voltage-mode control.







It can be found that the system exhibits a typical period-

doubling route to chaos. As shown in Fig. 5, the voltagemode dc drive system bifurcates to subharmonics and finally route to chaos as V_{in} successively increases. On the contrary, when V_{in} gradually decreases as shown in Fig. 7, the currentmode system emanates a period-doubling route to chaos. It indicates that the subharmonics and chaotic behavior caused by the same system parameter may be contrary when the drive system adopts different feedback control modes.

B. Bifurcation Diagrams Using Analytical Approach

Firstly, the voltage-mode dc drive system is analyzed. Based on the unified modeling in (22) for the period-1 orbit, the solution of duty ratio δ for a range of V_{in} varying from 40V to 160V is shown in Fig. 9. Hence, by using (23) and (24), the corresponding eigenvalues of the Jacobian matrix DP is shown in Fig. 10 in which one of the amplitudes is greater than 1 when V_{in} >110.8V. It indicates that the period-1 orbit is unstable when V_{in} >110.8V.

Similarly, for the current-mode dc drive system, the duty ratio δ and the corresponding characteristic multipliers $|\lambda_1, \lambda_2|$ for V_{in} varying from 30V to 200V is shown in Fig. 11. It can be found that one of the magnitudes is greater than 1 when $V_{in} < 102$ V, indicating that the period-1 orbit is unstable when $V_{in} < 102$ V.

It should be noted that the above analytical results closely agree with the previous numerical results. It is obvious that the required computational time based on the derived analytical solution is extremely less than that required for computation using the numerical brute-force algorithm. Thus, the proposed analytical approach can greatly facilitate the identification of the desired stable operating ranges of the dc motor drive system for different system parameters and conditions.

C. PSpice Verification

Based on the same operating parameters and conditions, realistic PSpice simulation of the drive system is performed. As shown in Fig. 12, the PSpice-simulated waveforms of v_{ω} , v_i and clock pulse at V_{in} =90V illustrate that the system is operating at the period-2 orbit with m_i =1. When V_{in} =45V, those waveforms are in chaos as shown in Fig. 4. These system behaviors closely agree with the previous theoretical bifurcation diagrams.

V. CONCLUSION

In this paper, the second-order generalized iterative maps that describes the nonlinear dynamics of both voltage-mode and current-mode controlled dc drive systems during the continuous conduction mode of operation has been derived. Based on the derived map, computer simulation reveals that the system exhibits a typical period-doubling route to chaos. Moreover, the effect on subharmonics and chaotic behavior caused by the same system parameter may be contrary when the drive system adopts different feedback control modes. The unified analytical modeling for analyzing subharmonics of both voltage-mode and current-mode dc drive systems as well as their stability analysis based on the evaluation of the characteristic multipliers have also been presented. These can greatly facilitate the identification of the desired stable operating ranges with different system parameters and conditions. The theoretical results have been verified by comparing with the bifurcation points occurred during PSpice simulation.



Eigenvalues









Fig. 12. PSpice-simulated v_{a} , v_{i} and clock pulse for period-2 orbit.

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